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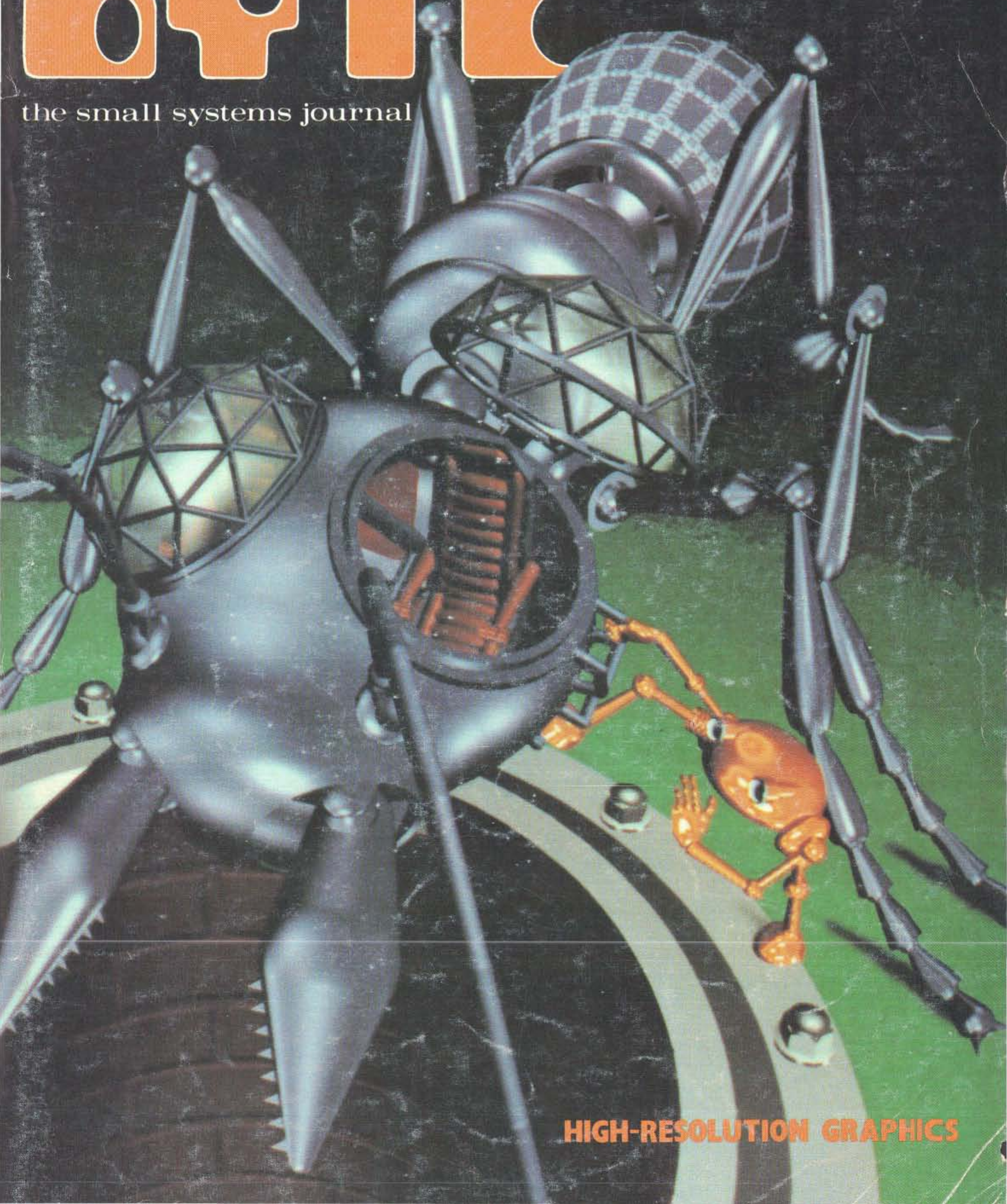
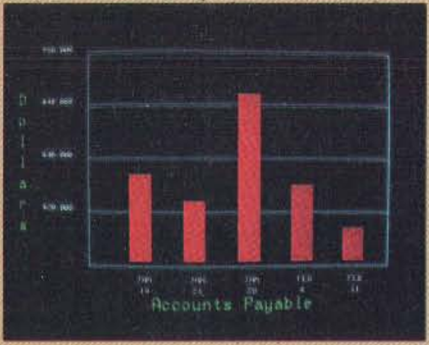
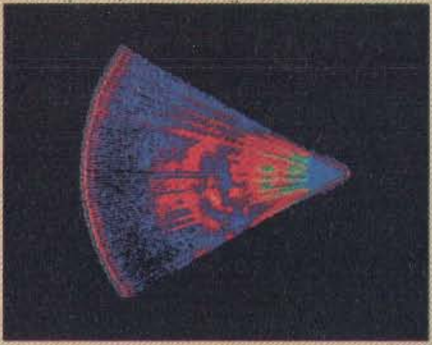
the small systems journal



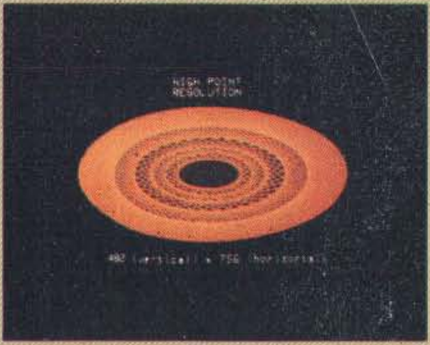
HIGH-RESOLUTION GRAPHICS



Management Information Display



Ultrasonic heart sector scan



High-resolution display with alphanumerics

Get the professional color display that has BASIC/FORTRAN simplicity

LOW-PRICED, TOO

Here's a color display that has everything: professional-level resolution, enormous color range, easy software, NTSC conformance, and low price.

Basically, this new Cromemco Model SDI* is a two-board interface that plugs into any Cromemco computer.

The SDI then maps computer display memory content onto a convenient color monitor to give high-quality, high-resolution displays (756 H x 482 V pixels).

When we say the SDI results in a high-quality professional display, we mean **you can't get higher resolution than this system offers in an** NTSC-conforming display.

The resolution surpasses that of a color TV picture.

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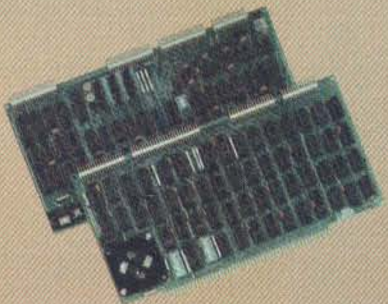
Besides its high resolution and low price, the new SDI lets you control with optional Cromemco software packages that use simple BASIC- and FORTRAN-like commands.

Pick any of 16 colors (from a 4096-color palette) with instructions like DEFCLR (c, R, G, B). Or obtain a circle of specified size, location, and color with XCIRC (x, y, r, c).

*U.S. Pat. No. 4121283



Model SDI High-Resolution Color Graphics Interface

HIGH RESOLUTION

The SDI's high resolution gives a professional-quality display that strictly meets NTSC requirements. You get 756 pixels on every visible line of the NTSC standard display of 482 image lines. Vertical line spacing is 1 pixel.

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The Model SDI has been used in scientific work, engineering, business, TV, color graphics, and other areas. It's a good example of how Cromemco keeps computers in the field up to date, since it turns any Cromemco computer into an up-to-date color display computer.

The SDI has still more features that you should be informed about. So contact your Cromemco representative now and see all that the SDI will do for you.

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Yes, the Cromemco Model Z-2H is in a class by itself in the computer field.

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- **RATFOR (RATional FORtran)**
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And more all the time.

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The Z-2H is clearly in a class by itself. We introduced it last summer. It's field proven. It's reliable.

And it's rugged. Housed in a sturdy, all-metal cabinet.

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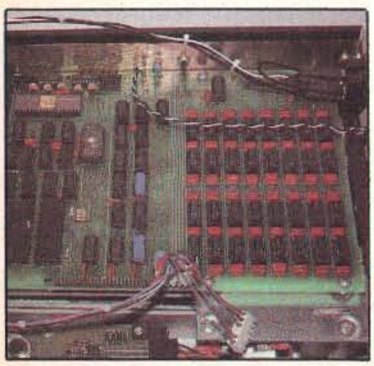

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In This Issue

The cover for this issue of BYTE is a still from a 90-minute computer-animated cartoon called *The Works*. The photo was provided by Dick Lundin and Lance Williams and is constructed from quadric surfaces and polygons, using texture-mapping and normal-perturbation techniques. The background was painted by Paul Xanter—programming credit also goes to Tom Duff and Duane Palyka. A trailer of *The Works* was shown at SIGGRAPH '80 (page 172), although the film itself may not be finished for another two years.

A number of the articles for this month's theme were solicited with the help of Jay Nickson and Ken Lodding; their editorial begins on page 6. Both are employed by DEC (Digital Equipment Corporation): Jay is the manager of the *human interface* program for simplifying man/machine communications, Ken is a senior software engineer whose long-term interests intermix art and computer graphics.

Publisher's Note

As most readers will have observed, the September Fifth anniversary issue marked the beginning of a new phase for BYTE. The jump from a 300-page to a 400-page issue means a 33% increase in the material presented to our readers each month.

Because advertisements tend to be more visible than editorial content (especially in a technical journal), some readers may suspect that the larger issues mean merely more ads. But, in fact, the larger issues have approximately one third more editorial content. The new size does create design and manufacturing problems, however. The solution to these problems includes a redesign of the editorial pages of BYTE to make the editorial content easier to find and use. We expect the new format to be implemented early in 1981.

We are confident that the increased editorial content and new format will make BYTE even more of a bargain as well as a more useful tool for our readers. And that, after all, is what it's all about.

Virginia Londoner
Publisher

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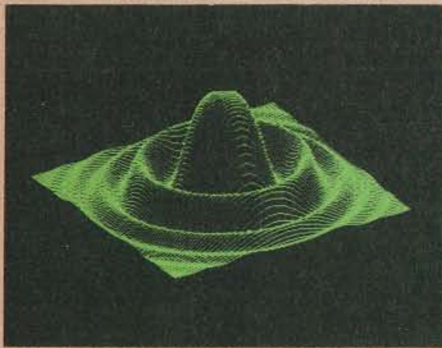

MICROANGELO

HIGH RESOLUTION GRAPHICS SINGLE BOARD COMPUTER

by
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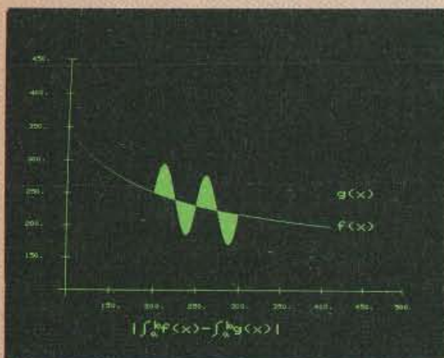

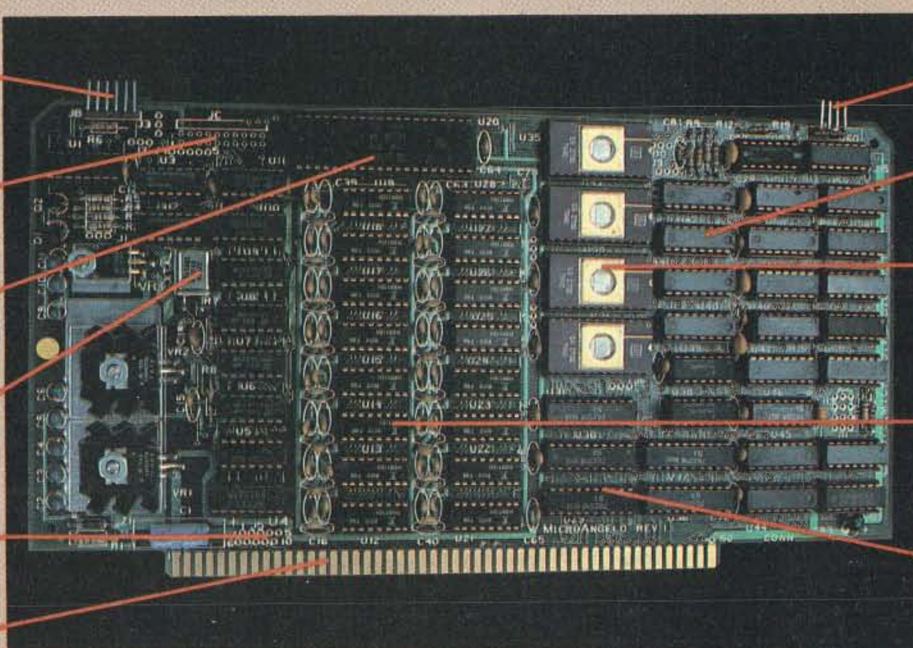

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Editorial

The World of Computer Graphics

Guest Editorial by Ken Lodding and Jay Nickson

Man is a visual animal. He surrounds himself with graphic images. Images are employed to convey information, to explain concepts, and to communicate feelings. The ability to draw is instinctive. It materializes in infants soon after the start of verbal development, perhaps to complement the slowly developing verbal skills. Although the ability to draw tends not to become as fully developed as verbal skills, images continue to provide much of the adult human communications ability. Pictures are a primary information-carrying channel: the histogram accompanying a financial article, the plot of a mathematical function, and the illustrations in BYTE are but a few examples.

The importance of graphics for conveying information arises from the nature of man's visual system. The eye provides an extremely high-bandwidth information channel for transferring the data to be processed by the brain's optic center. The importance of this channel can be seen from the redundancy built into the system and from the distribution of optic nerve fibers in the brain. It is believed that no less than six different brain sites are directly serviced by connecting optic nerve fibers. (See reference 4.) The fundamental importance of visual information is reflected in the old adage, "seeing is believing," and in the observation that *understand* is one of the synonyms of the word *see*. Text fails to use our native abilities to comprehend information fully because it presents data in a linear, sequential fashion. Contrast this with graphical images, which can be processed in a single viewing—a phenomenon called *preattentive perception*. (See reference 6.)

The computer has become a primary source or conveyor of information, yet the main interface between man and machine has remained the serially oriented text display. The net result is that, as the volume of data available to be presented increases, the user's communication channel becomes swamped with an avalanche of text output. The volume of this avalanche far too often restricts the comprehension of the information. The information is obscured as effectively as if it had been encrypted. The spectacle of the computer user literally buried under reams of printed output has ceased to be an amusing cartoon and has become a nightmare for too many. To cope with the flood of information, the computer user is turning to graphics.

The information-transfer rate of a graph can be many orders of magnitude greater than an equivalent text presentation. Conceptually, a graph has greater information density than a table. Compare the plot of a sine curve with a table of sine values. Each value within the table corresponds to a specific point on the graph. However, the plot displays a far greater number of points than could the most extended table. A high information-transfer rate results from the greater data density and the faster operation of the human mind and visual system. Patterns, periodic functions, trends, and comparisons can often be obtained "by inspection" of a graph, while understanding a tabular display requires much more time and effort. This is not, however, accomplished without a cost. The only penalty paid for speed is the loss of precision: a graph cannot be read to the same number of significant digits as can be obtained from a table. This loss of precision is not a problem, as the specific data value of interest can be extracted from the function or table of data used to generate the plot initially.

About the Authors

Ken Lodding and Jay Nickson are employed by the Digital Equipment Corporation in Merrimack, New Hampshire.

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"The last thing you need when you put your personal computer or small business system to work is a disk drive that you can't rely on. If the drive quits, your system is out of business."

That's why more and more manufacturers and dealers depend on Shugart disk drives for reliable data storage. These professionals don't want disk drive problems any more than you do. Shugart has a large family of drives, too—in all sizes and capacities to suit your system storage needs. For the smaller system, the original 5¼-inch Minifloppy™ stores 250 to 500 kilobytes (single or double-sided)—that's about 50 to 100 pages of printed material. Our single and double-sided 8-inch floppys store 800 to 1600 kilobytes. And for systems that need a larger data base, our 8-inch or 14-inch fixed disk drives store from 5 to 58 megabytes. No other manufacturer offers such a wide variety of disk storage for personal computer and small business systems.

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In addition to presenting data in a rapid, meaningful fashion, an important benefit of computer graphics is the ability to present images realistically. Plotting a topological surface, modeling DNA, creating an architectural rendering, and simulating a pilot's view from the cockpit of an aircraft are all enhanced by presenting the image in a manner which gives the viewer a sense that the picture is not an illusion. To achieve greater realism, a prime factor is to provide the illusion of depth. Perspective, hidden-line removal, shading, and highlighting all provide depth cues to the viewer. This month's computer-generated cover by Lance Williams of the New York Institute of Technology clearly illustrates the current state of the art as applied to an artistic endeavor. The same techniques are available and can be employed when graphically representing numeric data.

Three-Dimensional Graphics

To provide the illusion of depth, a three-dimensional model can be defined. Establishing the viewer's geometric relationship to the model and following the rules of perspective, the model image is mathematically projected onto a two-dimensional viewing plane. Although providing good visual depth cues (eg: parallel lines appearing to meet at a point), there is no real illusion of depth; in other words, the model image is still "flat." To correct this, the phenomenon of *stereopsis* (from the Greek, meaning "solid sight") can be employed. You may be familiar with the 1847 Brewster stereoscope. Here, the approach taken to give the illusion of depth was to photograph the same scene twice, having moved the camera about 6 cm sideways between photos. The two images could then be viewed through a stereoscope that utilized a prism and lens system to alter the image paths to the eye, so that the two views seemed to originate from a common point. (The old-fashioned stereopticon and the modern View-Master are variations on this theme.) The observer's visual system fused the two images, giving the illusion of a three-dimensional image.

Various computer-graphic techniques using the same principles have been developed. A common technique is to employ glasses with electro-optic shutter eyepieces to provide the image separation. With the electro-optic glasses, the *cyclopic* video display presents left- and right-perspective images in alternate frames, which are then synchronized with the electro-optic shutters. The left eye is presented with the left stereograph, while the right eye's view is blanked by the optical shutter; the image and shutter swap for the right eye. The viewer's internal visual system fuses the image to give the appearance of depth. For an example of this, see "The Future of Computer Graphics," page 22.

A different approach to providing left and right images to the visual system uses color to separate the images. Using a device called an *anaglyph*, the left view is presented in one color, and the right in a different color. Color filters control which eye sees what view. A program for generating and viewing anaglyphs is presented in the article "Three-Dimensional Graphics for the Apple II." (See page 148.) While the traditional colors employed are red and green, any two colors and corresponding filters could be used, because the illusion is based on the separation of the images, and has nothing to do with the particular colors. The phenomenon is as apparent to a

color-blind person as it is to a viewer with normal color vision. For those interested in further information, the book *Seeing* is an excellent reference on vision in general and stereopsis in particular. (See reference 4.)

A more recent and unique approach to presenting three-dimensional images is SpaceGraph, developed by Dr Larry Sher. His technique uses a vibrating mirror and a video display. The technique is to generate on the display two-dimensional "slices" of the three-dimensional object to be viewed. The slices are rapidly generated in synchronization with the dynamic motion of the mirror, the front slice being generated when the mirror is extended toward the viewer, the back slice when the mirror is concaved away from the viewer, and the intermediate slices as appropriate for the travel of the mirror between these extremes. The rapid sequence of images is fused by the viewer's visual system to give the illusion of a "space filling" object. (See reference 7.)

Those adventuresome souls who find three-dimensions insufficient for their purposes can use computer graphics as an aid for visualizing objects which, theoretically, exist in four or more dimensions. If you are interested in this area, *Hypergraphics* is a good introduction to the subject. (See reference 3.) The book includes hyperstereograms of such objects as hypercubes or tesseracts, hypercones, and other denizens of higher dimensions.

Animation is another technique that can assist in user comprehension of data. Often we are dealing with information gathered at discrete intervals over a period of time. Here, the problem of analyzing data is one of understanding what is occurring to the data elements over some length of time. Animation provides a looking glass into the time domain. Flowing, three-dimensional images can represent anything from an economic world model to a bridge under stress.

Hidden Benefits

There are times when animation provides the viewer with unexpected information—information which, in retrospect, was present but not readily discernible by any other method of examination. An interesting example of this situation involves the simulation of an internal combustion engine. The simulation, performed at a research laboratory, wrote out data in the conventional manner: stacks of numbers. At the same laboratory, some time after the engine simulation had been completed and used for experiments, a different group of researchers developed a computer-animation system. The engine simulation was selected as a good demonstration of the new graphics software, and a computer-generated film was produced. During the screening of the film it was noticed that small rectangular elements, used to represent idealized gas packets, displayed a strange, unexpected oscillation at their endpoints. Review of the animation software provided no explanation for this erratic behavior. Close examination of output from the original simulation revealed that the oscillations were indeed present. This fact had not been previously noticed because the information had been obscured by a combination of the tremendous amount of data, the smallness of the oscillation, and the extended period over which it occurred. What had in fact been found were acoustical-wave phenomena occurring within the cylinder of the engine, which could potentially be used for the development of more efficient engines. The events went unnoticed until a computer-generated movie was constructed.

In the 30 years since its beginnings, computer-generated graphics has grown steadily, but not spectacularly. Previously the costs of both the display and the computer resources needed to support graphic displays have limited the impact. Rapidly falling memory prices and television technology have renewed the interest in computer graphics. The combination of a television raster display and a memory-intensive, bit-mapped architecture makes possible a graphic system capable of providing full-color, dynamic images with previously unheard of realism and economy. "Micrograph, Part 1: Developing an Instruction Set for a Raster-Scan Display," describes the design and construction of a color-display processor that costs approximately $250 to build. (See page 64.) This is possible only because of the plummeting cost of hardware. This is a cost reduction of three orders of magnitude in 15 years, with color added for free!

Graphics Software

The advent of inexpensive graphics hardware has, not unexpectedly, spurred the development of graphics software. The traditional approach for supporting graphics has been to provide a collection of subroutines that perform the graphic-display functions. These subroutines are called from languages whose orientation is toward the manipulation of text and numerical data. This approach is fine if you only want to accumulate data and make a

Why not kill two birds with one stone?

If you have an Apple* and you want to interface it with parallel and serial devices, we have a board for you that will do both. It's the AIO.™

Serial Interface.

The RS-232 standard assures maximum compatibility with a variety of serial devices. For example, with the AIO you can connect your Apple* to a video terminal to get 80 characters per line instead of 40, a modem to use time-sharing services, or a printer for hard copy. The serial interface is software programmable, features three handshaking lines, and includes a rotary switch to select from 7 standard baud rates. On-board firmware provides a powerful driver routine so you won't need to write any software to utilize the interface.

Parallel Interface.

This interface can be used to connect your Apple* to a variety of parallel printers. The programmable I/O ports have enough lines to handle two printers simultaneously with handshaking control. The users manual includes a software listing for controlling parallel printers or, if you prefer, a parallel driver routine is available in firmware as an option. And printing is only one application for this general purpose parallel interface.

Two boards in one.

The AIO is the only board on the market that can interface the Apple to both serial and parallel devices. It can even do both at the same time. That's the kind of innovative design and solid value that's been going into SSM products since the beginning of personal computing.

The AIO comes complete with serial PROM's, serial and parallel cables, and complete documentation including software listings.

See the AIO at your local computer store or contact us for more information.

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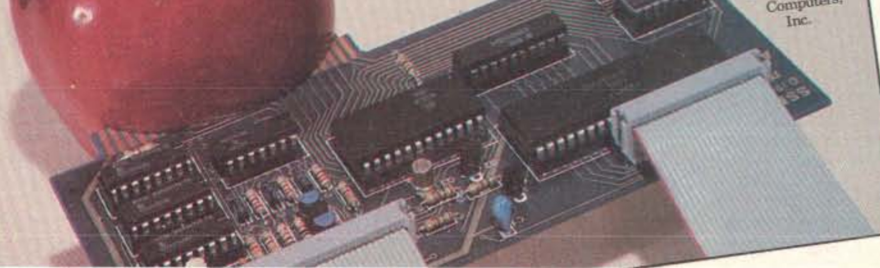

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Maybe we can save you a call.

Many people have called with the same questions about the AIO. We'll answer those and a few more here.

Q: Does the AIO have hardware handshaking?
A: Yes. The serial port accommodates 3 types—RTS, CTS, and DCD. The parallel port handles ACK, $\overline{ACK}$, BSY, STB, and $\overline{STB}$.

Q: What equipment can be used with the AIO?
A: A partial list of devices that have actually been tested with the AIO includes: IDS 440 Paper Tiger, Centronics 779, Qume Sprint 5, NEC Spinwriter, Comprint, Heathkit H14, IDS 125, IDS 225, Hazeltine 1500, Lear Siegler ADM-3, DTC 300, AJ 841.

Q: Does the AIO work with Pascal?
A: Yes. The current AIO serial firmware works great with Pascal. If you want to run the parallel port, or both the serial and parallel ports with Pascal, order our "Pascal Patcher Disk."

Q: What kind of firmware option is available for the parallel interface?
A: Two PROM's that the user installs on the AIO card in place of the Serial Firmware PROM's provide: Variable margins, Variable page length, Variable indentations, and Auto-line-feed on carriage return.

Q: How do I interface my new printer to my Apple using my AIO card?
A: Interconnection diagrams for many popular printers and other devices are contained in the AIO Manual. If your printer is not mentioned, please contact SSM's Technical Support Dept. and they will help you with the proper connections.

Q: I want to use my Apple as a dumb terminal with a modem on a timesharing service like The Source. Can I do that with the AIO?
A: Yes. A "Dumb Terminal Routine" is listed in the AIO Manual. It provides for full and half duplex, and also checks for presence of a carrier.

Q: What length cables are provided?
A: For the serial port, a 12 inch ribbon cable with a DB-25 socket on the user end is supplied. For the parallel port, a 72 inch ribbon cable with an unterminated user end is provided. Other cables are available on special volume orders.

The AIO is just one of several boards for the Apple that SSM will be introducing over the next year. We are also receptive to developing products to meet special OEM requirements. So please contact us if you have a need and there is nothing available to meet it.

SSM

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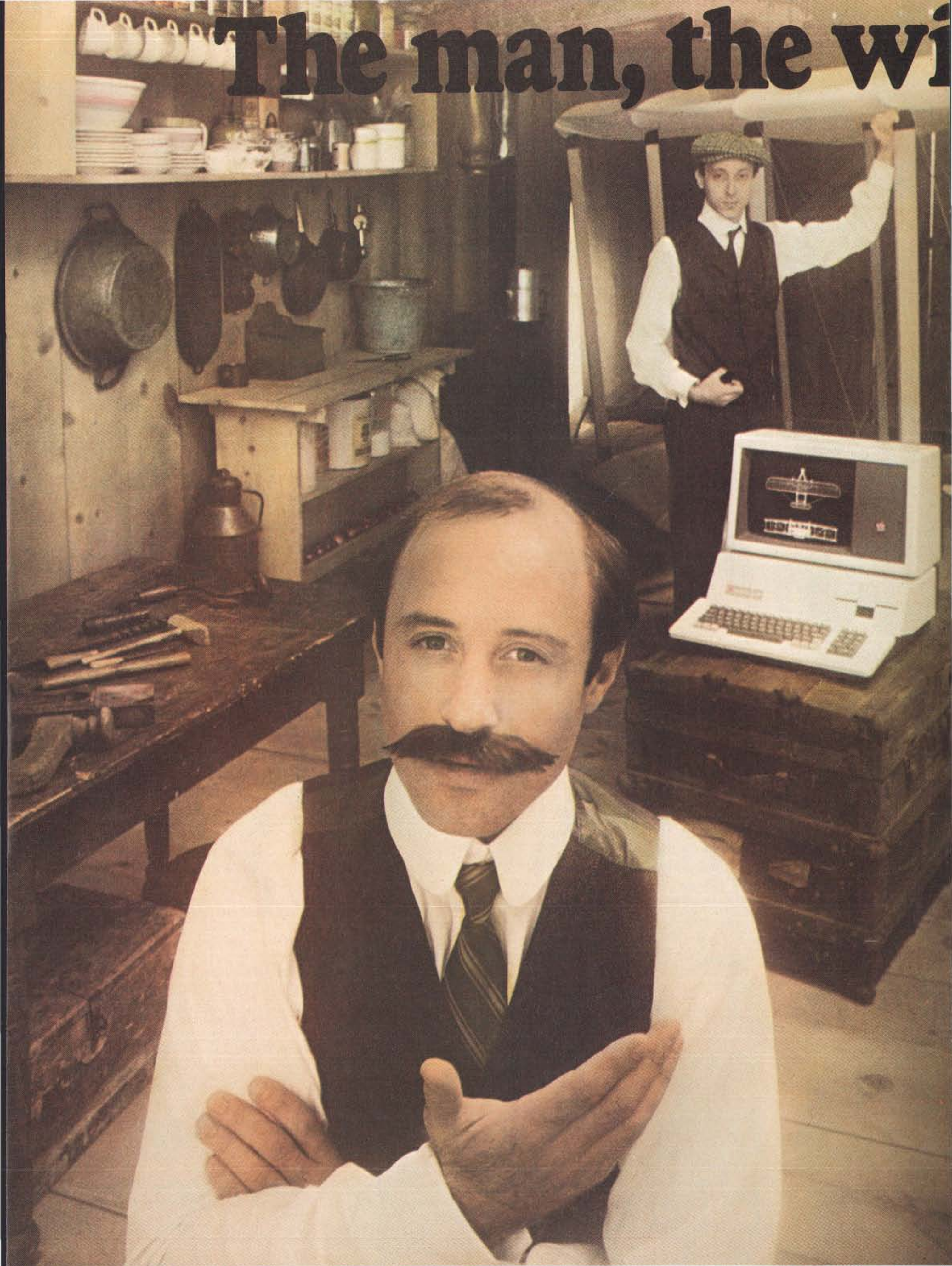
The man, the wi

ng and the Apple.

If you could talk to Orville Wright, he'd tell you the problems he faced as a turn-of-the-century engineer. You could tell him all about the technological solutions available to today's engineer and scientist… particularly a 20th century phenomenon that tests assumptions and defines models before a project gets off the ground. The Apple personal computer.

Computation, calculation, analysis...the power to pilot your projects.

With a highly-integrated system from the extensive Apple personal computer family, Orville and brother Wilbur would have increased their productivity. Perhaps even launched the Kitty Hawk Flyer well before 1903.

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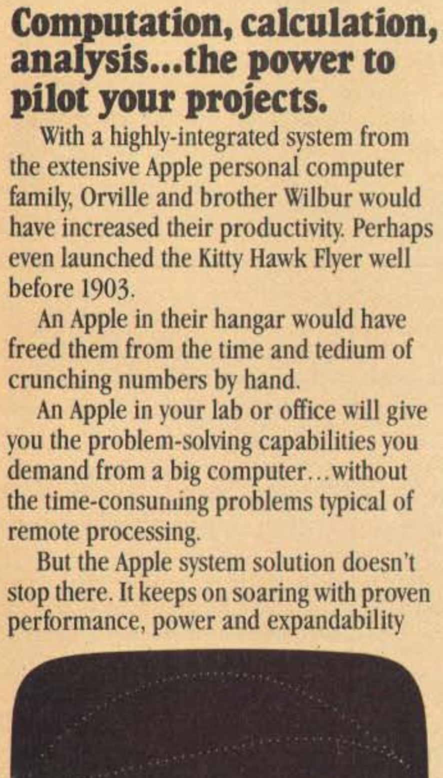
But the Apple system solution doesn't stop there. It keeps on soaring with proven performance, power and expandability



Apple's existing software library includes a program that plots the shape of an airfoil, given its parameters.

that's unparalleled for analyzing alternative paths of design and modeling a wide variety of physical processes.

Want more memory? Depending on your choice of system, Apple has memory expandable to 64K bytes or 128K bytes. Prefer wide displays? Choose 40 or 80 characters. Need to control instruments in the lab? Get on the IEEE 488 bus. Over 100 companies also supply peripherals for Apple because Apple is the most popular personal computer with the least complicated interface.

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Fluent in the same language that helped to design the 747, Apple FORTRAN lets you tackle differential equations at the touch of a key. And since more than 170 companies also offer software for the Apple family, you can have one of the most impressive program libraries ever…including vast subroutine libraries for math, science, engineering and statistics. When you write your own programs, the Apple also speaks in languages other than FORTRAN: Pascal, BASIC, PILOT and 6502 assembly language.

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apple computer

picture from it. The subroutine approach excludes the possibility of treating graphical objects as variables within the language, or using them within statements and expressions. Some research work has been done which includes the concept of graphical objects and operators within a language structure. To date, there have been a number of different approaches to the problem of handling graphical objects. Deeply intertwined in the problem is our fundamental lack of understanding of how to provide graphics support. Viewed from the perspective of a language, what fundamental primitives must be provided? What are the appropriate data types? How are expressions constructed? What operators need to be provided? The list of unknowns goes on and on. "Language Control Structures for Easy Electronic Visualization," by Dr Tom DeFanti, addresses this area. (See page 90.) Some examples of other, experimental, graphics languages are given in references 2 and 5. SHAZAM (Smalltalk's sHaded imAge Zippy Animated Moviemaker) is an interesting animated-movie language written in Smalltalk. (See reference 1.) In no way does this list exhaust the progress that has been made in graphics languages, but rather it reflects a small sampling of recent work.

All the aspects of graphics we have discussed allow us to construct windows into universes, real or imaginary. Computer graphics is exciting because with this tool we can witness the unraveling of a DNA molecule, or the collision of galaxies. We can watch the structure of the universe as it expands from the moment of the theoretical big bang, or, reversing entropy, see it collapse into the primordial particle. We can plot a mathematical function, view an economic trend, or travel faster than light to where robotic insects populate metallic worlds. Best of all, we can make it all seem real, because we can see it! ■

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Articles Policy

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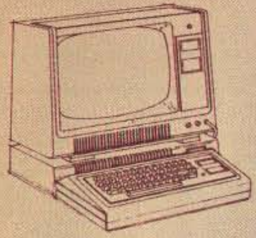

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Moore Praise Comes FORTH

If FORTH is trickery, give me more trickery.

In my view, FORTH is a common-sense approach to programming. Granted, there are also bits of pure genius thrown in.

It makes sense to put all the routines used by the operating system, compiler, parser, editor, etc, in one dictionary conveniently accessible to the user at all times. That is, if they will fit. One of the bits of genius of FORTH is that they do indeed fit with room to spare for user-defined routines. The result is instant liberation from the "systems man" who tries but can't please everyone. It is your computer, and with FORTH you have access to everything on it.

It makes sense to use a stack to pass parameters between routines and to separate this stack from the return-address stack. You end up with a language that is designed to compute rather than to be read. Every step in FORTH is directed toward computing a result. FORTH is a sequence of commands rather than statements as found in BASIC or Pascal. The functions of computing and documentation are separated. Hence I strongly disagree with Gregg Williams' advice (see August 1980 BYTE, page 130) that the user should introduce intermediate variables to improve readability. I concur with his objective, but I would encourage their use only in the commentary where they belong. There is no point to introducing unnecessary variables in the computing process. In the commentary, intermediate variables can and should be used very effectively to help describe the computations that are occurring on the stack without interfering with the process.

While FORTH takes away the expository statement, it does give back an important documenting feature, namely relative ease in preparing precise common-language definitions of each routine. All FORTH routines have a describable goal, and most of the action takes place on the stack. Hence FORTH routines tend to be simpler to describe. I have never seen a glossary for a language or operating system that comes even close to the completeness and conciseness of the fig-FORTH glossary supplied by the FORTH Interest Group. It is a gem, a complete English-language description of FORTH. Every routine on the computer is concisely defined in English.

You have to have faith that taking the sacred function of documenting out of the language and turning it over to the user to do as he sees fit will work. After a while, you begin to wonder if Milton Friedman didn't write FORTH for his television series *Free to Choose*.

Finally, it makes sense to give the programmer a shot at controlling the compiler, especially when the compiler has access to all the routines of the system. C H Moore has shown with FORTH that compilers do not have to be large inflexible systems which try to take into account every eventuality and really can't do it. The result of this bit of FORTH trickery is a powerful compiler so tiny that it can be made interactive and used on line with no batch processing, linking loader, or other monstrosity which we are accustomed to associate with a compiler.

How small (or big) is tiny? The fig-FORTH system supplied by the FORTH Interest Group for the 6502 contains 220 primitive routines (not including the Editor or Assembler) that occupy a total of 6221 bytes. By my count, 34 of these routines are compiler functions, and they occupy a total of 982 bytes. My guess is that this is an order of magnitude smaller than other compilers of comparable power. That is trickery.

If there ever is a contest for the all-time ingenious software development, I would like to nominate C H Moore's best, the { ;CODE } routine and/or its logical extension
{ <BUILDS ... DOES> }.

Edgar H Fey Jr
Edgar H Fey Jewelers Inc
1156 Fox Valley Ctr
Aurora IL 60505

Flash: Magic Exists!

I was delighted to see an issue of BYTE devoted to FORTH. As a user of and tinkerer with STOIC for 5 years, I heartily agree with the various authors' ravings about the extensibility, flexibility, and increase in productivity provided by FORTH. I was, however, amused at the many ways in which postfix (reverse-Polish) notation was rational-

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ized as being a better or more efficient way to do things even though it renders programs "write only" or at best difficult to read.

Since maintainability of programs becomes even more critical when productivity is increased tenfold or more, I feel that the requirement of postfix notation by FORTH is a serious shortcoming. There is nothing mystical about postfix notation; all compilers and interpreters must eventually reach this form because that is the order in which the computer must carry out its operations.

Over the past two years Jeff Morris and I have added various superstructures onto FORTH (one per application) that attempted to combine the better features of Pascal (eg: record structures, algebraic notation) with the power and flexibility of FORTH. The outcome of all of these experiments was a conceptual breakthrough which resulted in the invention of Magic. Magic has all the advantages of FORTH, plus, Magic programs are readable (thus maintainable).

For example, the FORTH (or Magic) statement:

```
B@ C@ + A@ * A!
```

can also be written in Magic as:

```
A: = A*(B + C)
```

and in fact compiles in three fewer words (since the @s are not needed), and the FORTH (or Magic) statement:

```
A@ B@ = IF
```

can also be written in Magic as:

```
IF(A.EQ.B)
```

Magic is a major enhancement to the basic compilation structure of FORTH (a metaFORTH), not simply an add-on superstructure. Magic programs typically compile more slowly (due to the increased complexity of the compiler) but require less memory and run faster than equivalent FORTH programs.

The concept of metaFORTH is discussed briefly in the article by Kim Harris. (See "FORTH Extensibility: or How to Write a Compiler in Twenty-five Words or Less," August 1980 BYTE, page 164.) This is the direction of the future and will be the source of some super-powerful programming tools in the next decade. Magic is a first step in that direction.

I hope and expect that new metaFORTH languages such as Magic will be developed so that FORTH users can have their cake and eat it too. The time has come to stop justifying the unreadability of postfix notation.

Arnold Epstein PhD
Director, Software Development
Octek Inc
7 Corporate Pl
S Bedford St
Burlington MA 01803

Needs Tektronix Secrets

Can a BYTE reader help me? I have a Tektronix 4051 computer which came with a BASIC interpreter. Some of my programs must run faster, and I would like to rewrite them in machine code. Tektronix states that machine code is unsupported on the 4051 and suggests spending another $10,500 for a faster Model 4052. Someone somewhere is programming the 4051 in machine code, as "Space Tag" on the demonstration tape is in machine code and runs incredibly faster than ordinary BASIC programs.

Richard Daily
800 Charlesgate Dr
St Louis MO 63122

Information Please

I recently acquired a Video Brain home computer built by A Umtech Company. The serial number is 003087 and the model number is 101A. It was built in either Santa Clara or Sunnyvale,

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California. I understand it has a Fairchild F-8 8-bit microprocessor. It has 1 K bytes of programmable memory and 4 K bytes of read-only memory.

What I am looking for are cartridge programs, which have a 45-terminal bus, the expander sets, or anything that would be interchangeable. Also, any information or leads would be gratefully appreciated by me and my friends.

Richard L Rowland
7072 Kenwood
Las Vegas NV 89117

An Overlooked FORTH Vendor

The staff at Datricon Corporation was both delighted and disappointed with the August 1980 BYTE. Our delight stems from the extensive coverage of the language FORTH and Charles H Moore's interesting article, "The Evolution of FORTH, an Unusual Language," page 76.

However, we were disappointed with BYTE's failure to mention Datricon's ACS 12-PRO or Datricon's 4 K D-FORTH. Datricon's implementation of FORTH resides in 4 K bytes of EPROM (erasable programmable read-only memory), produces code that can be placed into ROM (read-only memory), and provides for interrupt handling and the automatic setting of the data-transfer rate. Our ACS 12-PRO, with D-FORTH and the STD BUS interface, is a very powerful 6800-based single-board computer. A development package is also available for generating application EPROMs.

Jed W Heald, President
Datricon Corporation
7911 NE 33rd Dr
Suite 200
Portland OR 97211

*We at BYTE were surprised to find additional FORTH vendors advertising in our August 1980 issue. Other vendors include Rockwell International (for the AIM microcomputer, see page 67 of the August 1980 BYTE), Kenyon Microsystems (for 6809 systems, see page 104 of the same issue), Sirius Systems (for the Radio Shack TRS-80, see page 171), Quality Software (for the Exidy Sorcerer, see page 208), Eric Rehnke (for the KIM, SYM, and AIM computers, see page 290), the Software Farm (for the TRS-80, see page 292), and Professional Management Services (for the Alpha Micro, see page 294). FORTH vendors not listed in the August 1980 BYTE are invited to submit a two-paragraph product release, which will be published in a future BYTE "What's New?" column....***GW**

FORTH Is Better Than LISP, He Cs

Unlike BYTE's earlier issue on LISP, the August issue on FORTH did an excellent job in making this intriguing language readily understood. The articles did not come right out and say that FORTH is so machine-efficient due to the user preprocessing his logic into postfix notation, but most readers should realize this.

Although I can tolerate that sort of notation for a desk calculator, it is unbearable for computer data processing. Although the C language is philosophically different, it is a threaded language which is much preferable.

Dick Sims
185 Freeman St, Apt 951
Brookline MA 02146

Check Out a Computer

I always look forward to the new issue of BYTE and was especially eager to read the July 1980, Computers and Education issue. Arthur Luehrmann's article, "Computer Illiteracy—A National Crisis and a Solution for It," page 88, struck home on a point with which I wholeheartedly agree: "this country's general public is woefully ill-prepared to live and work in the Age of Information."

I was, however, disturbed by the fact that the role of public libraries was never mentioned. Public libraries are in a unique position to help solve the problem: they serve people of all ages, regardless of educational background; they are generally open more hours than schools; they are, perhaps more than any other institution, vitally interested in an information-aware public; they specialize in providing access to information, and they are free.

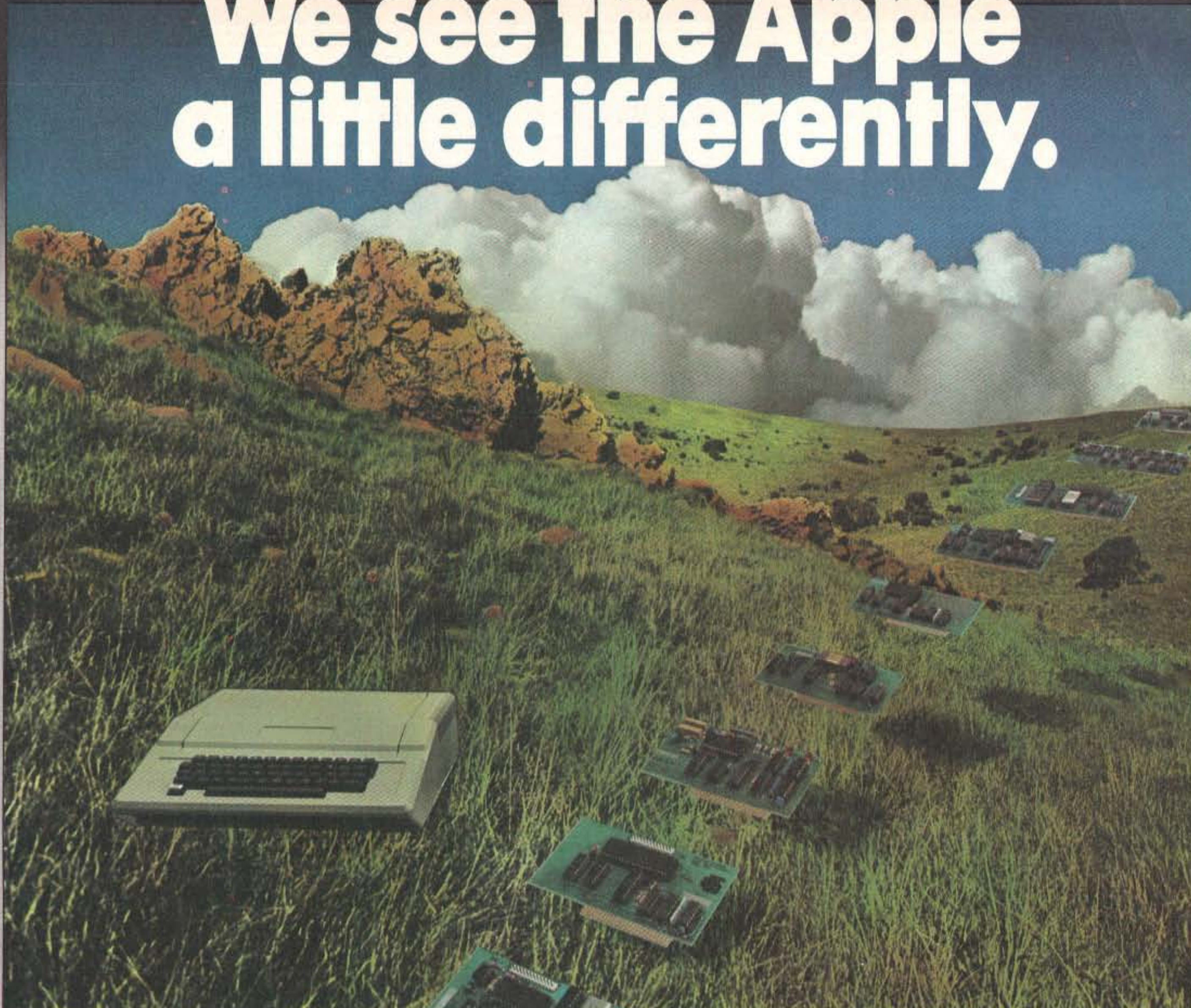
Many public libraries have microcomputers available for public use and provide a complement of interactive programs for individuals to learn with. Libraries that have done this report extensive and enthusiastic use of the equipment.

It's a sorry fact that most people have just never had the opportunity to even see a computer system. Until the opportunity to see, touch, and use computers is afforded, computers will remain shrouded in mystery for the vast majority of people of all ages. The public library is one of the best hopes we have to alleviate this problem.

Carlton A Sears
Adult Services Coordinator
Asheville-Buncombe Library System
67 Haywood St
Asheville NC 28801

Letters continued on page 122

We see the Apple a little differently.



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And we have tools to connect the Apple to the outside world, including A/D converters and interval timers with external interface.

We make components for the S-100 bus, the PET, and the TRS-80, too. We built our products to deliver hard-nosed value to the OEM, and to the inventor who knows the best, at prices that are unbeaten.

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The Future of Computer Graphics

Bruce Eric Brown
and
Stephen Levine
Lawrence Livermore National Laboratory
University of California
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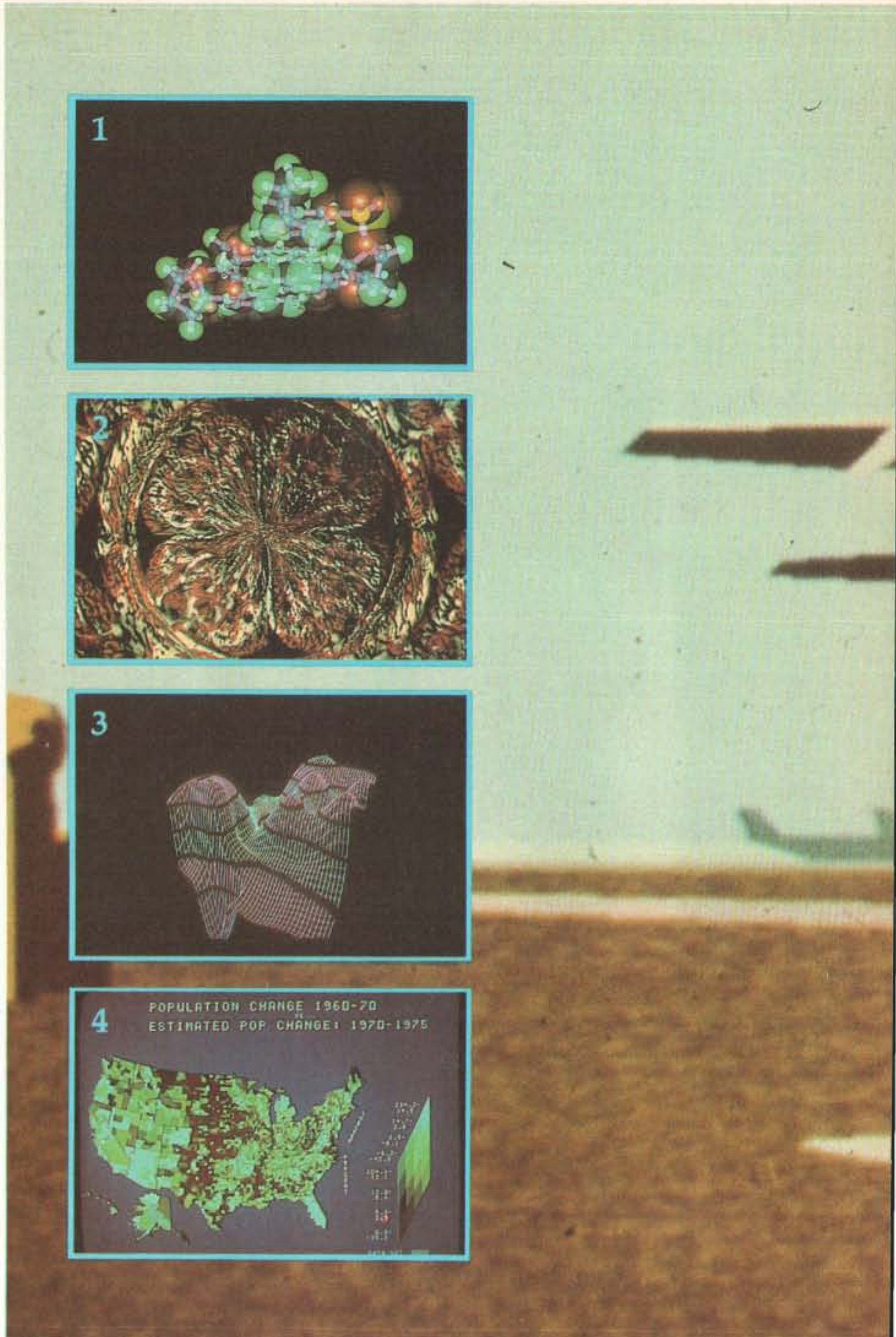
Livermore CA 94550



Predicting the future can place one in a very precarious position. Although technology is moving forward at such a pace that it is almost impossible to look a long way down the road, we *do* have a good idea of what the *near-future* trends will be. So here I will discuss where the trends in computer-generated graphics are headed.

Computer graphics is the fastest-growing segment of the computer industry. Although many existing computers already have graphics capabilities, the future is even brighter. Since personal computer users will make up the largest percentage of the computer graphics market, the standard color television receiver will be the most common display device. Research is continually going on in video-generation techniques, and we can expect the quality of video images to improve dramatically.

Also on the horizon is the use of networks. Best of all, the price of graphics systems should continue to fall, and as they do, the number of applications will increase drastically.

Editor's note:
It was only 5 years ago when the first annual computer graphics show was held. The Philadelphia show was sponsored by SIGGRAPH (the Association for Computing Machinery's Special Interest Group on Computer Graphics). At that time, the show attracted ten vendors and a few hundred visitors. SIGGRAPH-80, which was held this summer in Seattle, brought to that city over 100 vendors, about 6000 visitors, and filled twenty-four times the space of SIGGRAPH-75. So you can surmise how the the computer graphics field will continue to grow.... **SM**

Three Dimensions

This is an exciting time for experimentation with computer graphics. Looking into our crystal video display, we can see many changes coming within the next few years. True three-dimensional displays will become common. Researchers will finally be able to see their models in three dimensions without the need of special glasses, stereo pairs, or by viewing two-dimensional projections.

Already in existence are integral hologram displays made from computer-generated images. (An example is shown in photo 9.) The

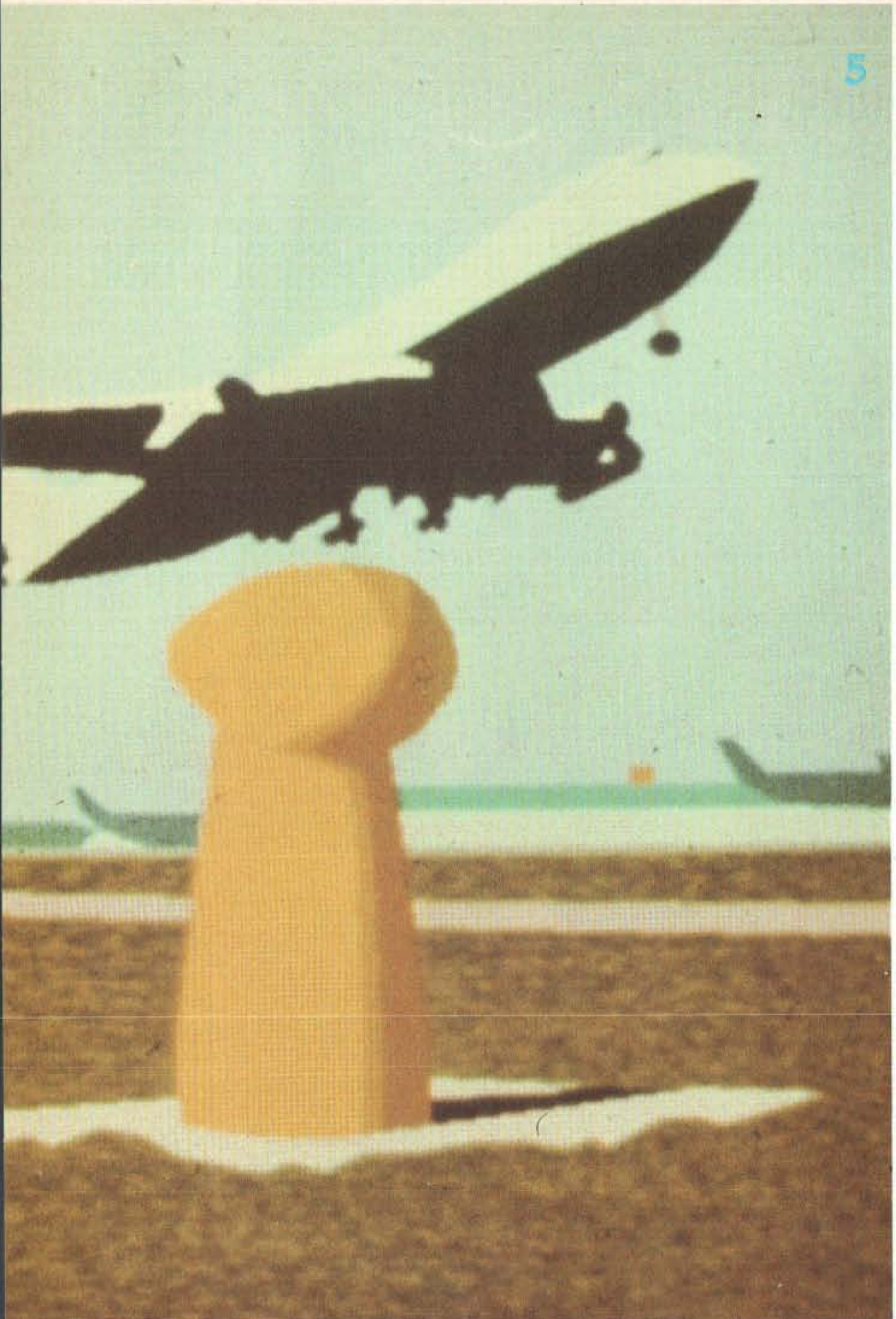


Photo 1: *A computer-generated composite view of a DNA molecule using both ball-and-stick and space-filling models. Using keyboard control, the configuration of the model can be changed and it can be rotated in any direction. Such models are already assisting scientists in their research and will have an even bigger role in the coming years. Photo courtesy of Nelson Max, Lawrence Livermore National Laboratory.*
Photo 2: *Computer-generated art by Los Angeles artist David M. As you can see, computer graphics could revolutionize the world of art.*
Photo 3: *A perspective view of a two-dimensional array of numbers. Photo courtesy of Melvin L Prueitt, Los Alamos Scientific Laboratory.*
Photo 4: *Census data plotted to show population changes. This is an example of the type of material which could be available on a computer network with wide-band capabilities, such as cable television. Courtesy of Edward Zimmerman, White House.*
Photo 5: *A ground-level view of a computer-generated airport scene used in a real-time flight simulator. Photo courtesy of Marconi Radar Systems.*

holograms are made by photographing 1080 computer-generated images on 35mm film and transferring them to the hologram. In a few years it will be possible to generate these directly; we might even see a laser-driven, computer-controlled, holographic-image output device.

There are currently several methods in use for displaying three-dimensional television images, but the most promising uses an interlaced television picture. The even scan lines display an image for viewing with the right eye and the odd scan lines have an image for the left eye. The screen is viewed through a pair of glasses whose lenses are made with PLZT (lead lanthanum zirconate titanate) ceramic. Voltage pulses synchronized with the display of the odd and even fields darken the left and right lenses alternately. As a result, the viewer sees a true three-dimensional image. Photo 10 is a composite view of a display showing the images for both the left and right eyes.

Raster-Scan Displays

Low-priced memory will also change the look of computer graphics. Up to the present, the market has been dominated by storage tubes and calligraphic (ie: stroke-writing) displays; however, raster-scan displays can be refreshed from a frame buffer of semiconductor memory. Therefore, in the coming years, we can expect the graphic-terminal market to be dominated by raster-scan devices. The standard display will be a color television receiver connected as a mirco-processor-controlled intelligent terminal. The cost of some of these graphics terminals will be at or near the cost of a modern color television receiver.

Raster-scan color television will probably be the graphics standard for the following reasons:

- The US video standard is well established.
- It has a large industry supporting it.
- The cost of developing another standard is prohibitive.
- The great numbers of personal computer users will help determine the trend. Why buy a color output monitor when you already have one or several available at home?

Top-of-the-line video displays will include devices with 1000-line resolution (already available) as well as a number with 2000-line resolution. The cost of these will be significantly higher than that of a modern color television receiver.

On a raster-scan display, each dot on the screen is known as a picture element or pixel. Since each pixel is displayed 30 times a second, the image generator must either generate 30 Hz or store the pixel intensities in memory. Frame-buffer systems usually use dual-ported memory which both stores the image and refreshes the display.

To simplify things, let's assume a square picture with the standard 500 lines and each line containing 500 pixels. To display a completely black-and-white line image with no shades of gray we would need 250,000 (500 by 500) bits or 32 K bytes of memory. In order to display gray levels, the number of bits used for each pixel must be increased. To display color, we either divide the number of bits available among the three primary colors (red, green, and blue) or use a color map. A color map takes each pixel value stored and outputs the three intensities: the most common method is to use 1 byte input and 3 byte output. The number of colors which can be displayed is the product of the number of output intensities for each color. At a given time, only a subset, which is limited by the input values, can be displayed. If we use 8 bits in, 24 bits out, we can display any 256 colors of the 16,777,216 available.

In the near future we should be seeing 2000-line resolution systems with 24 bits per pixel (1 byte for each of the three primary colors and 12 bits per color in the map). 12 megabytes of memory would be needed for such a system. With memory prices expected to continue to fall, in about 5 years the major cost element of such a system would be the monitor and electronics.

Vector Displays

Although it appears that raster-scan displays will have the major share of the graphics market, line-drawing (ie: vector-display) systems will continue to grow, though at a slower rate. There are basically two types of line-drawing systems: the storage tube and the refresh calligraphic writer.

Storage tubes available today have higher resolution and greater image stability than most refresh systems. One disadvantage of the storage tube is the lack of selective erasure. In order to remove one line the entire screen must be erased and redrawn. With refresh displays the line is removed from the display list and the line is redrawn on the next refresh cycle.

Calligraphic displays can display about 20,000 three-dimensional vectors or 100,000 two-dimensional vectors at 30 Hz. In the next few years we can also expect a doubling of these capacities.

Raster-scan display buffers can also be used to display vector images and should begin to replace calligraphic displays as faster hardware becomes available. Many users will probably prefer the somewhat slower speed of the raster scan since they are able to display continuous-tone color images.

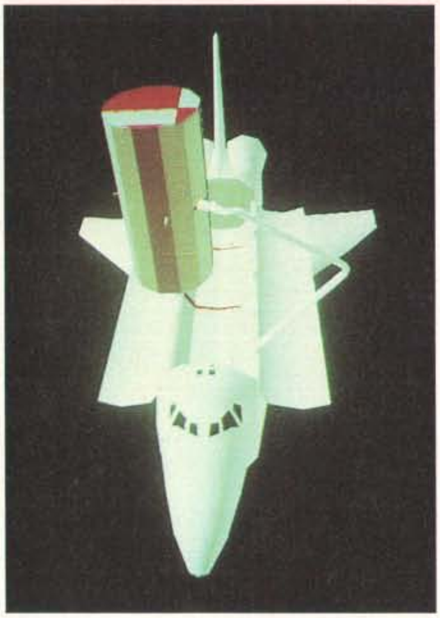


Photo 6 (above): *An example of the computer-generated graphics used to train space-shuttle pilots at the Johnson Space Center in Houston, Texas.*

Photo 7 (below): *The control panel for an experimental fusion reactor at Lawrence Livermore National Laboratory. Transparent touch panels mounted over the color video displays have eliminated most switches. To control the reactor, the operators need only to touch the screen over the desired control area shown on the screen. Photo courtesy of Glenn Spreckert.*

Input

One tool which should see much use in the future is a transparent touch panel mounted over the face of a video screen. As shown in photo 7, an automated nuclear-reactor control room is one of the many possible applications. (Note the lack of switches.)

Hard Copy

Currently, one of the major problems of graphic terminal users is how to satisfactorily get hard-copy output. The most common method is to use a camera to take a picture of the video screen. A device is also available which records the video output directly on film. Both of these methods leave much to be desired. The final solution may not necessarily come from the manufacturers of graphic terminals. The goal of copying machine companies is a dry method of putting a color image on a piece of paper (like the current, dry black-and-white-image method). At present, the device with the highest-quality color output is the film recorder. For raster output devices, the resolution of current recorders is 4000 by 4000 pixels, each with a range of 256 intensities. These devices use

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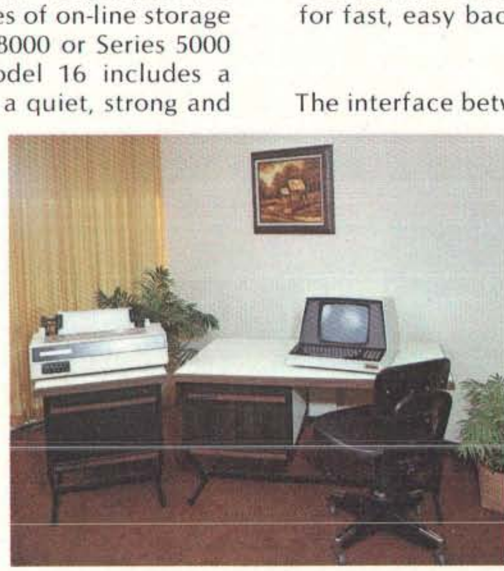

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as many as seven filters and multiple passes are made on the film to create full-color images. Additive-color red, green, and blue filters or subtractive-color yellow, cyan, and magenta filters are used. In both systems, the seventh color is neutral for plotting black-and-white images. We can expect to see more of these recorders available in the near future, and some of the stripped-down models should be available at lower prices.

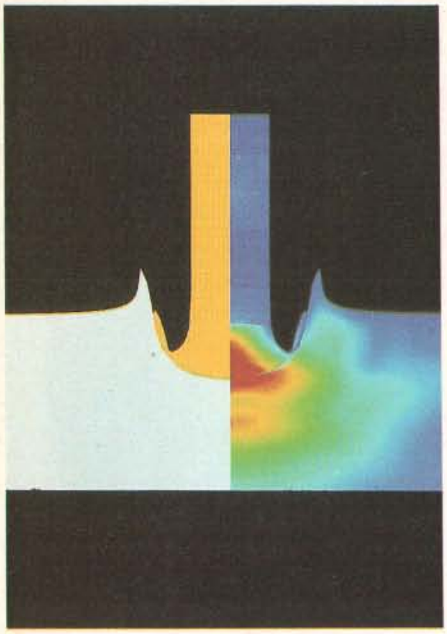

Photo 8: *A problem in hydrodynamics illustrated through the use of computer graphics. The photo is part of a series illustrating a steel rod impacting a steel plate. Color changes represent areas of varying stress. In the future, such graphics will be widely used in education. Photo courtesy of Lawrence Livermore National Laboratory.*

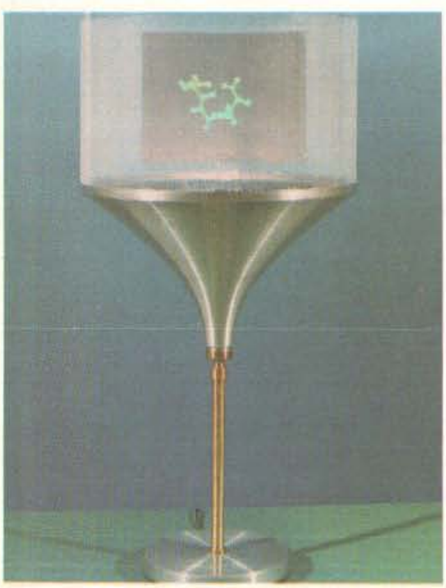

Photo 9: *Integral hologram of a molecule created by photographing 1080 computer-generated images on 35mm film and then transferring them to a hologram. In the future computers will be able to generate holograms directly. Photo courtesy of Donald L Vickers, Lawrence Livermore National Laboratory.*

Another group of devices which fit into this category of film output are COM (computer-output-on-microfilm) devices. Many of those currently available have graphic capability as well as variable intensity. At the present time, COM devices are mainly used for alphanumeric-fiche output. Currently only black-and-white machines are available, although color-fiche machines are expected to be produced in the future. The most important consideration is the need for high-quality, large-format color images. The resolution of current COMs is about 32,000 by 32,000 pixels. Although higher resolution is theoretically possible, such devices will not be produced until a need for them is demonstrated.

Laser recorders may soon capture a portion of the expanding graphics market. Since a laser beam has much more energy to deposit on film than a CRT (ie: video display) image, laser recorders will be much faster than existing methods. On a modern film recorder, one full-intensity pass at 4000 by 4000 pixels takes about 1 minute. To record the same amount of data, the laser requires 1 second or less. The energy of a laser beam is great enough that a split beam could record up to five copies at the same time.

A current weak link in laser systems is the deflection systems. Although solid-state methods are being developed, rotating mirrors are used today. Another drawback with any system that uses film is that unless users have their own processing facilities, film development takes at least 24 hours and sometimes much longer.

The Xerox 6500 color copier can be interfaced to a number of terminals for image-recording, or it can be connected to computers for direct output. Ink-jet plotters, printers with color ribbons, and flat bed-drum plotters with color pens are included in this class of output devices. Continued improvements in speed and color reproduction can be expected.

The brightest future is for the video disk. Today, these devices can hold 50 minutes (180,000 frames) of video per disk. Although the initial cost is high, the great number of frames available makes this device the ideal output and storage medium.

Computers — The Future

Although so far I've concentrated on graphics hardware, what about the future of the beast behind the display — the computer?

It seems likely that within a few years the home computer user will have a choice of several 32-bit virtual machines with at least a million words of expandable, central memory, and 100 million words of disk space. This type of system will be ideal for a color-frame buffer system.

Applications

Since pictures are a very efficient means of communication, the future applications of computer graphics are virtually unlimited. Photo 6 is a photograph of computer-generated graphics used to train space-shuttle pilots. Within the next few years, games and simulations with graphics of nearly the same quality will be available to the personal computer user. The PLZT glasses described earlier will be used to provide three-dimensional images for the would-be space-shuttle or 747 pilot. You can also expect the technology to be put to use in amusement parks. The Disneyland people have already used computer-generated graphics in some of their attractions and are continuing to develop them for future use.

Networks

There are a number of advantages to having your own, isolated personal computer, but connecting it to a network opens up a vast new world. Networks designed specifically for personal computer users, such as The Source, are already in existence. Unfortunately, the narrow bandwidth of conventional voice-grade telephone lines severely limits graphic capabilities.

One future possibility is the use of cable television for networks with graphic capabilities. Cable is increasingly available in all but the most rural areas and has wide bandwidth, portions of which are not used. Personal computer users could tap into this resource and use the extra bandwidth for local communication nets.

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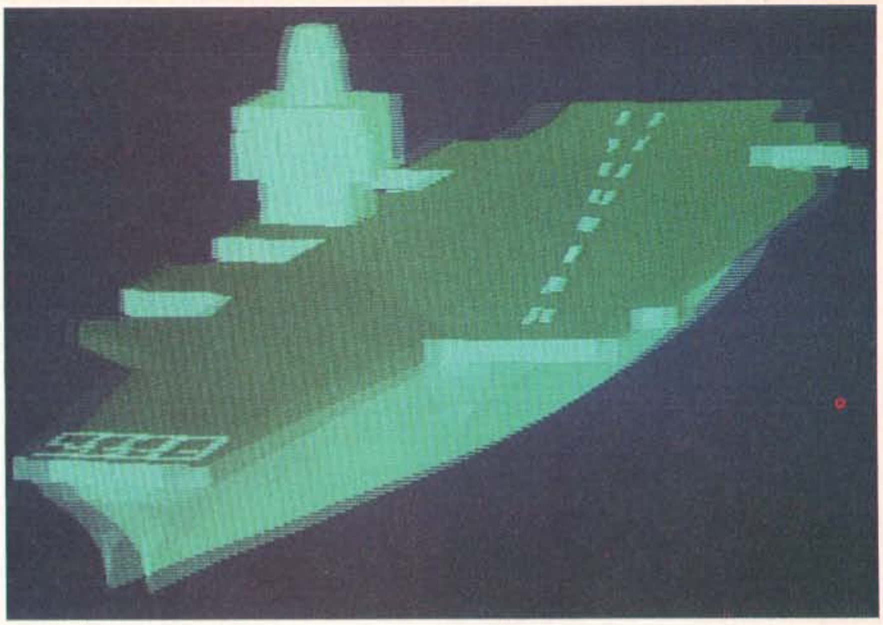

Photo 10: *Interlaced left-eye and right-eye view of a computer-generated image of an aircraft carrier. The image is viewed in three dimensions when the user wears glasses with lenses made of PLZT (lead lanthanum zirconate titanate) ceramic. The lenses by the right and left are darkened alternately by voltage pulses synchronized to the display. Photo courtesy of John A Roese and Larry E McCleary, the Naval Ocean Systems Center.*

cable-television company provide a main computer to control the network and act as a data base. The range of services which could be provided is virtually limitless. An example is shown in photo 4, where census data has been plotted to show population changes.

Exploring the Future

Computer graphics have exciting possibilities as an artistic medium. It's been said that computer-generated color graphics will revolutionize art in the same way that acrylics changed the world of artists who once worked with oil paints. Photo 2 shows computer-generated art by Los Angeles artist David M.

The simulators discussed earlier will also be widely used by filmmakers. Special effects, instead of being animated one frame at a time, could be programmed and filmed in real time. For instance, a director could ask for an airport scene on a clear day, as in photo 5. By changing a parameter, the same scene could be created on a foggy day.

The motion picture industry is in the forefront of developing and using sophisticated systems for computer-generated graphics. Increasingly higher levels of realism will be created in the future and the time-consuming tasks of creating special effects and editing will be performed using laser scanner/recorders and video disks. In terms of dollars, the movies will be one of the largest users of computer graphics for the near future.

Applications, as we've seen, are limited only by our present imaginations. Photo 1 shows a computer-generated composite view of a DNA (deoxyribonucleic acid) molecule using *both* ball-and-stick and space-filling models. Such displays will speed up the rate of research. The molecule model can be rotated, changed in configuration, and taken home for the scientist to use on his personal computer.

Classroom displays will greatly surpass the audio-visual methods commonly used today. Photo 8 shows a hydrodynamic problem with impact calculations displayed through color changes. A computer display of this sort could be created and updated in the midst of a lecture.

In the wide world of computer-graphic applications, we have only scratched the surface.■

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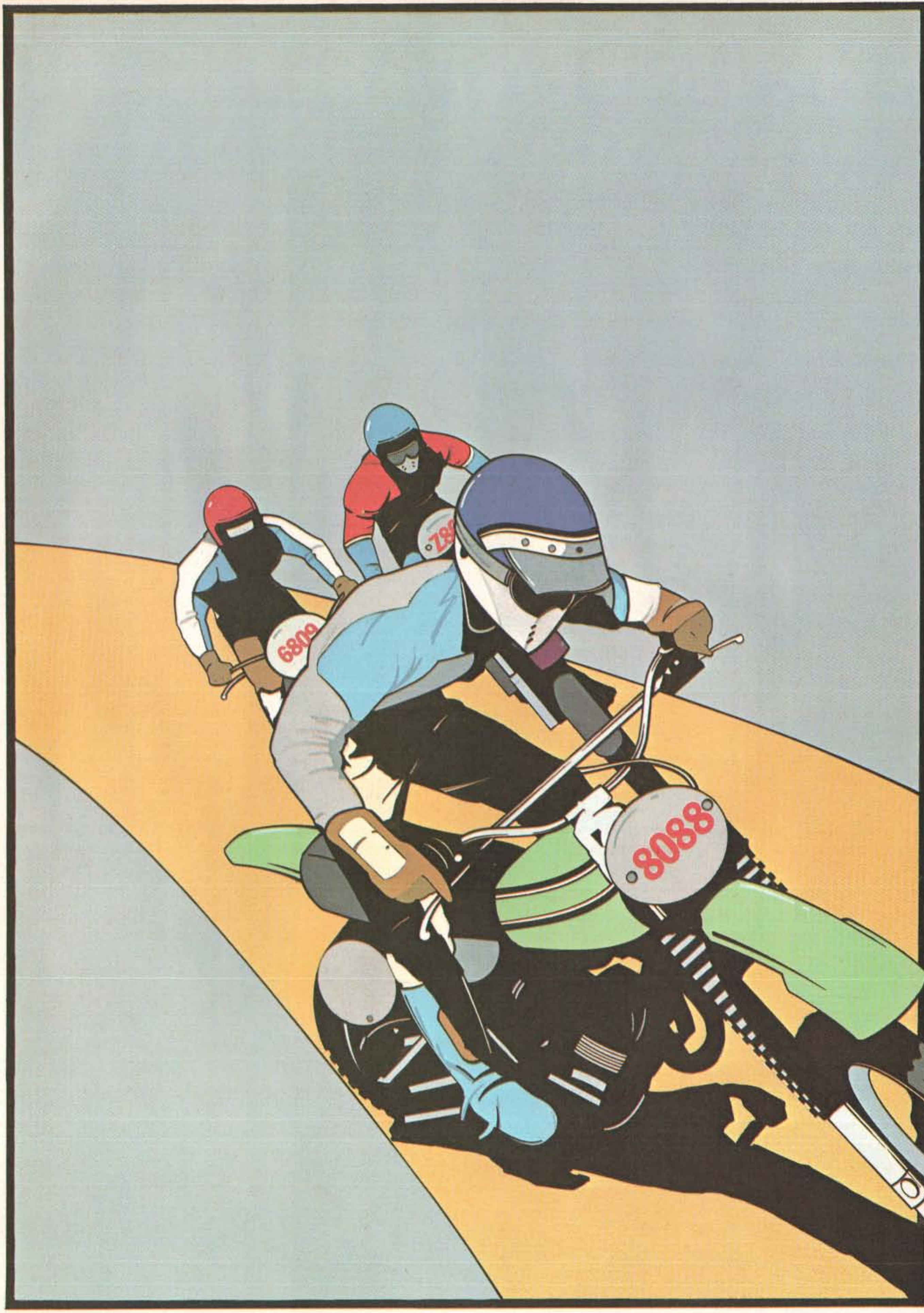

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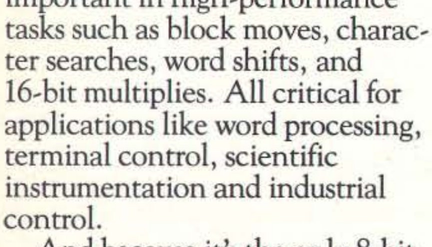

8-Bit Microprocessor Price Trends

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Multi/ co-processing	YES (with 8087, 8089)	NO	NO	NO
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Circle 17 on inquiry card.

Home In on the Range!

An Ultrasonic Ranging System

Steve Ciarcia
POB 582
Glastonbury CT 06033

Each month I try to present a hardware project that is both interesting and relatively easy to build. Unfortunately, it's not as simple as picking a topic and quickly whipping up some circuit. More often than not, I have a number of potential topics and projects on the fire at the same time. Some are in limbo and just waiting for the right parts. Others are postponed when it turns out that the necessary hardware is something that could be better built by NASA (National Aeronautics and Space Administration) than by a computer hobbyist.

One topic that has always interested me is the concept of automatic ranging. I became involved with this idea when I wrote an article entitled "I've Got You In My Scanner," November 1978 BYTE, page 76. The original article was about an infrared sensor and parabolic reflector mounted to rotate on a stepper-motor shaft. With computer-controlled stepping, the result was something like the sweep of a radar antenna. The project was sensitive to infrared and visible light.

The scanner, parabolic-reflector, and stepper-motor combination could easily tell the direction of a light source to an angular resolution of 7.5°. It could make a 180° sweep, stop, and then follow the brightest object in its field of view. By recognizing the absence of known light sources (when the light path is blocked), it could even function as part of an intrusion alarm.

Photo 1: *A computer-controlled, stepper-motor-driven infrared and ultrasonic ranging scanner. An infrared-sensitive photo Darlington transistor (GE L14F2) is mounted at the focus of a parabolic reflector, which is attached to the shaft of a stepper motor; the ultrasonic transducer is mounted above it.*

The infrared sensor and drive mechanism were described in a previous Circuit Cellar article, "I've Got You in My Scanner! A Computer Controlled Stepper Motor Light Scanner."

However, even though it could "see," the infrared scanner could not tell how far an object was in front of it, or detect the presence of a nonluminous body crossing its path. What I really wanted was a device that could provide the computer with range as well as direction. That's when I started hanging around the camera shop.

Polaroid to the Rescue

The automatic focusing system on the Polaroid SX-70 Sonar OneStep Land camera intrigued me. I had considered tearing a camera apart just to use the ranging unit for my scanner, but sanity prevailed and I went back to designing my own circuit. Somewhere between thoughts of "Who'd really build this thing anyway?" and "I hope everyone can find all these components," I started seeing ads from Polaroid offering just what I wanted, without the camera.

The solution came in the form of an Ultrasonic Ranging System Designer's Kit sold by Polaroid for $125. The kit contains a technical manual, two instrument-grade electrostatic ultrasonic transducers, a modified SX-70 ultrasonic circuit board, an experimental demonstrator display board, and two Polapulse 6 V batteries. With this unit I was able to enhance my original infrared-scanner

Diagrams and schematics of the Ultrasonic Ranging System Designer's Kit were provided through the courtesy of Polaroid Corporation.

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design to include automatic range detection. The new scanner system incorporating the Polaroid unit is shown in photo 1. More on this later.

Polaroid Ultrasonic Ranging System

The Polaroid Ultrasonic Ranging System Designer's Kit costs $125 (This offer is good until December 31, 1980. Photo 2 shows the Designer's Kit as received.), and is available from:

Polaroid Corporation
Ultrasonic Ranging Marketing
Department 465 E
20 Ames St
Cambridge MA 02139
telephone (800) 225-1618

Photo 2: *Polaroid Ultrasonic Ranging System Designer's Kit, which includes ultrasonic sonar transducers, electronic circuitry, and a detailed specifications booklet.*

Two primary components compose the ranging unit. They are the electrostatic transducer (see photo 3) and the ultrasonic transceiver board (see photo 4). Together these components are capable of detecting the presence and distance of objects within a range of approximately 0.9 feet (0.3 meters) to 35 feet (10.6 meters) with a resolution of ± 1.2 inches (± 30 mm, or 0.29% of range).

In operation, a pulse is transmitted toward a target, and the resulting echo is detected. The elapsed time between initial transmission and echo detection can be used to find the distance by taking this round-trip time and multiplying it by the speed of sound. For a transmitted pulse to leave the transducer, strike a target 2 feet (0.61 meters) away, and return to the transducer, it requires 3.55 ms (1.78 ms per foot, or 5.84 ms per meter, during the round trip).

Essential to system operation is the transducer (shown disassembled in photo 5). It acts as a speaker in the transmit mode and as an electrostatic microphone in the receive mode. The transducer is 1.5 inches (38.1 mm) in diameter and consists of a 0.003 inch (0.07 mm)-thick gold-plated foil stretched over a concentrically

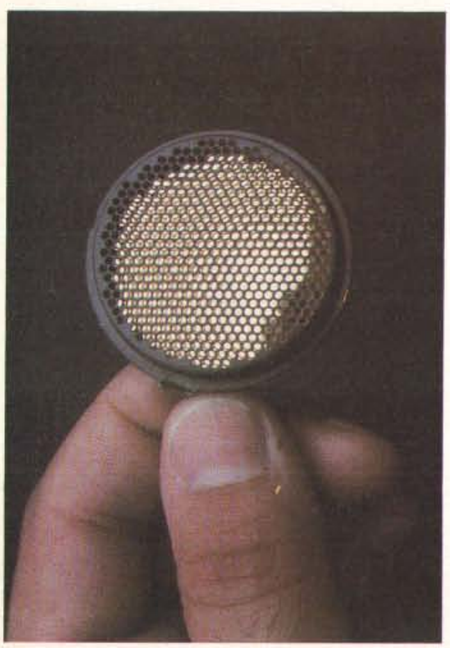

Photo 3: *Close-up view of the Polaroid Ultrasonic Transducer.*

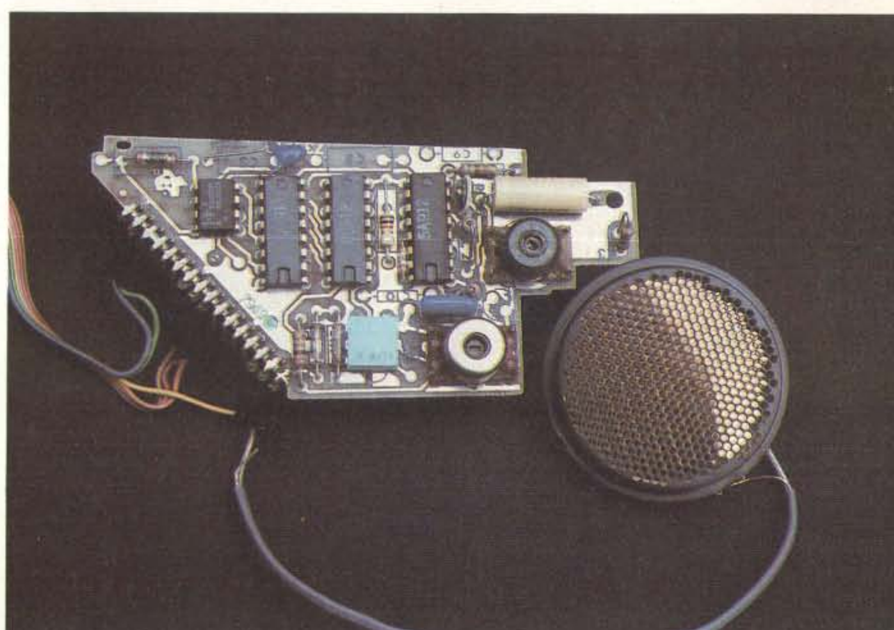

Photo 4: *Close-up of the ultrasonic circuit board, which contains custom analog and digital integrated circuits.*

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grooved aluminum plate. When the metallic backplate is in proximity to the foil, it forms a capacitor. The foil is the moving element which converts electrical energy into sound and the returning echo into electrical energy.

The diameter of the transducer determines the directionality of the transducer. The acoustical signal-strength lobe pattern, or acceptance angle, during operation is shown in figure 1. The graph indicates that the transducer is fairly directional.

When the unit is activated, the transducer emits a sound pulse. The crystal-controlled electrical pulse

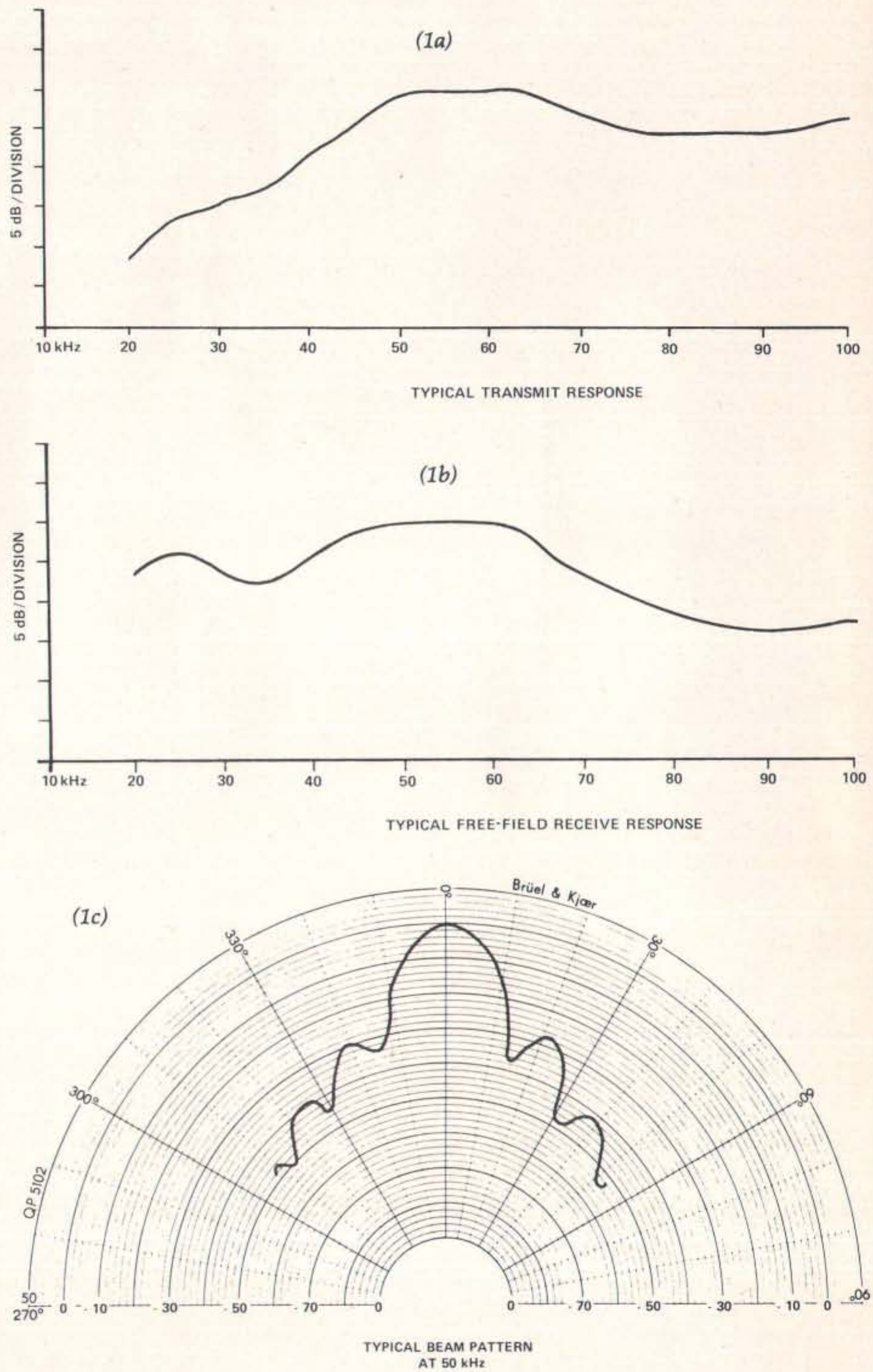


Figure 1: *Typical transmission frequency-response curve (1a), reception frequency-response curve (1b), and radial-beam pattern (1c) of the Polaroid ultrasonic transducer. The beam pattern was measured at 50 kHz, with dB values normalized to on-axis response.*

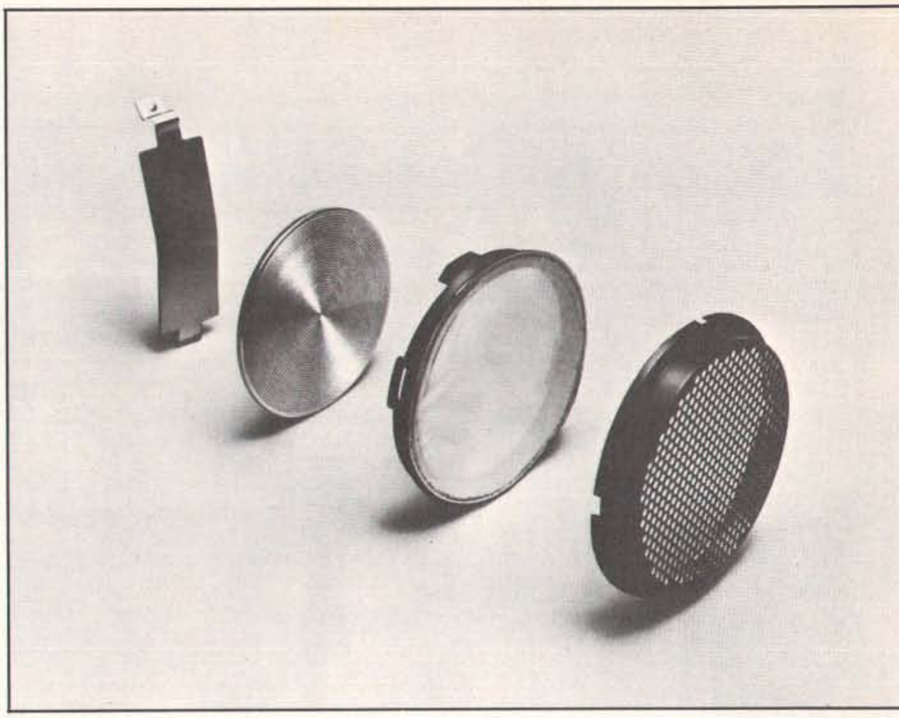


Photo 5: *Expanded view of the Polaroid ultrasonic sonar transducer. Behind a honeycomb grill, a 0.003-inch (0.07 mm)-thick gold-coated foil stretches over a concentrically grooved aluminum plate. The retainer at left holds the parts in place.*

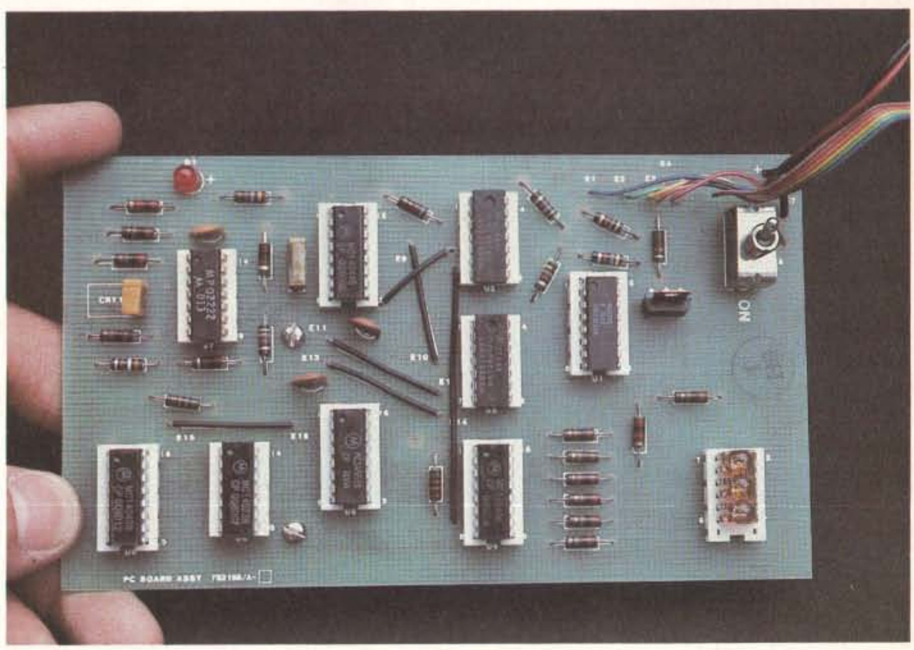
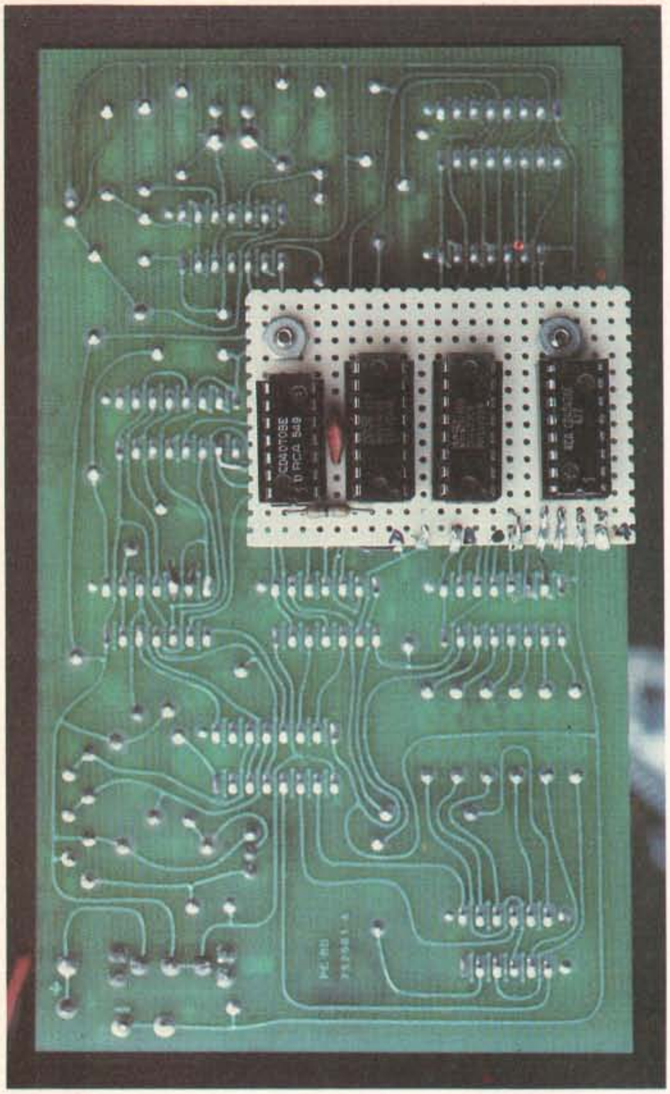


Photo 6: *The EDB, which contains the electronic circuitry shown in figure 4. The three-digit LED display is at the upper right.*

generated by the driver circuit is a 300 V high-frequency 1 ms "chirp" consisting of fifty-six pulses at four carefully chosen frequencies: eight cycles at 60 kHz, eight cycles at 57 kHz, sixteen cycles at 53 kHz, and twenty-four cycles at 50 kHz. This combination is used to overcome certain topographical characteristics of the area into which the signal is being transmitted, where a single frequency might be cancelled and no echo would be received.

Text continued on page 42



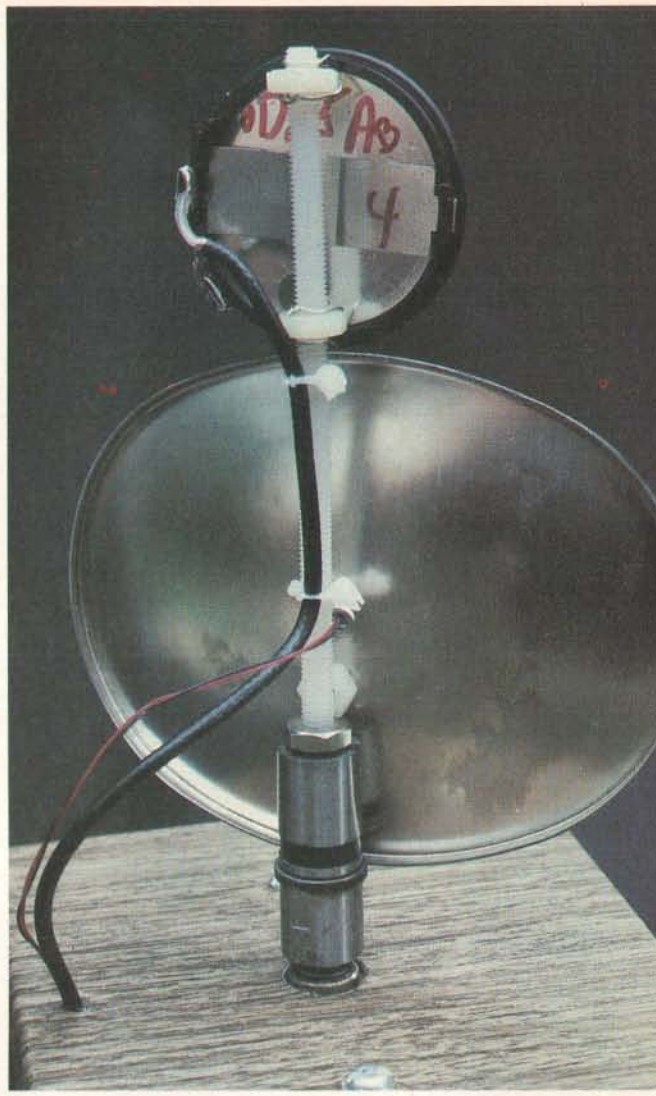


Photo 7: *The prototype of the interface circuit of figure 5 has been attached to the EDB. The interface allows a computer to read the three-digit distance value.*

Photo 8: *Close-up of the back side of the reflector and transducer of the scanner, showing the mounting apparatus.*

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Circle 24 on inquiry card. →

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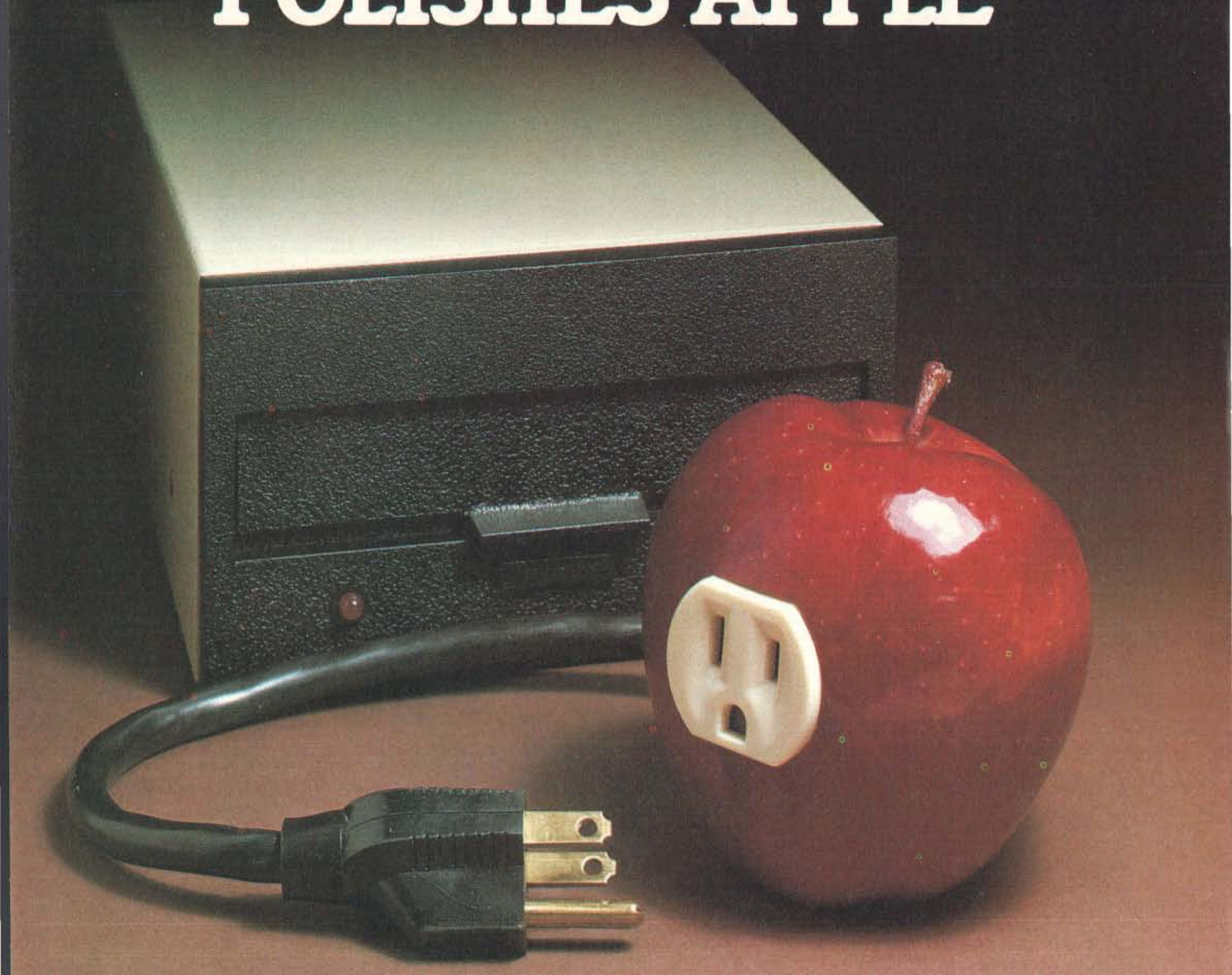

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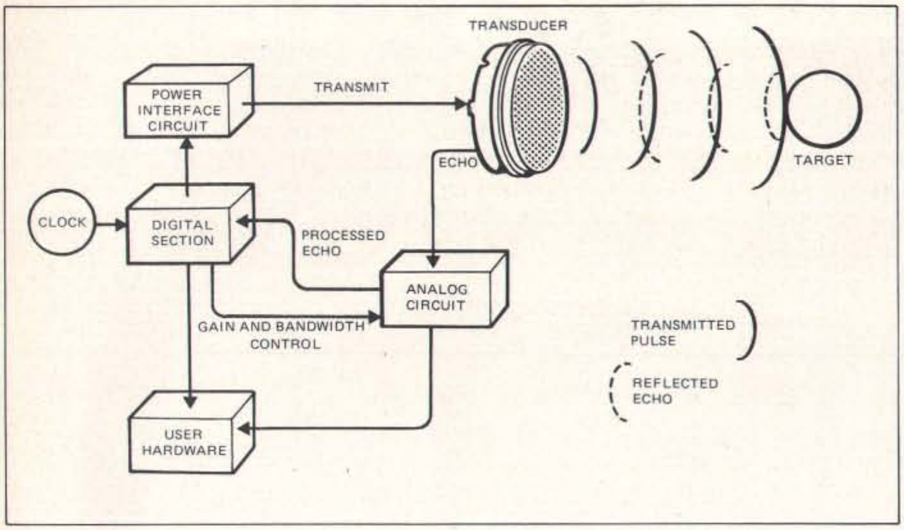


Figure 2: *Block diagram of the ultrasonic circuit. The circuit board contains a variety of custom components and is slightly modified from the unit used in SX-70 Land cameras. This circuit, as well as the EDB, is powered by a 6 V Polapulse battery. It seemed to work acceptably with a 5 VDC power supply.*

The block labelled "User Hardware" can be the EDB or any interface that can convert the ultrasonic circuit board's time-gated output into useful form.

Text continued from page 38:

The ultrasonic circuit board controls both the transmit and receive operating modes. It contains both digital and analog circuitry. In addition to transmitting the chirp and processing the echo, this circuit also tailors the amplifier sensitivity depending upon the object distance. Lower amplification is needed for close echoes, while higher amplification is needed for distant echoes. This is accomplished by increasing the amplifier gain and Q (ratio of reactance to resistance) in steps. Figure 2 is a block diagram of the ultrasonic circuit board.

Experimental Demonstration Board

The ultrasonic circuit board previously described is a modified camera assembly. The EDB (Experimental Demonstration Board, shown in photo 6) is not a camera component; it was designed specifically as a user interface to the ultrasonic board.

Text continued on page 48

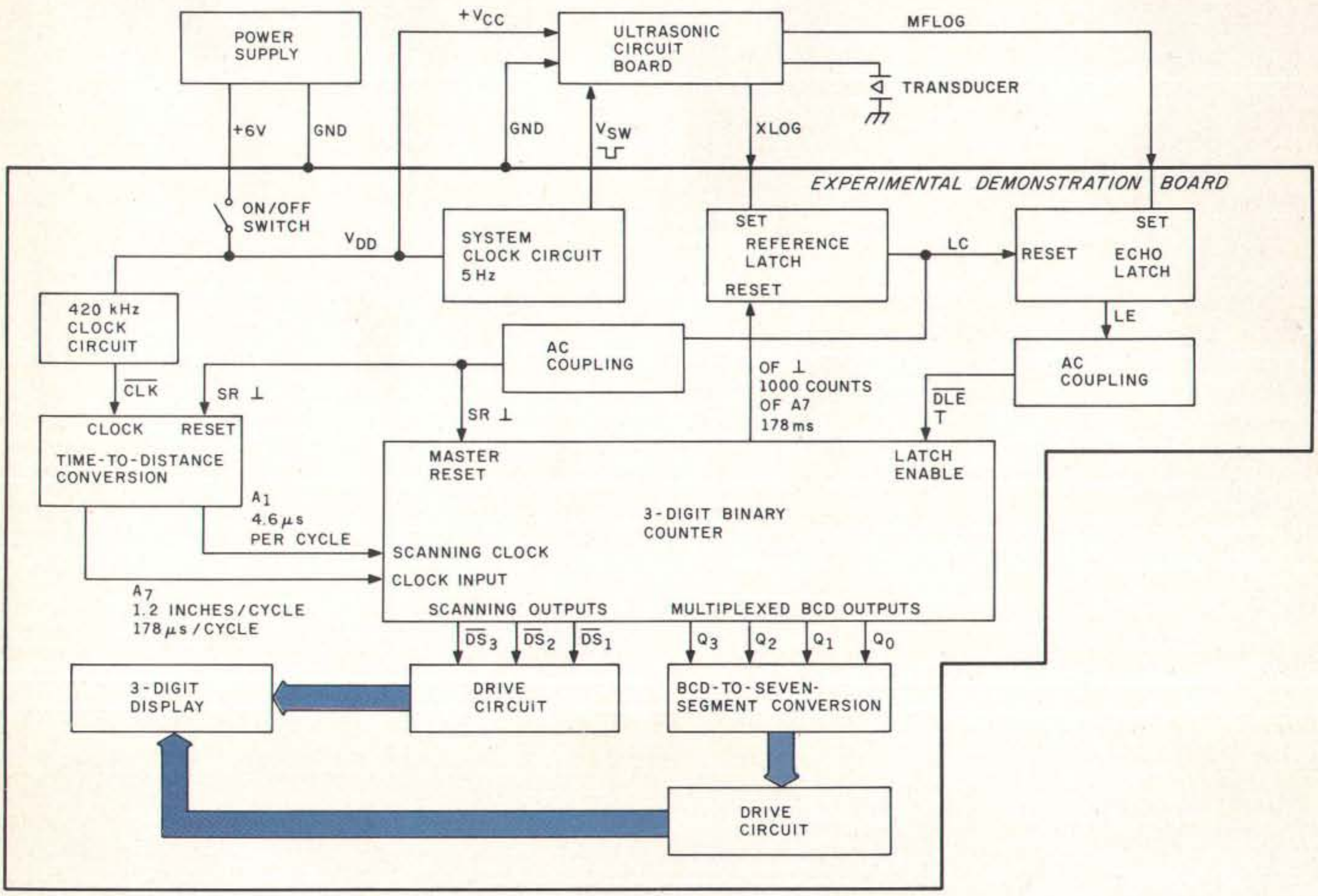
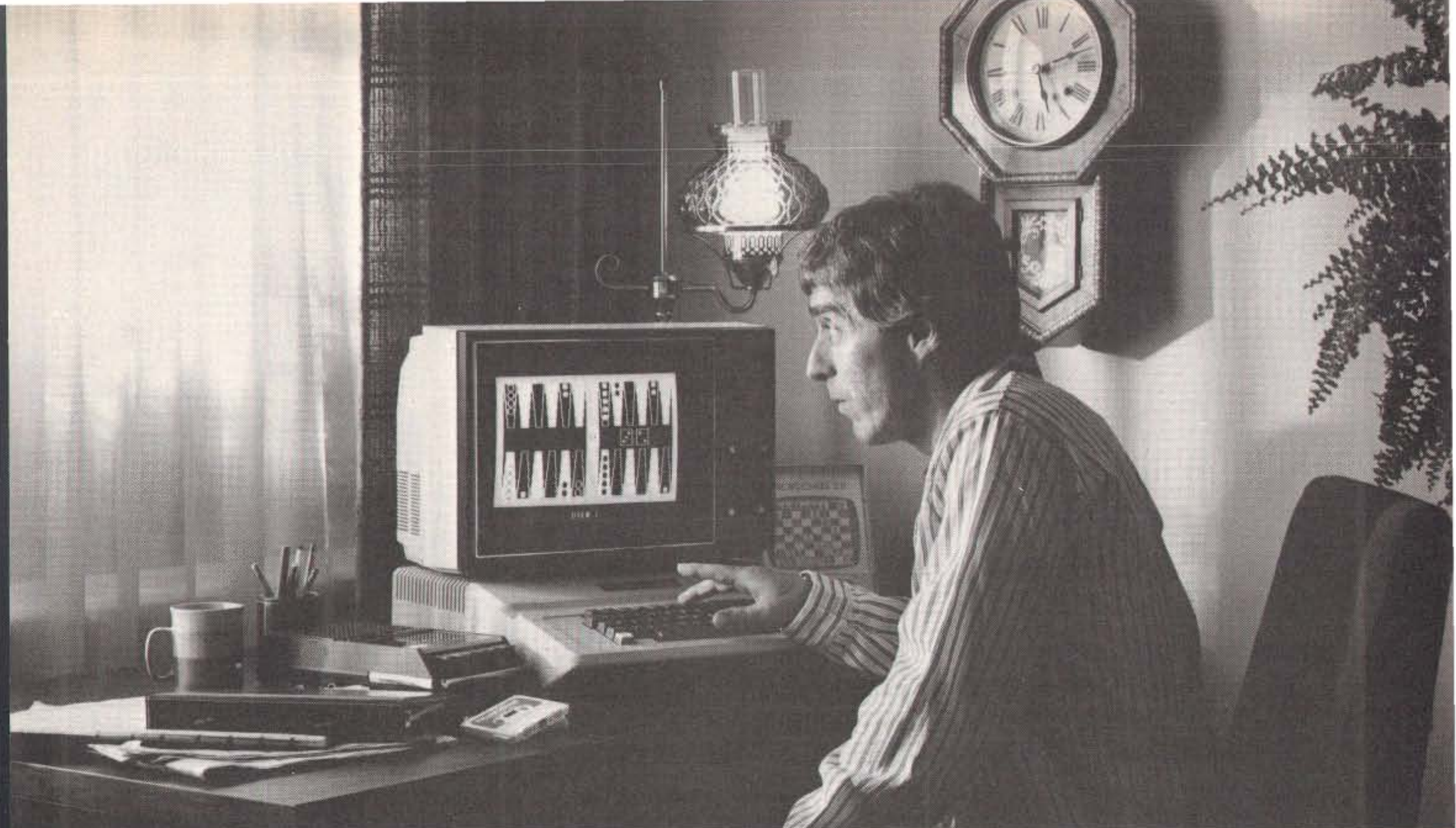


Figure 3: *Block diagram of the Polaroid Experimental Demonstration Board.*



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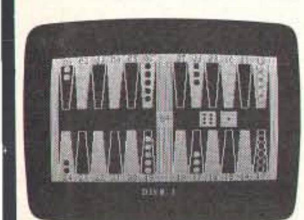

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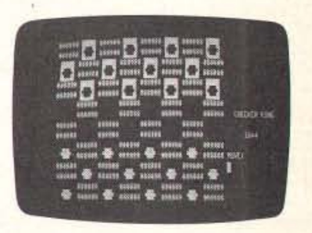

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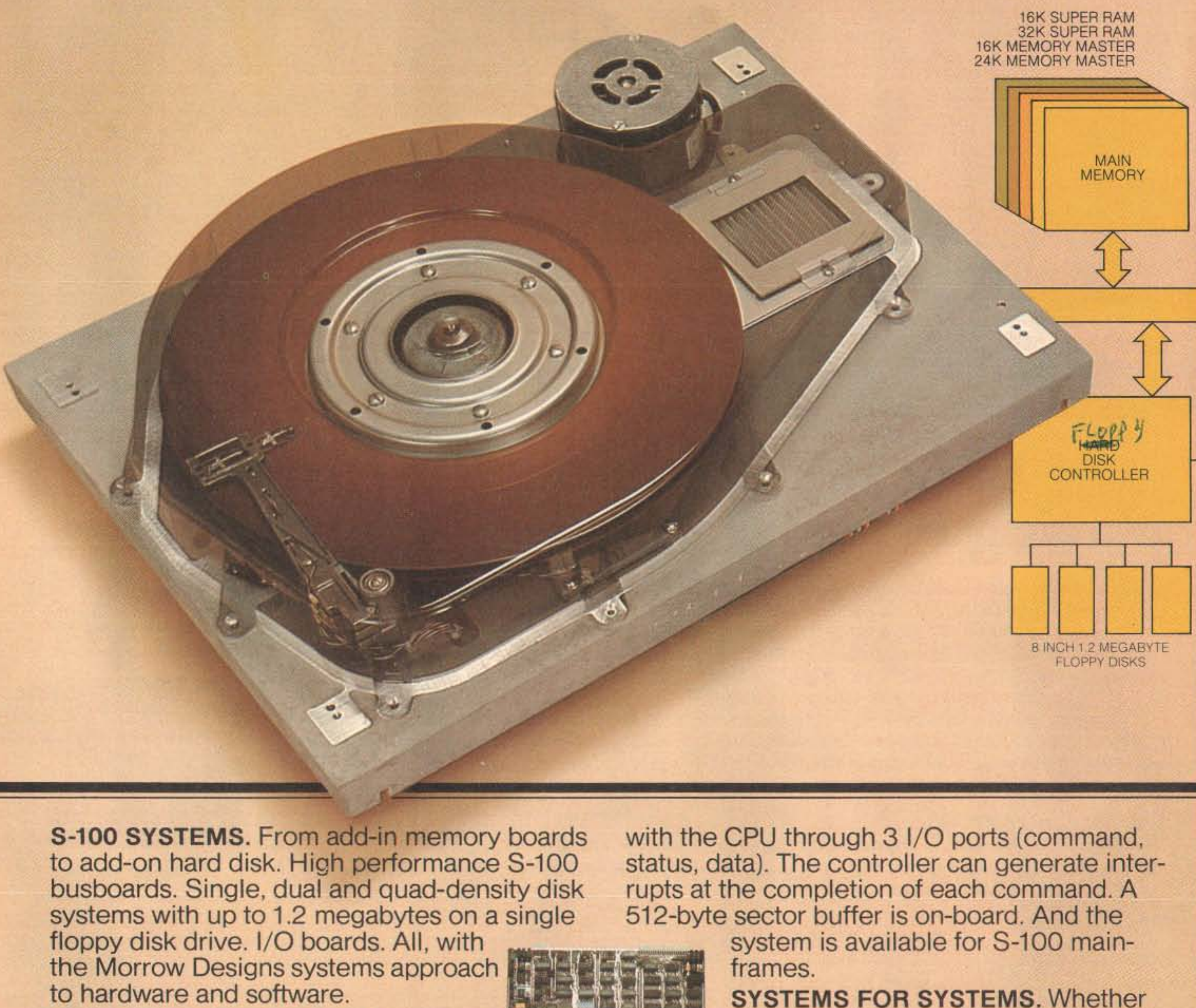

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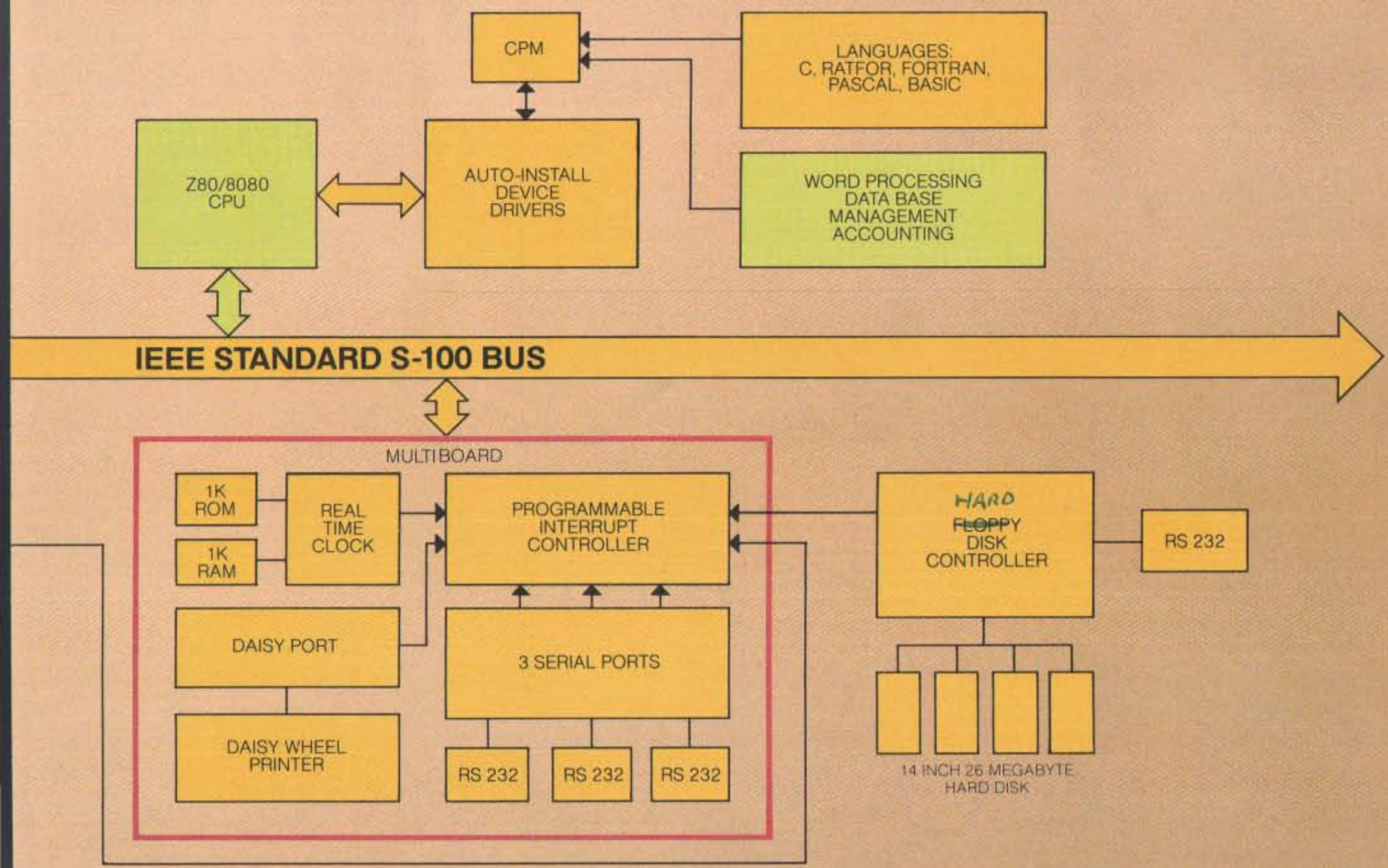

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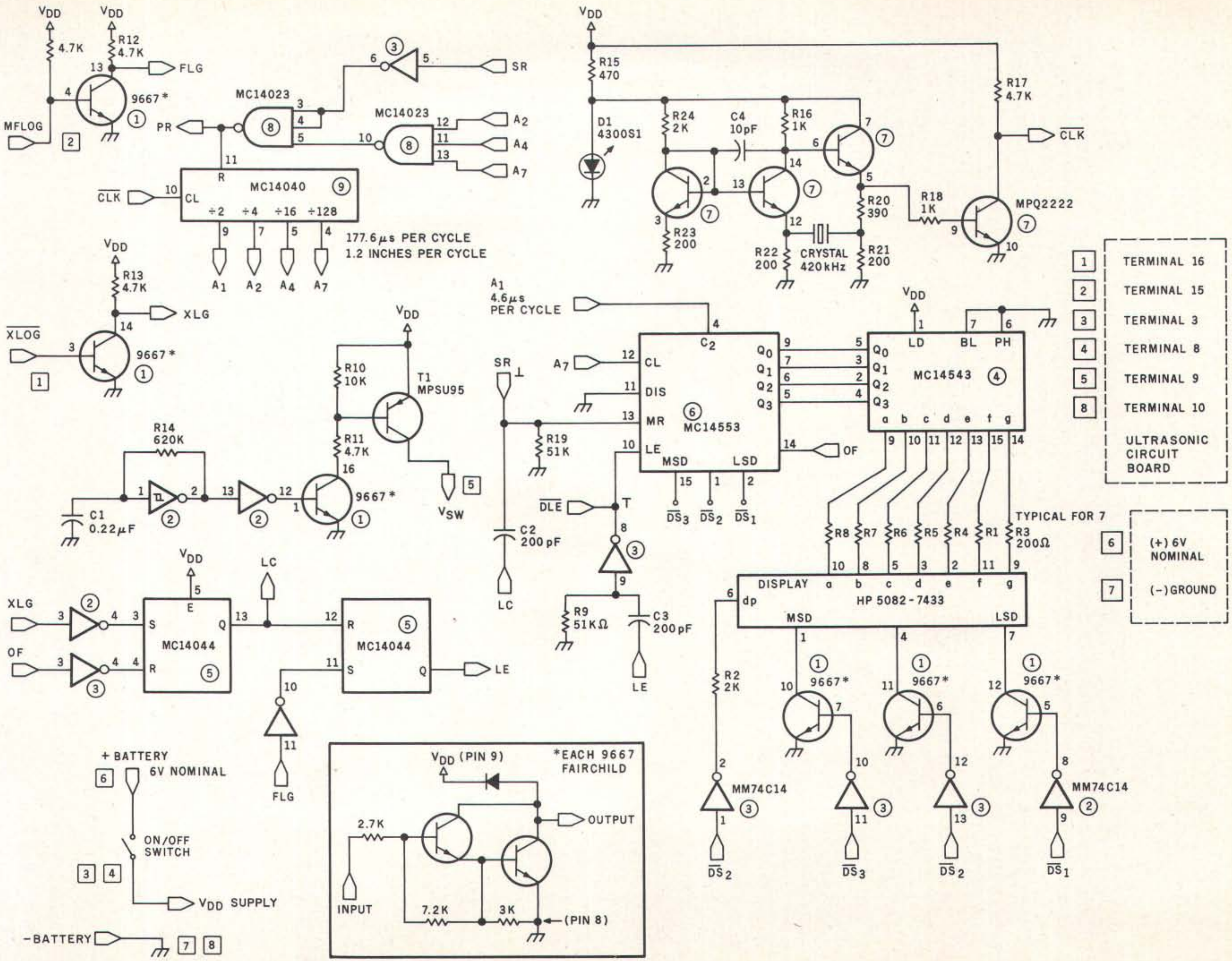

Figure 4: *Schematic diagram of the EDB. This board contains all the necessary circuitry to convert the raw data of the sonar transmit/receive time interval into a numeric distance value and display it on a three-digit LED display.*

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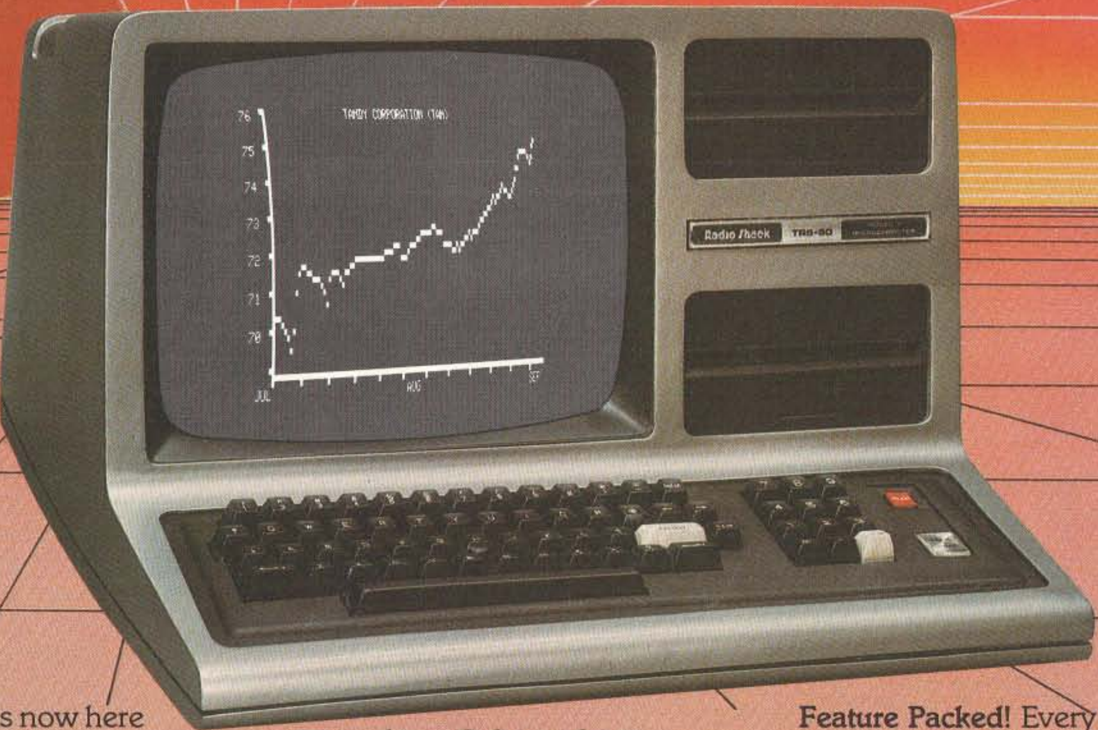

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Bit 1	Bit 0	Output Digit to Computer
0	0	$\overline{DS_1}$ (LSD)
0	1	$\overline{DS_2}$
1	0	$\overline{DS_3}$ (MSD)
1	1	n/a

Table 1: *Correspondence of the 2-bit digit-select codes with the EDB output data sent to the computer.*

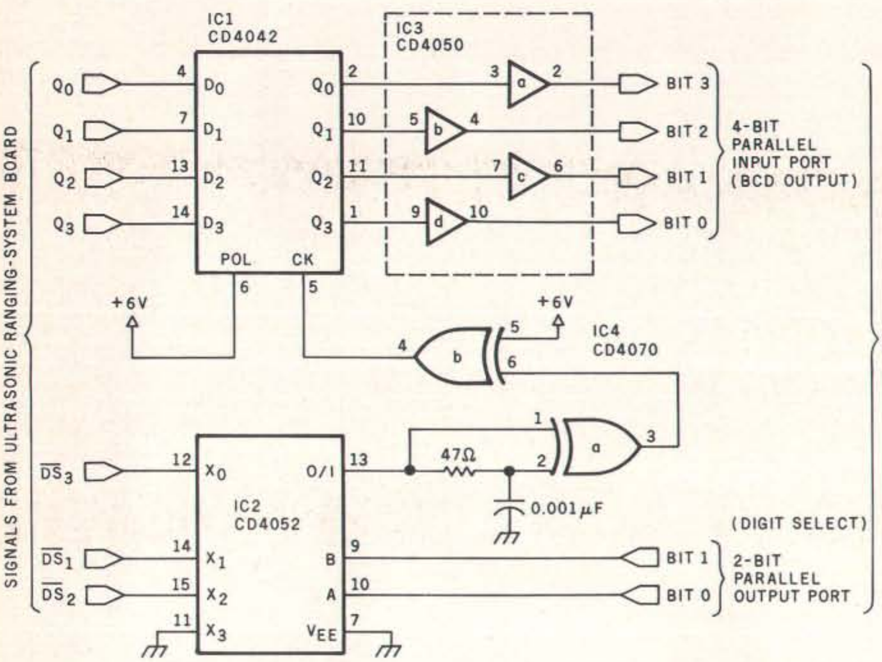

Figure 5: *Schematic diagram of an interface that allows a computer to directly read the three-digit LED display of the EDB, using four integrated circuits. Through 2 bits of a parallel output port, the computer sends a digit-select code and then reads the corresponding BCD value of the selected digit through 4 bits of a parallel input port.*

Number	Type	+6 V	GND
IC1	CD4042	16	8
IC2	CD4052	16	8
IC3	CD4050	1	8
IC4	CD4070	14	7

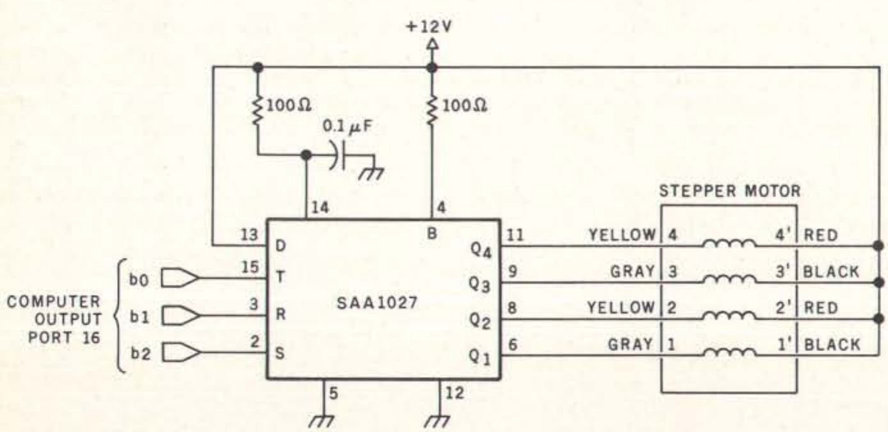

Figure 6: *Stepper motor and controller used in the infrared and ultrasonic scanner. The motor is a North American Philips K82701-P2 type, which turns 7.5° per step. It operates on 12 VDC.*

The SAA1027 integrated circuit is available from Signetics or from North American Philips, Cheshire, Connecticut, (203) 272-0301.

Text continued from page 42:

The EDB contains all the necessary electronic circuitry to convert the transmit/receive time interval into a figure indicating distance (in feet) and present it on a three-digit LED (light-emitting diode) display. Figure 3 is a block diagram of the EDB, while figure 4 shows the schematic diagram.

Connecting the EDB to the computer requires some thought. The output of the EDB is a three-digit display with a numeric output range of 00.9 to 35.0 in increments of 0.1 feet. The multiplexed display is controlled by a three-digit binary counter with strobed digit-select lines. It uses a single BCD (binary-coded decimal)-to-7-segment decoder/driver. At any instant, only one digit is energized, but because of the persistence of human vision, they all appear to be illuminated. Unfortunately, this multiplexed display output is not very computer-compatible and requires additional interface circuitry.

Decoding the EDB Output

Figure 5 is the schematic diagram of a four-integrated-circuit interface that decodes the counter output on the EDB and latches the digits while the computer reads them. Essentially the circuit consists of a three-input demultiplexer (IC2), an edge detector (IC4), a 4-bit latch (IC1), and an output buffer (IC3). The four-chip circuit is conveniently mounted on a piece of perforated circuit board and attached to the rear of the EDB, as illustrated in photo 7.

When the MSD (most-significant digit) of the LED display is energized, the $\overline{DS_3}$ line is low. The data on Q_0 thru Q_3 at this time form the BCD value of that number. Similarly, when $\overline{DS_2}$ goes low, the data lines will hold the second digit value. IC2 is a 4-to-1-line demultiplexer with the three digit strobes as inputs. A 2-bit TTL (transistor-transistor logic)-compatible parallel output from the computer determines which of these channels is routed through the multiplexer. To get $\overline{DS_1}$, the LSD (least-significant digit), the input code to the EDB interface would be 00. A binary code of 10 would set channel 3, allowing $\overline{DS_3}$ to go through. A summary of the codes is given in table 1.

The inputs to IC2 are offset by one channel due to the peculiar timing of the EDB. While the $\overline{DS_3}$ line is

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physically tied to channel 0 and would appear to be addressed with a 00 input code, the edge-detector timing of the circuit is such that we are not latching the current digit's value, but the *next* digit's value, when we address the channel. However illogical it may seem, the codes that work are stated in table 1.

When we have selected which digit we want to read by setting the proper multiplexer-input code, that digit value will be latched into IC1 and available as a BCD value to the computer. IC3 buffers the CMOS (complementary metal-oxide semiconductor) voltage levels of the EDB to the TTL level required by most computers. To read a three-digit range, we simply set the three multiplexer codes in succession. To obtain the distance indication, just add the three values as follows:

Distance = (MSD) × 10 + (2nd digit) × 1 + (LSD) × 0.1

This interface design is essentially speed-independent and can be driven equally well by an assembly-language or BASIC program. Listing 1 is a BASIC program that reads and displays the three-digit range determined by the ultrasonic ranging system.

```
RUN
**********                      STEP 1
**********
************
********************************
************
************
********************************
********************************
**************
******************************
****************************
****************************
****************************
**************
**************
**************
******************************
****************************
******************************
************
**********
********
********
********
********                        STEP 25
```

Figure 7a: *Bar graph of distance measurements taken by the scanning system as the ultrasonic transducer was pivoted in twenty-five steps through a 180° sweep around the Circuit Cellar (each asterisk represents approximately one-half foot). Note correspondence with floor plan in figure 7b.*

A More Sophisticated Scanner

The original article, "I've Got You in My Scanner!," previously mentioned, has been reprinted in the book *Ciarcia's Circuit Cellar,* volume 1, available from BYTE Books. Photo 8 is a close-up of the updated version of the scanner, which now includes the ultrasonic ranging detector. The basic scanner consists of a North American Philips stepper motor (12 V type K82701-P2) and integrated-circuit controller (SAA1027) with an infrared-sensitive photo Darlington transistor (General Electric type L14F2) fixed at the focus of a parabolic reflector mounted on the shaft. I used a Radio Shack solar cigarette lighter, catalog number 61-2797, as the parabolic reflector. The driver circuit for the stepper motor is outlined in figure 6. The original article explained the infrared sensing system in detail.

The new scanner has the ranging detector mounted on the stepper-motor shaft, above the parabolic reflector. Both point in the same direction. The stepper motor is driven through the SAA1027 with 3 bits of a parallel output port. To drive the motor clockwise, bit 1 is set low, bit 2

Text continued on page 56

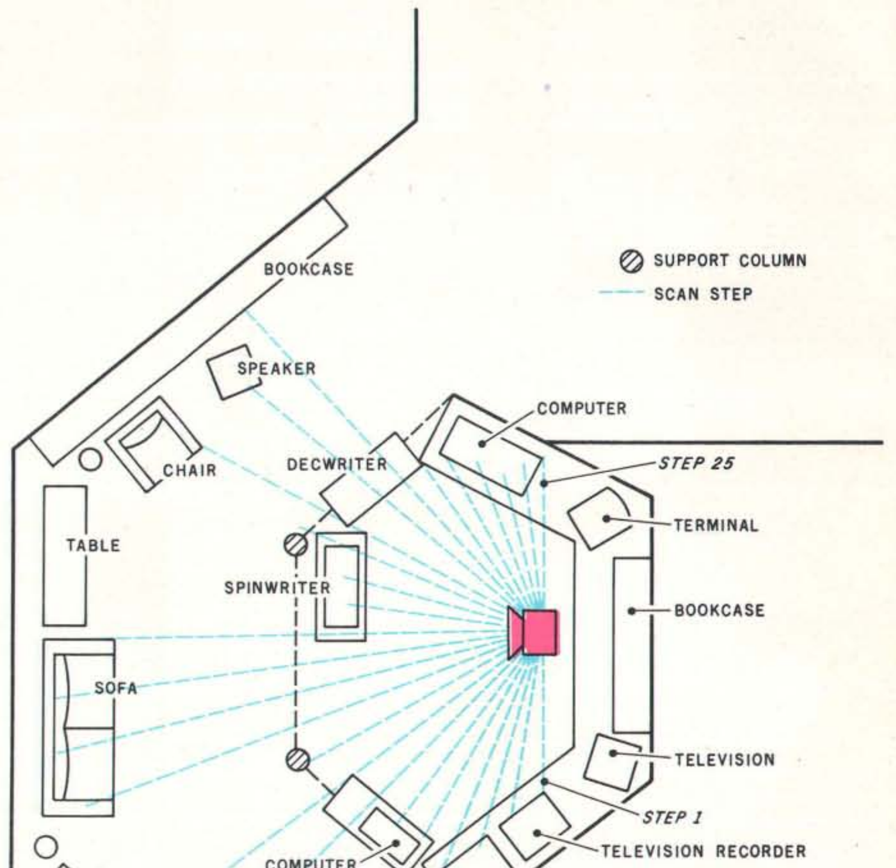

Figure 7b: *Floor plan of Circuit Cellar showing location of scanner and beam paths to room objects during the twenty-five steps in the scanning sweep. Bar graph of figure 7a shows relative distance to the nearest obstruction in the beam path at each step.*

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Listing 1: *A BASIC program that uses the interface circuit shown in figure 5 to read the three-digit distance value from the EDB and display the distance on the computer printer. A sample execution follows the BASIC-language statements.*

```
100 REM    THIS PROGRAM ALLOWS A COMPUTER TO READ AND DISPLAY
110 REM    DISTANCE AS MEASURED BY THE POLAROID ULTRASONIC
120 REM    RANGING SYSTEM DEMONSTRATOR BOARD. RANGE .9 TO 35 FT.
130 REM
140 REM
150 GOSUB 250
160 PRINT"DISTANCE TO TARGET IS ";S;" FEET"
170 GOTO 150
180 REM
190 REM
200 REM    THIS ROUTINE SETS AND READS THE 3 DIGITS ON THE
210 REM    RANGING BOARD.
220 REM    IT IS A THREE STEP PROCESS: SET THE DIGIT; READ THE
230 REM    DIGIT VALUE; AND MASK OFF EVERYTHING EXCEPT THE 4 BIT
240 REM    CHARACTER.
250 FOR T=0 TO 2
260 OUT 16,T
270 S(T)=INP(16)
280 S(T)=S(T) AND 15
285 S=(S(2)*10)+(S(1)*1)+(S(0)*.1)
290 NEXT T
300 RETURN

RUN

DISTANCE TO TARGET IS  3.3  FEET
DISTANCE TO TARGET IS  3.4  FEET
DISTANCE TO TARGET IS  3.5  FEET
DISTANCE TO TARGET IS  3.4  FEET
DISTANCE TO TARGET IS  3.3  FEET
DISTANCE TO TARGET IS  3.4  FEET
DISTANCE TO TARGET IS  3.3  FEET
DISTANCE TO TARGET IS  3.4  FEET
DISTANCE TO TARGET IS  3.4  FEET
DISTANCE TO TARGET IS  3.5  FEET
DISTANCE TO TARGET IS  3.3  FEET
```

Listing 2: *A BASIC program that causes the scanner to make a 180° scanning sweep in twenty-five steps and prints the distance measurements in the form of a bar graph. Figure 7a shows the output from the execution of this program on the system set up in the Circuit Cellar.*

```
100 REM THIS PROGRAM MAKES A 180 DEGREE SCAN AND RECORDS THE
110 REM DISTANCE TO SOLID OBJECTS EVERY 7.5 DEGREES.
120 REM
130 REM STEPPER MOTOR CONTROLLER ATTACHED TO PORT 18
140 REM ULTRA SONIC RANGING UNIT ATTACHED TO PORT 16
150 REM
160 REM
170 DIM Z(25)
180 OUT 18,1 :OUT 18,255 :REM PRESET STEPPER CONTROLLER
190 REM
200 REM CLOCKWISE SCAN
210 REM BIT 2 IS SET HIGH AND BIT 0 IS TOGGLED
220 FOR D=0 TO 24
230 OUT 18,5
240 GOSUB 470
250 OUT 18,4
260 NEXT D
270 REM
280 REM COUNTERCLOCKWISE SCAN
290 REM BITS 1 AND 2 ARE HELD HIGH AND BIT ZERO IS TOGGLED
300 FOR D=0 TO 24
310 OUT 18,7
320 GOSUB 570
330 OUT 18,6
340 NEXT D
350 REM
```

Listing 2 continued on page 56

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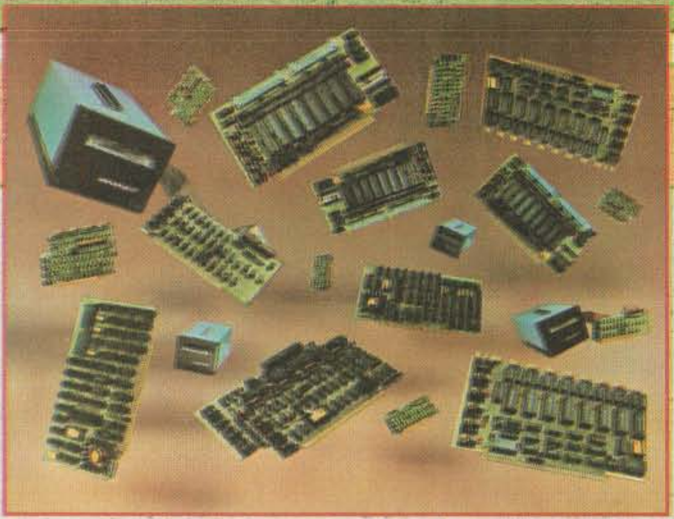

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Circle 32 on inquiry card.


IN CONTROL

Listing 2 continued:

```
360 REM
370 REM PLOT RANGES AS BAR GRAPH
380 FOR D=0 TO 24
390 FOR W=1 TO INT(Z(D))
400 PRINT"**";
410 NEXT W
420 PRINT" "
430 NEXT D
440 GOTO 220
450 REM
460 REM
470 REM STEP DELAY AND RANGE SAMPLE ROUTINE
480 FOR T=0 TO 2
490 OUT 16,T
500 S(T)=INP(16) :S(T)= S(T) AND 15
510 NEXT T
520 Z(D)=(S(2)*10)+(S(1)*1)+(S(0)*.1)
530 FOR Q=0 TO 10 :NEXT Q
540 RETURN
550 REM
560 REM
570 FOR Q1=0 TO 100 :NEXT Q1
580 RETURN
```

Listing 3: *A short BASIC program that demonstrates one method for using the ultrasonic scanning device in a security system.*

```
100 REM THIS PROGRAM DEMONSTRATES HOW THE ULTRASONIC RANGING
110 REM BOARD CAN BE USED AS AN INTRUSION DETECTOR.
120 REM
130 REM
140 A=1 :GOSUB 220 :REM  TAKE FIRST DISTANCE READING
150 GOSUB 330
160 A=2 :GOSUB 220 :REM  TAKE SECOND DISTANCE READING
170 IF ABS(X(1))-ABS(X(2))>=.3 THEN GOTO 280
180 IF ABS(X(2))-ABS(X(1))>=.3 THEN GOTO 280
190 GOTO 140 :REM CONTINUE SCAN
200 REM
210 REM
220 FOR T=0 TO 2
230 OUT 16,T
240 S(T)=INP(16) :S(T)=S(T) AND 15
250 NEXT T
260 X(A)=(S(2)*10)+(S(1)*1)+(S(0)*.1)
270 RETURN
280 PRINT" I GOT YOU IN MY SCANNER AT ";X(2);" FEET."
290 REM AN ALARM ROUTINE WOULD BE PLACED HERE
300 GOTO 140
310 REM
320 REM
330 REM SAMPLE RATE DELAY TIMER
340 FOR Y=0 TO 200 :NEXT Y
350 RETURN


RUN

I GOT YOU IN MY SCANNER AT 11.4 FEET.
```

Text continued from page 50:
is held high, and bit 0 is toggled to produce each step. To drive the motor counterclockwise, bits 1 and 2 are held high, and bit 0 is toggled for each step. The new scanner can read the distance at each step.

Listing 2 is a program that causes the scanner to make a 180° scan and prints out the distance measurements in the form of a bar graph, demonstrated here in figure 7a.

To help you understand the mode of operation and value of the ranging device, I have also sketched the area of the Circuit Cellar where the measurements were taken. (See figure 7b.)

The scanner (the red object in figure 7b) was placed on a tripod at a

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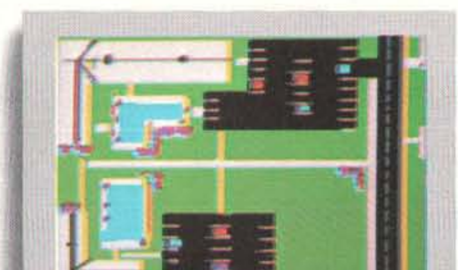

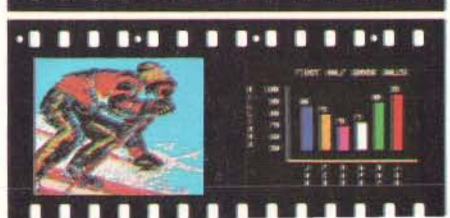

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Videoprints shown were produced by an Apple II with graphics tablet, or by live images on ¾″videotape.

Circle 34 on inquiry card.

"Polaroid," "Polacolor" and "SX-70" are registered height of 5 feet (1.5 meters), about 2 feet (0.6 meters) in front of my desk area. The parabolic reflector was pointed 90° to the left of center so that a 180° scan resulted in it ending up pointing 90° right of center. At each of the twenty-five steps it took to reach this point, it measured the distance to the nearest obstruction to its line of detection. For comparison, the blue dotted lines in figure 7b show where each step should have been and what should have been in the way of the sonar "beam."

The program of listing 2 printed the graph bar corresponding to each step, starting with step 1. At the position reached after step 1, the system recorded a distance of about 5 feet (1.5 meters) to the VTR (videotape recorder) on the counter top. The same result was obtained for the next two steps. At the position reached after step 4 (about 30° around), the scanner was pointing between the stereo system and the TRS-80 computer on the desk to the right. This was indicated by a reading of about 15 feet (4.6 meters), measuring the distance to the bookcase on the far wall.

The next couple of steps had the TRS-80 directly in the path of the scanner beam, and then the path of the beam was open to the far wall again for a couple of steps. The rest of the scan was similarly significant in that the range detector accurately described the perimeter from its viewpoint. Most important, however, was the demonstration of the sensitivity of the ranging device. At steps 9 and 16, the only object in the path between the scanner and the wall was a 4-inch (10 cm) ceiling-support column about 7 feet (2.1 meters) away. In both cases the obstruction was accurately identified.

We now have a device that can rotate to a particular position and accurately measure the distance to any object it "sees." A practical use of the range detector is as a security device. When the wall is known to be 16 feet (4.8 meters) away from the scanner, a sudden reading of 9 feet (2.7 meters) indicates that someone or something just moved in front of the range detector. The program of listing 3 allows the range detector to be used as a motion detector.

In Conclusion

I have demonstrated only two uses for the Polaroid Ultrasonic Ranging System Demonstrator Kit. The majority of applications I've heard about thus far have been independent projects that utilize the ranging system *without* the additional capabilities of a computer. They include a walking cane (with audio feedback) for the visually handicapped, a 0 to 35 foot (0 to 11 meter) altimeter for the *Gossamer Albatross* aircraft (for its English Channel crossing), and as an electronic "dip stick" for measuring liquid levels in storage tanks.

I hope that once you realize how easy it is to attach this automatic ranging system to a computer, you'll have as much fun experimenting with it as I have. Unfortunately, a new problem has arisen. Until now, one of the major reasons I haven't attempted to build a robot was the amount of expense and technical effort required to make it "see." Now I'll have to find a new excuse.■

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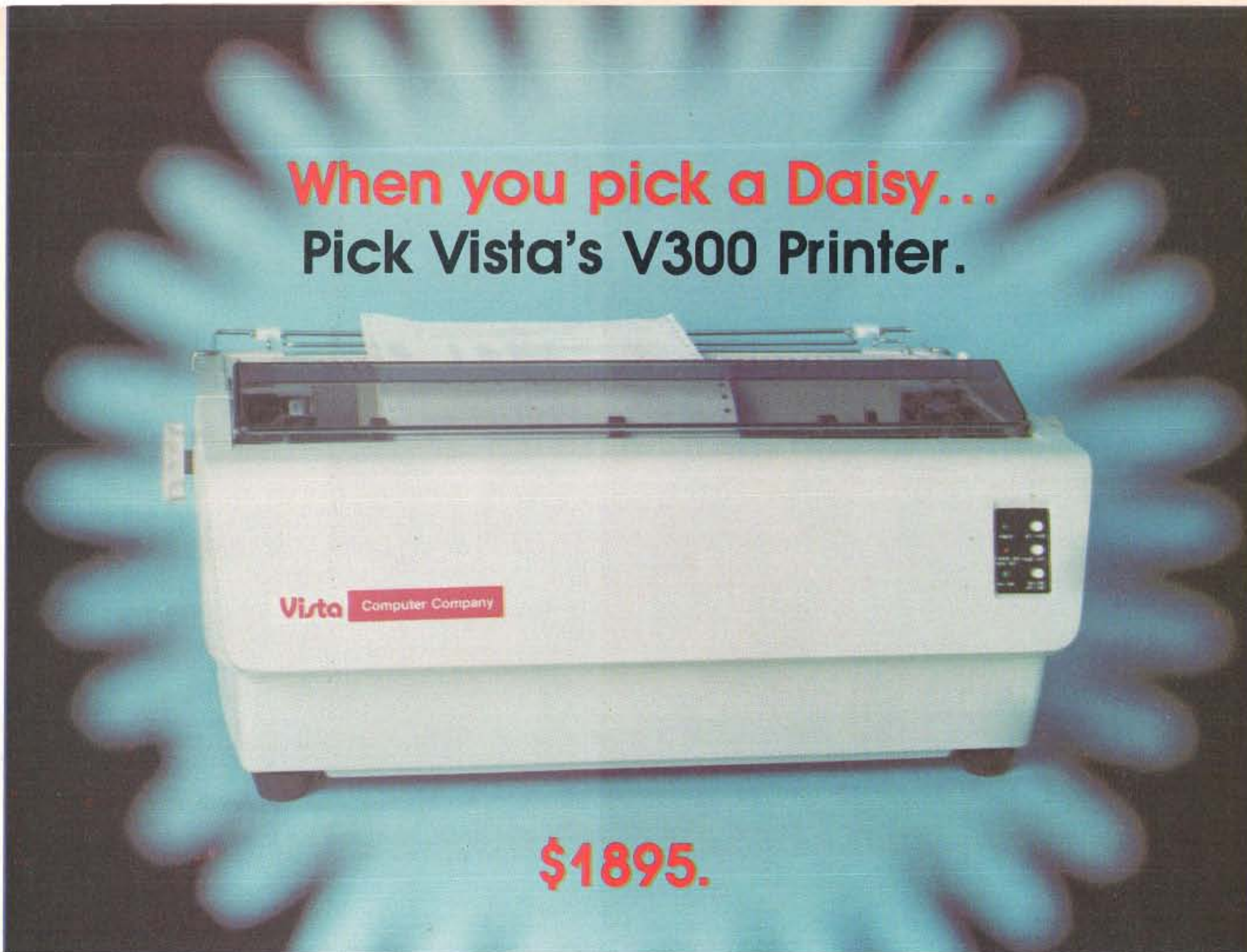

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OUR MESSAGE.

Kinetic String Art for the Apple

Louis Cesa, 305 Doris Ave, Vestal NY 13850

The accompanying photographs were produced using high-resolution graphics on the Apple II computer. As interesting as the pictures are, they do not do justice to the real-time art that takes place on the screen. The photographs show only time slices at different stages in the development of the kinetic string art. On the screen one can see shapes forming and gradually being replaced by other shapes in a continuous display of color and motion.

Algorithm Description for Kinetic String Art Program

1. *Initialize Variables:*
X1=X2=Y1=Y2=CNT1=CNT2=0;
DIM C(150), TX1(150), TX2(150), TY1(150), TY2(150);
AT=1

2. *Erase the line from* TX1(AT), TY1(AT) *to* TX2 (AT), TY2(AT) *of color* C(AT).

3. *If* CNT1=0 then choose a new random color and a new random CNT1.
COLOR=1+RND(3)
CNT1=5×(1+RND(10))

4. *If* CNT2=0 *then choose new step sizes for* DX1, DY1, DX2 *and* DY2 *and a new random* CNT2:
DX1=RND(9)−4
DY1=RND(9)−4
DX2=RND(9)−4
DY2=RND(9)−4
CNT2=5×(1+RND(10))

5. *Compute new* X1, Y1, X2, Y2 *for next line and test for screen boundaries. For example,*

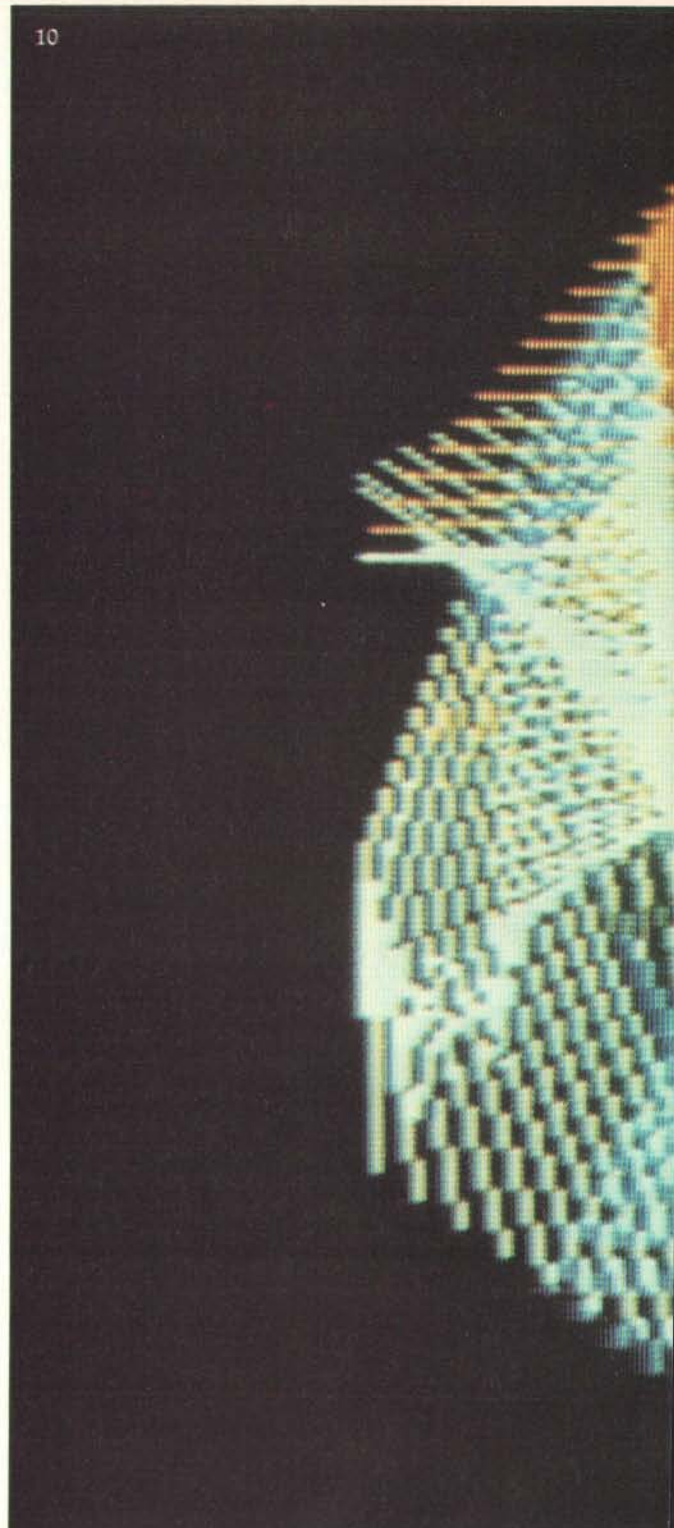
```
470 PX1=X1+DX1
480 IF PX1> =0 AND PX1< = MX THEN 500
490 PX1=X1: DX1=-DX1
500 X1=PX1
```

6. *Draw the new line from* X1, Y1 to X2, Y2.
7. *Store the coordinates and color of the new line in:*

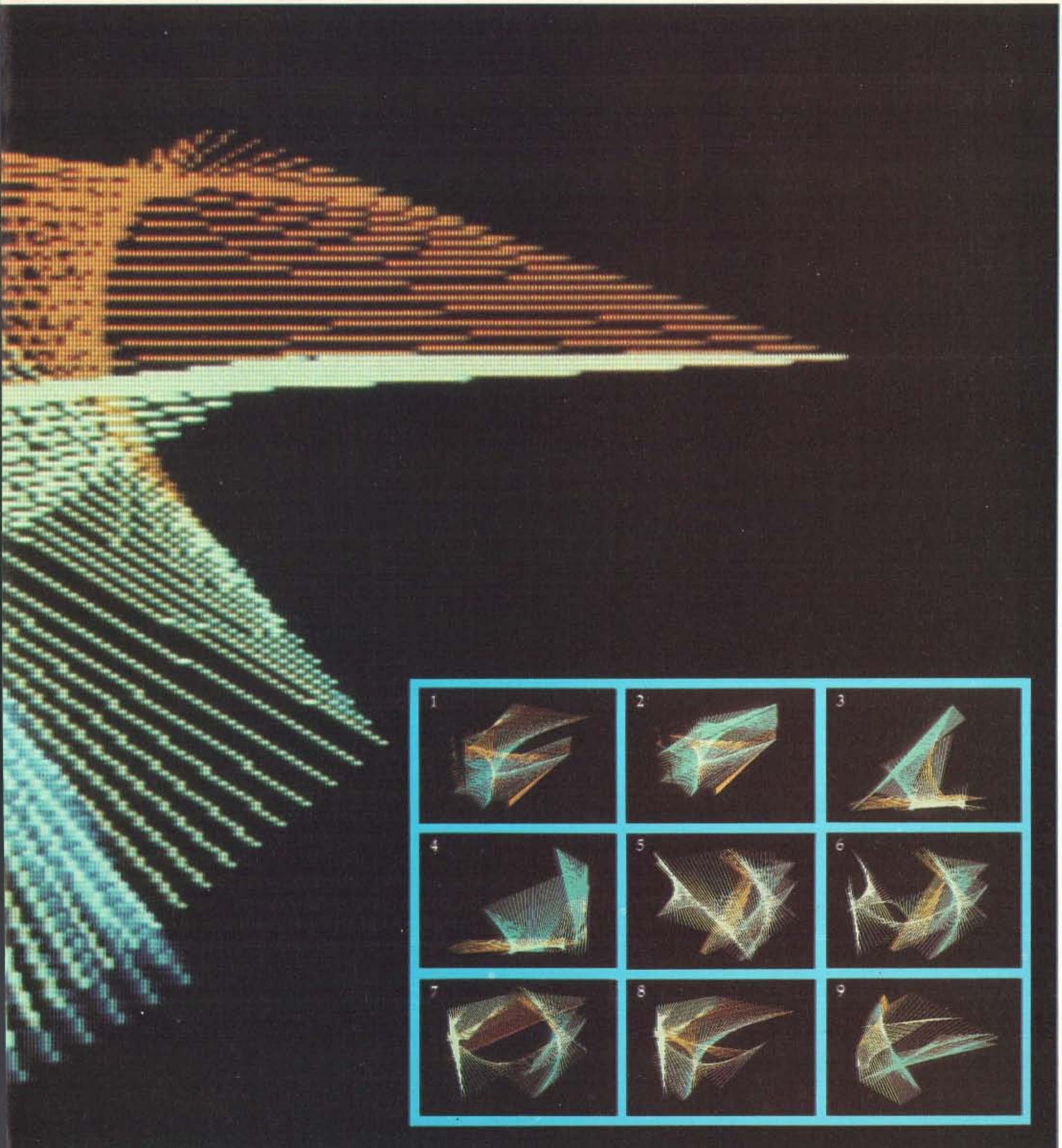
C(AT), TX1(AT), TX2(AT), TY1(AT), TY2(AT)

8. *Step AT to next position in table.*
AT=AT+1
IF AT > 150 THEN AT=1

9. *Go to step 2.*



The algorithm used is quite simple. (See textbox. Contractual agreements preclude publishing a listing of the program.) The pictures are drawn by a line segment making a random walk on the screen. An initial pair of endpoints is chosen at random; also chosen at random are color, number of lines to be drawn with that color, step size for each endpoint (in the x and y directions), and number of times that the step sizes are to be used. Successive lines are drawn by advancing the endpoints of the line by the chosen step size in the x and y directions.



Whenever the number of times that an action was to be executed (such as number of lines to be drawn in a given color) is exhausted, new random values for that quantity and for the number of times that the quantity should be used, are chosen. If a point attempts to walk off the screen, it is reflected back.

The designs in the accompanying photographs are formed by 150 lines. The program was coded so that when the 151st line is added, the first line is deleted, and so on. This is done by a routine that keeps track of each line segment currently on the screen. When the table contains 150 lines, this routine erases the oldest line segment before adding a new one. (This effect can be noted in photos 1 and 2.) Interesting effects can be obtained by using different algorithms to choose the new line to be added at each iteration. For example, an interesting effect is obtained with just 10 lines on the screen and choosing random endpoints for each new line (essentially a visual image of white noise). ■

Micrograph

Part 1: Developing an Instruction Set for a Raster-Scan Display

E Grady Booch
4314 Driftwood Dr
Colorado Springs CO 80907

Simply stated, computer graphics is the technique of visual communication from computer to man. (See reference 14.) Interactive computer graphics is an important subset of this broad field and relates to computer-generated displays that can interact with a user in real or near-real time. Interactive graphics started with attempts to use the CRT (cathode-ray tube) as a computer output device. (See reference 12). The Whirlwind I in 1950 and Sketchpad in 1963 are examples of early attempts at interactive computer-graphics systems. Since that time, two distinct classes of CRT-based devices have been developed for use in interactive graphics: calligraphic (or vector) devices and raster-scan (as in a television receiver) devices.

The area of vector graphics "has for several years been sufficiently mature to justify efforts at standardization within it." (See reference 8.) A large body of information is available on the design of such systems. (See reference 13.) However, the same is not necessarily true of raster-scan devices. Until recently, raster-scan technology has not been economically feasible. Decreasing hardware costs, especially for memory, have facilitated the trend toward raster-scan displays. (See reference 3.) The emergence of raster-scan displays has a side benefit, namely that "raster-scan technology is the only economical way to achieve color in full-sized displays." (See reference 4.)

For the microcomputer user, this means that he can add moderate-resolution color graphics to a system at an affordable price, using raster-scan technology. The benefits of color graphics for the personal computer are obvious: not only are color displays dazzling and eye-catching, but more important, they add a new dimension for communicating with a computer. Microcomputers with color-graphics capabilities have been available for some time, such as the Apple II and the Compucolor. Within the past year, however, Motorola and AMI (American Microsystems Incorprated) have released a LSI (large-scale integration) chip, called a video-display generator, which performs all the video functions necessary to produce a color-graphics and alphanumerics display on a standard, unmodified color television. As a result, low-cost color-graphics displays are now possible for the personal computer user.

This three-part article presents the theory, design, and construction of a low-cost, color-graphics display processor called Micrograph, which is based on the Motorola MC6847 video-display generator. (See photo 1.) Essential characteristics of Micrograph are described in the text box. In the remainder of this article, I will review the characteristics of interactive computer-graphics systems, followed by an overview of the Micrograph design. Subsequent articles will concern the hardware construction details for Micrograph and the software necessary to control the system.

About the Author

E Grady Booch is currently a computer systems design engineer with the Air Force Space and Missile Test Center. He is involved with the development of a high-resolution color-graphics system for tracking missile launches. Grady received his bachelor of science and master of science degrees in computer science from the United States Air Force Academy and the University of California, Santa Barbara, respectively.

Micrograph Features:

- *64 by 64, 128 by 128, and 256 by 192 pixel resolutions are available.*
- *Up to eight different colors are displayed at one time.*
- *It contains a single-board processor, based on Zilog Z80 processor and Motorola MC6847 Video Display Generator.*
- *Construction cost: about $275 .*
- *High-level graphics primitives support.*
- *Both graphics and alphanumerics are supported.*
- *It interfaces to a host microcomputer via three 8-bit input/output ports (status, input, and output) and by radio-frequency or video entry to a standard, unmodified color television.*

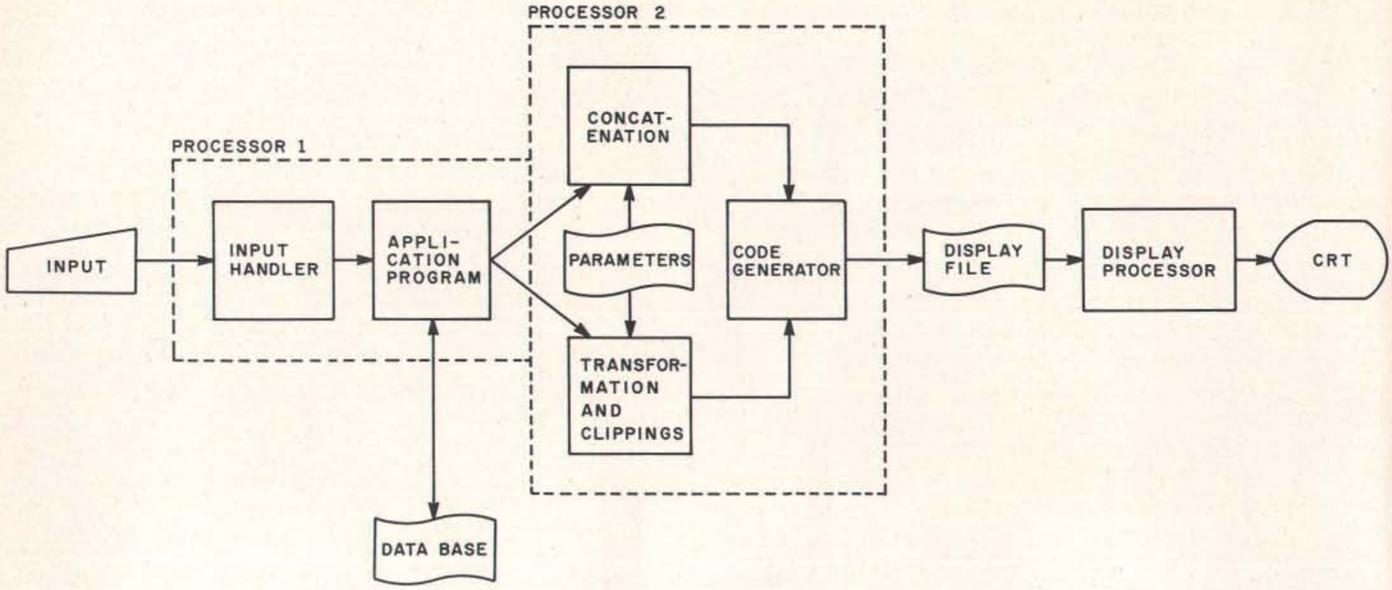


Figure 1: *A general block diagram of an interactive graphics-display system. The functions of Processor 1 and Processor 2 may be performed by the same device; however, the output of Processor 1 must be a structured abstract of the image to be displayed, for the graphics package (Processor 2) to operate. (The figure is from* Principles of Interactive Computer Graphics, *by Newman and Sproull. Copyright 1973, used with permission of McGraw-Hill Book Company.)*

Background on Interactive Computer-Graphics Systems

Newman and Sproull, in their book *Principles of Interactive Computer Graphics* (reference 12), present an excellent model of a generalized interactive graphics system, as reproduced in figure 1. Processor 1, which is not necessarily a different physical processor than Processor 2, handles program-specific processing for a particular graphics application. The output of this processor is generally a structured, abstract representation of the set of images that will be displayed.

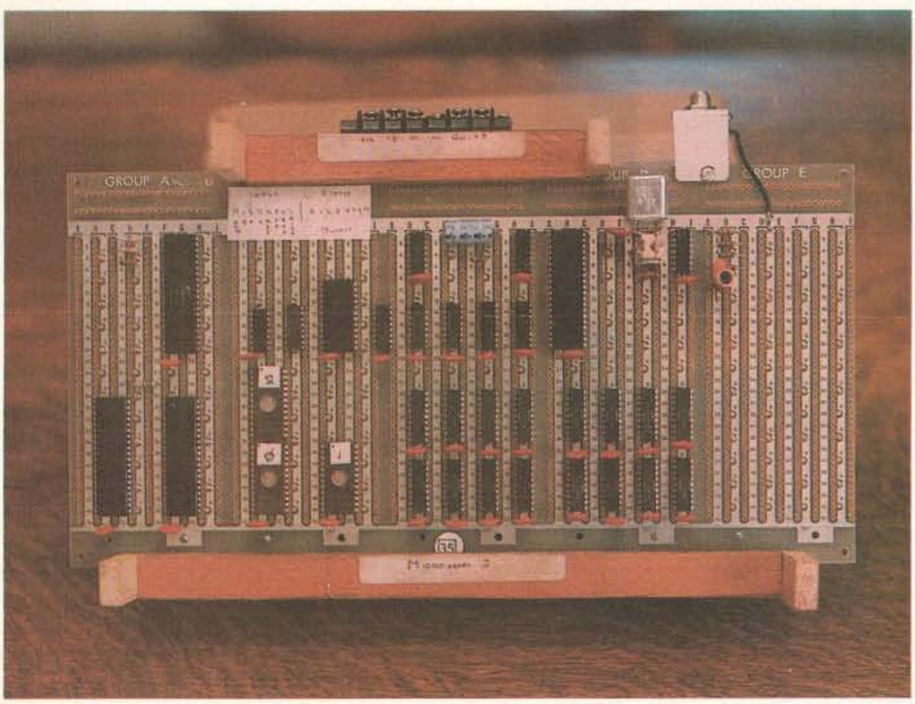


Photo 1: *A view of the completed Micrograph prototype, based on the Motorola MC6847 video-display generator. Use of this integrated circuit greatly simplifies hardware design by eliminating the complex divider-chains usually found in homebrew video displays.*

Processor 2 represents the processing that is to be handled by a graphics package, as it is commonly called. This processor manipulates the abstract representations, performing transformations (such as rotation, translation, and scaling) and clipping as needed. The output of this processor is generally a display file consisting of instructions that are meaningful to a physical display processor. The display processor uses these instructions to produce an image upon some type of display device. For interactive graphics, these processes must occur very rapidly.

Numerous graphics packages for commercial systems exist to handle the requirements of Processors 1 and 2. SIGGRAPH (Special Interest Group on Computer Graphics) of the ACM (Association for Computing Machinery) has proposed a standard for such systems. However, for our purposes, we must turn our attention to the display processor itself. Before examining the design for a color-

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The design of UniFLEX, with its hierarchical file system and device independent I/O, allows the creation of a variety of complex support programs. There is currently a wide variety of software available and under development. Included in this list is a Text Processing System for word processing functions, BASIC interpreter and precompiler for general programming and educational use, native C and Pascal compilers for more advanced programming, sort/merge for business applications, and a variety of debug packages. The standard system includes a text editor, assembler, and about forty utility programs. UniFLEX for 6809 is sold with a single CPU license and one years maintenance for $450.00. Additional yearly maintenance is available for $100.00. OEM licenses are also available.

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graphics display processor, we must consider the characteristics of calligraphic and raster-scan displays.

Comparison of Display Devices

Four basic technologies exist to support interactive graphics:

- calligraphic
- raster-scan
- storage-tube
- plasma

Three of these devices (calligraphic, raster-scan, and storage-tube) are CRT-based, but only two (calligraphic and raster-scan) are adaptable to interactive, rapidly moving displays.

Calligraphic displays produce images by drawing vectors using endpoint information. A relative or absolute position is presented to the display, and the electron beam is deflected from its current position. Analog methods of vector generation can produce high-resolution vectors. Symbols are usually generated as a collection of vectors. Special hardware may also exist to produce circles and arcs, but these features are generally not cost-effective.

Calligraphic displays can achieve resolutions of up to 4096 by 4096 pixels (ie: picture elements) which corresponds to 16,777,216 elements (which is why I don't consider 256 by 256 pixels or even 512 by 512 pixels as "high resolution"). (See reference 11.) Therefore, a 21-inch-diagonal rectangular CRT will typically have a spot size of 0.02 inches (0.5 mm). (See reference 9.) Vectors using these techniques will appear sharp rather than granular. Several thousand vectors may be displayed flicker-free.

Calligraphic displays can produce color images using beam-penetration tubes. This type of CRT has multiple layers of phosphor coating on the face of the tube. Individual colors (usually four different colors) are produced by varying the anode voltage and hence the depth of beam penetration.

Raster-scan displays produce an image much like commercial television by generating a full screen of horizontal lines. This set of lines (the raster) is modulated in the Z axis (intensity and color) to produce an image. Vectors are drawn using digital scan-line-conversion techniques which compute every point along the vector. Symbols are usually generated using a character generator which directly plots each point of the symbol.

Raster-scan displays can achieve resolutions up to 2048 by 2048 in monochrome and 1024 by 1024 in color, which corresponds to roughly one million pixels (for color). (See reference 9.) The limited resolution for color displays results from the difficulty in producing shadow masks and the granularity of the phosphor-dot triples used in constructing the CRT. Because of the nature of the raster-scan CRT, the individual dots have insignificant overlap and therefore vectors appear coarse and stair-stepped. However, techniques such as *ordered-dithering* and *anti-aliasing* algorithms exist to reduce the effect of granularity. (See references 7, 10, and 12.) Stair-stepping (or aliasing) is most noticeable in near-

Glossary

Aliasing: *As used here, a granular or stair-stepped appearance in an image caused by the display screen being divided into a finite number of elements. This effect is most noticeable on low-resolution displays and on high-resolution displays with near-horizontal or near-vertical lines.*

Calligraphic Display: *A display that produces an image from a collection of vectors and points, by directing the electron beam in the X and Y directions corresponding to the vector endpoints.*

Display Processor: *A special-purpose peripheral processor that is dedicated to producing a visual image on some type of display (usually a CRT) based on special graphics instructions in a display list.*

Instancing: *The technique of defining one image, then being able to perform transformations to reproduce the same image in several different places on the display.*

Pixel: *A picture element.*

Raster-Scan Display: *A display that produces images, just as in television, by amplitude modulation of the Z-axis beam along a full screen of horizontal lines (the raster).*

Scan-Line Conversion: *An algorithm used to calculate each individual point along a vector, given the starting and ending points.*

Transformation: *Modifications of an image, such as translation (movement in the X, Y, or Z axis), rotation (also in any axis), and scaling (also in any axis).*

CALLIGRAPHIC DISPLAY		RASTER-SCAN DISPLAY	
Advantages	Disadvantages	Advantages	Disadvantages
• High resolution (4096 by 4096).	• Analog circuitry often requires adjustment.	• Digital circuitry is quite reliable.	• Moderate resolution (1024 by 1024 color).
• Thousands of vectors can be displayed.	• Limited colors (usually four).	• Many colors possible (more than 2^{16}).	• Digital scan-line conversion is slow.
	• Display has low brightness.	• Display is high intensity.	
	• Limited intensities are possible.	• Many (gray scale) intensities exist.	
	• Shading of large areas impossible.	• Shading areas is simple.	
	• Flicker occurs when too many vectors are displayed.	• Display does not flicker.	
	• Ghosting occurs on rapidly moving displays.	• Display has high contrast.	

Table 1: *Comparison of calligraphic (ie: vector) and raster-scan displays.*

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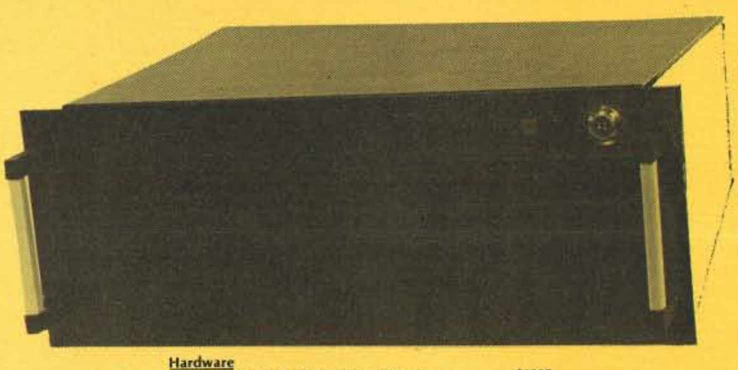

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68K MiniFrame (256K bytes RAM)	$6995
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68,000 Assembler	$350
Industrial Pascal Compiler	$400
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Documentation	
Hardware Documentation	$100
Software Documentation	$100

Processor Type	Motorola MC68000 Motorola MC6809 for I/O, Disk, and memory management.
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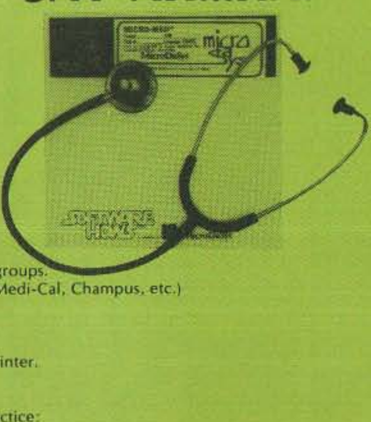

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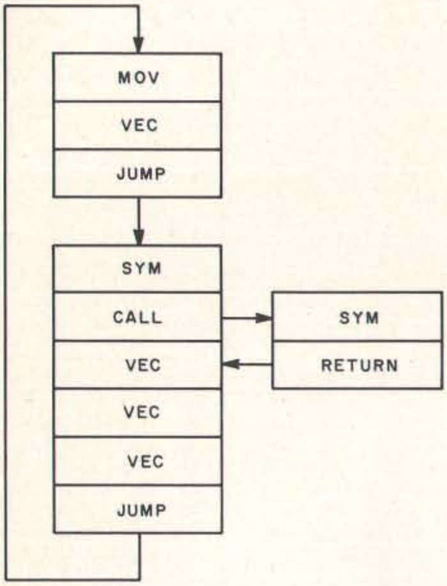

Figure 2: *The display list of primitive instructions performed by the display processor of a calligraphic (ie: vector) display. The loop is performed repeatedly by the processor to guide the display electronics. A new or modified display is produced by altering the display list.*

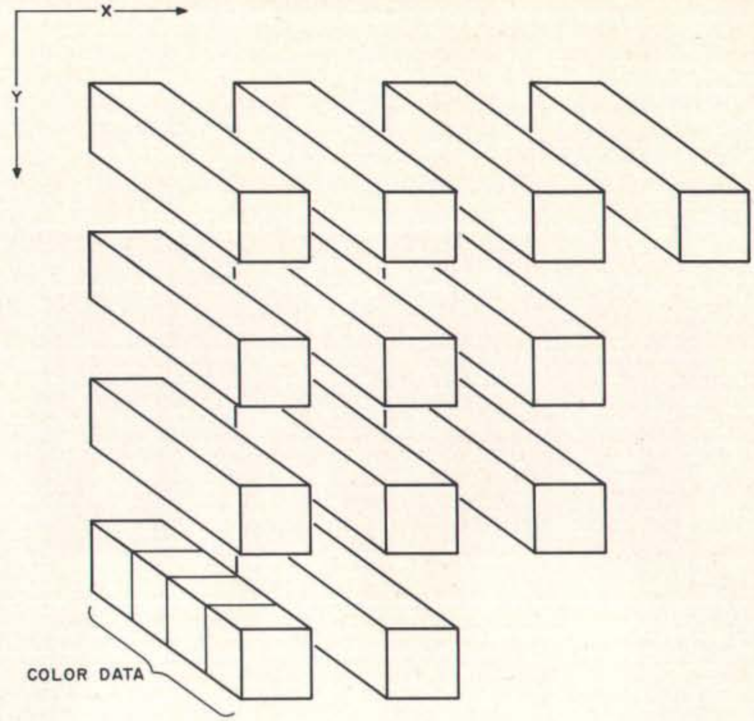

Figure 3: *A color raster-scan frame buffer. Each pixel (ie: picture element) on the screen is represented by a unique set of X and Y coordinates. Every coordinate is associated with some amount of color information (in this case, 4 bits). This data may be used to specify an address in a color-look-up table such as figure 4.*

vertical and near-horizontal lines. Any number of vectors, up to and including a full CRT screen, can be displayed without flicker.

Color raster-scan displays produce their images by exciting triads of dots or rectangles at each pixel. Each triad generally consists of one red, one blue, and one green element. Different colors (in excess of 2^{16}) can be produced by exciting each element at different levels of intensity.

Clearly, the use of each type of display is associated with certain advantages and disadvantages, as summarized in table 1.

Controlling a Calligraphic Display

As mentioned previously, a calligraphic display draws vectors based upon endpoint information. Even the most complex images can be created as a collection of vectors. Because of the short persistence of the CRT phosphors required for a fast calligraphic display, once a vector is drawn, it will disappear very quickly, typically in just a few milliseconds. Thus, the entire display must be continuously refreshed to avoid flicker and a loss of portions of the image. Refresh rates vary with the intensity of the display, but the image must be refreshed at least 30 times per second.

These requirements give rise to a structure called a *display list*. As figure 2 indicates, a display list is simply a collection of primitive instructions for the display processor. The display processor repeatedly scans this list to send vector-drawing information to the display electronics. To modify a display, Processor 2 (of figure 1) simply points the display processor to a new display list, or inserts or deletes a portion of the existing list. Generally, a display list is stored external to the display processor in the host-processor memory and is addressed via DMA (direct memory access).

Numerous instruction sets have been devised for calligraphic-display processors. Since displays at this primitive level are very difficult to control, the trend is toward higher-level graphics languages. However, all primitive instruction sets must contain certain basic features, including primitives to move the beam, draw a line, draw a character, call a subroutine, and change colors or intensity.

Controlling a Raster-Scan Display

Unlike calligraphic displays, raster-scan displays generally employ what is known as a frame buffer. The frame buffer is essentially a block of memory that maintains a one-to-one correspondence with the set of pixels. In other words, there exists one memory location for every pixel. A pixel can be specified in one or more bits, as figure 3 indicates. Thus, color information for a pixel is stored at each memory location. In color raster-scan displays, this memory location does not necessarily hold physical color information, but often supplies a pointer to a color-look-up table, as figure 4 indicates. Thus, for example, a pixel may be specified by 4 bits, but the color information may be translated to any sixteen of a possible 2^{16} colors. This technique allows the display of many different colors with a conservation of memory. The techniques of *contrast stretching* and *pseudocoloring* can be easily achieved with a color-look-up table.

A raster-scan display does require a large amount of memory to implement the frame buffer. For example, a display with a resolution of 512 by

512 by 8 requires 256 K bytes of memory. This drawback is one of the primary reasons that raster-scan devices have only recently become cost-effective.

Using a frame buffer, an image is drawn by inserting color information into the memory location corresponding to the appropriate pixel. This architecture has the feature of producing flicker-free images; however, to draw vectors the display processor must calculate every point along the vector. Scan-line-conversion algorithms that calculate the points of a vector (given the endpoints) exist, but such algorithms are slow compared to analog techniques used in calligraphic displays. Once an image is written into the frame buffer, it will be continuously displayed. Refresh is not required by the host, but the image cannot be modified as a calligraphic display can.

Clearly, the characteristics of color raster-scan displays present control problems unlike those for calligraphic displays. We must therefore not only exploit the inherent color-display potential, but we must also deal with the problems of selectively updating a raster-scan display. As the next section indicates, we can adapt calligraphic control techniques to effectively control a color raster-scan display.

Primitives for a Color Raster-Scan Display

To develop an instruction set for a color-graphics display processor, we must first establish our requirements. We assume as a minimum that these primitive instructions will be executed by an intelligent display processor having both a single-frame buffer and a color-look-up table. Therefore, we require that:

- The set of graphics primitives must permit the construction of any image within the physical limitations of the raster-scan display. The set doesn't need to be minimal: efficiency is a more important characteristic.
- The graphics primitives must be implementation-independent. The primitives must be applicable to any resolution and not be constrained by word size or any similar characteristic of the target processor.
- The graphics primitives must be

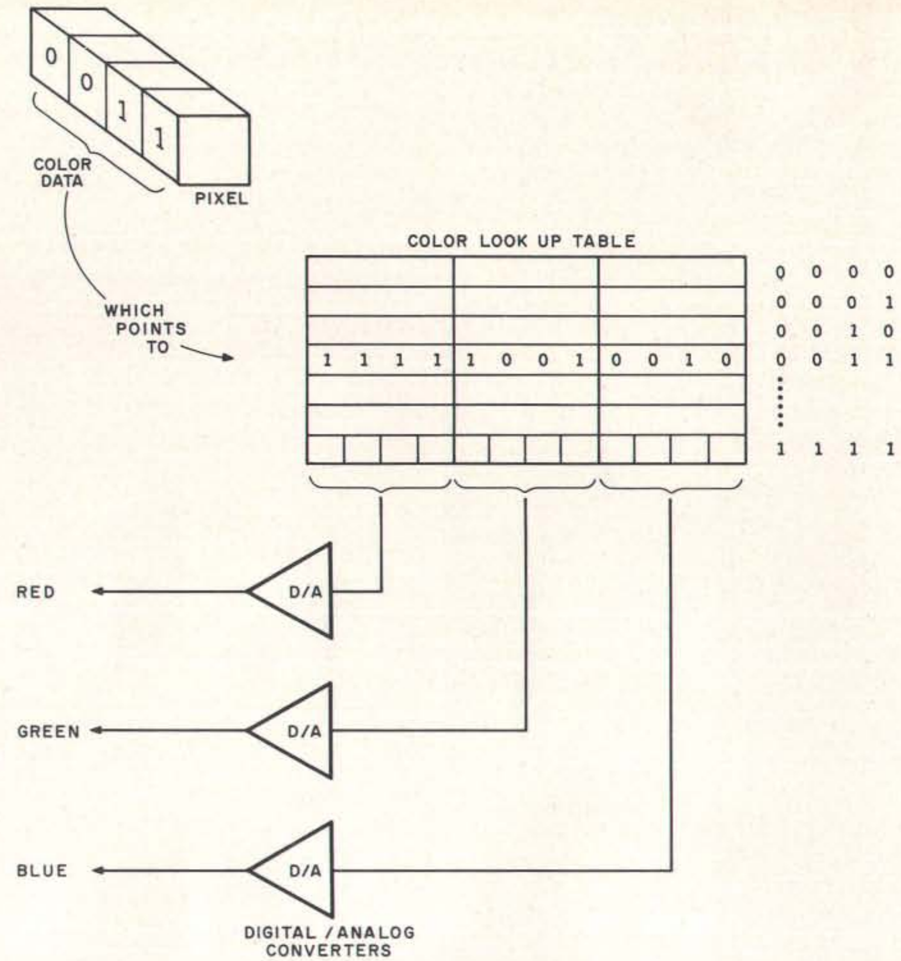

Figure 4: *Color-look-up table. Using this scheme, a 4-bit value from the frame buffer (shown in figure 3) can select one of sixteen predefined colors. In this example, each color is composed of various intensities of red, green, and blue. Other systems may specify colors by indicating values for intensity, hue, and saturation.*

adaptable to a display-list structure, since display lists are a well-established form of control for display processors and hence permit straightforward integration with generalized graphics-support software in the host processor.

Graphics Primitives

As explained previously, we know that raster-scan and calligraphic displays are architecturally different. However, our third requirement indicates that both classes of displays must at least appear identical to the user. Therefore, our graphics primitives become an abstraction for the control of a raster-scan display. We must design a set of primitives independent of the actual architecture of the display. Just as with the benefits of using a high-level programming language, the use of abstractions in controlling a graphics display allows the user to concentrate upon producing images rather than concerning himself with the mechanics of the implementation.

Before examining the primitives for a color raster-scan display, it is important that you understand two very critical abstractions. First, it is necessary that the user visualize the display processor as manipulating a two-dimensional Cartesian surface, with the origin of the space at some predefined location (usually the center, or lower left-hand corner) on the display surface. There may or may not be a direct mapping of pixel data in the display-processor memory to this surface: the actual implementation should be invisible to the user.

From the previous section, we know that the display processor doesn't need to be concerned with identification of objects that are displayed in this space, but rather we

need only to be able to manipulate the pixel data that forms these objects.

The second abstaction which we must develop concerns *graphics-display registers*. These registers are defined in the display processor and may be addressed by the user to set up global image parameters, such as current vector type, or to provide immediate processor-status information, such as the current X and Y position. Clearly, these registers may be implemented in diverse portions of the display hardware. Concerning the second requirement, it is important that the user sees these registers as an easily addressable set that may be referenced by the host processor. As we shall see, the use of graphics-display registers helps reduce the scope of some of the graphics primitives that are necessary to control a color raster-scan display.

It is evident, as with any graphics display, that the minimum set of instructions we need includes only a point-positioning and a vector-drawing primitive. But clearly, this set is by no means efficient. Thus, I will present and defend the set of graphics primitives for a color raster-scan display which will be implemented in Micrograph. Next I will present the primitive instructions in their mnemonic form in order to maintain their implementation independence.

As with a calligraphic display, one of the most fundamental operations we perform is point positioning. Since a raster-scan display does not produce an image by beam movement, but rather by Z-axis modulation, we must abstract current X and Y coordinates, which may also be addressed as graphics-display registers. To increase the utility of a move primitive (ie: primitive instruction specifying a movement), we must include several options. To begin, both absolute and relative point positioning are necessary. The need for absolute positioning is obvious; relative positioning permits an entire display to be defined relative to a single point in the image, which is an essential feature if subroutines and instancing are to be supported.

Furthermore, remember that the elements of an image are often closely spaced: thus, we need options for long and short movement. With a long movement, we may express a point position in the full-screen coordinates (for either absolute or relative positioning). With a short movement, we may express a point position with a limited maximum value (such as 0 to 7, again either absolute or relative). Therefore, it's possible to decrease display-list memory requirements with the use of short movements, which take less storage than a long instruction. Finally, it is often necessary to simply plot a single point. To do so, we must include the option to illuminate or not. If we illuminate, we obviously must include a parameter for the color of the point. Mnemonically, our move primitive can be represented as:

MOV T,M,C,I,(±)X,(±)Y

where:

T = type (*S*hort or *L*ong movement)
M = mode (*A*bsolute or *R*elative positioning)
C = color
I = illuminate (*Y*es or *N*o)
X = X position or offset (with a sign on the relative mode)
Y = Y position or offset (with a sign on the relative mode)

For example, the primitive:

MOV S,R,4,Y,+3,−4

moves the current X,Y position by an offset of (3, −4) and illuminates that point in a color whose code is 4.

The next obvious primitive we need performs vector drawing. With the same justification as for the move primitive, we must permit the options of long and short vectors. We assume that the starting point of the vector is the current X,Y position, and the endpoints are determined by either absolute or relative positioning. Just as with a move primitive, we must also be able to specify the color of the vector. Finally, we must be able to define the current vector type, such as solid, dashed, or dotted vectors. Experience indicates that such line types are rarely used. Therefore, rather than specifying this parameter in the primitive itself, we assume that we have available a graphics-display register that defines the current line type. Mnemonically, our vector primitive

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can be represented as:

VEC T,M,C,(±)X,(±)Y

where:

T = type (*S*hort or *L*ong movement)
M = mode (*R*elative or *A*bsolute endpoints)
C = color
X = X position or offset (with a sign in the relative mode)
Y = Y position or offset (with a sign in the relative mode)

For example, the primitive:

VEC L,A,15,255,180

draws a vector (with the color coded 15) from the current X,Y position to the pixel (255,180).

We must have an instruction that allows us to call a subroutine. Such a primitive is essential to support object instancing. Furthermore, since we assume the existence of an intelligent target display processor, we must expand our primitive to permit a call to a display-processor subroutine. Such an option allows the user to execute his own predefined routines, which can possibly decrease the image-generation time and reduce some of the processing burden from the host for often-used routines. Clearly, this option is not essential, but it does allow the user to exploit the full capabilities of the display processor. Mnemonically, our call primitive (ie: primitive instruction to call a subroutine) can be represented as:

CALL T,N

where:

T = type of subroutine (*P*rocessor or *G*raphics)
N = name or number of subroutine

For example, the primitive:

CALL G7

calls the grapics subroutine number 7.

Along with the call primitive, we obviously must have a primitive which allows us to return from a subroutine. Our return primitive instruction can be represented as:

RET

Text is often an element of a display and therefore warrants its own primitive. It is important to realize that text usually occurs as a string of symbols rather than a single symbol. Therefore, we must include an option to display a number of contiguous symbols. Furthermore, in terms of the symbols themselves, we may wish to use either a standard alphanumeric font or a user-defined font. Therefore, we assume the availability of a programmable symbol generator. As will be explained, the user may define his own set of symbols and then display a string of symbols by using the symbol primitive, passing it the codes for the appropriate symbols. Mnemonically, our symbol primitive can be represented as:

SYM N,$S_0..S_{n-1}$

where:

N = number of symbols in the string
S_1 = symbol code

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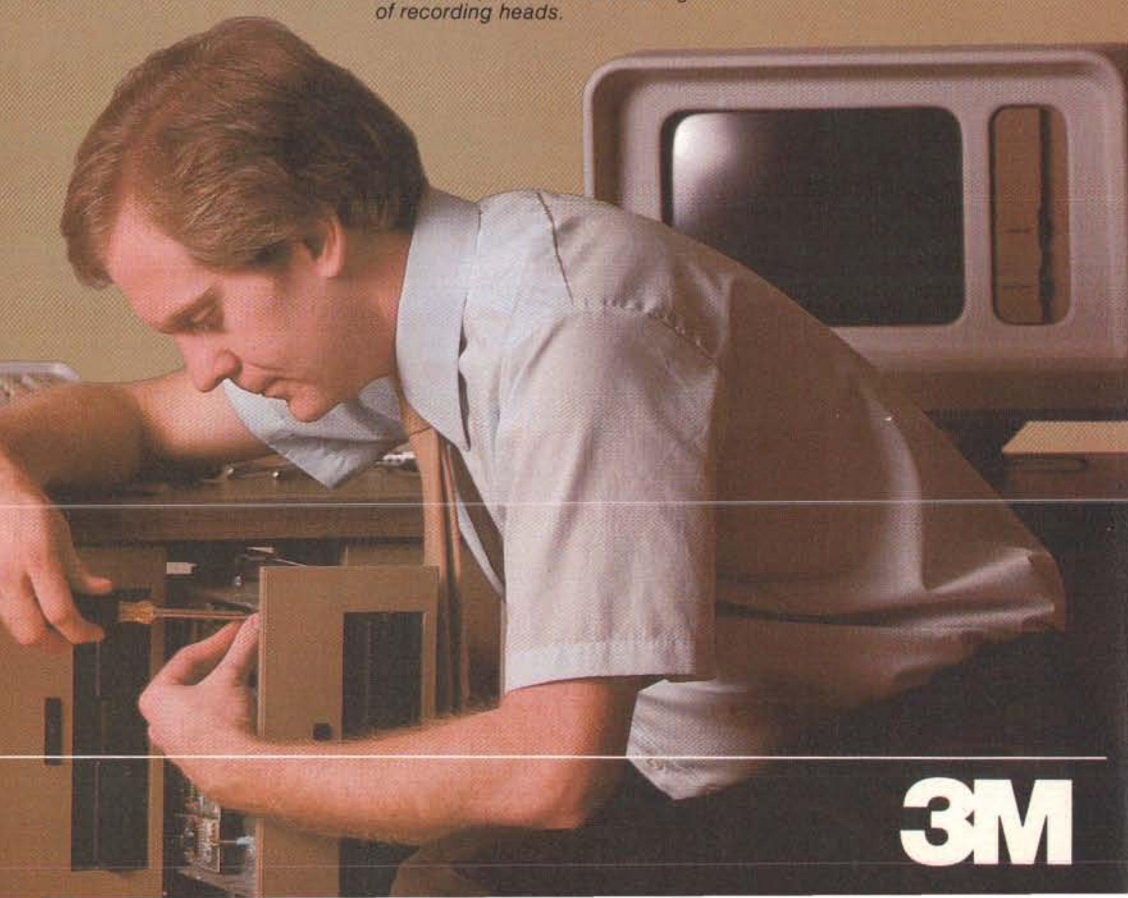

3M

For example, if we have defined a 128-character ASCII (American Standard Code for Information Interchange) set of symbols, the primitive:

SYM 5,68,80,77,80,83

displays the string "COLOR".

Also, as noted earlier, we may need to synchronize our display with the display frame rate, especially if we wish to perform animation with smooth movements. Therefore, we need a primitive that suspends display processing until the end of a frame or until after a certain number of frames. Mnemonically, our wait primitive can be represented as:

WAIT N

where:

N = number of frames to wait

For example, the primitive:

WAIT 7

suspends processing for seven frames.

Since we have assumed the existence of a color-look-up table to facilitate pseudocoloring and contrast-stretching, we must provide some method of controlling such a structure. There are two common methods for the organization of such tables. One method allows for the definition of a color by the proportions of red, green, and blue elements (the colors which physically make up a pixel). This method is easily performed in hardware, but it is not readily adaptable to common English color descriptions (such as hot pink or sea green). A preferred method, which we shall use, defines a color by its hue, intensity, and saturation. This classification refers to, respectively, the gradation of color (red, pink, purple), the brightness of the color, and the purity, or amount of black, in the color (dark red, fire-engine red).

We abstract the existence of a three-part table (which will actually be implemented in hardware) that is used as a color-look-up table. Since this table is user-alterable, we will refer to its parts as *color memories.* (They would usually be implemented as programmable-memory elements.) In order to generalize this primitive, we need to be able to update the entire table, one entire portion of the table (hue, intensity, or saturation), or all the parameters for a given color code. This table will allow selection of 2^n colors out of a 2^{i+h+s} color set where n is the pixel size in bits and i, h, and s are, respectively, the word size of the intensity, hue, and saturation color memory. For example, if $n = i = h = s = 4$, we can select one of sixteen colors out of a 2^{12} color set. Mnemonically, our load-color-memory primitive can be represented as:

LCRAM R,M,(A,)C_i

where:

R = reference (*I*ntensity, *H*ue, or *S*aturation color memory, or *A*ll)
M = mode (*S*ingle address or *A*ll addresses in table)
A = address (optional)
C = color data for the color memory

For example, the primitive:

LCRAM A,S,2,5,7,2

DETACHABLE INSERT

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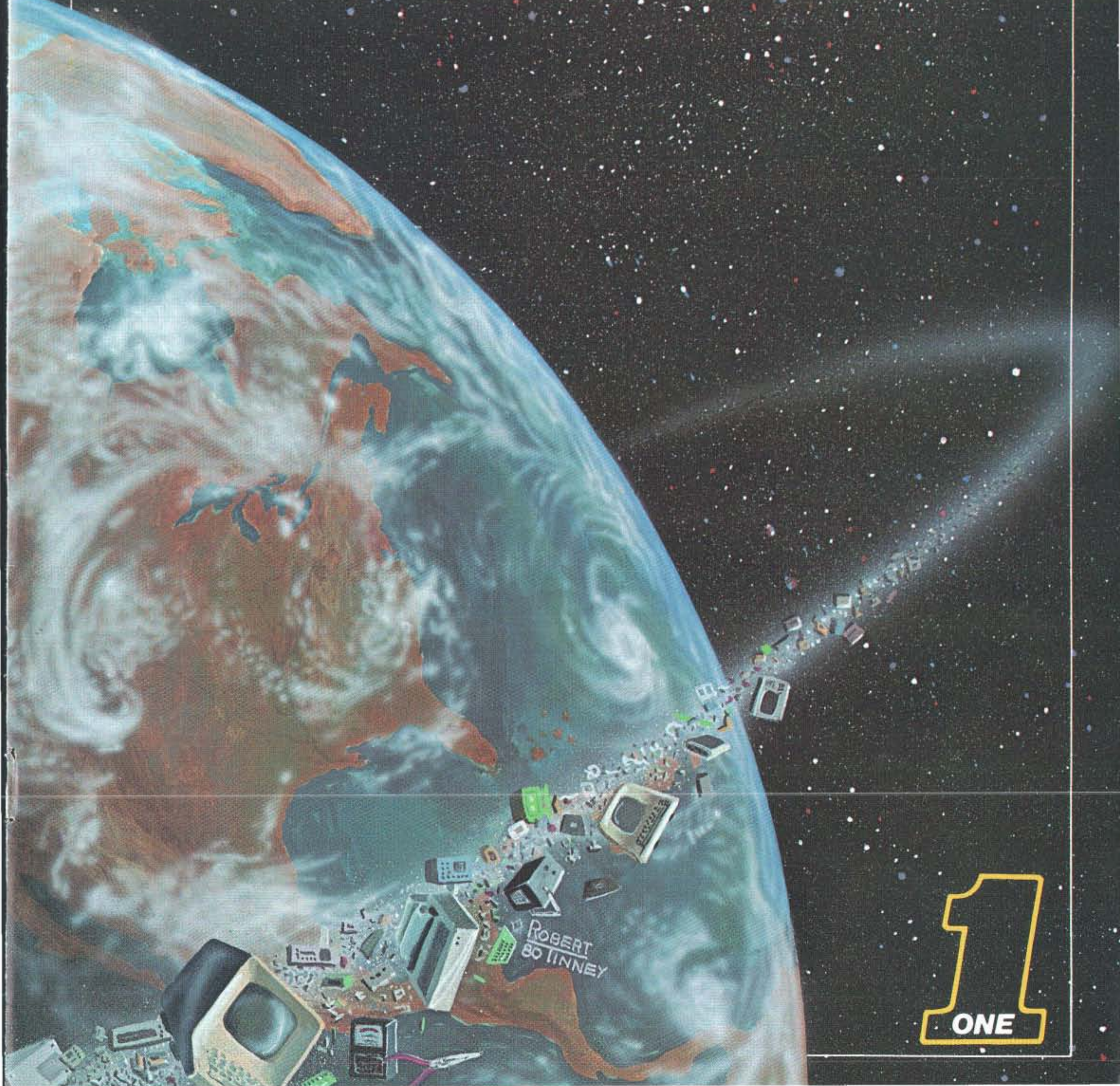

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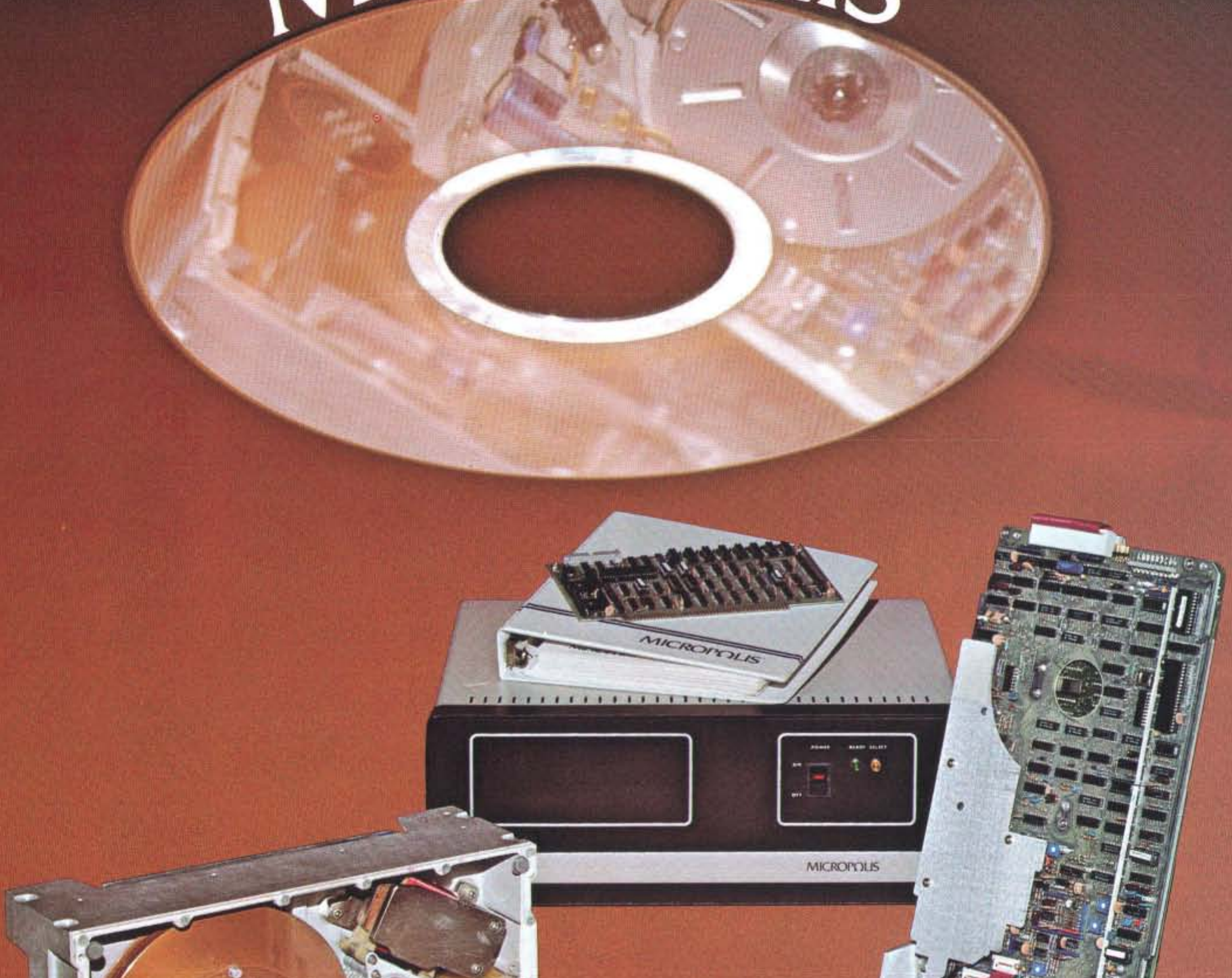

PRIORITY ONE ELECTRONICS

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Dynamic RAMs	19	Novation	15	Video Displays	14
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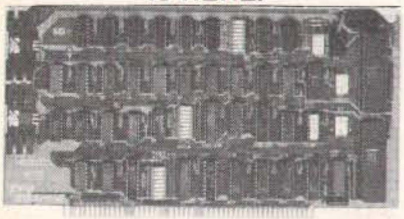
PRIORITY ONE ELECTRONICS

GODBOUT

THE DUAL PROCESSOR BOARD IS HERE!



GBT161 8085 CPU BOARD
GBT1612 8085/8088 CPU BOARD

CPU boards will never be the same again. 8088 CPU gives true 16 bit power with a standard 8 bit S-100 bus; an 8085 gives compatibility with CP/M and 8080 software access up to 16 megabytes of memory, meets IEEE S-100 bus specifications, runs 8085 and 8088 code in existing main frame, runs at 5 MHz, and is built to the same stringent standards that have established our leadership in S-100 bus components.

TECHNICAL OVERVIEW

The CPU 8085/88 Dual Processsor board was specifically designed to make it easy for the S-100 Bus user to get into the world of 16 bit micros, while at the same time preserving compatibility with existing hardware and software.

We accomplished this goal by choosing the Intel 8088 16 bit CPU, (an 8 bit bus version of the 8086), and the 8085A 8 bit CPU. The 8085 provides both hardware and software compatibility with the current crop of S-100 peripherals, and the 8088 provides for greatly enhanced software capability while maintaining an 8 bit external bus for hardware compatibility.

The user may switch back and forth between the two processors with a simple software command. For example, this allows the user to let the 8085 run his currently available (and familiar) disk operating system while letting the 8088 run the more advanced applications software. One processor would then "call" the other to handle the task most suited to it.

This environment is also extremely effective when trying to develop new software for the 8088. One may use tools available that run under the 8085 (such as CP/M and Microsoft's 8086/88 Macro Cross-Assembler that runs under CP/M) to write the new code and then simply switch over to the 8088 to try it out. PROM's need not be burned and erased and systems pulled apart to transfer the code to the 8088 system.

Both processors currently run at 5 MHz which maximizes bus throughput. A switch is provided to slow the 8085 down to 2 MHz for software dependent timing loops that want to run at that speed and are not easily changed. Intel says that an 8 MHz 8088 and faster 8085's are on the way, and this board was designed with the faster chips in mind. By merely changing a crystal you may upgrade the board to use a faster processor when they become available. Current state of the art in UART's (used in serial I/O boards) is barely able to cope with this 5 MHz bus rate, so a switch is provided on the CPU 8085/88 to add one wait state to every I/O cycle.

Power-on-jump circuitry is provided that allows the CPU to begin it's execution at any 256 byte boundary (within the lower 64K). A switch is provided to disable this feature. A switch is also provided to allow the power-on-jump circuitry to be active at power-on-only, or to work at power-on and every time a RESET occurs (jump-on-reset).

The 8085 can directly address 64K bytes of memory, but our built-in Memory Manager scheme allows access to the full 16 megabytes available per the IEEE S-100 standard. The 8088 can directly address 1 megabyte, but our Memory Manager is smart enough to know which processor is in control. Thus the 8088 uses only the upper four bits of the Memory Manager so it too can access the 16 megabyte address space.

The CPU 8085/88 rigidly adheres to the IEEE S-100 standard to insure compatibility with future S-100 components, but should also work quite well with most well designed pre-IEEE hardware. For example, provision is made to use the IMSAI front panel even though it doesn't exactly fit into the new standard.

Many long hours of thought and revision went into this product and the people at Compupro are confident that it will provide years of solid service. We sincerely hope that you will enjoy it.

BOARD WITH 8085 ONLY

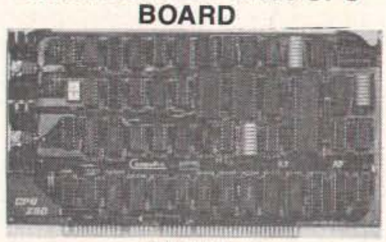
		List Price	Our Price
GBT161U	Unkit		$235.00
GBT161A	Assembled & Tested	$325.00	$305.00
GBT161C	200 hr. Burn In Test	$425.00	$399.00

BOARD WITH 8085 & 8088

GBT1612U	Unkit		$295.00
GBT1612A	Assembled & Tested	$425.00	$399.00
GBT1612C	200 hr. Burn In Test	$525.00	$498.00

GODBOUT

ENHANCED Z80 S-100 CPU BOARD



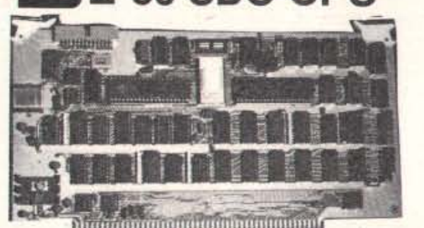
GBT160
Z80 CPU

- 4-6 MHz Z80 CPU
- IEEE S-100 Bus Compatible
- On Board Prom Sockets For Up To 8K Prom
- Power On Jump Start To Any 256Byte Boundary
- On Board Memory Manager For Direct Addressing For Up To 16 M-Bytes
- Fully Maskable Vectored Interrupts
- Wait State Generation For All Machine Cycles
- Bypassing Of All Supply Line To Suppress Transients
- All IC's Are Socketed

		List Price	Our Price
GBT160U	Unkit		$225.00
GBT160A	Assembled & Tested	$295.00	$280.00
GBT160C	200 hr. Burn In Test	$395.00	$375.00

California Computer Systems

Z-80 SBC CPU

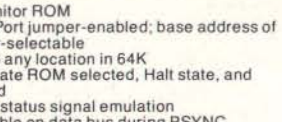


CCS2810
Z80 CPU

- 2 or 4 MHz operation, switch-selectable
- 158 instructions, including the 8080's 78 instructions for complete 8080 software compatibility.
- On-board RS-232-C Serial Port with software-controllable handshaking, serial data format, and baud rate
- On-board 2K monitor ROM
- ROM and Serial Port jumper-enabled; base address of serial port jumper-selectable
- Power-on jump to any location in 64K
- Three LEDs indicate ROM selected, Halt state, and interrupts enabled
- 8080 control and status signal emulation
- Status bits available on data bus during PSYNC
- Standard S-100 front panel control signals implemented
- DMA Control, status, address, and data bus disable signals implemented
- NMI, REFRESH, 2/4 MHz STATUS, and MREQ available
- PHANTOM overlay of on-board ROM jumper-enabled
- M1 Wait State and 8080 I/O Address Mirroring jumper-enabled
- Separate crystals for control of CPU clock and baud rate generator
- Full buffering of data and address lines

	List Price	Our Price
CCS2810	$300.00	$280.00

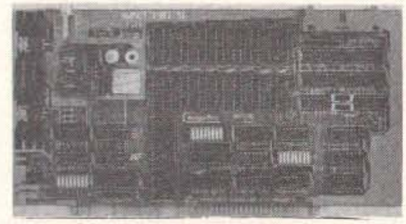
Z+80 CPU
REV II



+ 1K RAM On board + 2 Programmable Timers + Power On Jump to On-Board 1K or 2K EPROM (2708-2716) Can be Addressed on any 1K or 2K boundary + 2 separate parallel ports—full TTL buffered—one input and one output + Programmable Baud Rate Selection (110 to 9600) + On-Board EPROM May be Used in Shadow Mode, Allowing Full 64K RAM to be Used + On-Board USART for Synchronous or Asynchronous RS-232 Operation (Serial I/O Port) Configured for direct connect to terminal satisfies requirements of all modem signals in terminal device.

QTCZ80R2BB	Bare Board	$ 60.00
QTCZ80R2K	Kit	$190.00
QTCZ80R2A	A&T	$280.00
QTCZ801KM	1K Memory Kit	$ 12.00

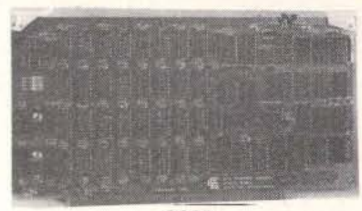
GODBOUT



SPECTRUM
S-100 COLOR GRAPHICS BOARD

- Uses the MC6847 LSI IC
- Uses 1372 color encoded/generator
- Alphanumeric/graphics in 8 colors
- Ultra dense 256 x 192 full graphics
- 8K bytes, on-board low power RAM
- One full duplex parallel I/O port with attention, enable & strobe bits with power for running joysticks, keyboards, etc.
- A parallel port for graphics mode control
- Board may be used as a 4MHz RAM for program storage

		LIST PRICE	OUR PRICE
GBT144U	UNKIT		$299.00
GBT144A	A&T	$399.00	$349.00
GBT144C	CSC	$449.00	$399.00
GBT2D	SUBLOGIC UNIVERSAL GRAPHICS INTERPRETER SOFTWARE		$35.00



2065
64K DYNAMIC RAM MODULE

The 2065 provides your S-100 system with 64K of fast, reliable memory. Compatible with the IEEE proposed standards for the S-100 bus, the 2065 features the popular 4116-type dynamic RAMs, requires no Wait states when used with a 4 MHz CPU, and supports most front panels.

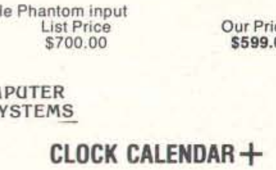
- Designed to IEEE proposed S-100 bus standards
- Supports IMSAI-type front panels
- Operates with either an 8080 or Z-80 based S-100 system, providing processor-transparent refreshes with both
- Bank-select system allows system memory expansion and is compatible with Cromemco products
- Bank select port's address is jumper-selectable
- Any 16K block can be made bank-independent
- All 64K can be made bank-enabled on power-on and reset
- Configuration as a 16K, 32K, or 48K board without the removal of RAMs
- Fully buffered address and data lines
- Fail-safe refresh circuitry for extended Wait states
- Board configuration with reliable, easy-to-configure Berg jumpers
- Supports DMA
- Jumper-selectable Phantom input

	List Price	Our Price
CCS-2065A	$700.00	$599.00

QT COMPUTER SYSTEMS

"QUICK & TIMELY"

CLOCK CALENDAR +



+ Time of Day in Hours, Minutes and Seconds + 24 Hour Time Format + Month and Day Date Function + Simple Read Instructions Allow Simple Interface to Basic CPM, Etc. + Will Run With 4MHZ Processors + Can be Located at any Group of 4 I/O Port Addressed + On Board Battery Back-up

QTCCCSBB	Bare Board	$ 45.00
QTCCCSK	Kit	$100.00
QTCCCSA	A&T	$150.00

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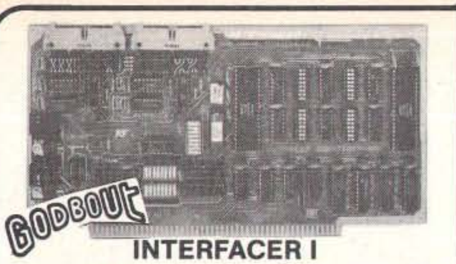
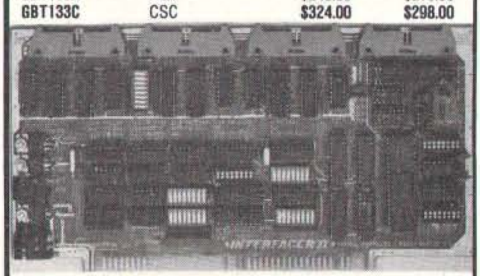


INTERFACER I

Our I/O board gives you inparalleled flexibility and operating convenience. We include such features as:

- 2 independently addressable serial ports (dip switch selectable addresses)
- Real LSI Hardware UARTs for minimum CPU housekeeping
- RS232C, current loop (20mA), & TTL signals on both ports
- Precision, crystal-controlled Baud rates up to 19.1 KBaud (Individually dip switch selectable)
- Transmit & receive interrupts on both channels, jumperable to any vectored interrupt line
- Industry standard RS232 level converters with five RS232 handshaking lines per port
- Optically isolated current loop with provisions for both on-board & off-board current sources
- UART parameters, interrupt enables, & RS232 handshaking lines are software programmable with power-on hardware default to customer specified hard-wired settings for maximum flexibility
- Port connectors mate directly to ribbon cable & DB25 connectors in standard pinouts
- RS232 lines will conform to either master or slave configurations
- Board gives full feature operation with both 2 & 4 MHz systems
- Low power consumption: +8V @ 450mA; +16V @ 150mA; −16V @ 70mA max.
- No software initialization required for board operation, although board parameters may be altered by software

		LIST PRICE	OUR PRICE
GBT133U	UNKIT		$199.00
GBT133A	A&T	$249.00	$219.00
GBT133C	CSC	$324.00	$298.00



INTERFACER II 3PTS

- 1 independently addressable serial port
- RS232C; 20mA current loop, & TTL signals
- Precision crystal controlled Baud rate generator
- Up to 19.2K Baud
- Transmit and receive interrupts, jumperable to and vectored interrupt line
- Five RS232 handshaking lines
- Optically isolated current loop
- 3 parallel I/O
- Utilizes LSTTL octal latches for latched I/O data with 24mA drive current
- Enable & strobe bits on each port (each with selectable polarity)
- Interrupts for each input port
- Separate 25 pin connector with power for each channel and a status port for interrupt mask & port status

		LIST PRICE	OUR PRICE
GBT150U	UNKIT		$199.00
GBT150A	A&T	$249.00	$219.00
GBT150C	CSC	$324.00	$298.00

I/O +

+ Two Independent SYNC/ASYNC Serial Ports + One Strobed Eight Bit Parallel Input Port With Handshaking + Three Eight Bit Parallel Ports (Undedicated, User Configured) + Three Independent Sixteen Bit Timers + Eight Level Priority Interrupt Controller + Large Prototyping area has regulated +5VDC, +12VDC, −12VDC + Two software programmable baud rate generators with crystal controlled frequencies (±0.1%)

QTCIOBB	Bare Board	$ 69.00
QTCIOK	Kit	$275.00
QTCIOA	A&T	$375.00

California Computer Systems

4-PORT SERIAL I/O INTERFACE

- Includes four individually software-controlled asynchronous serial ports
- Meets IEEE proposed S-100 standards
- Employs low-power Schottky devices
- Fully buffers inputs and outputs
- Features reliable, easy-to-configure Berg jumper plugs

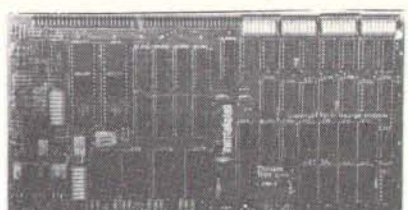
The Serial Ports:

- Provide full handshaking meeting RS-232-C specifications (DCE)
- Include three control registers and three status registers per port
- Allow clock divisors from 1 to 65535 for baud rate control
- Double-buffer data to eliminate the need for precise synchronization
- Allow 5 to 8 bit words; even, odd, or no parity; 1, 1.5, or 2 stop bits
- Provide false start bit detection
- Check for parity, framing, and overrun errors
- Separately interrupts over any of the S-100 vectored interrupt lines
- Generate software-enabled, prioritized Transmitter Empty, Received Data, Receiver Status, and Peripheral Status interrupts
- Perform Line Break Generation and Detection

ROM Circuitry

- Allows on-board 2716 2K EPROM (user supplied)
- Includes jumper-configured address comparator to locate ROM at any 2K boundary
- Controls jumper-enabled PHANTOM output for overlay of identically-addressed memory
- Uses FF Detect to disable output when empty locations addressed

	LIST PRICE	OUR PRICE
CCS2710	$340.00	$319.00



Switch-programmable 8-port I/O Interface

THE SWITCHBOARD

Specifications:

Eight I/O Ports: I/O ports DIP switch selectable for location on any boundary of the I/O address space divisible by 8.

Two RS232C/TTY current loop serial ports: • fully independent serial ports • stop bit length selection • parity enable selection • parity even/parity odd selection • seven or eight bit word length selection • sixteen selectable baud rates from 50 to 19.2K.

One serial status port: • serial port #1 — least significant 4 bits • serial port #2 — most significant 4 bits • receiver buffer full status • transmitter buffer empty status • parity error status • over-run error status.

Four Independent Parallel I/O Ports: Thirty-two lines of I/O available. Each group of eight lines DIP switch selected as input or *latched* output. Attention status bit for each group of eight I/O lines.

Separate STATUS Port: One latched attention status bit for each parallel I/O port. Attention bit selected by DIP switch to latch on positive or negative pulse or level. Status bit reset automatically by input reference of associated port.

Separate STROBE Port: Eight independent strobe lines. Each line DIP switch selectable to be positive or negative strobe.

4K RAM Option: Eight 2114-3L 1Kx4 read/write static memory chips. Addressable by DIP switch on any 4K boundary. May be completely disabled via DIP switch so as to disappear from the address space of the CPU.

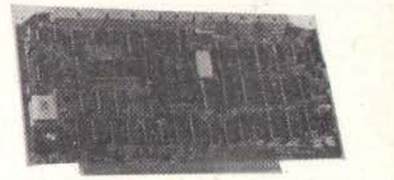
4K EPROM Option: Four 2708 1Kx8 erasable programmable read only memory. Addressable by DIP switch on any 4K boundary. May be completely disabled via DIP switch so as to disappear from the address space of the CPU.

Phantom Disable: DIP switch selection to allow the PHANTOM line to disable RAM and EPROM memory resident on the board.

Specifications and prices subject to change without notice.

	LIST PRICE	OUR PRICE
THT-SW8K		$199.00
THT-SW8A	$259.00	$239.00

California Computer Systems



2 Serial Port + Parallel Port

- Meets IEEE proposed S-100 standards
- Employees low-power Schottky devices
- Fully buffers inputs and outputs
- Features reliable, easy-to-configure Berg jumper plugs

The Parallel Ports:

- Provide full four-line-per-port handshaking and 8-bit data transfer
- Include separate TTL-compatible input and output latches
- Feature invertible handshaking (with jumpers) and data (through chip replacement)
- Are addressable at any even-based pair of I/O addresses
- Input four bits to user-formatted status register

The Serial Ports:

- Meet RS232-C interface specifications (DCE)
- Operate at standard (75 to 19,200) or peripheral-generated baud rates, separately hardware- or software-selected for each port.
- May be located at any block of 4 I/O addresses, with separate registers locatable at any of the 4 addresses

Port A:

- Transmits and receives in asynchronous mode
- Features jumper-selectable data format
- Inputs four bits to user-formatted status register

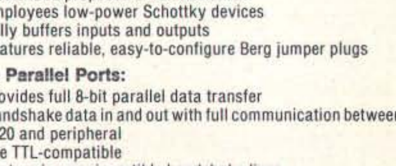
Port B:

- Transmits and receives in synchronous or asynchronous mode
- Features software-controlled mode, synchronization, and data format
- Includes an 8-bit status register

ROM Circuitry:

- Allows on-board 2716 2K EPROM (user-supplied)
- Includes jumper-configured address comparator to locate ROM at any 2K boundary
- Controls jumper-enabled PHANTOM output for overlay of identically-addressed memory
- Provides jumper-enabled wait states
- Uses FF Detect to disable output when empty location addressed

	LIST PRICE	OUR PRICE
CCS2718	$360.00	$339.00



4-Port Parallel I/O Interface

- Includes three input/output ports and one output-only port
- Provides seven-bit status register
- Meets IEEE proposed S-100 standards
- Employees low-power Schottky devices
- Fully buffers inputs and outputs
- Features reliable, easy-to-configure Berg jumper plugs

The Parallel Ports:

- Provides full 8-bit parallel data transfer
- Handshake data in and out with full communication between 2720 and peripheral
- Are TTL-compatible
- Feature jumper-invertible handshake lines
- Can be assigned to four sequential I/O ports, the base address being a multiple of four

ROM Circuitry:

- Allows on-board 2716 2K EPROM (user supplied)
- Includes jumper-configured address comparator to locate ROM at any 2K boundary
- Controls jumper-enabled PHANTOM output for overlay of identically-addressed memory
- Uses FF Detect to disable output when empty locations addressed

	LIST PRICE	OUR PRICE
CCS2720	$250.00	$239.00

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PRIORITY ONE ELECTRONICS

GODBOUT

MEET THE ECONORAM FAMILY... ALL ECONORAMS FROM COMPUKIT INCLUDE:

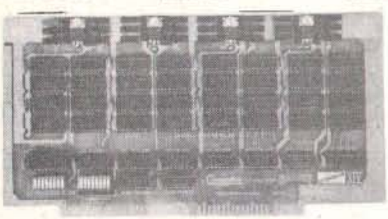
- Fully static memory used throughout to promote reliable operation and facilitate direct memory access. (DMA)
- 4 MHz with Z80-5 MHz with 8085
- Buffered tri-state outputs and buffered inputs
- All lines buffered; address and data lines buffered to 1 low power Schottky TTL load, all other lines buffered to less than 1 TTL load
- Onboard regulation
- DIP switch address selection and deselection (no wire jumpers)
- Low power Schottky support ICs
- S-100 boards have WRITE strobe selections switch—allows use of memory with or without front panel
- All ICs are socketed (including support chips)
- Unique multi-block configurations for addressing flexibility
- Industry standard board sizes
- High quality, double sided, plate through, solder-masked and legended circuit board.
- LOW current consumption and guaranteed specs
- 1 year parts and labor limited warranty on A&T and CSC products
- 1 year parts only warranty on UNKIT and KIT products

Most ECONORAMs come in 3 forms: UNKIT (U)—(this means that all sockets, disc capacitors are already soldered in place for easy assembly), fully assembled & tested (A), or qualified under the Certified System Component (C) high-reliability program (200 hour burn-in, guaranteed 4MHz operation over full temperature range, serial numbered, immediate replacement in event of failure within 1 year of invoice date).

8K ECONORAM IIA

We realize that this may not look like the 8K, Econoram II board you've known and loved for so many years; however, at Godbout, good things don't come to an end—they just get better! Our NEW 8K Econoram IIA board retains all the best selling features of the old Econoram II PLUS is now 4 MHz STANDARD—still static—with **ultra** low power consumption. S-100 compatible. Single supply required—guaranteed maximum current under 900mA. typical boards draw 700 to 800mA. Phantom feature is included on the new Econoram IIA & is switch selectable. Organized as two 4K independently addressable blocks. Includes switched WRITE protect—block & board disable. Also, has provision for memory management. Shipping Weight 2 lbs.

	List Price	Our Price
GBT139U Unkit		**$159.00**
GBT139A A&T	$189.00	**$179.00**
GBT139C CSC	$239.00	**$225.00**



ECONORAM XIV

16K x 8 for S-100. Addressable on any 4K boundary. Direct addressing on up to 24 address lines. Fully meets IEEE S-100 buss specs. Low power, hi-speed static memory. Operates up to 5 MHz with newest 8085/8086/8088 CPUs. Can be used with 8080, Z80, 8085, 8086, 8088, Z8000, etc.

	List Price	Our Price
GBT143U Unkit		**$279.00**
GBT143A A&T	$349.00	**$299.00**
GBT143C CSC	$429.00	**$399.00**

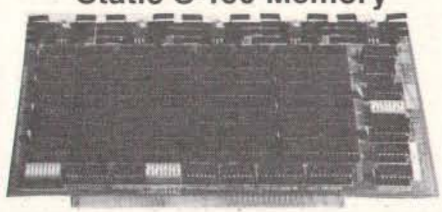
Extender/Terminator

- Active and/or dynamic termination
- All power lines fused for protection
- All S-100 lines labeled and numbered
- Can be used as an extender and/or terminator
- Solder mask both sides of board
- Silkscreened reference designations
- Gold plated fingers

CCS-2520AK Kit 1 lb. **$39.95**

GODBOUT

Static S-100 Memory



32K ECONORAM XX

32K Bank Select. IEEE S-100 compatible. Features one 32K block that can be addressed on 4K boundaries. Compatible with the IEEE proposed standard of 24 address lines as well as all currently used bank select configurations. Any or all of the eight 4K byte blocks may be disabled to create as many windows in memory to avoid any system memory conflicts.

	List Price	Our Price
GBT164U16 16K RAM Unkit		**$319.00**
GBT164A16 16K RAM A&T	$399.00	**$359.00**
GBT164C16 16K RAM CSC	$479.00	**$439.00**
GBT164U24 24K RAM Unkit		**$429.00**
GBT164A24 24K RAM A&T	$539.00	**$485.00**
GBT164C24 24K RAM CSC	$629.00	**$579.00**
GBT164U32 32K RAM Unkit		**$559.00**
GBT164A32 32K RAM A&T	$699.00	**$599.00**
GBT164C32 32K RAM CSC	$799.00	**$720.00**

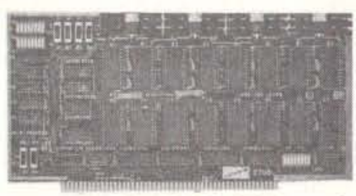
CK-023 MEMORY MANAGER

The Memory Manager board is designed to extend the addressing capabilities of S-100 microcomputers beyond 64K bytes—up to 16M bytes in Mode 1 and up to 512 bytes in Mode 2.

The first mode of operation utilizes one 8 bit out-put port (selectable to any I/O addressing) to drive the proposed IEEE S-100 extended address lines A16 thru A23 for full 24 bit addressing.

The second mode of operation utilizes the same 8 bit output port, but only the 3 least significant bits are used. These feed a 3 line to 8 line decoder which drives one of eight qualifier lines high on S-100 buss pins 59 thru 66 to enable Godbout Econoram boards with on-board qualifier capability. This can be used with Econorams IIA, VII (16 & 24K), X, XII (16 & 24K), XIII & Econoram 2708. This board is not available in UNKIT form. Shipping weight 1 lb.

	List Price	Our Price
GBT151K Kit		**$59.00**
GBT151A A&T	$85.00	**$79.00**
GBT151C CSC	$100.00	**$89.00**



ECONOROM 2708

Has provisions for wait states for 4MHz operations. Configured as four 4K blocks—each independently addressable and disabable. Power-on jump. Does **NOT** include 2708s. Includes all support chips, sockets, regulators, heat sinks, etc. Sold in UNKIT form **only.** Shipping Weight 2 lbs.

GBT125U Unkit **$85.00**

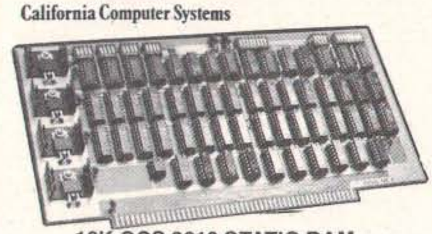
VCT-3690-12 Card Extender

Card Extender has 100 contacts — 50 per side on .125 centers. Attached connector is compatible with S-100 Bus Systems.

1 - 4	5 - 24
$26.64	**$24.18**

VCT-3690 6.5" 22/44 pin .156 ctrs. Extender **$15.66**
VCT-3690-4 7.5" 36/72 pin .1 ctrs. Extender **$22.76**
VCT-3690-6 11" 22/44 pin .156 ctrs. Extender **$18.80**

California Computer Systems



16K CCS 2016 STATIC RAM ASSEMBLED & TESTED—100% BURN IN

All boards tested at 4 MHz for Z80

Features:

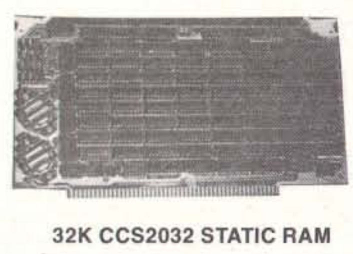
- Fully static
- Uses popular 2114 static RAMS
- +5 volt operation only
- Bank Select available by bank port and bank byte
- Phantom Line capability
- Addressable in 4K blocks
- 4K blocks can be addressed anywhere within 64K in 4k increments
- Full QA testing of all modules at 4 MHz operation
- Meets IEEE proposed S-100 signal standards
- LED indicators for board selection and bank selection
- FR-4 EPOXY PC boards
- Solder masked on both sides
- Silk screen of part number and part designator

	List Price	Our Price
CCS-2016BA 450ns 2MHz	$349.95	**$295.00**
CCS-2016BB 300ns 4MHz	$389.95	**$329.00**
CCS-2016BX Bare Board only		**$29.95**

16K CCS2116 STATIC RAM

- Fully static 2114 RAMs
- 16K of memory divided into four 4K blocks
- Memory blocks separately addressable at 4K boundaries in 64K
- Configurations of 4K, 8K, or 12K can be accomplished without the removal of RAMs
- Hardware-assignment of memory blocks to any of eight memory banks using bank-port/bank-byte scheme compatible with Alpha Micro, Cromemco, and others
- Jumper-selectable bank-independence for each memory bank
- Fully buffered address and data lines
- Board and bank selection indicated with LEDs
- Jumper-enabled Phantom memory overlay and Wait state generation
- Full QA testing of all modules at 4 MHz operation
- Fully assembled & tested

	List Price	Our Price
CCS2116A	$349.95	**$299.00**



32K CCS2032 STATIC RAM

- Fully static 2114 RAMs
- 32K of memory divided into four 8K blocks
- Memory blocks separately addressable at any 8K boundaries within 64K
- Hardware-assignment of memory blocks to any of 8 memory banks using bank-port/bank-byte scheme compatible with Alpha Micro, Cromemco, and others
- Bank-dependence of each memory block jumper-selectable
- Bank-dependent memory can be enabled or disabled on reset
- Configurations of 8K, 16K, or 24K accomplished without removal of components
- Jumper-enabled Phantom memory overlay
- Jumper-enabled Wait states
- Easy-to-use Berg jumper plugs
- Board and Bank selection indicated with LEDs
- Compatible with IMSAI-type front panels
- Fully buffered address and data lines
- Full QA testing of all modules at 4 MHz

	List Price	Our Price
CCS2032A	$710.00	**$599.00**

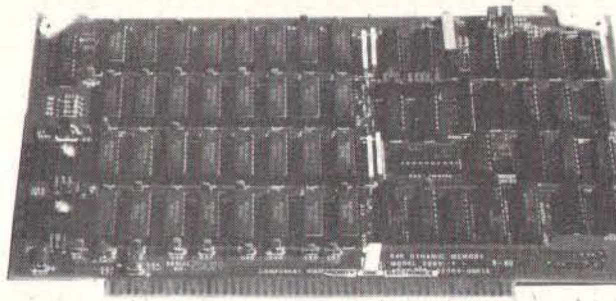
PLEASE ORDER BY PART NUMBER

PRIORITY ONE ELECTRONICS

THE UNIVERSAL IEEE-S100 DYNAMIC MEMORY CARD

PRI-EXP1-16
$299.00

PRI-EXP1-48
$419.00



PRI-EXP1-32
$359.00

PRI-EXP1-64
$479.00

PRI-EXP1-16	16K Assembled & Tested $299.00
PRI-EXP1-48	48K Assembled Tested $419.00
PRI-EXP1-32	32K Assembled & Tested $359.00
PRI-EXP1-64	64K Assembled Tested $479.00

SHIPPING WT. 4 LBS.

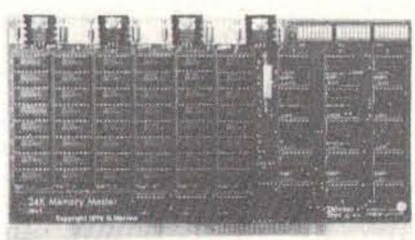
- *User expandable from 16 to 64 K*
- *2 or 4 MHz operation*
- *North Star compatible*
- *Cromemco compatible*
- *Designed to IEEE proposed S-100 bus standards*
- *Supports IMSAI-type front panels*
- *Operates with either an 8080 or Z-80 based S-100 system, providing processor-transparent refreshes with both*
- *Bank-select system allows system memory expansion and is compatible with Cromemco products*
- *Bank select port's address is jumper selectable*
- *Any 16K block can be made bank-independent*
- *All 64K can be made bank-enabled on power-on and reset*
- *Configuration as a 16K, 32K, or 48K board without the removal of RAMs*
- *Fully buffered address and data lines*
- *Fail-safe refresh circuitry for extended Wait states*
- *Board configuration with reliable, easy-to-configure Berg jumpers*
- *Supports DMA*
- *Jumper-selectable Phantom input*
- *Uses Popular 4116 RAMS*
- *Assembled & tested*
- *All ICs in sockets*
- *Power supply: Unregulated +8, +16, and −16 volts Maximum power draw: 400 mA at +8 volts 175 mA at +16 volts 5 mA at −16 volts Dissipation: less than 8 watts*
- *Temperature: 0 to 70 degrees Celsius*
- *Humidity: 0 to 90% noncondensing*
- *PC Board*
- *FR-4 glass epoxy*
- *Solder mask on both sides*
- *Gold-plated connector fingers*
- *Silk screen component outlines, reference numbers, and part designations*

THE EXPANDABLE 1™ 64K Dynamic Ram board provides your S-100 system with 64K of reliable, high-speed dynamic RAM. Compatible with most of the major S-100 systems on the market, including those with front panels, it supports DMA operations and requires no Wait states with the current microprocessors.

THE EXPANDABLE 1™ is designed for memory expansion; it allows you to expand your memory up to 512K. Through the bank select system, you can hardware-assign your board to any level or combination of levels of 64K and then software-select the bank you wish to work with. When the board's bank is selected, the Bank LED is lit. The Expandable 1™'s bank select system is compatible with the bank select systems used by Cromemco, North Star, and others.

In addition, ***THE EXPANDABLE 1***™ gives you flexible memory. Any 16K memory block can be completely disabled or can be made independent of the bank select system, allowing it to be enabled any time it is addressed, regardless of which bank is selected. All 64K can also be enabled every time you turn on or reset your system, without the board's bank being software-selected first. When an enabled 16K block is addressed, the Board LED is lit.

THE EXPANDABLE 1™ also gives you reliable memory. Its dynamic memory refresh circuitry provides processor-transparent refreshes during normal operations with a Z-80 or 8080 CPU. It also provides for memory refresh during DMA and extended Wait states when normal refresh generation is inhibited.



MEMORY MASTER 24K STATIC S-100 MEMORY WITH BANK SELECT LOGIC

The SuperRam MEMORY MASTER 24K is ideal for building large S-100 systems with bank select logic capability.

The MEMORY MASTER 24K is configured as three 8K blocks, each individually addressable and write-protectable via a DIP switch.

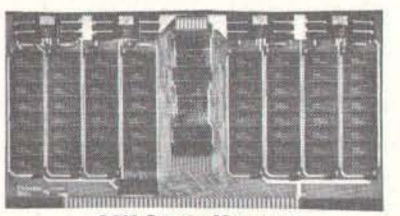
The MEMORY MASTER 24K offers the ability to utilize bank select logic, the preferred method of expanding memory over 64K on the S-100 bus. There is an on-board switch-addressable I/O device and a bit-select jumper block to implement the bank select logic for the memory management software from Cromemco, AlphaMicro, North Star, etc.

Specifications:

- 24K STATIC random access memory board using type 4044/5257-3L 4Kx1 memory components.
- Three individual 8K memory blocks, each addressable on any 8K boundary.
- Entire board enabled or disabled through a bit selected jumper of an addressable on-board I/O port.
- Switch selection to allow memory to be enabled or disabled at power on/reset.
- Switch selection to disable board when PHANTOM is active.
- Cycle time 320 ns. • Access time 320 ns.
- Power requirements 2.5 amps typical, 2.8 amps worst case.
- Single supply voltage 7-10 volts.
- 5" x 10" epoxy glass circuit board with solder mask (both sides) and parts legend.
- S-100 compatible.

	List Price	Our Price
THT-MM24A A&T	$549.00	**$499.00**

SuperRam™



32K Static Memory

The SuperRam 32K provides two 16K blocks, each addressable and write-protectable via a DIP switch. For power-up sequencing, the PHANTOM line can be enabled or disabled by means of a DIP switch. The SuperRam 32K static is built with fast, exceptionally low powered static memory parts, reducing its power requirements to just 2.7 amps.

The SuperRam 32K is available assembled tested or in kit form. It meets the Proposed IEEE Standard for S-100 bus and is compatible with 2 MHz 8080, 4 MHz Z-80 and 5 MHz 8085 systems.

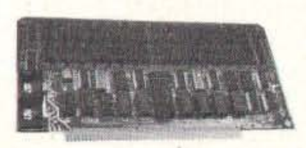
Specifications:

- 32K STATIC random access memory board using type 4044/5257-3L memory components.
- Two individual write-protectable 16K memory blocks, each addressable on any 16K boundary.
- Switch selection to disable a board when PHANTOM is active.
- Cycle time 320 ns. • Access time 320 ns.
- Power requirements 2.7 amps typical, 3.2 amps worst case.
- Single supply voltage 7-10 volts.
- 5" x 10" epoxy glass circuit board with solder mask (both sides) and parts legends.
- S-100 compatible.

	List Price	Our Price
THT-SR32A A&T	$699.00	**$629.00**

QT
QUICK & TIMELY

LOOK TO QT FOR THE BIG+



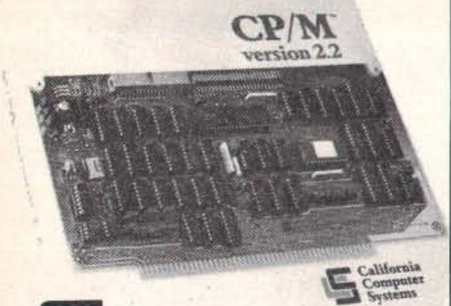
EXPANDABLE + DYNAMIC MEMORY (16K to 64K)

+ Works with the following Z-80 CPU Boards: Cromemco Systems, S.D. Systems, SSM (CB2A), Jade (Big Z), Q.T. (Z+80) and many others + Uses 3242 Refresh Chip with delay line + Four layer PC Board insures a quiet board + Supports 16K, 32K, 48K, or 64K or memory + 24 Address lines per IEEE specifications + Optional M1 Wait state allows error-free operations with faster processors + Optional PHANTOM disable + Uses Z-80 Refresh signal + Bank on/off signal selected by I/O port 40 (Hex) per industry standard + Bank in use determined by convenient DIP switch selection of data bus bits + Low power consumption—5 watts + Convenient LED indication of bank in use

Typical access time of board
—(1) using (4116-200ns) (4MHz) 240ns
—(2) using (4116-150ns) (6MHz) 200ns

QTC-EXPBB	Bare Board	$ 70.00
QTC-EXP16K	16K Kit	$280.00
QTC-EXP16A	16K A&T	$325.00
QTC-EXP32K	32K Kit	$360.00
QTC-EXP32A	32K A&T	$425.00
QTC-EXP48K	48K Kit	$480.00
QTC-EXP48A	48K A&T	$550.00
QTC-EXP64K	64K Kit	$525.00
QTC-EXP64A	64K A&T	$625.00

WE ACCEPT VISA, and MASTER-CHARGE



California Computer Systems

CCS2422 FLOPPY DISK CONTROLLER WITH CP/M VERSION 2.2

- Compatible with the IEEE proposed S-100 bus standards
- Controls any combination of 5¼ and 8" drives up to four
- Double-Sided and Side Select signals implemented for double-sided drives
- Reads and writes diskettes conforming to the IBM 3740 format standard for single-density diskettes and the IBM System 34 format standard for double-density diskette
- Z-80 compatible, ROM-resident bootstrap loader for loading CP/M Into system memory from diskette
- ROM-resident MOSS 2.2 Disk Monitor
- Plug-compatible with Shugart 800/850 and 400/450 drives
- Fast Seek for voice-coil drives hardware- or software-enabled
- Can be assigned to one of eight banks through bank byte/bank port system used by Cromemco and others
- Optional Auto Wait circuitry for wait on data or board status register when data register is not ready for data transfer
- PINT, NMI, or VIZ-V17 can be used to interrupt the CPU when the 2422 is ready for data transfer or a new command
- Write Precompensation circuitry for double-density diskettes
- Digital phase locked loop for read data separation
- LEDS to indicate ROM, Bank, and Board Select
- Address Decoding ROMs handle ROM and register addressing
- Optional Wait State for ROM
- Assembled & Tested

	List Price	Our Price
CCS2422	$400.00	$375.00

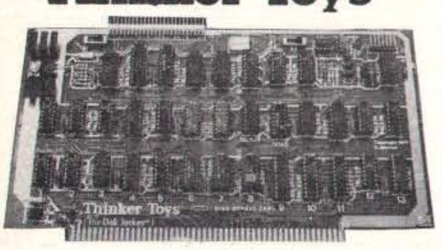
CALIFORNIA COMPUTER SYSTEMS SOFTWARE

CCS-2601 CP/M Version 2.2 Microcomputer Control Program	$150.00
CCS-2610 MAC-CP/M Macro Assembler	$90.00
CCS-2620 SID-CP/M Symbolic Instruction Debugger	$75.00
CCS-2630 TEX-CP/M Text Formatter	$75.00
CCS-2640 DESPOOL—CP/M Background Print Utility	$50.00

All software comes with manuals & 8" diskette

CP/M, MAC, SID, TEX, and DESPOOL are registered trademarks of Digital Research.

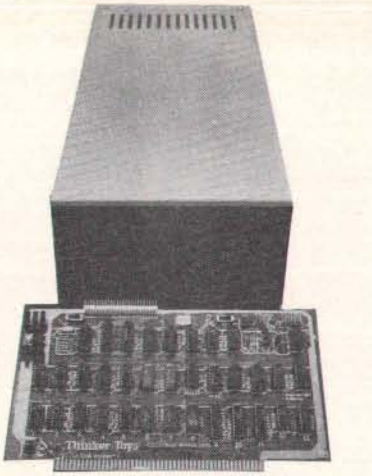
Thinker Toys™



DISK JOCKEY I FLOPPY DISK CONTROLLER

Specifications
- S-100 compatible
- Plug compatible with Shugart SA800/850 disk drives
- Capable of handling up to four disk drives
- Contains on-board serial I/O port and 256 byte cache buffer; on-board ROM with bootstrap, home, seek, read data, write data, serial input and serial output functions.
- All software pre-interfaced to the controller's on-board I/O port for immediate start-up
- Single voltage +7-10 volts @ 700 ma.

	List Price	Our Price
THT-DJ1K		$179.00
THT-DJ1A	$229.00	$219.00



DISCUS 1 FULL-SIZE, SINGLE-DENSITY DISK MEMORY SYSTEM

The DISCUS I full-size, single-density disk memory system offers the opportunity to move your S-100 system into the large storage capability and high speed access of an 8" drive.

DISCUS I gives you 250,000 bytes per diskette with five times the access speed of a mini-floppy. Yet, the cost is only slightly higher.

DISCUS I full-size, single-density disk system is complete in every way—complete with all hardware, all software and completely assembled and tested.

Specifications:
Data Specifications and Formats
- 250,000 byte capacity per standard 8" floppy diskette
- Soft-sectored IBM-compatible format: 77 tracks/26 sectors per track/128 bytes per sector
- Includes Disk/ATE™ disk operating system with integral monitor, assembler and text editor & BASIC-V advanced virtual disk BASIC capable of addressing up to 1 megabyte
- Software customized for SOL and Exidy available
- Patches for CP/M* included
- Optional CP/M* Microsoft BASIC, and FORTRAN available.

	List Price	Our Price
THT-D1S Single Drive	$995.00	**$950.00**
THT-D1D Dual Drive	$1790.00	**$1595.00**

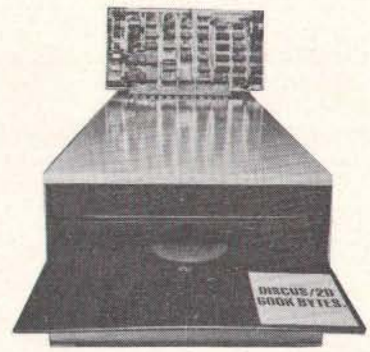
DISK JOCKEY 2D FLOPPY DISK CONTROLLER

Specifications:
- Plug compatible with Shugart, Remex and Siemens single- or double-sided drives
- Double/single-density capability utilizing MFM and FM data formats
- Western Digital 1791 LSI floppy disk controller chip
- Uses 2K of S-100 address space:
  —1K PROM with built-in disk drive and I/O utility subroutines incorporating memory mapped I/O
  —1K 2114-3L 300 ns access time RAM for disk data buffering and general purpose use
- Starting address of memory space is 340:000 (E000 hex) for compatibility with other popular ROM based systems
- Phase-locked data separator and crystal controlled disk data write precompensation capability to insure the highest standards of data integrity in double density mode
- Compatible with all 2 MHz and 4 MHz systems which conform with the proposed IEEE standards for the S-100 bus
- 1602 UART with crystal-controlled baud-rate generator
- Sixteen switch selectable baud rates from 50 to 19,200 bits/second
- TTY current loop and industry standard RS232C serial interface
- Power-on jump circuitry for automatic bootstrap loading from the disk drive
- Power supply requirements: +8v @ 1200 ma; +16v @ 150 ma; −16v @ 70 ma
- ROM utility subroutines:

Bootstrap load	Seek
Terminal input	Set sector
Terminal output	Set DMA address
Home	Disk read
Disk write	DMA status
Select drive	Disk status
Terminal panic detect	Disk error
Terminal status	Switch density

	List Price	Our Price
THT-DJ2DK		$379.00
THT-DJ2DA	$429.00	$409.00

Thinker Toys™



DISCUS/2D™ DOUBLE DENSITY DISK SYSTEM

Why not go all the way to the professional/industrial standard of 600K byte/side disk memory with your S-100 system? The new DISCUS/2D™ full-size, double-density floppy disk system is actually less expensive than many mini-floppy systems.

And Thinker Toys™ hasn't just made full-size, double-density disk memory affordable...we've made it more functional.

Thinker Toys™ has developed BASIC-V,™ a virtual disk BASIC that lets you address all 600K bytes (expandable to 1 megabyte) as if were main memory. The data format is soft-sectored and compatible with IBM's new System 34. And DISCUS/2D™ accepts both single-density and double-density disks for complete flexibility in data storage.

And DISCUS/2D™ is even more attractive because it's priced and delivered as a truly complete system. It's complete with all hardware. It's complete with all necessary software. And it's completely assembled, tested and warranted.

Specifications:
- CP/M V2.2 standard
- Plug compatible with Shugart, Remex and Siemens single- or double-sided drives
- Double/single-density capability utilizing MFM and FM data formats
- Western Digital 1791 LSI floppy disk controller chip
- Uses 2K of S-100 address space:
  —1K PROM with built-in disk drive and I/O utility subroutines incorporating memory mapped I/O
  —1K 2114-3L 300 ns access time RAM for disk data offering and general purpose use
- Starting address of memory space is 340:000 (E000 hex) for compatibility with other popular ROM based systems
- Phase-locked data separator and crystal controlled disk data write precompensation capability to insure the highest standards of data integrity in double density mode.
- Compatible with all 2 MHz and 4 MHz systems which conform with the proposed IEEE standard for the S-100 bus
- 1602 UART with crystal-controlled baud-rate generator
- Sixteen switch selectable baud rates from 50 to 19,200 bits/second
- TTY current loop and industry standard RS232C serial interface
- Power-on jump circuitry for automatic bootstrap loading from the disk drive
- Power supply requirements: + 8V @ 1200 ma; + 16V @ 150 ma; −16V @ 70 ma.
- ROM utility subroutines:

Bootstrap load	Seek
Terminal input	Set sector
Terminal output	Set DMA address
Horne	Disk read
Disk write	DMA status
Select drive	Disk status
Terminal panic detect	Disk error
Terminal status	Switch density
Discus 2D	Discus 2 + 2
Double density	Double density
Single sided	Double sided

	List Price	Our Price
THT-D2DS Single Drive	$1199.00	**$1098.00**
THT-D2DD Double Drive	$1994.00	**$1798.00**

	List Price	Our Price
THT-D22S Single Drive	$1545.00	**$1395.00**
THT-D22D Double Drive	$2740.00	**$2450.00**

THINKER TOYS SOFTWARE

THT-CP/M14 For DISCUS I	$125.00
THT-CP/M14E For EXIDY	$125.00
THT-CP/M22 Dual density	$150.00
THT-MEB Microsoft ext. basic	$299.00
THT-MFT Microsoft Fortran	$399.00
THT-RFT Rational Fortran	$85.00
THT-CCMP "C" Complier	$600.00
THT-REFORM Reformatter	$195.00

ORDER TOLL FREE (800) 423-5633

DRIVE A HARD BARGAIN!

THINKER TOYS™

26 MEGABYTES

Suddenly, S-100 microcomputer systems can easily handle 100 million bytes. Because Morrow Designs™ now offers the first 26 megabyte hard disk memory for S-100 systems—the DISCUS M26™ Hard Disk System.

It has 26 megabytes of useable memory (29 megabytes unformatted). And it's expandable to 104 megabytes.

The DISCUS M26™ system is delivered complete—a 26 megabyte hard disk drive, controller, cables and operating system—for just $4995. Up to three additional drives can be added, $4495 apiece.

The DISCUS M26™ system features the Shugart SA4008 Winchester-type sealed media hard disk drive, in a handsome metal cabinet with fan and power supply.

The single-board S-100 controller incorporates intelligence to supervise all data transfers, communicating with the CPU via three I/O ports (command, status, and data). The controller has the ability to generate interrupts at the completion of each command to increase system throughput. There is a 512 byte sector buffer on-board. And each sector can be individually write-protected for data base security.

The operating system furnished with DISCUS M26™ systems is the widely accepted CP/M 2.0*

LIST PRICE
$4995.00
OUR PRICE
$4495.00

GENERAL SYSTEM INFORMATION

The DISCUS M26 is supplied with the CP/M 2 Operating System prerecorded on the disk. Both low level drivers and bootstrap software are furnished in the documentation and on the disk itself. The drivers are assembled to run at F400 hex and assume that the controller has I/O addresses 50, 51, 52 and 53 hex. For recovery from a catastrophic software crash, Morrow Designs can supply a floppy diskette which reformats the disk and reinstalls copies of the operating system.

The controller can run up to four hard disks for a total of 104 megabytes of on-line storage. It is implemented in forty-seven MSI and SSI TTL integrated circuits. A DIP switch is provided for setting the starting I/O address of the controller. The four addresses are:
0-Control and data status
1-Controller command register
2-Drive select and control register
3-Data In and Out

The lowest address set into the switch must be divisible by four. Through the "Drive select and control register," the controller can select any of the four drives and up to sixteen heads within a drive. Also accessible through this register are the step and direction command lines which are used to move the eight read/write heads from one track to another.

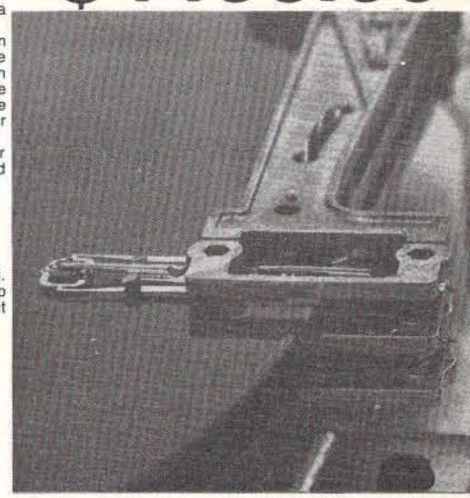
Data transfers are handled through an on-board finite state machine fashioned out of standard TTL and a medium-sized PROM (64 x 8). The commands are:

- write a sector header
- read a sector header
- read a sector of data
- write a sector of data
- reset the internal data buffer pointer to the beginning of the data buffer
- reset the internal data buffer pointer to the beginning of the sector header buffer

The CPU transfers data to the controller by first resetting the internal data buffer pointer to the beginning of either of the two data areas. Next, it fetches data from the appropriate location in memory and performs successive outputs to the data port of the controller. Each reference to the data port automatically increments the pointer to the buffer.

Transferring data from the controller is accomplished in a similar fashion. The pointer is positioned and then the CPU does successive inputs and finally stores the data in an appropriate place in memory. The data buffer is stable between operations and can be referred to again. The buffer must NOT be touched during disk data transfer operations.

Provision is made for the interconnection of controller interrupts to any of the S-100 Bus vectored or unvectored interrupts.



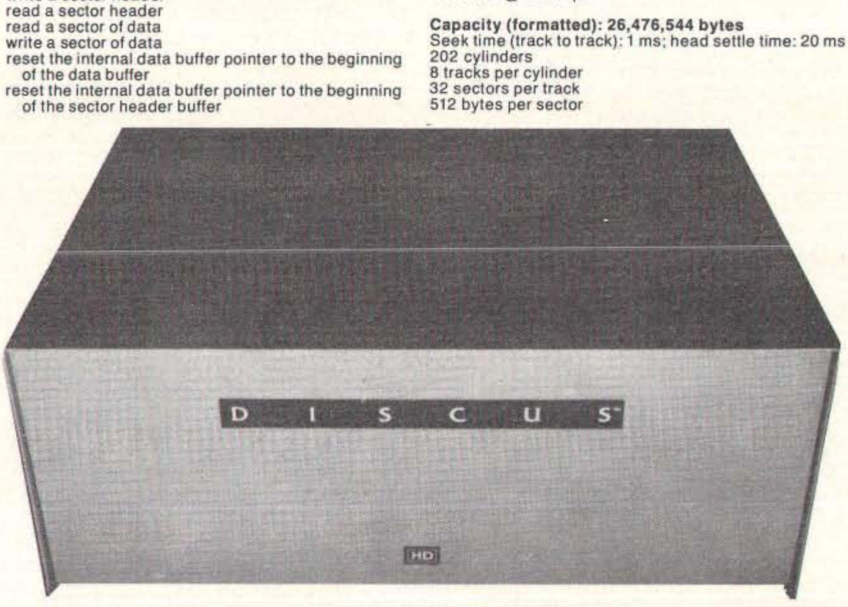
SPECIFICATIONS

Enclosure
Cabinet dimensions: 6⅞'' high, 16⁵⁄₁₆'' wide, 30½'' long. Cabinet is both rack mountable or useable as table top model. (Available Fall 1980: rack slides and desk height rack.) Color: beige.

Power Supply (internal to cabinet)
+24 v @ 3.0 amps
+5 v @ 3.0 amps
−14 v @ 1.5 amps

Drive Power Requirements
115 v AC @ 3.0 amps, or
230 v AC @ 1.5 amps

Capacity (formatted): 26,476,544 bytes
Seek time (track to track): 1 ms; head settle time: 20 ms
202 cylinders
8 tracks per cylinder
32 sectors per track
512 bytes per sector



CP/M® SOFTWARE OPTIONS

For anyone currently running CP/M 2, we will provide a program that automatically integrates the DISCUS M26 into the operating system. This program will be provided on an IBM standard diskette, and it will leave existing terminal and disk I/O unchanged. Thus the M26 can be readily added to most S-100 systems.

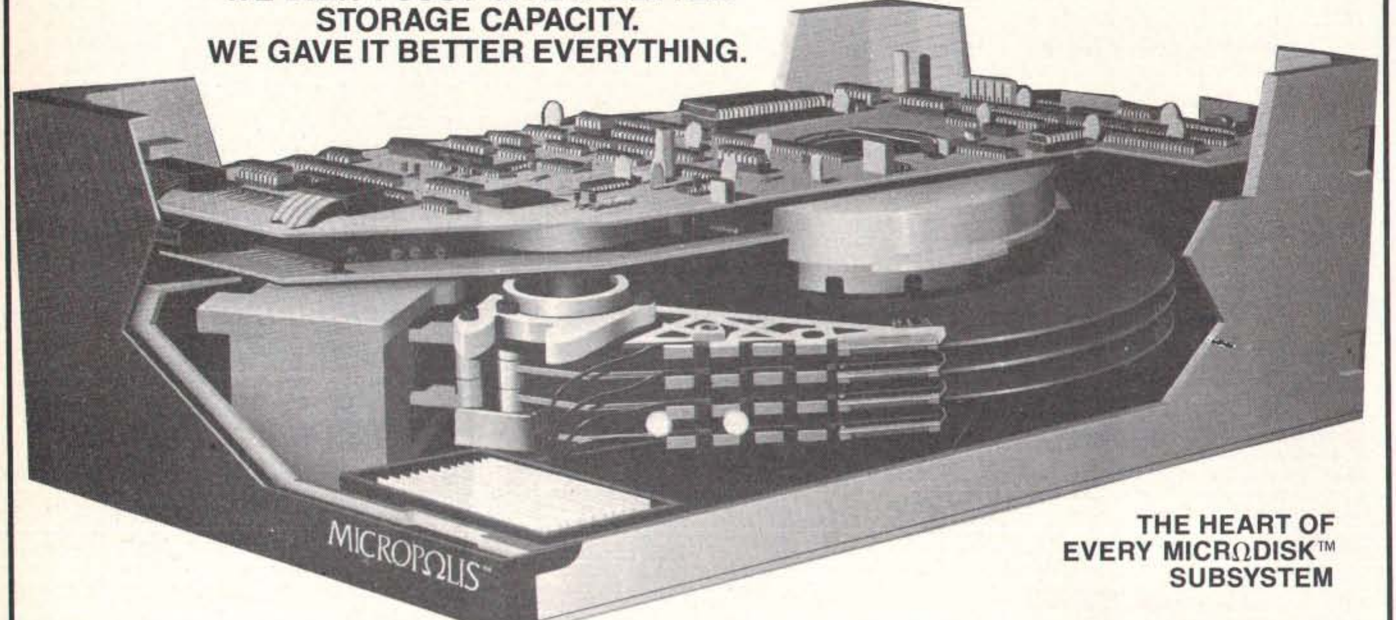
For systems without CP/M 2, we offer CP/M in several configurations. The standard release provides disk I/O for the DISCUS M26 and the Disk Jockey, and terminal I/O for the Disk Jockey or Switchboard serial ports. Optionally, we provide terminal I/O for the SOL keyboard and monitor or for the EXIDY keyboard and monitor. Terminal I/O can also be left blank (jump-to-self) to allow custom patching.

Finally, for CROMEMCO users, we plan to offer a CP/M 2 expanded to full CDOS compatibility and interfaced to the M26 and to standard CROMEMCO floppys. This is sold and serviced by MICAH, 1250 Pine St., #102, Walnut Creek, CA 94596, (415) 933-2783. It will also be available directly from Morrow Designs.

	List Price	Our Price
THT-M26S Subsystem	$4995.00	**$4495.00**
THT-M26A Add-on hard disk drive	$4995.00	**$3995.00**
THT-M26HDC Hard disk controller		**$ 695.00**

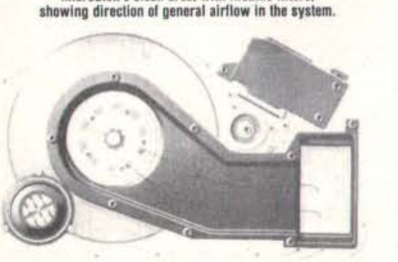
Shipping Weight: THT-M26S&A 50 lbs.
THT-M26HDC 3 lbs.

THE MICROPΩLIS MICRΩDISK™ WE DIDN'T JUST GIVE IT BETTER STORAGE CAPACITY. WE GAVE IT BETTER EVERYTHING.



MICROPΩLIS™

MicroDisk's clean area, with lifetime filters, showing direction of general airflow in the system.



The MicroDisk's drive mechanics consist of a die cast deck whose lower half is devoted to the recording heads, platters, and voice coil motor components. This lower half is the closed circuit clean area, and air is circulated through it by disk rotation induced flow. The flow is directed through a 0.3 micron absolute filter. So there's never any reason to expose the MicroDisk's clean area to unsterile air.

Most other manufacturers violate the rules for sealed "clean" areas in their drives. By placing active components inside the clean area — components that would require unsealing the area to replace or repair faulty parts.

But we decided it didn't make any sense to offer a clean area that wouldn't stay clean.

So there are no active electronic parts in the MicroDisk's clean area. The electrical connection between this area's components and the electronics package is made via a PCB which seals the clean area from the rest of the casting. The single chip preamps are located in sockets accessible without violating the integrity of the clean area.

So you can depend on it not only to be clean, you can depend on it to work.

The Quartz-locked direct-drive motor. Another first from Micropolis.

In order to increase reliability and capacity of the MicroDisk's drive system, we added an element that no one else in the industry has: Quartz crystal speed control.

Rotational drive for the MicroDisk is provided by a Quartz-locked, direct-drive, brushless D.C. Micro-Motor™ only ¾" high. This innovative combination allows a very low profile, and lets three platters reside in an envelope only 4.62" high.

It also makes the design of the Micropolis (or your) controller very simple, and lets us put a full 5% more data on the drive itself by holding the rotation to an accuracy of 0.5%.

And the simplicity of the Quartz-locked MicroMotor gives the drive something else: a very long life.

The MicroDisk lets you put the brakes on.

We've included something on the MicroDisk that some other drive manufacturers curiously omit — a braking mechanism. They claim that a brake is unnecessary and superfluous. And no threat to your disk.

They're wrong.

Some manufacturers use the head to stop the disk from rotating when power is cut. This means that the head slows the disk by friction, letting the disk rotate many times before it stops.

That's why we gave the MicroDisk a brake. It extends the life of the disk by not allowing the head to act as the braking mechanism. That's longer disk life for you, and a higher MTBF in the critical clean area.

Quite frankly, it makes us wonder why people do it any other way.

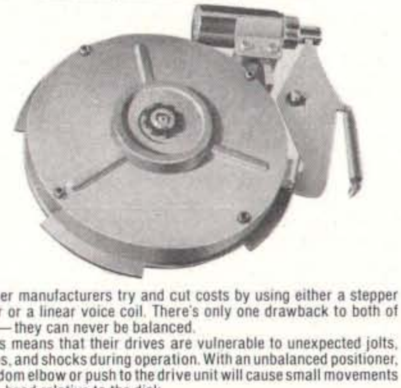
The MicroDisk rotary voice coil positioner, a balanced swing arm mechanism supported on two pre-loaded bearings.

The MicroDisk rotary voice coil positioner. A truly balanced system.

In keeping with the Micropolis policy of not skimping on quality, we gave the MicroDisk a rotary voice coil positioner. It's a balanced swing arm mechanism supported on two pre-loaded bearings.

The voice coil is attached to the system on the opposite side of the bearings from the heads, and the whole mechanism is statically balanced.

The innovative Quartz-locked, direct-drive, brushless D.C. Micro-Motor, shown with braking mechanism.



Other manufacturers try and cut costs by using either a stepper motor or a linear voice coil. There's only one drawback to both of them — they can never be balanced.

This means that their drives are vulnerable to unexpected jolts, bumps, and shocks during operation. With an unbalanced positioner, a random elbow or push to the drive unit will cause small movements of the head relative to the disk.

Imagine your stereo turntable. What happens when someone nudges it while a record is playing? The unbalanced pick-up arm skips over your record. In a disk system even very small movements are serious during a write operation.

There's yet another disadvantage to using a stepper motor instead of a rotary voice coil. It's slower. For multi-user system applications it's unacceptably slow. And, during the "power-down" sequence, or in case of power failure, it can't move the head to a neutral area. So the head lands in your data area on the disk, and may destroy the data. Forever. The MicroDisk's rotary voice coil, however, always lands the head in a neutral area. So data is never threatened.

The MicroDisk has a Dedicated Servo Surface. This is the bottom surface of the bottom disk, and is not used for data. Rather, it's used for positioner information that allows the heads to know where they are. The positioner contains feedback that tells the heads continually when they're on track, so they won't ever write off track even in a severe vibration environment. With a stepper motor, the head never knows where it is — it's like driving in the dark.

But with the MicroDisk's rotary voice coil, you know just where you are.

Think of it as the end of flying blind.

Functional Specifications

Encoding Method	EPM
Platters	1, 2 or 3
Data Surfaces	1, 3 or 5
Data Heads	1, 3 or 5
Available Tracks	580

Environmental

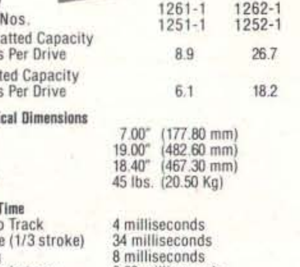
Ambient Temp. Range	50-104°F (10-40°C)
Relative Humidity	10-80% non condensing

Power Requirements

110V 60Hz, 3.5A (max.)
220V 50Hz, 1.5A (max.)

Specifications subject to change without notice.

The Micropolis MicroDisk 8-inch drive (left), and Industry standard 8-inch floppy disk drive, shown side-by-side.



Capacity

Model Nos.	1261-1 1251-1	1262-1 1252-1	1263-1 1253-1
Unformatted Capacity M Bytes Per Drive	8.9	26.7	44.5
Formatted Capacity M Bytes Per Drive	6.1	18.2	31.1

Mechanical Dimensions

Height	7.00" (177.80 mm)
Width	19.00" (482.60 mm)
Depth	18.40" (467.30 mm)
Weight	45 lbs. (20.50 Kg)

Access Time

Track to Track	4 milliseconds
Average (1/3 stroke)	34 milliseconds
Settling	8 milliseconds
Average Latency	8.33 milliseconds
Transfer Rate	7.375 M Bits Sec.

The Micropolis Intelligent Controller, whose board fits inside the drive, providing optional error correction.

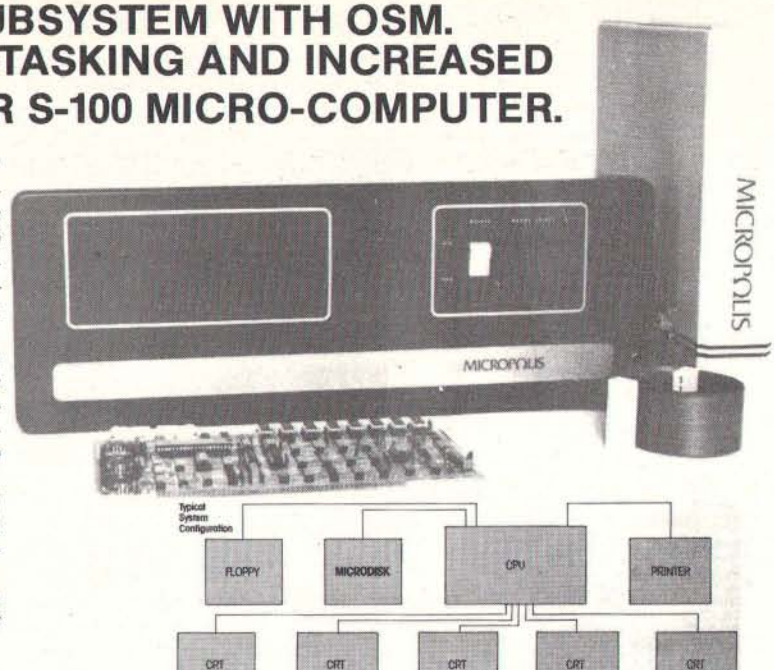
THE MICROPΩLIS MICRΩDISK™
8″ RIGID DISK SUBSYSTEM WITH OSM. MULTI-USER, MULTI-TASKING AND INCREASED CAPACITY FOR YOUR S-100 MICRO-COMPUTER.

The Micropolis Microdisk is an 8″ Winchester disk storage subsystem designed for an S-100 Bus minicomputer using an 8080, 8085, or Z-80 microprocessor with a clock frequency of 2-4 MHz. The subsystem, including the OSM software package, provides you with a true multi-user, multi-tasking system.

The Microdisk subsystem consists of the OSM multi-user multi-tasking, multi-access software package, full documentation. Intelligent Disk Adapter board, interface cable, and from one to eight Microdisk storage modules, depending on your needs.

The Microdisk subsystem offers upward expansion of existing program and data files previously created for Micropolis, Macrofloppy and Metafloppy subsystems.

The Microdisk delivers high capacity, file sizes up to 65,000 records in length, fast access, and excellent data reliability.



Micropolis OSM. Another first for Microdisk.
Micropolis OSM is a set of operating system modules which requires a Microdisk subsystem as the primary file device. It relies on the Intelligent Disk Adapter in the subsystem to perform the File Management for the Microdisk. I/O interface cards with appropriate interrupt capability are necessary to support multiple terminals. Bank switchable memory may be used in the host computer to expand memory capacity, although it is not required.

OSM uses techniques of processor control, I/O handling, memory, task and resource management to create a true multi-user environment. Using the OSM Executive, users at several terminals can invoke and interact with different application programs written in assembly language or BASIC. The system provides an assembler, a source text editor, and a Disk Extended BASIC Interpreter to support the development, execution, and maintenance of application programs. General system management is provided by Filecopy, Disk Archiving, and other utilities.

The main OSM module is loaded into the host computer memory by the Microdisk bootstrap process, and is permanently resident during system operation. The bootstrap process also loads the Microdisk File Management programs into the Intelligent Disk Adapter. Included in the resident module are programs for Interrupt Servicing, Processor Conrol, Physical Device Drivers, Logical Device Handlers, Memory Management, Task Management, the system Supervisor, the OSM Executive, and the DEBUG Executive.

Nonresident OSM Modules.
Some of the OSM modules are separate programs which are loaded from the disk when required, and run under the controls and management of the main OSM resident module. These include an 8080/8085 assembler, a source text editor, a FILECOPY utility, a Disk Archiving utility, and the Micropolis Disk Extended BASIC Interpreter.

OSM Disk Archiving Utility.
The Disk Archiving program is an optimized utility that can be used to back-up copy the contents of a Microdisk unit onto multiple floppy disk volumes.

OSM Memory Management.
The Microdisk Memory Manager controls the allocation of memory blocks to tasks, provides dynamic program demand allocation for assembly language programs, and specifiable partition sizes for user BASIC programs. Programs can also be allocated portions of memory from different bank switchable memory boards, if the address ranges do not overlap. For assembly language programs, the address ranges may be noncontiguous. (A BASIC program uses an address-contiguous memory area.)

OSM Executive.
The OSM Executive lets you control the normal operation of your computer system with dialogue through interactive terminals, and offers command dialogue access to the Supervisor facilities. Included in the OSM Executive are controls and commands to initiate a task, name and set the execution priority for a task, load a program as a task, suspend a program, resume a program, place a program in background execution mode, cancel a program, terminate a task, transfer a terminal to another task, view the system status, and view the logical device status.

Access is also provided to the Microdisk and the floppy disk File Managers by commands to re-name a disk file, change the filetype, filemode or password of a disk file, change the Microdisk master password, view the directory of a disk drive unit, and view the amount of free space on a disk drive unit.

OSM Disk File Management.
Important elements of the Microdisk File Management design include the following: named file access with password protection, dynamically expandable files, sequential record access, direct record access, alphanumeric keyed sequential access, alphanumeric keyed random access, separate key files, multiple key files, user definable file types, multiple open files with four level access mode locks, automatic read-after-write verification, and re-try logic for maximum data integrity.

There is also a common interface scheme between the floppy disk and the Microdisk File Managers. All the preceding features (except passwords and keyed access modes) apply to the floppy as well. Floppy disk formatting is compatible with the Micropolis PDS 4.0 format.

OSM BASIC File Management.
Micropolis OSM BASIC's file management capabilities are performed through the Microdisk and floppy disk File Managers in OSM. Each file stored on a disk is identified by a file name which may be up to ten characters long, and the files may have an optional password. Files can be BASIC program files, assembly language object files, or data files, and the Microdisk will support index files. Also, permanent and write protect attributes may be assigned on a per file basis.

There are statements to RENAME files, modify the end-of-file MARKER (EOF), and change file attributes (ATTR). Functions are also available to find the name, size, and attributes of a file, as well as to find out how much space is free on a given disk unit.

The File Managers automatically maintain placement of files on the disk, with initial space for a new file allocated by the system at the time of creation. Existing files are automatically extended as necessary without any required preallocation. When a file is SCRATCHed, its space returns automatically to available status.

OSM BASIC.
When BASIC is loaded, you have a powerful set of tools for developing, testing, executing, and maintaining BASIC programs.

OSM BASIC program lines can be as long as 250 characters, and may include multiple statements, the maximum line number being 65,529.

Micropolis OSM BASIC has 11 immediate mode commands, which are as follows: SAVE a file, LOAD a file, SCRATCH a file, LIST a program, DELETE lines from a program, RUN a program, CNTL/C to interrupt a running program, CONT to continue an interrupted program, CNTL/X to cancel an input line, and FLOW and NOFLOW to enable and disable the flow trace debugging aid.

OSM BASIC File Programming.
Micropolis OSM BASIC data file programming is easy to use. You can open files simultaneously for sequential and random (direct) access in both read and write modes — with only one OPEN statement. Up to ten files may be open at one time — with no special buffering provisions necessary.

The CLEAR option lets you open a file for rewrite instead of append, while END option provides you with an on-endfile-goto-capability. And the ERROR option gives you an on-error-goto-capability.

You can also open Microdisk files for keyed sequential and keyed direct access. One or more index key files may be associated with a given data file. Index files may be handled separately or they may be automatically coupled to an open data file. Data is written to, and read from, files using PUT and GET statements with variable lists. This allows a mixture of numeric and string variables. Files must be CLOSEd after use.

This file I/O structure also extends to your printer and terminal output files, to afford you a high degree of device independence. And additional options on the OPEN statement (PAGESIZE and ENDPAGE) facilitate the pagination of output reports. In fact, print files may be easily diverted to terminal or disk simply by changing a file name in the OPEN statement.

The Microdisk subsystem. A timely arrival from Micropolis.
The Microdisk's hardware is available in one, two or three platter configurations, with delivery of formatted storage capacities of 6.2, 18.7, and 31.2 megabytes.

Winchester technology is incorporated in the Microdisk design, which is packaged in two sections. The lower half, which contains the platters, disk heads, and positioner, is completely sealed. The upper half, which is accessible for maintenance, contains four circuit boards, including the Micropolis intelligent controller.

When you get right down to it, the Microdisk subsystem is a much-needed, timely solution for capacity-starved microcomputer users.

Maybe that's one reason why, in just four years, Micropolis is already the second-largest producer of 5¼-inch floppy disk drives — for both OEMs and end-users.

And we won't be number two forever.

MICRODISK SUBSYSTEMS

PART NO.	NO. OF PLATTERS	FORMATTED MEMORY SPACE (M BYTE)	LIST PRICE	OUR PRICE
MCP12611	1	6.1	$4923.00	$4190.00
MCP12621	2	18.2	$5148.00	$4435.00
MCP12631	3	31.1	$5673.00	$4935.00

For S-100 bus Systems, you must order controller and OSM Software Package to the right

S-100 BUS CONTROLLER AND OSM SOFTWARE PACKAGE ON ¼″ METAFLOPPY SINGLE (M2), OR DOUBLE (M4) DISKETTES

PART NO.	MEDIA TYPE	LIST PRICE	OUR PRICE
MCPOSM1002	5¼″ M2 Diskette	$660.00	$560.00
MCPOSM1004	5¼″ M4 Diskette	$660.00	$560.00

PREREQUISITE: To install your Microdisk subsystem, you must have access to a Metafloppy (M2, or M4) subsystem.

MICRODISK ADD-ON DRIVES

PART NO.	NO. OF PLATTERS	FORMATTED MEMORY SPACE (M BYTE)	LIST PRICE	OUR PRICE
MCP12511	1	6.1	$4458.00	$3995.00
MCP12521	2	18.2	$4683.00	$4250.00
MCP12531	3	31.1	$5208.00	$4695.00

PLEASE ORDER BY PART NUMBER

MICROPOLIS™ DISK DRIVE SUBSYSTEMS

NOW DOUBLE SIDED

At Micropolis, complete means complete.

Some suppliers offer only hardware and call that complete. At Micropolis complete means everything: hardware and software and documentation.

The hardware set is complete with S-100/8080/Z-80 compatible controller, drive(s), cable-even a built-in Autoload bootstrap ROM to eliminate tiresome button pushing.

Our full Disk Extended BASIC and DOS, assembler and editor software comes complete, too. On its own diskette, ready to go. Software from Micropolis includes a DOS and Disk Extended Basic designed for 8080/Z80-based microcomputers.

DOS is a complete package, including an assembler, editor, file management functions and utilities, which provides total support for 8080 programming. BASIC is a self-contained package which provides a powerful set of tools for developing, testing, executing, and maintaining BASIC programs.

BASIC is designed for microcomputers with at least 24K bytes of RAM and a Micropolis MetaFloppy disk system. DOS can be used alone in a 16K bytes memory system.

Activating the built-in Auto load ROM brings up the system under control of the DOS executive. BASIC can be accessed by issuing a simple DOS command.

Both packages are designed for flexible, efficient programming. Both packages use the same file structure and file management scheme for total compatibility. 8080 programs created under DOS can be loaded and accessed from BASIC. Data files created under BASIC can be processed by user written application programs running under the DOS.

At Micropolis, complete means **COMPLETE.**

Imagine getting all the capacity of an 8-inch floppy in a 5¼-inch format.

MetaFloppy can give you this higher capacity because it packs more data into every disk. You get the capacity of larger 8-inch drives with the lower price and smaller packaging of 5¼-inch drives.

An ordinary 5¼-inch floppy provides just 35 tracks/drive and stores only 70K bytes. Not nearly enough for anything useful. So instead, we use 77 tracks—each with 16 sectors of 256 bytes/sector—to yield a capacity of 315K bytes per side. That's more than four times an ordinary 5¼-inch floppy! And why we call this one "quad density."

Combine two of these drives in a compact dual module and you can copy diskettes from one drive to the other, or rearrange data files, or whatever. The dual unit stores 630K bytes. Enough for almost anything. But just in case that isn't enough, our controller can handle two duals (or four singles). That means your micro can have more than a million bytes of formatted disk storage.

If that still isn't enough, on special order you can add a second controller with up to four more drives. That will give you a grand total of over 2,500,000 bytes of storage on-line. This means, if your application keeps growing, we've got you covered in easy steps. And you get all these bytes at surprisingly low cost.

Faster than a speeding bullet.

At Micropolis, we don't skimp on performance to deliver maximum capacity. So you can expect professional operating speed and efficiency. Like checks and balances, such as automatic read verification after writing, that you would expect in a sophisticated data processing system.

Like fast track-to-track positioning time of only 30 milliseconds. And a data transfer rate of 250,000 bits per second.

Up, Up, and away!

Metafloppy gets along well with almost everyone.

So choose the microcomputer you want. MetaFloppy's controller is completely compatible with the S-100/8080/Z-80 buss. It just plugs into your MITS 8800, IMSAI 8080, COMPAL-80, SOL-20, Polymorphic 88, CROMEMCO, TDL, or similar micro and it's ready to go. The memory mapped controller/bootstrap may be origined at any 1K byte boundary in the 48K to 64K byte region of memory.

For small businesses, for engineers who want to develop their own software, or for the advanced hobbyist, MetaFloppy is ideal.

METAFLOPPY DISK SUBSYSTEMS

Subsystems complete with BASIC, DOS, Assembler/Editor, Users Manual, S-100 Controller, complete Calling, Cabinet, and Power Supply.

DOUBLE SIDED

PART #	DESCRIPTION	LIST PRICE	OUR PRICE
MCP-1054-4	Four-disk system with 2,520,000 bytes (formatted) on-line storage capacity. 50 lbs.	$4,622.00	$3,995.00
MCP-1053-4	Two-disk system with 1,260,000 bytes (formatted) of on-line storage capacity. 25 lbs.	$2,605.00	$2,295.00
MCP-1043-4	One disk system with 630,000 bytes (formatted) of on-line storage capacity. 16 lbs.	$1,501.00	$1,298.00

SINGLE SIDED

PART #	DESCRIPTION	LIST PRICE	OUR PRICE
MCP-1054-2	Four-disk system with 1,260,000 bytes (formatted) on-line storage capacity. 50 lbs.	$3,290.00	$2,895.00
MCP-1053-2	Two-disk system with 630,000 bytes (formatted) of on-line storage capacity. 25 lbs.	$1,895.00	$1,650.00
MCP-1043-2	One disk system with 315,000 bytes (formatted) of on-line storage capacity. 16 lbs.	$1,145.00	$999.00

Subsystems complete with BASIC, DOS, Assembler/Editor, Users Manual, S-100 Controller, Cabinet and Data Cable.

Requires DC Regulator Kit (Part # MCP-109101), to convert raw S-100 Buss DC supplies to voltages necessary for the Drive Module.

DOUBLE SIDED

PART #	DESCRIPTION	LIST PRICE	OUR PRICE
MCP-1041-4	One-disk system with 630,000 bytes (formatted) of on-line storage capacity	$1,419.00	$1,250.00

SINGLE SIDED

PART #	DESCRIPTION	LIST PRICE	OUR PRICE
MCP-1041-2	One-disk system with 315,000 bytes (formatted) of on-line storage capacity.	$1,045.00	$940.00

ADD-ON STORAGE MODULES

The MetaFloppy Add-On Storage Modules provide additional storage by adding more drives to the controller supplied with the subsystems. A maximum of four drives can be attached to each controller by use of the daisy chain cables.

Add-On modules do not include Controller, User's Manual or diskettes.

DOUBLE SIDED

PART #	DESCRIPTION	LIST PRICE	OUR PRICE
MCP-1033-4	Two-disk 1,260,000 byte add-on with enclosure and power supply. Requires daisy chain cable. 25 lbs.	$2,022.00	$1,860.00
MCP-1023-4	One-disk 630,000 byte add-on with enclosure and power supply. Requires daisy chain cable. 16 lbs.	$930.00	$855.00
MCP-1021-4	One-disk 630,000 byte add-on with enclosure. Requires daisy chain cable and regulator kit. 8 lbs.	$834.00	$765.00

SINGLE SIDED

PART #	DESCRIPTION	LIST PRICE	OUR PRICE
MCP-1033-2	Two-disk 630,000 byte add-on with enclosure and power supply. Requires daisy chain cable. 25 lbs.	$1,395.00	$1,279.00
MCP-1023-2	One-disk 315,000 byte add-on with enclosure and power supply. Requires daisy chain cable. 16 lbs.	$645.00	$595.00
MCP-1021-2	One-disk 315,000 byte add-on with enclosure. Requires daisy chain cable and regulator kit. 8 lbs.	$545.00	$498.00

WE ACCEPT VISA, and MASTER-CHARGE

MICROPOLIS™ DISC DRIVE SUB SYSTEMS

At Micropolis, we pack 100% more capacity into a 5¼-inch floppy.

Meet the Micropolis MacroFloppy. From the Greek "macro," meaning "large, great."

The MacroFloppy: 1041 and :1042 disk drive subsystems give you up to two times greater storage capacity than ordinary 5¼-inch floppy disks. 143K bytes, to be exact. For less money than you'd think possible. Both substsems, in fact, are the lowest cost approaches to adding the capabilities of a floppy to your S-100 system.

The 70K (or thereabouts) bytes provided by most 5¼-inch floppies have never been enough. Expecially today, when you need more high-speed random access storage than ever. To help you work with larger data files. And use programs bigger than your computer's memory.

MacroFloppy provides you with that needed extra storage. And so economically.

Two complete subsystems. Ready to go.

Both the MacroFloppy: 1041 and :1042 are fully assembled, tested, burned-in, and tested again. For zero start-up pain and long term reliability. They're also backed up by our famous Micropolis factory warranty.

They both come complete with the powerful, field-proven Micropolis extended disk BASIC. And both subsystems allow upward capability of BASIC programs with the high-capacity MetaFloppy quad density floppy subsystems.

So you can use the low-cost MacroFloppy for program development, and upgrade later to the high capacity MetaFloppy.

In other words, if your application keeps growing, we've got you covered.

At Micropolis, you get it all. From a single source.

Some suppliers offer only hardware and call that everything. At Micropolis, everything means everything: hardware, software, and documentation.

The MacroFloppy: 1041 comes packaged inside a protective enclosure (without power supply). And includes an S-100 controller. Interconnect cable. And 2 diskettes, one of them the Micropolis BASIC, a compatible DOS, assembler and editor. The:1041 is even designed to be used either on your desk top, or to be integrated right into your S-100 chassis. A d.c. voltage regulator kit, 1091-01, is available to convert your S-100 chassis raw supplies to levels suitable for the :1041, at small additional cost.

The MacroFloppy: 1042 comes with everything the :1041 has, and more. Such as d.c. regulators, its own line voltage power supply, and to top it off, a striking cover. Making it look right at home just about anywhere.

To complete the package, we even include a comprehensive User's Manual including software descriptions, that tells you just what to do, and how to do it.

Because we know that you need it all.

MACROFLOPPY DISC SUBSYSTEMS

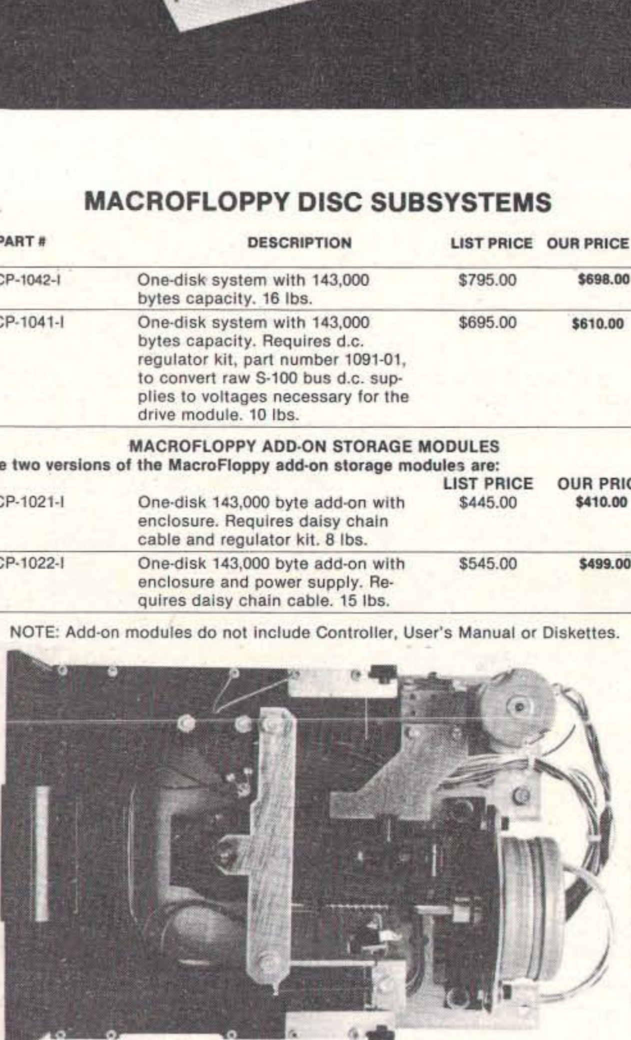
PART #	DESCRIPTION	LIST PRICE	OUR PRICE
MCP-1042-I	One-disk system with 143,000 bytes capacity. 16 lbs.	$795.00	**$698.00**
MCP-1041-I	One-disk system with 143,000 bytes capacity. Requires d.c. regulator kit, part number 1091-01, to convert raw S-100 bus d.c. supplies to voltages necessary for the drive module. 10 lbs.	$695.00	**$610.00**

MACROFLOPPY ADD-ON STORAGE MODULES

The two versions of the MacroFloppy add-on storage modules are:

		LIST PRICE	OUR PRICE
MCP-1021-I	One-disk 143,000 byte add-on with enclosure. Requires daisy chain cable and regulator kit. 8 lbs.	$445.00	**$410.00**
MCP-1022-I	One-disk 143,000 byte add-on with enclosure and power supply. Requires daisy chain cable. 15 lbs.	$545.00	**$499.00**

NOTE: Add-on modules do not include Controller, User's Manual or Diskettes.

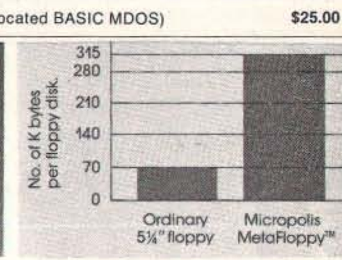


CABLES (1 lb.)

MCP-1083-01	Standard interface cable A, with 2 connectors for use with 1 storage module attached to controller.	**$25.00**
MCP-1083-02	Daisy chain interface cable B, with 3 connectors for use with 2 storage modules attached to controller.	**$35.00**
MCP-1083-03	Daisy chain interface cable C, with 4 connectors for use with 3 storage modules attached to controller.	**$50.00**
MCP-1083-04	Daisy chain interface cable D. with 5 connectors for use with 4 storage modules attached to controller.	**$65.00**
MCP-1092-06	Power cable A, to be used in conjunction with 1015/1016 series drives.	**$ 8.00**
MCP-1092-07	Power cable B, to be used in conjunction with 1021/1041 series systems.	**$ 8.00**

ACCESSORIES

MCP-1091-01	Regulator kit for 1041, 1021, 1015 and 1016.	**$20.00**
MCP-1092-01	Relocatable bootstrap kit.	**$25.00**
MCP-1093-03	Poly 88 kit (relocated BASIC MDOS)	**$25.00**



Each one of our floppy disks has a remarkable storage capacity, eliminating the need to keep bothersome stacks of paper.

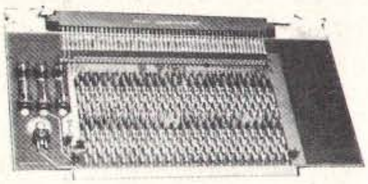
PLEASE ORDER BY PART NUMBER

GODBOUT

CK-017 ACTIVE TERMINATOR

Active termination promotes reliable and accurate data transfer by minimizing the ringing, cross-talk, overshoot, noise and other gremlins that can occur with unterminated lines. Also saves considerable energy compared to passive termination systems, thereby putting less strain on your power supply and keeping heat out of the enclosure. All lines (except power & ground) terminated to 2.7 V through 270 ohms.

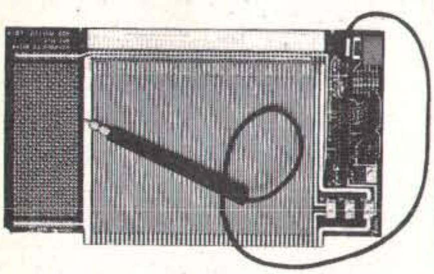
GBT-106U Kit 1 lb. $34.95



Extender/Terminator

- Active and/or dynamic termination
- All power lines fused for protection
- All S-100 lines labeled and numbered
- Can be used as an extender and/or terminator
- Solder mask both sides of board
- Silkscreened reference designations
- Gold plated fingers

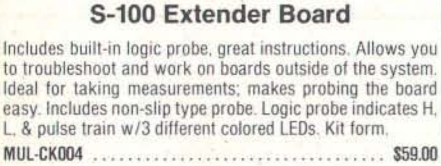
CCS-2520AK Kit 1 lb. $39.95



S-100 Extender Board

Includes built-in logic probe, great instructions. Allows you to troubleshoot and work on boards outside of the system. Ideal for taking measurements; makes probing the board easy. Includes non-slip type probe. Logic probe indicates H, L, & pulse train w/3 different colored LEDs. Kit form.

MUL-CK004 .... $59.00



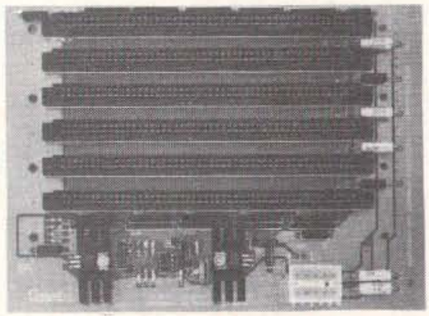
MUL-CK011
S-100 Relay/Opto-isolator Kit

S-100 compatible, 8 fast reed relays respond to an 8 bit word. Also, 8 opto-isolators accept an 8 bit word from outside and send it to your computer for handshaking or further control purposes. Good for model railroad, burglar alarm, audio switching, automated display, ham radio and other uses.

MUL-CK011K .... Kit $129.00
MUL-CK011 .... A&T $179.00

GODBOUT

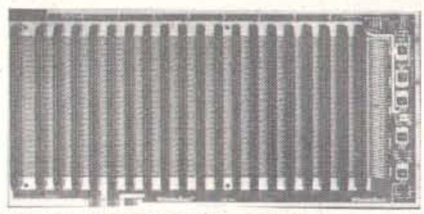
S-100 MOTHERBOARDS



S-100 MOTHERBOARDS
WITH ACTIVE TERMINATION & SHIELDING

- 6, 12, or 20 slot
- Active termination for buffering to 2.7 volts at 280 ohms
- Standby current, a low 15-20 MA
- Shielding further reduces noise factor
- All power is brought out to a block connector for convenience.
- S-100 connectors are .125" pin to pin on 3/4" centers.
- Assembled and tested.

		LIST PRICE	OUR PRICE
GBT-153U	UNKIT 6 SLOT		$ 89.00
GBT-153A	A&T 6 SLOT	$129.00	$119.00
GBT-154U	UNKIT 12 SLOT		$129.00
GBT-154A	A&T 12 SLOT	$169.00	$149.00
GBT-155U	UNKIT 20 SLOT		$174.00
GBT-155A	A&T 20 SLOT	$214.00	$189.00



THE WUNDERBUSS with Noiseguard®
High-performance S-100 bussboards

Shown: **WÜNDERBUSS™/20**

Capacity: 20 positions
Dimensions: 17 1/4" x 8 1/2"

Specifications:

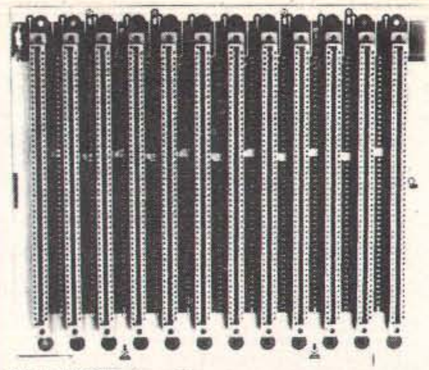
- Edge Connectors: S-100 type, .125" spacing on 3/4" centers.
- Shielding: Every signal fully shielded by both interconnected ground lines, 2nd cross-coupled ground planes.
- Termination: Active termination of each line. Termination network includes LM 201 op amp and 2 PNP/NPN pairs for buffering to 2.4 volts at 180 ohms.
- Mounting: Holes at each edge connector position, plus auxiliary holes to fit IMSAI cabinet.
- Power Connectors: "Fast on" connectors at all 10 positions.
- Power Required: 7 to 10 volts; 14 to 20 volts; —14 to —20 volts.
- Peripheral Power Outputs: 5 volts at 1 amp, 12 volts at 500ma; — 12 volts at 500ma.
- Circuit Board: Double-sided glass epoxy with plated through holes. Solder mask on both sides and part legend.

KITS (less S-100 connectors)

THT-WB8K	$54.00
THT-WB12K	$65.00
THT-WB20K	$76.00

A&T (with connectors)

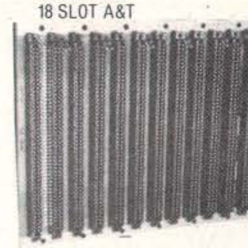
	LIST PRICE	OUR PRICE
THT-WB8A	$144.00	$136.00
THT-WB12A	$175.00	$165.00
THT-WB20A	$226.00	$209.00



SILENCE + MOTHERBOARDS

+ No Need for Termination + Very High Crosstalk Rejection + LED Power Indicator + Fits in Most Mainframes + 6, 12, and 18 Slots Available + Has Operated in 14 MHz Quietly.

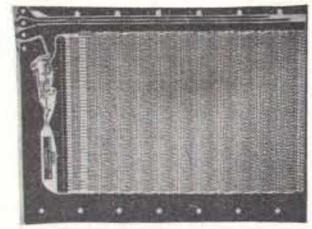
QTC-MB6BB	6 SLOT BARE BOARD	$ 25.00
QTC-MB6K	6 SLOT KIT	$ 40.00
QTC-MB6A	6 SLOT A&T	$ 50.00
QTC-MB12BB	12 SLOT BARE BOARD	$ 30.00
QTC-MB12K	12 SLOT KIT	$ 70.00
QTC-MB12A	12 SLOT A&T	$ 90.00
QTC-MB18BB	18 SLOT BARE BOARD	$ 50.00
QTC-MB18K	18 SLOT KIT	$100.00
QTC-MB18A	18 SLOT A&T	$140.00



MOTHERBOARD
WITH ACTIVE TERMINATION

- Twelve-slot capability using standard S-100 connectors
- Low-reactance pin connectors to eliminate ringing and data error
- "Double path" trace pattern which halves the line inductance
- On-board voltage regulator
- Active termination to regulate signal line voltages
- Wide spacing of bus lines
- Criss-cross bus lines to minimize signal noise
- Distributed by-passing of all power lines
- Capability of supporting CPUs at 4 MHz and above
- Solder mask on both sides of board
- Simple, strong mounting

CCS-2501A $119.95



VCT-8803 Motherboard for S-100 Bus Microcomputer

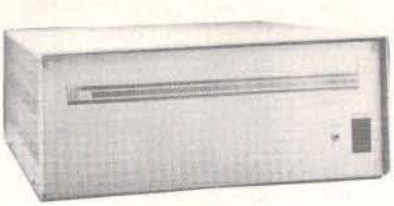
Mounts 11 receptacles with 100 contacts or 10 receptacles plus interconnectors to smaller boards for expansion. Connectors mount with tabs protruding through .038 inch (1 mm) diameter holes in rows spaced .250 inch (6.4 mm) on each connector position and 0.75 inch (19 mm) between connector positions. Includes etched circuit and instructions for active or passive terminations plus 12 tantalum capacitors for +5, +12, —12 volt buses, and spacers for mounting in Vector VP1 or VP2 cases. G-10 epoxy glass board with 2 ounce copper, solder plated circuitry plus solder mask to avoid accidental short circuits. Large buses: +5 V and GND (10 amps), +12 V or 16 V (7 amps). Current ratings are per MIL-STD-275 with 10° rise.

Shipping weight 2 pounds (.9 kg) $29.50

ORDER TOLL FREE (800) 423-5633

PRIORITY ONE ELECTRONICS

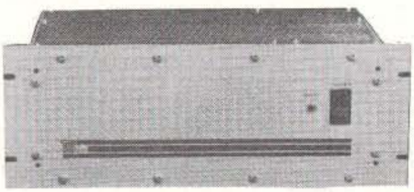
TEI



MAINFRAMES

THE BMW OF S-100 MAINFRAMES

From the power supply through the sturdy chassis, TEI constructs and assembles each mainframe with great care. Every TEI mainframe utilizes a constant voltage transformer (CVT) which delivers clean, regulated power at the proper level, reducing the heat in the computer cards. The output voltage on the transformer remains nearly even with the input voltage varying from approximately 85V to 140V. This means the mainframe will never notice voltage variations or even a brownout. It also provides 100 dB noise rejection to protect the computer from voltage spikes and line noise.



TEI 22 slot and 12 slot mainframes offer a S-100 motherboard which is grounded, shielded and actively terminated for high speed operation now or for later upgrading. Each mainframe is shipped completely assembled, tested and burned in, with fan, washable filter, all connectors and card guides. Rackmount models are available in both 22 and 12 slot mainframes. The combination of the lowest noise bus, a regulated CVT power system and a rugged chassis produces a mainframe without equal.

S-100 MAINFRAMES		LIST PRICE	OUR PRICE
TEI-MCS 112	12 Slot Desk	$685.00	$615.00
TEI-MCS 122	22 Slot Desk	$845.00	$760.00
TEI-RM 12	12 Slot Rackmount	$800.00	$720.00
TEI-RM 22	22 Slot Rackmount	$945.00	$850.00
Shipping Weight:	On 12 Slot Mainframes	35 Lbs.	
	On 22 Slot Mainframes	50 Lbs.	

S-100 MAINFRAMES WITH 3 CUTOUTS FOR 5¼" FLOPPY DISK DRIVES INTERNAL CABLING IS PROVIDED

TEI-TF12	12 Slot	$695.00	$625.00
TEI-RM12	12 Slot	$795.00	$715.00

Shipping Weight: 40 Lbs.

- S-100 compatible
- Industrial/commercial quality construction
- Flip-top cover
- Excellent cooling capability
- 12 slot capability (uses model 2501A)
- Input 105, 115, or 125 VAC
- Output +8 VDC20A, + − 16 VDC 4A
- Active termination of all bus lines
- Fan and circuit breaker included
- Rugged construction

		List	Our Price
CCS-2200A	Assembled & Tested 35 lbs	$399.00	$369.95
CCS-2200AK	Kit 35 lbs		$349.95

THINKER TOYS 8" DISK DRIVE CABINET

George Morrow is recognized as a pioneer and an industry leader because of his innovative designs and participation on the S1000 IEEE Standards Committee. George has designed these floppy disc enclosures with power supply, which incorporates the following features:

- Internal data cable and power cables
- Data signals are terminated at the back of the cabinet with a socket/header connector
- Regulated power supply with protection circuitry
- Clean, attractive appearance
- Stackable
- Accommodates Shugart SA801R, SA800R, SA581R, SA850R, Siemans FDD120/200 Series, Demex 2000, Remex 4000
- Vented chassis for cool, reliable operation
- Color Beige
- Shipping weight: 20 lbs. (single drive cabinet) 30 lbs. (dual drive cabinet)

THT-DC1	Single Drive Cabinet	$ 250.00
THT-DC2	Dual Drive Cabinet	$ 375.00

BUY CABINET AND SHUGART 801R DRIVES AND $AVE

SINGLE CABINET

THT-S801R1P $ 720.00

DUAL CABINET

WITH ONE DRIVE		WITH TWO DRIVES	
THT-D801R1P	$825.00	THT-D801R2P	$1250.00

Shipping Weight: Add 15 lbs per drive

EXTERNAL DATA CABLES

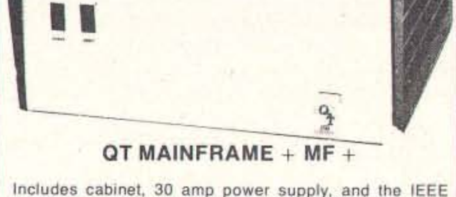
PRI-50 SK SK	Socket to Socket	$ 19.95
PRI-50 CE SK	Cardedge to Socket	$ 19.95
PRI-50 CE CE	Cardedge to Cardedge	$ 19.95

(Accepts 2 each 5¼" Disk Drives)

MF + MD

Includes cabinet, 18 amp power supply, IEEE S-100 Motherboard (6-12 slot) and dual-mini-disk provision with disk drive power supply. The QT + MF + MD is fan-cooled, has AC line filter to eliminate EMI, and is fully assembled and factory tested. Power and reset switches are located on the front panel.

QTC-MF+MD12 $500.00 **QTC-MF+MD6** $450.00
QTC-MF+MD Without Mother Board $400.



QT MAINFRAME + MF +

Includes cabinet, 30 amp power supply, and the IEEE S-100 motherboard (12 or 18-slot). The QT MF+ is fan-cooled, has AC line filter to eliminate EMI, and is fully assembled and factory tested. Power and reset switches are located on front panel.

QTC-MF+12 $450.00 **QTC-MF+18** $500.00
QTC-MF+ Without Mother Board $350.00

SHUGART SA801R

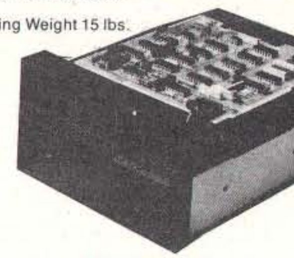
SHU-801R $499.00
2 OR MORE $470.00

Capacity	Single Density	Double Density
Unformatted		
Per Disk	3.2 megabits	6.4 megabits
Per Track	41.7 kilobits	83.4 kilobits
IBM Format		
Per Disk	2.0 megabits	n/a
Per Track	26.6 kilobits	n/a
Transfer Rate	250 kilobits/sec.	500 kilobits/sec.
Latency (average)	83 ms	83 ms
Access Time		
Track to Track	8 ms	8 ms
Average	260 ms	260 ms
Setting Time	8 ms	8 ms
Head Load Time	35 ms	35 ms

Shugart's SA801 standard floppy disk drive is the established industry leader with over 85,000 units installed around the world. This floppy disk drive application leadership is backed by 17 patents and a technical staff with hundreds of man-years of disk drive engineering experience.

The SA801R floppy disk drive is mechanically and electronically the same as the SA801 except it has a narrower chassis width plus side and bottom mounting posts to facilitate installation of two drives side-by-side in a standard 19" RETMA rack.

Shipping Weight 15 lbs.



SHUGART SA400

ACTIVITY LIGHT	
WRITE PROTECT CIRCUITRY	
CAPACITY	110 KB
TRACKS	35
HEADS	1
BIT TRANSFER RATE	125K bits/sec.
RECORDING DENSITY	2600BPI max.
AVERAGE ACCESS TIME	550 msec.
DRIVE DIMENSIONS	3.25"H x 5.75"W x 8"L
SHIPPING WEIGHT	4 LBS.

SHUGART SA400 5¼"
110 KB, 35 tracks
SHU-SA400 .... $295.00

IN A CABINET WITH POWER SUPPLY
LBO-SA400 PC $395.00

GODBOUT S-100 MAINFRAMES

- Rugged metal frame
- Line cord
- Line filter
- fan
- Fuse holder
- 2 Convenience outlets
- Cardguides
- Base is drilled to accept all vector & Godbout motherboards
- Desk and rack mount
- Back panel has cutouts for 12-DB25, 3-DA15, & 2-DD50 connectors

GBT-BOX DESK $289.00
GBT-BOX RACK $329.00

SHIPPING WEIGHT: 23 LBS

PLEASE ORDER BY PART NUMBER

PRIORITY ONE ELECTRONICS

CENTRONICS® LETTER QUALITY 737 DOT MATRIX PRINTER

Standard Features:
- 80 cps—proportional spaced model
- 50 cps—monospaced mode
- proportional spacing, plus 10 cpi and 16.7 cpi
- Nx9 (proportional) or 7x8 (monospaced) dot matrix
- 3 way paper handling system
- 96 character ASCII
- microprocessor electronics
- expanded print
- right margin justification
- print underlining
- 9-wire free flight print head
- bidirectional stepper motor paper drive
- full one line buffer
- 21 lpm with 80 columns printed
- 58 lpm with 20 columns printed
- 6 lines per inch vertical spacing
- paper tear bar
- parallel or serial interface
- 50 to 9600 baud switch selectable

	List Price	Our Price
CEN-7371 Parallel	$ 995.00	**$895.00**
CEN-7373 Serial	$1045.00	**$950.00**

CENTRONICS 730 DOT MATRIX PRINTER

Standard Features:
- 100 characters/second
- 80 characters/line
- 10 characters/inch
- 3-way paper handling system
- 7x7 dot matrix
- 96 character ASCII
- microprocessor electronics
- unidirectional print at 5.0 ips
- high speed return approximately 10 ips
- 21 lpm with 80 columns printed
- 58 lpm with 30 columns printed
- 80 character buffer
- 6 lpi vertical
- Parallel or serial interface

	List Price	Our Price
CEN-7301 Parallel	$795.00	**$725.00**
CEN-7303 Serial	$845.00	**$775.00**

CENTRONICS® MICROPRINTER
Nonimpact Desk-Top Printers

Features:
• 150 Lines Per Minute • 96 Characters—Upper and Lower Case • 5/10/20 Characters/Inch—Software Selectable • Elongated characters • Underlining • Simplified Operation • Quiet • Audio alarm • Long Life—Only 4 Moving Parts • Small Size and Light Weight • No Toners/Ribbons Required •

The paper requires no toners or ribbons. Instead, it carries a conductive aluminized coating which is vaporized by a low voltage discharge from the print head to produce highly readable characters.

CEN-P1 .... **$349.00**

CONDUCTIVE ALUMINIZED PAPER FOR THE CENTRONICS P1 AND S1 PRINTER
CEN-P1S1PAP—CASE OF 5 ROLLS **$29.95**
Shipping Weight: 4 lbs.

MODEMS

THE STAR MODEM from LIVERMORE

FEATURE FITS GTE HANDSETS!

2 YEAR WARRANTY

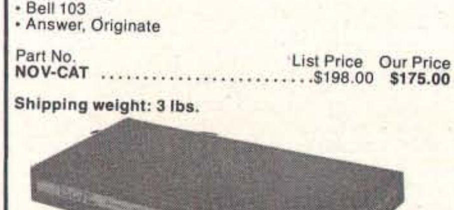
EXCLUSIVE ACOUSTIC CHAMBERS
The exclusive triple seal of Livermore's new flat mounted cups locks the handset into the acoustic chamber yielding superior acoustic isolation and mechanical cushioning. Designed to adapt to most common handsets used throughout the world, the STAR offers the utmost in flexibility and transmission reliability.

Specifications:
- **Data Rate:** 0 to 300 baud
- **Compatibility:** Bell 103 and 113; CCITT
- **Frequency Stability:** ±0.3 percent. Crystal controlled
- **Receiver Sensitivity:** −50 dBm ON, −53 dBm OFF
- **Modulation:** Frequency shift keyed (FSK)
- **Carrier Detect Delay:** 1.2 seconds ON; 120 msec OFF
- **EIA Terminal Interface:** Compatible with RS 232 specifications
- **Teletype Interface:** 20 milliampere current loop
- **Optional Interfaces:** IEEE 488; TTL; TTY 43
- **International (CCITT) frequencies available**
- **Switches:** Originate/Off/Answer; Full Duplex/Test/Half Duplex
- **Indicators:** Transmit Data, Receive Data, Carrier Ready, Test
- **Power:** Supplied by 24 VAC/150 MA UL/CSA listed wall-mount transformer. Input 115 VAC, 2.5 watts. (A 220 VAC, 50 Hz adaptor is available upon request.)
- **Dimensions:** 10″ x 4″ x 2″
- **Weight:** 1.74 lbs. (3 lbs. shipping weight including AC adaptor.)
- **Warranty:** Two years on parts and labor, excluding the AC adaptor which carries the manufacturer's warranty

Part No.	Description	List Price	Our Price
LIV-STAR	RS232, TTL Modem	$199.00	**$149.00**
LIV-STAR20M	RS232, 20MA Current Loop	$199.00	**$149.00**
LIV-STAR-V21	CCITT European Standard	$229.00	**$219.00**
LIV-IEEE	IEEE 488 Standard	$395.00	**$298.00**
LIV-IEEE-V21	IEEE 488, CCITT Standard	$465.00	**$398.00**

CABLES

Part No.	Description	Price
CND-RS2328F	RS232 8 Cond 8 ft.	**$19.95**
LIV-I2I	IEEE to IEEE 2 Meter	**$59.95**
LIV-I2PET	IEEE to Pet 2 Meter	**$59.95**

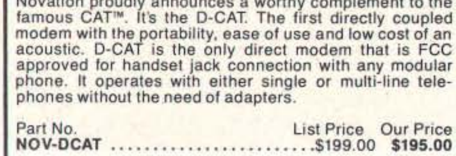


NOVATION CAT MODEM

- 0-300 Baud
- Bell 103
- Answer, Originate

Part No.	List Price	Our Price
NOV-CAT	$198.00	**$175.00**

Shipping weight: 3 lbs.



NOVATION D-CAT — NEW

Data Rate: 0-300 Baud. **Compatibility:** Bell 100 series. **Interface:** EIS RS232 C. **Modes:** Data, Talk, Monitor, Normal and Test. **Duplex:** Half, Full. **Indicators:** Ready, Power

Novation proudly announces a worthy complement to the famous CAT™. It's the D-CAT. The first directly coupled modem with the portability, ease of use and low cost of an acoustic. D-CAT is the only direct modem that is FCC approved for handset jack connection with any modular phone. It operates with either single or multi-line telephones without the need of adapters.

Part No.	List Price	Our Price
NOV-DCAT	$199.00	**$195.00**

YOU MUST HAVE A PHONE WITH MODULAR CONNECTORS.

Shipping weight: 3 lbs.

PRINTERS

ANACOM 150 DOT MATRIX PRINTER

Features:
- 150 Characters per second
- Bidirectional printing, logic seeking
- 9 x 9 Dot matrix
- Upper and lower case with descenders
- Double width characters
- 10 characters per inch—horizontal
- 6 or 8 lines per inch—vertical
- Uses a snap-in cartridge ribbon
- Adjustable tractors
- Original and 4 copies
- Paper out sensor
- 90 day factory warranted
- Parallel or serial interface

Printers	List Price	Our Price
ANA-150P Parallel	$1350.00	**$1095.00**
ANA-150S Serial	$1350.00	**$1095.00**

REPLACEMENT PERSONALITY MODULES
ANA-PC Parallel Module **$75.00**
ANA-SC Serial Module **$75.00**
ANA-RIBBON Cartridge Nylon **$15.00**

OPTIONS
Options must be ordered with printer
ANA-600ELH 600 Million Impact Extended Life Head **$150.00**
ANA-BELL Programmable Bell Circuit **$75.00**
ANA-240V50C 240VAC 50 Cycle Power Supply . **$30.00**

Novation

A NEW BREED OF MODULAR MODEMS FROM NOVATION

NOV-4102D Originate Only **$340.00**
NOV-4103B Auto Answer/Manual Originate ...**$399.00**
NOV-4113B Auto Answer Only **$365.00**

Novation presents the first fully integrated modern system for the future.

Their new line of 300 baud direct connect modems are the best for reliability, economy and convenience.

Novation's direct connect modems meet all requirements for the FCC ruling on Part 68, eliminating the cost and need for a Bell DAA (data access arrangement). It saves even more through a simplified, economical installation procedure, using a modular T-adapter—a Novation exclusive.

MODULATION: FSK, phase coherent. **OPERATING MODE:** 4102 Originate, 4103B Auto Answer/Manual Originate, 4113B Auto Answer Only. **COMMUNICATION MODE:** Full, Half duplex. **PHONE LINE INTERFACE:** Direct-connect per FCC Part 68. Modular Plug mates with standard telephone Modular Jack (USOC-RJ11C). FCC registration No. AU492X 70157-PC-N. Ringer equivalence 0.3A, 1.0B. **BELL COMPATIBILITY:** 4102D 103A, 4103B 103A, 4113B 113B, **CARRIER DETECT:** Sensitivity: −50dBm On delay: 150 ms (+, −15ms) Off Delay: 50 ms (+, −5ms) Noise Rejection 0dBm **TRANSMITTER: 4102D** On Delay: Coincident with carrier detect—Off Delay: Coincident with carrier loss—Level −9dBm **4103B** On Delay: Originate: Coincident with carrier in originate Answer: 2.5 sec (+, −.5 sec) after automatic answer. Off Delay: Originate: Coincident with carrier loss. Answer: Coincident with line disconnect. Level −9dBm (factory fixed) **4113B** On Delay: Originate: Coincident with carrier in originate. Answer: 2.5 sec (+, −.5 sec) after automatic answer. Off Delay: Originate: Coincident with carrier loss. Answer: Coincident with line disconnect. Level: −9dBm (factory fixed) **INTERNAL SWITCH OPTIONS: 4102D:** None **4103B** Abort Timer 18 Sec. Abort Timer: 6 sec. Carrier Loss Disconnect: 1 sec. Disconnect Enable: On/Off. Tone Ringer: On/Off. Force DTR: On/Off **4113B** Abort Timer: 18 sec. Abort Timer: 6 sec. Carrier Loss Disconnect: 1 sec. Disconnect Enable: On/Off. Tone Ringer: On/Off. Force DTR: On/Off. **ADDITIONAL FEATURES:** 4102D: None, 4103B: None, 4103B, 4113B: Built in audio ringer with level adjustment. **TERMINAL INTERFACE:** EIA RS232C Outputs: Mark (Off): −8v, Space (On): +8V Input¢: Mark (Off): −3 to −25V, Space (On): + to +25V, TTY, 20MA active current loop: Mark: 20MA, Space: no current. Pin Connects: (Write for additional specifications) **POWER REQUIREMENTS:** 117VAC +, −10%, 60Hz, 16 W **STANDARD CABLES AND ACCESSORIES:** UL listed wall-mount 24VAC power transformer. Plug-to-plug 6-foot telephone modular cable. **OPTIONAL CABLES:** EIA RS232C 6-foot, 8-wire cable, TTY 6-foot, 4-wire cable, EIA RS232C/TTY Y-cable, Modular T-adapter (connects modem and telephone to phone line) **SIZE:** 12″L, 5⅜″W, 2¼″H **WEIGHT:** 1 Lb. **SHIPPING WEIGHT:** 3 Lbs.

WE ACCEPT VISA, and MASTER-CHARGE

PRIORITY ONE ELECTRONICS

TRS-80* MEMORY EXPANSION KITS

You're busy writing programs. Suddenly, in the middle of a line you run out of memory space!
In the words of a famous San Francisco detective,
"What will you do? What will YOU do?"

Now that you have discovered some of the things your TRS-80 can do for you games, business, education—you want to do more. You want to go beyond the simple program, but when you type in the PRINT MEM command, there is no memory space left. What will you do?*

If you're smart, you will do what thousands of other TRS-80 owners have done. You will dash off an order to Priority 1 Electronics for our 16K Memory Expansion Kits. Don't pay a penny more or settle for anything less. Our Memory Expansion Kits come complete with eight factory prime 16K dynamic RAM chips, easy to follow step-by-step instructions, and a 100% guarantee. What more could you ask for?*

TRS-16KEXP This kit allows you to expand from 16K to 32K or from 32K to 48K in your Expansion Interface **$50.00**

TRS-16KEY This kit includes the programming jumpers necessary to expand from 4K to 16K in your TRS-80* Keyboard **$53.00**

NOTE: Apple owners, this terrible fate can happen to you, too. Order our Apple II** Memory Expansion Kit.

APL-16K **$50.00**

THE PRIORITY 1 ELECTRONICS MEMORY EXPANSION KIT
Don't program without it.

*TRS-80 is a registered trademark of Tandy Corp.
**Apple II is a registered trademark of Apple Computer Corp.

MODEL T2400AA

The Mini-8100S is an S-100 size card providing a compact S-100 adapter for the TRS-80™ computer. Designed for those who possess or will possess an S-100 mainframe. It has the same adapter circuitry as the Mini-8100, but plugs exactly into an S-100 motherboard.

Features:
- S-100 Bus Interface
- Fully Socketed
- Solder Mask and Silk Screened Component Legend
- Not Recommended for Use With Expansion Interface

Ordering Information:	List	Our Price
CCS-T2400AA Mini-8100S	$125.00	**$112.61**

MODEL T5400AA

The Mini-8100 opens up the vast world of S-100 products for your TRS-80™ Computer, providing an S-100 Bus adapter/motherboard for memory expansion and extended I/O capabilities. This combination four-slot motherboard and S-100 interface connects to the TRS-80 or expansion interface.

Features:
- S-100 Bus Interface
- 4-Slot Motherboard
- Fully Socketed
- Solder Mask and Silk Screened Component Legend

Ordering Information:	List	Our Price
CCS-T5400AA Mini-8100 Assembled & Tested	$155.00	**$139.60**

MODEL T5416ABA

The 8100 allows a Radio Shack TRS-80 computer to be interfaced to the popular S-100 Bus for memory expansion and extended I/O capabilities.

The 8100 opens up a whole new world of peripheral devices to the TRS-80 owner. For example, you can now easily add more memory, floppy disc systems, PRCK boards, printer interfaces, multi-purpose I/O boards, AC device controllers, and a whole host of other varied peripherals.

The 8100 has its own built-in 6-slot motherboard which includes our unique card guide system which keeps the boards in their places.

The 8100 is designed to sit on a table top next to your TRS-80 and connects to it via a ribbon cable. A second TRS-80 connector allows other TRS-80 devices to be connected at the same time.

Not only do you get an S-100 bus interface and motherboard, but the 8100 has more on-board options.

Features:
- S-100 Bus Interface
- 6-Slot Motherboard
- Accepts 8, 16K or 4K Dynamic RAMs
- Programmable Baud Rate Selection
- Selectable Even, Odd or No Parity
- †Serial RS-232-C/20 ma Current Loop
- †Parallel Input and Output
- Addressable in 4K Blocks Via Dip Switches
- DCE or DTE Jumper Selectable
- 5-, 6-, 7- or 8-Bit Word Length with 1-, 1½- or 2-Stop Bits

Ordering Information:	List	Our Price
CCS-T5416ABA Complete 8100 Assembled & Tested	$375.00	**$337.00**

NEWDOS/80

A NEW ENHANCED NEWDOS FOR THE TRS-80® MODEL 1 FOR THE 1980s

Apparat Inc. announces the most powerful Disk Operating System for the TRS-80.® It has been designed for the sophisticated user and professional programmer who demands the ultimate in disk operating systems.

NEWDOS/80 is not meant to replace the present version of NEWDOS 2.1 which satisfies most users, but is a carefully planned upward enhancement, which significantly extends NEWDOS 2.1's capabilities. This new member to the Apparat NEWDOS family is upward compatible with present NEWDOS 2.1 and is supplied on Diskette, complete with enhanced NEWDOS+ utility programs and documentation. Some of the NEWDOS/80 features are:
- New BASIC commands that support files with variable record lengths up to 4095 Bytes long.
- Mix or match disk drives. Supports and track count from 18 to 80. Use 35, 40 or 77 track 5" mini disk drives or 8" disk drives, or any combination
- A security boot-up for BASIC or machine code application programs. User never sees "DOS READY" or "READY" and is unable to "BREAK," clear screen, or issue any direct BASIC statement including "LIST."
- New editing commands that allow program lines to be deleted from one location and moved to another to allow the duplication of a program line with the deletion of the original.
- Enhanced and powerful RENUMBER that allows relocation of subroutines.
- Powerful program chaining, & enhanced debug.
- Device handling for routing to display and printer simultaneously.
- CDE function; simultaneous striking of the C, D and E keys will allow user to enter a mini-DOS to perform some DOS commands without disturbing the resident program.
- Upward compatible with NEWDOS 2.1 and TRSDOS 2.3
- Includes Superzap 3.0 and all Apparat 2.1 utilities.

Shipping weight: 3 lbs.

APP395 .... **$149.00**
APP395M NEWDOS/80 when purchased with any Micropolis Drive on the same order **$100.00**

NEW FROM SAMS BOOKS NOW IN TWO VOLUMES

TRS-80 INTERFACING
BOOK 1 AND BOOK 2

SHIPPING WEIGHT 1 LB.

INTRODUCTION, BOOK I

The Radio Shack TRS-80 computer provides abundant computer power at moderate cost, allowing broad versatility. Like many TRS-80 users, you, perhaps, are content to apply your computer to home, business, or personal uses where standard peripherals, such as keyboard, display, cassette recorder, etc., are appropriate. Or, maybe, you are like a growing number of others who desire to have their TRS-80 actually control external devices, sense external events, and perhaps, even transfer information between the computer and peripheral circuits of their own design. If you are among the latter group, then TRS-80 interfacing will introduce you to the internally generated signals available and show you how each can be used under BASIC language program control. The book is for the reader with a good understanding of commands in Level II BASIC and would be found midway between the beginning computer user and the advanced programmer/hardware designer. 190 pages.

SAM 21633 .... **$8.95**

ADVANCED TECHNIQUES, BOOK 2

This book will introduce you to some of the advanced interfacing techniques that will allow you to do real things with your Radio Shack TRS-80 computer. You will find that these techniques can be applied to computer applications in your home, research laboratory, or school laboratory. You will learn how the computer can be used to drive high-current and high-voltage loads, to generate voltage and current signals, and to measure unknown voltages and currents. Since many computers are being used to control devices that are located some distance from the central processing unit, the author has included a chapter on serial communication and remote control. Remote control circuits are provided that allow you to control Universal Asynchronous Receiver/Transmitter (UART) chips, analog-to-digital converters, digital-to-analog converters, and other devices that can be located from several feet from the computer to several thousand feet from it.

SAM 21739 .... **$9.95**

MICROPOLIS DISK DRIVES FOR THE TRS-80 COMPUTER

- Plug-In Compatible with the TRS-80 Disk Drive
- 35 Tracks
- Faster Than a Speeding Bullet (30ms)
- Available in 77 Track
- Soft Sector
- Metallic Gray and Black Cabinet
- Power Supply, Fuse, and Line Cord
- Single- or Dual-Drive Configuration

		LIST PRICE	OUR PRICE
SINGLE DRIVES			
MCP 10271	35 TRACK	$ 545.00	CALL
MCP 10272	77 TRACK	$ 645.00	**$ 575.00**
DUAL DRIVES			
MCP 10371	35 TRACK	$1195.00	CALL
MCP 10372	77 TRACK	$1395.00	**$1195.00**

AS FEATURED IN TRS-80 INTERFACING

Interface your TRS-80 to the "real world" the faster and easier way.

The Jumper. A 24" 40-conductor flat ribbon cable assembly with a socket connector on one end, a card-edge connector on the other. It's preassembled and every line is pretested.

APP-924150-24 .... **$11.95**

The Header. Copper alloy 770 for instant plug-in access to the PC board.

APP-923875-R **$3.49**

The Solderless Breadboard. It's our famous "super-strip" for unlimited freedom in the layout and implementation of your circuits.

APP-923252-R **$17.00**

THE VISTA V-80 DISK DRIVE SYSTEM

- 23% more storage capacity than TRS-80
- 120 day warranty
- 40 track patch at NO CHARGE

THE VISTA V80:

widen the ability of your TRS-80

The Vista V80 Mini Disk System is the perfect way to widen the capabilities of your TRS-80™ Micro-computer. Quickly and inexpensively. Our $395 price tag is about $100 less than the Radio Shack equivalent. Our delivery time is immediate. And our system is fully interchangeable. That's just the start.

It will give you 23% more storage capacity by increasing useable storage from 55,000 to 65,000 bytes per drive with our new software patch.

It can work 8 times faster than the TRS-80 Mini-Disk system, because track-to-track access is 5ms versus 40ms for the TRS-80. You can realize this added speed once the new double disk expansion interface is available without expensive modification of the existing unit.

It has a better warranty than any comparable unit warranty available—a full 120 days on all parts and service. When you consider how much more goes into the Vista V80, that shows a lot of faith in our product.

A full 3 amp power supply means you have 2½ times the power necessary to operate the V80, and full ventilation insures that there will be no problems due to overheating.

The Vista V80 Mini Disk System requires Level II Basic with 16K RAM Expansion Interface (it operates from the Radio Shack interface system). It comes complete with a dependable MPI Minifloppy disk drive, power supply, regulator board and vented case. It's shipped to you ready to run—simply take it out of the box and plug it in. You're in business. From the company that means business—Vista Computer Company.

DISKETTES

Part No.	Sectoring	Application	Box of 10
VRB-MD 525-01	Soft Sector	TRS-80, Apple	$ 29.95

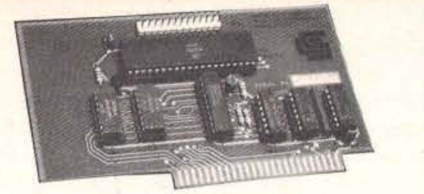
VIS-V-80 Single drive system **$395.00**
VIS-V-80-2 Two drive system **$770.00**
VIS-V-80-4 Four drive system **$1450.00**
PRI-34CEEE-2 Two drive cable **$29.95**
PRI-34CEEE-4 Four drive cable **$39.95**

Four Drive and Two Drive Systems come complete with Data Cable.

WE ACCEPT VISA, and MASTER-CHARGE

PRIORITY ONE ELECTRONICS

APPLE PERIPHERALS



PARALLEL INTERFACE

- Two bi-direction 8 bit buses for interface to periferals
- Two programmable control registers
- Two programmable data direction registers
- Four individually controlled interrupt input lines; two useable as peripheral control outputs
- Handshake control logic for input and output peripheral operation
- High impedance 3 state and direct transistor drive peripheral lines
- Programmable interrupts
- CMOS drive capability on side A peripheral lines
- 2 TTL drive capability on all A and B side buffers
- Power down ROM
- Supports interrupt daisy chain
- Allows DMA daisy chain
- 256 bytes firmware (ROM) or softwares (RAM) space available

	List	Our Price
CCS-7720A Assembled (with cable and Software) 1 LB.	$119.95	**$107.95**
CCS-7325A Cable Assembly, 25 P "D" to dual 13P header		**$24.95**
CCS-7620A A Firmware ROM, General purpose		**$24.95**

CENTRONICS PARALLEL INTERFACE

The Model 7728 interfaces high-speed Centronics-type parallel interface printers to the Apple II* computer. The 7728 provides eight-bit parallel data output bus, four-bit status input, Data Strobe and Acknowledge handshake signals, and printer Reset signal to ensure compatibility with a wide selection of printers.

Driver firmware is provided in an on-board 256-byte ROM.

- Pin-for-pin compatibility with Centronics printers
- Handshaking provided with Data Strobe and Acknowledge signals
- ASCII character output controlled by on-board ROM
- Eight-bit parallel output bus
- Four-bit status input
- Printer Reset signal
- Compatibility with standard Apple II printer command
- Flexible character/line format
- Auto line feed and video echo are under software control
- Interrupt daisy chain supported with arbitration logic
- DMA daisy chain pass-through provided
- Software-programmable interrupts
- Jumper-selectable IRQ signal
- Berg plug jumpers provided for all jumper-selectable features

	List	Our Price
CCS7728 Assembled	$119.95	**$107.95**
CCS-7340A Cable for the Integral Data IDS-440 Paper Tiger		**Call**
CCS-7379A Cable for Centronics printers, the Okidata Microline 80, or the Microtek MT-88T		**$31.95**
CCS-7388A Cable for the MPI 88T		**$29.95**
CCS-7001A RAM pack for ROM replacement		**$19.95**
CCS-7601A Unburned ROM pack		**$19.95**

SYNCHRONOUS SERIAL INTERFACE

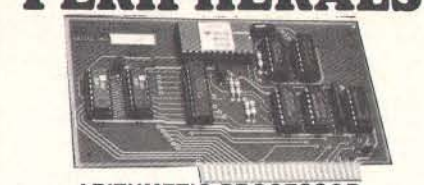
- Conforms to RS-232-C (configuration A thru E)
- Supports half or full duplex operation
- DTE type configuration
- Failsafe RS-232-C operation
- 14 STD CLK rates 50-19.2K BAUD plus EXT CLK
- BAUD rates dip switch selectable
- All BAUD rates crystal controlled
- Programmable interrupts from transmitter, receiver, and error detection logic
- Programmable SYNC code register
- Standard synchronous signaling rate per RS-269/ANSI X3.1-1975
- Peripheral/modem control functions
- Three bytes of fifo buffering on both transmit and receive date
- 7, 8, or 9 bit transmission
- Optional odd, even, or no parity bit
- Parity, overrun, and overflow status checks
- Power down prom
- 256 bytes firmware (ROM) or software (RAM) space available
- Supports interrupt daisy chain
- Allows DMA daisy chain

	List	Our Price
CCS-7712A Assembled (with cable & software) 1 LB.	$179.95	**$159.00**
CCS-7325A Cable Assembly, 25P "D" to dual BP header		**$24.95**
CCS-7001A RAM (for ROM replacement)		**$19.95**
CCS-7601A ROM (unburned, empty)		**$19.95**
CCS-7610A Firmware ROM General Purpose		**$24.95**

APPLE II® EXTENDER BOARD

A handy tool when debugging or testing modules in the APPLE II® computer.

CCS-7520A Assembled **$24.95**



ARITHMETIC PROCESSOR

- Based on AMD AM9511 device
- Fixed point 16 and 32 bit operation
- Floating point 32 bit operation
- Binary data formats
- Add, subtract, multiply, and divide
- Trigonometric and inverse trignometric functions
- Square roots, logarithms, exponentiation
- Float to fixed and fixed to float conversions
- Stack oriented operand storage
- Programmed I/O data transfer
- End signal selectable interrupt
- Supports interrupt daisy chain
- Allows DMA daisy chain
- Powered down ROM
- 256 Bytes firmware (ROM or software (RAM) space available

	List	Our Price
CCS-7811B For use with Apple II	$399.95	**$359.00**
CCS-7001A RAM (for ROM replacement)		**$19.95**
CCS-7601A ROM (unburned, empty)		**$19.95**
CCS-7811C For use with Apple II plus		**$359.00**

ASYNCHRONOUS SERIAL INTERFACE

- Parity, overrun, and framing error checks
- Optional divide by 16 clock mode
- False start bit detection
- Software programmable interrupts
- Data double buffered
- One or two stop bit operation
- Power down PROM
- 256 bytes firmware (ROM) or software (RAM) space available
- Supports interrupt daisy chain
- Allows DMA daisy chain
- 134.5 BAUD available for selectric interface
- Conforms to RS-232-C (configuration A thru E)
- Supports half or full duplex operation
- DCR type interface
- Failsafe RS-232-C operation
- 14 STD CLK rates 50-19.2K BAUD plus EXT CLK
- BAUD rates dip switch selectable
- All BAUD rates crystal controlled except EXT
- 8 and 9 bit transmission
- Optional even, odd, and no parity bit
- Programmable control register

	List	Our Price
CCS-7710A Assembled (with cable & software) 1 LB	$159.95	**$159.00**
CCS-7325A Cable assembly, 25P "D" to dual 13P header		**$24.95**
CCS-7001A RAM (for ROM replacement)		**$19.95**
CCS-7601 ROM (unburned, empty)		**$19.95**
CCS-7610A Firmware ROM General Purpose		**$24.95**

GPIB INTERFACE

With the Model 7490 General Purpose Instrument Bus Interface, your Apple II® computer can operate with instruments which fall under the IEEE 488-1978 communication bus standards. Up to fifteen inter-connected controllers, talkers, and/or listeners can be interfaced on the bus. Such devices include counters, signal generators, digital multimeters, and color graphics output devices.

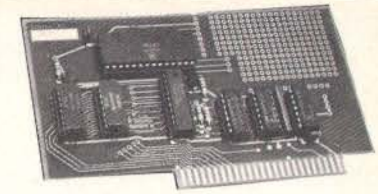
- Controller/talker/listener functions of IEEE 488 standards implemented
- Capability of handling up to 15 interconnected devices on the bus
- Five bus management lines and three data-byte-transfer lines provided
- Full buffering of all 16 signal lines
- Bi-directional 8-bit data bus
- An on-board parallel interface adapter which contains control, data direction, and data output registers for reliable operation
- Data transferring performed asynchronously
- Full handshaking with peripherals
- DMA daisy chain pass-through support
- Allowance for interrupt daisy chain
- Software-programmable interrupts
- Operation in a paging mode with eight 256-byte pages of firmware
- Controller firmware stored in a 2K x 8-bit EPROM (preprogrammed at the factory)
- 3 foot GPIB-compatible cable provided

	List	Our Price
CCS-7490A Assembled	$300.00	**$269.00**

APPLE MEMORY EXPANSION KITS 4116's RAMS

from Leading Manufacturers. 1,000's of sets sold! 100% guaranteed. (16K x 1 200/250ns)

APL-16K ... **8 for $50.00**



PROGRAMMABLE TIMER MODULE

- Flexible external interface patch area for custom interface applications
- Selectable prescaler on timer 3 capable of 4mhz input
- Programmable interrupts
- Readable down counter indicates counts to go to time-out
- Selectable gating for frequency or pulse width comparison
- Three asynchronous external clock and gate/trigger inputs internally synchronized
- Three maskable outputs to patch area
- Power down ROM
- Supports interrupt daisy chain
- Allows DMA daisy chain
- 256 byte firmware (ROM) or software (RAM) space available

	List	Our Price
CCS-7440A Assembled 1 LB	$114.95	**$103.00**
CCS-7001A RAM (for ROM replacement)		**$19.95**
CCS-7601A ROM (Unburned, empty)		**$19.95**

PROM MODULE

The 7114A PROM MODULE permits the addition or replacement of the Apple II firmware without the physical removal of the Apple II ROMS. This allows software/firmware replacement, change, and/or patch to be made on a ROM or BYTE BASIS. An on-board enable/disable toggle switch is also availale.

- BYTE oriented program overlay
- Selectable prom overlay
- Power down of PROMS
- 14K PROM space available
- Uses +5 volt 2716 type proms
- Allows use of DMA/interrupt daisy chains

	List	Our Price
CCS-7114A Assembled (without PROMS) 1 LB.	$79.95	**$75.00**

3¾ DIGIT BCD A/D CONVERTER

The 7470 allows conversion of a DC voltage to a BCD number for computer monitoring and analysis. Typical inputs would be DC inputs from temperature or pressure transducers.

- Selectable interrupt on end of conversion
- 200US per conversion
- -4 to +4 VDC full scale
- Plus or minus .05% non-linearity
- Plus or minus 1 count quantization
- Correctible offset error
- Temperature coefficient adjustment
- Calibration adjustment
- Input offset adjustment
- Floating inputs
- Overange and sign indicators
- Input filter
- Power down ROM
- Supports interrupt daisy chain
- Allows DMA daisy chain
- 256 byte firmware (ROM) or software (RAM) space available

	List	Our Price
CCS-7470A Assembled and Calibrated 1 LB.	$111.95	**$99.95**

CCS-7001A RAM $19.95 ....CCS-7601 ROM $19.95

CALENDAR/ CLOCK MODULE

The Model 7424 Calendar/Clock Module for the Apple II* computer provides accurate real time counting for a wide variety of applications. The 7424 can be software-programmed for either a 12-hour or 24-hour format, and automatically adjusts February to 29 days for leap years. Each decimal digit of data is separately addressed, allowing the user complete freedom of data format. An optional back-up battery maintains accurate time counting when the Apple is powered down or during power outages. To prevent accidental setting of the clock, the time-setting signal to the clock can be jumper-disabled.

- MSM5832 Clock to count seconds, minutes, hours, days of week and month, months, and years
- Berg plug jumpers provided for all user-selected features
- Optional back-up battery provided for maintaining time during power-down of the Apple or during power outage
- 12 hour or 24 hour format
- Automatic adjustment of February to 29 days for leap years
- Jumper-enabled time setting to prevent accidental timing setting
- BASIC program listing provided for setting time, day, month, and year
- Interrupt daisy chain arbitration
- DMA daisy chain pass-through
- Jumper-selectable IRQ generation at hour, minute, second, or millisecond intervals
- Low-power Schottky devices
- Jumper-selectable 256-byte ROM or RAM logic
- 3 jumper-selectable CCS drivers and space for user-burned driver on 1K EPROM

	List	Our Price
CCS-7424A Assembled	$125.00	**$109.95**

PRIORITY ONE ELECTRONICS

3M Scotch® BRAND

If it's worth remembering, it's worth Scotch Data Recording Products.

DISKETTES

Part No.	Sides/ Density	Sectoring	List Price Box of 10	Price Box of 10
		8"		
MMM-740-OP	1/single	Soft-IBM	$50.00	$39.95
MMM-740/2-OP	2/single	Soft-IBM	$88.00	$75.00
MMM-740-32P	1/single	32-Shugart 801	$50.00	$39.95
MMM-740/2-32P	2/single	32-Shugart 801	$88.00	$75.00
MMM-741-0	1/double	Soft-Shugart Dbl	$70.00	$59.00
MMM-743-0	2/double (2 head)	Soft-IBM	$100.00	$88.00
		5"		
MMM-744-OK	1/single	Soft-Shugart SA400 (TRS-80)	$50.00	$39.95
MMM-744-10K	1/single	Soft/10 SA400	$50.00	$39.95
MMM-744-16K	1/single	Soft/16 Micropolis	$50.00	$39.95

BE SURE TO ORDER YOUR KASSETTE/10 LIBRARY CASE BELOW

Verbatim

Removable magnetic storage media

5¼ DISKETTES

Part No.	Sectoring	Application	# of Heads	Box of 10
VRB-MD 525-01	Soft Sector	TRS-80 Apple	1	$29.95
VRB-MD 525-10	Hard 10 Sector	North Star	1	$29.95
VRB-MD 525-16	Hard 16 Sector	Micropolis	1	$29.95
VRB-MD 540-01	Soft Sector	40 Track Cert.	1	$38.95
VRB-MD 540-10	Hard 10 Sector	40 Track Cert.	1	$38.95
VRB-MD 540-16	Hard 16 Sector	40 Track Cert.	1	$38.95
VRB-MD 550-01	Soft Sector	40 Track Cert.	2	$55.00
VRB-MD 550-10	Hard 10 Sector	40 Track Cert.	2	$55.00
VRB-MD 550-16	Hard 16 Sector	40 Track Cert.	2	$55.00
VRB-MD 577-01	Soft Sector	77 Track Cert.	1	$48.00
VRB-MD 577-10	Hard 10 Sector	77 Track Cert.	1	$48.00
VRB-MD 577-16	Hard 16 Sector	77 Track Cert	1	$48.00

VRB-MD 577 Series comes with reinforced hub ring mounted.

8" DISKETTES

VRB-FD32-1000	Hard Sector	Shugart 801R	1	$37.00
VRB-FD34-1000	Soft Sector	IBM 3740	1	$37.00
VRB-FD32-2000	Hard Sector	Flippy	1	$66.00
VRB-FD34-2000	Soft Sector	Flippy	1	$66.00

ALL VERBATIM DISKETTES ARE DOUBLE DENSITY CERTIFIED

HOUSINGS FOR MAGNETIC MEDIA

- Capacity: 50 diskettes
- Vinyl dust cover
- 5 pressboard dividers
- 3 metal dividers
- Will cross-file in a standard letter-size file drawer

P.E.C.

Shipping weight: **MM-1 5 lbs. FM-2 9 lbs.**

MMM-MM1	(for 5¼" diskettes)	$28.50
MMM-FM-2	(for 8" diskettes)	$37.00

THE PROTECTOR.

- Capacity: 50 Diskettes
- Sizes for 8" and 5¼" Diskettes
- Rugged, smoked plexiglass construction
- Helps keep dust, dirt and grime from contaminating valuable diskettes

Shipping Weight: **VRBPRT5** 3 lbs. **VRBPRT8 5 lbs.**

Part Number	Description	Price
VRB-PRT5	For 5¼" Diskettes	$29.95
VRB-PRT8	For 8"	$39.95

DON'T LET DUST OR SCRATCHES DIRTY YOUR DATA!

3M CRASHGUARD 14" HARD DISKS

SOMETHING YOU CAN DEPEND ON

Each "Scotch" brand top loading and front loading cartridge is coated with CRASHGUARD®—3M's exclusive protective disk coating which guards against oxide build-up on the read/write heads and the hazards of head-to-disk interference. You can depend on CRASHGUARD to protect you from data checks (phantom errors), damaged heads, computer downtime and data loss.

Shipping weight: 10 lbs

Part No.	Sectoring	Application	Price
MMM-910-12	12 Sector	DEC PDP-11	$100.00
MMM-910-16	16 Sector	DEC PDP-8	$100.00
MMM-910-24	24 Sector	TI 980	$100.00
MMM-920-12	12 Sector	CDC HAWK	$120.00
MMM-920-24	24 Sector	Microdata 2754	$120.00
MMM-9331-48	48 Sector	Datapoint 10/MB	$160.00

PREVENT THE USE OF DAMAGED DISK CARTRIDGES WITH

SHOCKWATCH

Media equipped with Shockwatch give an immediate signal that a shock great enough to cause a malfunction has been experienced.

The dramatic change from clear to vivid red attracts the attention of all operating personnel and assures its removal from service **before** significant financial loss.

The Shockwatch impact detector is a length of precision-bore capillary tubing, containing a precise thread of red liquid. Mechanical shock or acceleration exceeding a predetermined level disrupts the relationship between the liquid and the tube. Immediately, vivid red liquid fills the tube. This simple change in appearance, from clear to brilliant red, is a positive indicator that the cartridge or pack has received a shock sufficient to cause probable damage.

MMM-SWCART $8.95

Shipping weight: 1 lb.

HARDHOLE HUB REINFORCING DISK PROTECTORS

- Protects disks and diskettes from wear!
- Repairs damaged disks!

Now you can save those "ruined" disks with simple insertion of our DISK PROTECTOR! Inserting a protector on a new disk will increase its life times over! Easy to install, just slip a protector ring onto the precision tool, then slip on the disk! No glueing, no drying time, no heat! One tool lasts indefinitly, each disk requires one protector ring.

Part No.	Description	Price
VRB HDHL5	5¼" Diskette Protector Ring (pkg. 50)	$3.50
VRB HDHL5A	5¼" Applicator Tool	$5.00
VRB HDHL8	8" Diskette Protector Rings (pkg. 50)	$7.00
VRD HDHL8A	8" Applicator Tool	$10.00

Shipping Weight: 4 oz.

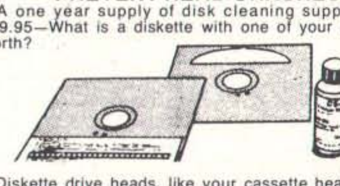
KASETTE/10 LIBRARY CASE

Shipping Weight: 8" 2 lbs. 5¼" 1 lb.

Part Number	Size	Color	Price
MMM-KS10GY	8"	Grey	$4.50
MMM-KS10BK	8"	Black	$4.50
MMM-KS10BU	8"	Blue	$4.50
MMM-KS10BG	8"	Beige	$4.50
MMM-KM10GY	5¼"	Grey	$4.25
MMM-KM10BK	5¼"	Black	$4.25
MMM-KM10BU	5¼"	Blue	$4.25
MMM-KM10BG	5¼"	Beige	$4.25

PREVENT HEAD CRASHES

A one year supply of disk cleaning supplies cost $29.95—What is a diskette with one of your programs worth?



Diskette drive heads, like your cassette heads, need periodic mantenance to assure efficient and error-free operation. Unlike other peripheral devices, the read/write head(s) on diskette drives are extremely difficult to clean without partially disassembling the drive. The unique concept of the diskette head cleaning kit allows the user to clean the drive heads without dissembly in just minutes. Available for 8" or 5¼", both single and double sided disk drives. Kit contains 2 cleaning diskettes, a 4 oz. bottle of CS-85 cleaning solution and easy-pour dispenser. Wt. 12 oz. A 1 year supply if you clean your disk once a week.

Catalog No.	Description	Price
VRB-FD08	8" Disk Drive Cleaning Kit	$29.95
VRB-FD05	5¼" Disk Drive Cleaning Kit	$29.95

3M SCOTCH® BRAND PERSONAL COMPUTING TAPE WITH LEADER

MMM-PCC-10	10 Minute	$1.30
MMM-PCC-20	20 Minute	$1.40
MMM-PCC-30	30 Minute	$1.50

PLEASE ORDER BY PART NUMBER

PRIORITY ONE ELECTRONICS

MICROCOMPUTER PRODUCTS

8080 SERIES

PART NO.	DESCRIPTION	PRICE
INS 808AN	8 BIT CPU	$6.50
DP8212N	8 BIT I/O PORT	$2.95
DP8214N	PRIORITY INTERUPT CONTROL	$5.25
DP8216N	BI-DIRECTIONAL BUS DRIVER	$2.95
DP8224N	CLOCK GEN AND DRIVER (2MHz)	$3.25
DP8224-4N	CLOCK GEN AND DRIVER (4MHz)	$9.95
DP8226N	INV BI-DIRECTIONAL BUS DRIVER	$3.50
DP8228N	SYSTEM CONTROLLER & BUS DRIVER	$5.55
DP8238N	SYSTEM CONTROLLER & BUS DRIVER	$5.55
INS8250N	ASYNCH COMM ELEMENT	$15.00
INS8251N	PROGRAMMABLE COMM INT.	$7.50
INS8253N	PROGRAMMABLE INTERNAL TIMER	$17.95
INS8255N	PROG. PERIPHERAL INTERFACE	$6.80
INS8257N	PROG. DMA CONTROLLER	$16.45
INS8259N	PROG. INTERUPT CONTROLLER	$18.00

Z80 SERIES

PART NO.	DESCRIPTION	PRICE
Z80A	8 BIT CPU (4MHz)	$14.95
Z80PIO	PARALLEL INTERFACE (2.5MHz)	$9.95
Z80APIO	PARALLEL INTERFACE (4MHz)	$14.95
Z80CTC	CTC (2MHz)	$9.95
Z80ACTC	CTC (4MHz)	$13.95
Z80DMA	DMA CONTROLLER (2MHz)	$39.95
Z80ADMA	DMA CONTROLLER (4MHz)	$45.00
Z80SIO0	SERIAL INTERFACE (2MHz)	$56.00
Z80ASIO0	SERIAL INTERFACE (4MHz)	$59.95
Z80SIO1	SERIAL INTERFACE (2MHz)	$56.00
Z80ASIO1	SERIAL INTERFACE (4MHz)	$59.95
Z80SIO2	SERIAL INTERFACE (2MHz)	$56.00
Z80ASIO2	SERIAL INTERFACE (4MHz)	$59.95

6502 SERIES

PART NO.	DESCRIPTION	PRICE
6502	8 BIT CPU	$12.95
6502A	8 BIT CPU	$18.95
6520	PIA	$8.95
6522	PIA	$10.95
6530-002	RAM/ROM I/O TIMER	$21.95
6530-003	RAM/ROM I/O TIMER	$21.95
6530-004	TIM, RAM/ROM	$21.95
6530-005	RAM/ROM I/O TIMER	$21.95
6532M	RAM/ROM I/O TIMER	$21.95
6551M		$21.95

6800 SERIES

PART NO.	DESCRIPTION	PRICE
MC6800P	8 BIT CPU	$11.95
MC6802P	MPU, CLK, RAM	$17.95
MC6808P	MPU, CLK	$9.95
MC6809P	MICROPROCESSOR	$34.95
MC6821P	PIA	$5.95
MC6828P	PRIOR. INTERUPT ADAPT.	$14.95
MC6840P	PTM	$14.95
MC6845P	CRT CONTROLLER	$31.00
MC6847P	COLOR VIDEO DISPLAY GEN.	$14.95
MC6850P	ASYNCH. COMM. INT. ADAPT.	$5.41
MC6852P	SYNCH. SER. DATA ADAPT.	$5.79
MC6854P	ADVANCED DATA LINK CONT.	$24.95
MC6860P	0-600 BPS MODEM	$10.89
MC6862P	0-2400 BPS MODEM	$12.00
MC6875L	MPU CLOCK GEN.	$7.40
MC66710P	CHAR. GEN ASCII SHFTD W/GRK	$12.50
MC66750P	CHAR. GEN ALPHA, NUM, CTRL CHAR.	$12.50

MOTOROLA, Semiconductors BOOKS AND LITERATURE

STOCK NUMBER	TITLE	PRICE
MOT-B0412	CMOS	$5.95
MOT-B0421	VOLT REG.	$3.95
MOT-B0422	LO POWER SCHOTTKY	$3.95
MOT-B0427	M6800 REF. MANUAL	$3.95
MOT-B0428	UNDERSTAND MICRO	$3.95
MOT-B0435	POWER DATA	$6.50
MOT-B0436	MICRO DATA LIB	$8.95
MOT-B0437	BASIC MICRO AND THE M6800	$15.95
MOT-B0438	PROG. THE M6800 MICRO	$8.95
MOT-B0439	BASIC I/C ENGR. AND THE 6800	$29.95
MOT-B0440	LINEAR INTERFACE	$5.95
MOT-B0441	LINEAR CIRCUIT	$6.50
MOT-CTG12	MASTER SEL. GUIDE	$3.95

OPTO-ISOLATORS

	TYPE	VISO	ICO(MA)	VCEO	1-24	25-99
4N27	NPN	1500V	2.0	30V	$1.10	$ .92
4N28	NPN	500V	2.0	30V	$1.10	$ .92
4N30	NDL	1500V	30	30V	$1.40	$1.10
4N31	NDL	1500V	10	30V	$1.25	$1.00

MOTOROLA Semiconductors

STATIC RAMS

PART NO.	DESCRIPTION	1-7	8-24	25-99	100+
2102AN-L	1024 x 1 450NS LP	$1.60	$1.40	$1.15	$1.00
2102AN-2L	1024 x 1 250NS LP	$1.75	$1.60	$1.25	$1.10
2114N-3L	1024 x 4 300NS LP	$6.95	$5.65	$5.00	$4.50
5257N-3L	4096 x 1 300NS LP	$9.50	$6.90	CALL	CALL
7489	16 x 4 60NS TTL	$2.99	$	$2.25	$1.75

DYNAMIC RAMS

PART NO.	DESCRIPTION	1-7	8-24	25-99	100+
4116AC20	16K x 1 200NS	$7.95	$6.25	$	$5.20
	CALL FOR VOLUME PRICING ON 4116 DYNAMIC RAMS				
5290J-2	16K x 1 150NS (4116)	CALL	CALL	CALL	CALL

E PROMS

PART NO.	DESCRIPTION	1-7	8-24	25-99
1702A	256 x 1US UV ERASABLE	$ 6.95	$ 6.00	CALL
2708	1024 x 8 450NS UV ERASABLE	$ 9.00	$ 8.50	CALL
TMS2716	3 SUPPLY 2K x 8 EPROM	$19.95	CALL	CALL
2716	5V ONLY 2K x 8 EPROM	$20.00	$18.00	$15.00
2732	5V ONLY 4K x 8 EPROM	CALL	CALL	CALL

BUS DRIVERS & RECEIVERS

PART NO.	DESCRIPTION	PRICE
8T26N	QUAD BUS DRV/RCVR EXT	$2.75
8T28N	QUAD 3 STATE BUS DRV/RCVR	$2.75
8T96N	HEX INVERTER TRI-STATE	$1.95
8T97N	HEX BUFFER TRI-STATE	$1.95
8T98N	HEX INVERTER TRI-STATE	$1.95
8131N	QUAD TRI-STATE LINE RECEIVER	$3.00
MC1488P	RS232 QUAD LINE DRIVER	$1.18
MC1489P	RS232 QUAD LINE RECEIVER	$1.18

UARTS

PART NO.	DESCRIPTION	PRICE
AY51013A	UART 30K BAUD (-12V +5V)	$5.95
TR1602B	UART 20K BAUD (-12V +5V)	$5.95

MEK6800 MICROCOMPUTER MODULES

PART NO.	DESCRIPTION	PRICE
MEK6809D4A	6809 MPU BOARD, CASS INT, 1K RAM, DEBUG MONITOR (MOKEP)	$490.00
MEK6809D4B	SAME AS ABOVE, WITH ADDR & DATA BUFFERS AND RS-232 INTERFACE	$528.00
MEK68KPD	KEYPAD/DISPLAY WITH PWR. SUPPLY FOR MEK6809D4A	$275.00
MEK6802D5	6802 SBC, 25 KEY KEYPAD, 2K MONITOR WITH DEBUG, PWR SUPPLY	$305.00
MEK68IOE	I/O MODULE, CASS INT., RS-232C INT, DIA PORT (MOKEP)	$325.00
MEK68RRE	RAM/ROM MODULE, 1K RAM EXPAN. TO 8K, EDITOR/ASSEMBLER ON ROM (MOKEP)	$469.00
MEK6800ABC	ADAPTER/MOTHERBOARD FOR MEK6800 D2 AND MOKEP PRODUCTS	$215.00
MEK6809EAC	EDITOR/ASSEMBLER FOR THE MEK6809EAC (CASS. 300 BAUD)	$110.00
MEK6800D2A	6800 EVALUATION KIT II, PIA I/O, CASS INT, 256 BIT RAM	$409.00
MEK6802D3	6802 SBC HEX KEYPAD, PARALLEL I/O, MONITOR, 16 BIT TIMER COUNTER	$305.00
MEK6802D3C	SAME AS ABOVE WITHOUT KEYPAD & LED DISPLAYS (MOKEP)	$269.00
MEK68IO	SAME AS MEK68IO LESS RS-232 DRIVER ROM	$305.00
MEK6800AB	SAME AS MEK6800ABC LESS EDITOR/ASSEM CASSETTE	$169.00
MEK68MB5	MOTHERBOARD FOR MOKEP PRODUCTS	$139.00
MEK68MM16	16K RAM FOR THE MOKEP FAMILY	$359.00
MEK68MM32	32K RAM FOR THE MOKEP FAMILY	$479.00
MEK68R2	PROGRAMMABLE CRT INTERFACE FOR THE MEK6800AB (MOKEP)	$359.00
MEK68R2M	PROGRAMMABLE CRT INTERFACE FOR THE MEK6802D3	$359.00
MEK68CC	CARD CAGE FOR THE MEK68MB5 MOTHERBOARD	$ 85.00
MEK68RR	SAME AS MEK68RRE LESS EDITOR ASSEMBLER ON ROM	$359.00
MEK68WW	WIRE WRAP MODULE FOR THE MEK6800AB	$ 69.00
MEK68WW01	WIRE WRAP MODULE FOR THE MEK68MB	$ 69.00
MEK68EP	EPROM PROGRAMMER FOR THE MOKEP FAMILY	$419.00
MEK68CMB	10 SLOT MOTHERBOARD AND CARD CAGE FOR THE MOKEP FAMILY	$209.00
CHROMA68	6808 SBC WITH COLOR GRAPHICS	$149.00

SPST DIP SWITCHES

PART NO.	NO. OF POSITIONS	PRICE 1-9	10-24	25-99
DIP-SW4	4	$1.50	$1.40	$1.28
DIP-SW5	5	$1.60	$1.49	$1.36
DIP-SW6	6	$1.70	$1.59	$1.45
DIP-SW7	7	$1.80	$1.68	$1.53
DIP-SW8	8	$2.00	$1.86	$1.70
DIP-SW9	9	$2.25	$2.10	$1.92
DIP-SW10	10	$2.50	$2.33	$2.12

CALL FOR QUANTITY PRICING

PRIORITY ONE ELECTRONICS

74LS TTL

PART #	DESCRIPTION	PRICE
74LS00	QUAD 2-IN NAND GATE	$ .40
74LS01	QUAD 2-IN NAND GATE (OC)	.40
74LS02	QUAD 2-IN NOR GATE (OC)	.40
74LS03	QUAD 2-IN NAND GATE (OC)	.40
74LS04	HEX INV	.42
74LS05	HEX INV	.42
74LS08	QUAD 2-IN AND GATE	.40
74LS09	QUAD 2-IN NAND GATE (OC)	.40
74LS10	TRIP 3-IN NAND	.40
74LS11	TRIP 3-IN AND	.42
74LS12	TRIP 3-IN NAND GATE (OC)	.42
74LS13	DUAL SCHMITT TRIG	.78
74LS14	HEX SCHMITT TRIG	2.20
74LS15	TRIP 3-IN NAND	.42
74LS20	DUAL 4-IN NAND GATE	.40
74LS21	DUAL 4-IN NAND GATE	.40
74LS22	DUAL 4-IN NAND GATE (OC)	.40
74LS26	QUAD 2-IN NAND GATE (HV)	.42
74LS27	TRIP 3-IN NOR GATE	.42
74LS30	SNGL 8-IN NAND GATE	.40
74LS32	QUAD 2-IN OR GATE	.46
74LS37	QUAD 2-IN NAND BUFF	.46
74LS38	QUAD 2-IN NAND BUFF (OC)	.46
74LS40	DUAL 4-IN NAND BUFF	.42
74LS42	BCD-DECIMAL DECODER/DRIVER	.92
74LS47	BCD-7 SEGMT DECODER/DRIVER	1.78
74LS48	BCD-7 SEGMT DECODER/DRIVER	1.78
74LS49	BCD-7 SEGMT DECODER/DRIVER	1.78
74LS51	QUAD 2-IN AND-OR-INV GATE	.40
74LS54	QUAD 2-IN AND-OR-INV GATE	.40
74LS55	DUAL 4-IN AND-OR-INV GATE	.40
74LS73	DUAL J-F F/F W/PRESET + CLR	.60
74LS74	DUAL "D" F/F	.58
74LS75	QUAD LATCH	.82
74LS76	DUAL J-K F/F W/PRESET + CLR	.62

74LS TTL

PART #	DESCRIPTION	PRICE
74LS78	DUAL J-F F/F W/PRESET, COMM CLK + CLR	$ .63
74LS83	4-BIT FULL ADD	1.37
74LS85	4-BIT MAG COMP	1.58
74LS86	QUAD EX-OR GATE	.58
74LS90	BINARY COUNTER	.95
74LS93	BINARY COUNTER	.95
74LS107	DUAL J-K F/F W/CORNER PWR PINS	.62
74LS109	DUAL J-K F/F POS EDGE	.60
74LS112	DUAL J-K F/F NEG EDGE	.62
74LS113	DUAL J-K F/F NEG EDGE	.62
74LS114	DUAL J-K F/F NEG EDGE	.62
74LS122	RETRIGGERABLE MON MULTIVIBRATOR	1.55
74LS123	DUAL RETRIG. MONO. MULTIVIBRATOR	1.70
74LS125	QUAD BUFF (TS)	.74
74LS126	QUAD BUFF (TS)	.74
74LS132	QUAD SCHMITT TRIG	1.20
74LS136	QUAD EX-OR GATE	.69
74LS138	EXP. SNGL 3/8 DECODER	.95
74LS139	EXP. DUAL 2/4 DECODER	.95
74LS151	SNGL 8-1 MUX	.84
74LS153	DUAL 4-1 MUX	.84
74LS154	SNGL 4-16 DECODER	2.10
74LS155	DUAL 2-4 DEMUX	1.55
74LS156	DUAL 2-4 DEMUX (OC)	1.55
74LS157	QUAD 2-1 MUX	.84
74LS158	QUAD 2-1 MUX (INV OUT)	.95
74LS160	PRESET DECADE CTR	2.20
74LS161	PRESET BINARY CTR	1.20
74LS162	PRESET DECADE CTR (SYN CLR)	2.20
74LS163	PRESET DECADE CTR (SYN CLR)	1.20
74LS164	8-BIT S/R	2.20
74LS168	SYN DECADE U/D CTR	3.75
74LS169	SYN BINARY U/D CTR	3.75
74LS170	4x4 REG FILE	3.15

74LS TTL

PART #	DESCRIPTION	PRICE
74LS173	QUAD "D" REG (TS)	$1.89
74LS174	HEX "D" F/F	2.05
74LS175	QUAD "D" F/F	1.95
74LS190	U/D DECADE CTR	1.20
74LS191	U/D BINARY CTR	1.43
74LS192	U/D DECADE BINARY	3.05
74LS193	U/D BINARY CTR	3.05
74LS196	PRESET DECADE CTR	1.98
74LS197	PRESET BINARY CTR	1.98
74LS221	DUAL MONO. MULTIVIBRATOR (SCH TRG)	1.70
74LS240	OCTAL INV BUS/LINE DRVR	2.81
74LS241	OCTAL BUS/LINE DRVR	2.81
74LS242	QUAD BUS TRSCVR/INV	2.81
74LS243	QUAD BUS TRSCVR	2.81
74LS244	OCTAL 3 STAT DRVR	2.81
74LS245	OCTAL BUS TRSCVR	2.53
74LS247	BCD-7 SEGMT DECODER/DRIVER	1.68
74LS253	DUAL 4-IN MUX (TS)	1.85
74LS257	QUAD 2-IN MUX	1.95
74LS258	QUAD 2/1 MUX	1.95
74LS266	QUAD EX-NOR GATE	.66
74LS279	QUAD SET/RESET LATCH	.88
74LS283	4-BIT FULL ADD	1.76
74LS299	FOUR BIT BINARY COUNTER	2.20
74LS365	HEX BUFF (TS)	.88
74LS366	HEX INV (TS)	.88
74LS367	HEX BUFF (4-2) (TS)	.88
74LS368	HEX INV (4-2) TS	.88
74LS373	TRI-STATE OCTAL D-FLIP-FLOP	2.00
74LS374	TRI-STATE OCTAL D-FLIP-FLOP	2.00
74LS386	QUAD EX-OR GATE	.69
74LS670	4x4 REG FILE (TS)	3.57

74 TTL

PART #	DESCRIPTION	PRICE
7400	QUAD TWO-INPUT GATE	$ .32
7401	QUAD TWO-INPUT GATE (OPEN COLL.)	.32
7402	QUAD TWO-INPUT NOR GATE	.32
7403	QUAD TWO-INPUT GATE (OPEN COLL.)	.32
7404	HEX INVERTER	.36
7405	HEX INVERTER (OPEN COLLECTOR)	.36
7406	30V-40MA HEX INVERTER	.45
7407	30V-40MA HEX BUFFER	.45
7408	QUAD 2-INPUT POSITIVE AND GATE	.32
7409	QUAD 2-INPUT AND GATE (OC)	.32
7410	TRIPLE THREE -INPUT GATE	.32
7411	TRIPLE THREE-INPUT AND GATE	.32
7413	DUAL SCHMITT TRIGGER	.54
7414	HEX SCHMITT TRIGGER	1.25
7416	15V-40MA HEX INVERTER	.45
7417	15V-40MA HEX BUFFER	.45
7420	DUAL FOUR-INPUT GATE	.32
7423	EXPAND DUAL FOUR-INPUT NOR GATE	.36
7425	DUAL FOUR-INPUT NOR GATE	.36
7426	QUAD 2-INPUT INTERFACE NAND GATE	.36
7427	TRIPLE THREE-INPUT NOR GATE	.36
7430	EIGHT-INPUT GATE	.32
7432	QUAD 2-INPUT OR GATE	.36
7437	QUAD TWO-INPUT NAND BUFFER	.42
7438	QUAD 2-INPUT NAND BUFFER (OC)	.42
7440	DUAL FOUR-INPUT BUFFER	.32
7441	BCD-DECIMAL DECODER/DRIVER (NIXIE)	1.30
7442	BCD-TO-DECIMAL DECODER	.77
7445	BCD-TO-DECIMAL DECODER/DRIVER	1.43
7446	BCD-7 SEG DECODER/DRIVER (30 VOLT)	1.32
7447	BCD-7 SEG DECODER/DRIVER (15 VOLT)	1.32
7448	BCD-7 SEG DECODER/DRIVER (ACTIVEHI)	1.32
7450	EXPAN. DUAL AND-OR-INVERT GATE	.32
7451	DUAL AND-OR-INVERT GATE	.32
7453	EXPANDABLE AND-OR-INVERT GATE	.32

74TTL

PART #	DESCRIPTION	PRICE
7454	AND-OR-INVERT GATE	$ .32
7460	DUAL FOUR-INPUT EXPANDER	.32
7470	EDGE-TRIGGERED J-K FLIP-FLOP	.50
7472	J-K MASTER SLAVE FLIP-FLOP	.45
7473	DUAL J-K FLIP-FLOP	.45
7474	DUAL D FLIP-FLOP	.45
7475	QUAD LATCH	.73
7476	DUAL J-K FLIP-FLOP	.45
7483	4-BIT BINARY FULL ADDER	.99
7485	FOUR-BIT MAGNITUDE COMPARATOR	1.30
7486	QUAD EXCLUSIVE-OR GATE	.40
7489	64 BIT RAM O.C.	2.98
7490	DECADE COUNTER	.73
7491	SERIAL-IN, SERIAL-OUT 8-BIT SHIFT REG	.99
7492	DIVIDE-BY-TWELVE COUNTER	.73
7493	FOUR-BIT BINARY COUNTER	.73
7495	4-BIT RIGHT-SHIFT, LEFT-SHIFT REGIST	.88
7496	5-BIT SHIFT REG. (PARA-IN, PARA-OUT)	.99
74107	DUAL J-K FLIP-FLOP	.99
74109	DUAL J-K FLIP-FLOP (FSC 9024)	.50
74121	ONE SHOT	.50
74123	DUAL ONE SHOT	.77
74125	TRI STATE QUAD BUFFER (DM8093)	.59
74126	TRI STATE QUAD BUFFER (DM8094)	.59
74132	QUAD SCHMITT TRIGGER	.99
74141	NIXIE DRIVER	1.35
74145	BCD TO DECIMAL DECODER DRIVER	1.21
74147	10/4 PRIORITY ENCODER	1.98
74148	8/3 PRIORITY ENCODER (9318)	1.98
74150	SIXTEEN LINE MULTIPLEXER	1.50
74151	EIGHT LINE MULTIPLEXER	1.10
74153	DUAL FOUR-INPUT MULTIPLEXER	1.10
74154	4 TO 16 LINE DECODE/DEMUX	1.65
74155	DUAL 2 TO 4 DEMULTIPLEXER	.99
74156	DUAL 2 TO 4 DEMUX (OC)	1.21

74 TTL

PART #	DESCRIPTION	PRICE
74157	QUAD 2-INPUT MULTIPLEXER (9322)	1.10
74160	PRESETTABLE DECADE COUNTER (9310)	1.32
74161	PRESET. DECADE BINARY COUNTER (9316)	1.32
74162	PRESET. DECADE CTR (SYNCH CLEAR)	1.32
74163	PRESET. BINARY CTR (SYNCH CLEAR)	1.32
74164	SERIAL-IN, PARA-OUT 8-BIT SHIFT REG.	1.43
74165	PARA-IN, SERIAL-OUT 8-BIT SHIFT REG	1.43
74166	8-BIT SHIFT REGISTER	1.43
74170	4 x 4 REGISTER FILE	2.42
74173	TRI ST. QUAD K F.-FLOP (DM8551N)	1.80
74174	HEX D FLIP-FLOP	1.21
74175	QUAD D FLIP-FLOP	1.21
74176	PRESET. DECADE COUNTER (DM8280)	1.32
74177	PRESET. BINARY COUNTER (DM8281)	1.32
74180	PARITY GENERATOR/CHECKER	1.21
74181	ARITHMETIC LOGIC UNIT	2.75
74182	CARRY LOOK AHEAD	1.10
74184	BCD-TO-BINARY COUNTER	2.10
74185	BINARY-TO-BCD CONVERTER	2.10
74189	64-BIT RAM (TRI-STATE)	3.65
74190	SYNCH. DECADE UP/DOWN COUNTER	1.43
74191	SYNCH. BINARY UP/DOWN COUNTER	1.43
74192	DECADE UP/DOWN COUNTER (DM8560N)	1.43
74193	BINARY UP/DOWN COUNTER (DM8563N)	1.43
74194	4-BIT, BI-DIREC. UNIV. SHIFT REG	1.10
74195	4-BIT PARALLEL SHIFT REG	1.10
74194	PRESET. DECADE COUNT. (DM8290N0	1.21
74198	PARA-IN, PARA-OUT 8-BIT SHIFT REG	2.20
74199	PARA-IN, PARA-OUT, 8-BIT SHIFT REG	2.20
74251	TRI STATE DM74151 (DM8121)	1.10
74284	4 x 4 MULT. (MOST SIG BIT) T.S.	4.95
74285	4 x 4 MULT. (LEAST SIG BIT) T.S.	4.95
74365	TRI STATE HEX BUFFER (DM8095)	.95
74366	TRI STATE HEX INVERT. (DM8096)	.95
74367	TRI STATE HEX BUFFER (4-2)	.95
74368	TRI STATE HEX INVERT. (4-2)	.95

LINEAR

PART #	DESCRIPTION	PRICE
LM301AN	IMPROVED 709 OP AMP	$ .49
LM307N	IMPROVED 741 OP AMP	.69
LM308N	PRECISION OP AMP LOW POWER	.79
LM311N	VOLTAGE COMPARATOR (DIP)	.84
LM311H	VOLTAGE COMPARATOR (TO-5)	1.42
LM324N	LOW POWER QUAD OP AMP	1.19
LM339N	LOW POWER & OFFSET VOLTAGE OP AMP	.80
LM348N	LOW POWER QUAD 741 OP AMP	1.31
LM358N	SINGLE ENDED LOW POWER DUAL OP AMP	.84
LM380N	2.5 WATT POWER AUDIO AMP	1.49
NE555V	TIMER I.C.	.55
NE555V/10	TIMER I.C. 10 PAK	4.50
NE556A	DUAL 555 TIMER	1.00
NE565A	PHASE LOCKED LOOP	1.49
LM710CN	HI SPEED VOLT COMP. (MC1710CP)	1.18
LM710CH	HI SPEED VOLT COMP. (MC1710CG)	1.20
LM711CN	DUAL V/COMP W/COMM OUTPUT (MC1711CP)	.64
LM711CH	DUAL V/COMP W/COMM OUTPUT (MC1711CG)	1.18
LM733CN	DIFF VIDEO AMP (MC1733CP)	1.00
LM741CN	G.P. OP AMP (MC1741CPI)	.49

PART #	DESCRIPTION	PRICE
LM741CN/10	G.P. OP AMP (10 PAK) (MC1741CPI)	3.99
LM741CH	G.P. OP AMP (MC1741CG)	.79
LM747CN	DUAL 741 (MC1747CP2)	1.00
LM747CH	DUAL 741 (MC1747CG)	1.25
LM748CN	NON-COMP OP AMP (MC1748CP1)	.56
LM748CH	NON-COMP OP AMP (MC1748CG)	.90
MC1330AP	LOW LEVEL VIDEO DET.	1.83
MC1349P	HI GAIN VIDEO IF AMP	1.49
MC1350P	VIDEO IF AMP	1.29
MC1352P	VIDEO IF AMP WITH AGC	1.69
MC1358P	SOUND IF AMP	1.90
MC1408LS	8-BIT D/A CONVERTER	4.69
MC1436CG	HI VOLT OP AMP	4.41
MC1439P1	HI SLEW RATE OP AMP	2.84
MC1445L	HI FREQ OP AMP	2.97
MC1456CP1	LINEAR OP AMP	1.20
MC1458CG	DUAL 741	.98
MC1458CP1	DUAL 741	.69
MC1488P	RS-232 DRIVER	1.18
MC1489	RS-232 RECEIVER	1.18
MC1496P	BAL. MOD-DEMOD	1.31
MC3302P	L/PWR L/OFST QUAD COMP.	.80
MC3310P	WIDE BAND AMP	.98
MC3340P	ELECTRONIC ATTENUATOR	1.50
MC3410P	QUAD OP AMP	.89

LINEAR

PART #	DESCRIPTION	PRICE
MC3403P	QUAD DIFF. OP AMP	1.20
MC3423P1	OVR/VOLT PROT CIRCUIT	1.31
LM3900	QUAD OP AMP	.90
RC4136	QUAD 741	1.79
MC4741CP	QUAD 741	1.40
IC8038	FUNCTION GENERATOR	4.50
MC75450P	DUAL PERIPH. DRIVER (AND)	1.80
MC75451P	DUAL PERIPH. DRIVER (AND)	1.25
MC75452P	DUAL PERIPH. DRIVER (NAND)	1.25
MC75453P	DUAL PERIPH. DRIVER (OR)	1.25
MC75454P	DUAL PERIPH. DRIVER (NOR)	1.25
MC75491P	QUAD LED SEGMENT DRIVER	.91
MC75492P	HEX LED DIGIT DRIVER	1.31

FET LINEAR

PART #	DESCRIPTION	PRICE
LF355N	JFET OP AMP L/CURRENT	$1.49
LF356N	JFET OP AMP WIDEBAND	1.49
LF357N	JFET OP AMP HI-SPEED	1.49

CA, AK, HI, CALL (213) 894-8171

PRIORITY ONE ELECTRONICS

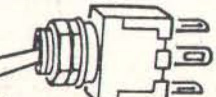
4000 Series CMOS

PART #	PRODUCT DESCRIPTION	PRICE 1-24	PRICE 25-99
4000	DUAL 3-INPUT NOR GATE + INV.	$ .40	$ .33
4001	QUAD 2-INPUT NOR GATE	.40	.33
4002	DUAL 4-INPUT NOR GATE	.40	.33
4006	18-BIT STATIC SHIFT REGISTER	1.42	1.18
4007	DUAL COMPLEMENTARY PAIR PLUS INV.	.40	.33
4008	4-BIT FULL ADDER WITH PAR. CARRY	1.75	1.04
4011	QUAD 2-INPUT NAND GATE	.40	.33
4012	DUAL 4-INPUT NAND GATE	.40	.33
4013	DUAL D FLIP/FLOP	.72	.60
4014	8-BIT STATIC SHIFT REGISTER	1.25	1.04
4015	DUAL 4-BIT STATIC SHIFT REGISTER	1.47	1.22
4016	QUAD BILATERAL SWITCH	.72	.60
4017	DECADE COUNTER/DIVIDER	1.25	1.04
4018	PRESETTABLE DIVIDE BY "N" COUNTER	1.14	.95
4020	14-STAGE RIP.-CARRY BINARY COUNTER	1.47	1.22
4021	8-BIT STATIC SHIFT REGISTER	1.25	1.04
4022	DIVIDE BY 8 COUNTER/DIVIDER	1.25	1.04
4023	TRIPLE 3-INPUT NAND GATE	.40	.33
4024	7-BIT BINARY COUNTER	1.06	.89
4025	TRIPLE 3-INPUT NOR	.40	.33
4027	DUAL J-K FLIP/FLOP	.72	.60
4028	BCD-TO-DECIMAL DECODER	1.02	.85
4029	PRESETTABLE UP/DOWN COUNTER	1.42	1.19
4032	TRIPLE SER ADDER	2.19	1.83
4034	8-BIT SHIFT REGISTER	3.50	2.91
4035	4-BIT SHIFT REGISTER	1.86	1.55
4040	12-BIT BINARY RIPPLE COUNTER	1.47	1.22
4042	QUAD D. LATCH	1.06	.89
4043	QUAD TRI-STATE NOR R/S LATCH	.99	.83
4044	QUAD TRI-STATE NAND R/S LATCH	.99	.83
4046	PHASE LOCKED LOOP	1.57	1.31
4049	HEX INVERTING BUFFER	.72	.60
4050	HEX BUFFER	.72	.60
4051	SINGLE 8-CHANNEL MULTIPLEXER	1.30	1.08
4052	DIFFERENTIAL 4-CHANNEL MULTIPLEXER	1.30	1.08
4053	TRIPLE 2-CHANNEL MULTIPLEXER	1.30	1.08
4066	QUAD BILATERAL SWITCH	.80	.66
4068	8-INPUT NAND GATE	.40	.33
4069	HEX INVERTER	.40	.33
4070	QUAD EXCLUSIVE OR GATE	.40	.33
4071	BUFFERED QUAD 2-INPUT OR GATE	.40	.33
4072	DUAL 4-INPUT OR GATE	.40	.33
4073	TRIPLE 3-INPUT AND GATE	.40	.33
4075	TRIPLE 3-INPUT OR GATE	.40	.33
4076	TRI-STATE QUAD LATCH	1.57	1.31
4077	QUAD ECLSV NOR GATE	.40	.33
4078	8-INPUT NOR GATE	.40	.33
4081	BUFFERED QUAD 2-INPUT AND GATE	.40	.33
4082	DUAL 4-INPUT GATE	.40	.33
4093	QUAD 2-INPUT NAND SCHMITT TRIGGER	.78	.65
4094	8-BIT BUS-COMP SHFT STR LATCH	3.01	2.51
4099	8-BIT ADDRESSABLE LATCH	1.73	1.44
4160	DEC CNTR ASYNC CLR	1.28	1.06
4161	BIN CNTR ASYNC CLR	1.28	1.06
4162	DEC CNTR SYNC CLR	1.28	1.06
4163	BIN CNTR SYNC CLR	1.28	1.06
4174	HEX D FLIP FLOP	$ 1.28	$ 1.06
4175	QUAD D FLIP FLOP	1.28	1.06
4194	4-BIT UNIV. SHIFT REG.	1.42	1.18
4404	PULSE CODE MOD/DEMOD (CODEC)	34.40	32.48
4406	PULSE CODE MOD/DEMOD (CODEC)	43.00	35.83

PRIORITY ONE ELECTRONICS supplies only "B" type, Buffered, CMOS devices if manufactured by Motorola Semiconductor.

PART #	PRODUCT DESCRIPTION	PRICE 1-24	PRICE 25-99
4414	PCM FILTER	23.76	19.80
4408	BIN TO PHONE PLS CONV.	10.04	8.37
4409	BIN TO PHONE PLS CONV.	10.04	8.37
4410	2 OF 8 TONE ENCDR	9.28	7.73
4411	BIT RATE FEQ. GEN.	11.68	9.73
4412	UNIV. LW-SPEED MODEM	14.11	11.76
4415	QUAD PREC. TIMER/DRIVER	5.65	4.71
4419	2 OF 8 KYPD TO BIN ENCOR	3.30	2.75
4443	uP A/D CONVERTER	2.85	2.38
4457	R/C TRANSMITTER	4.50	3.75
4458	R/C TRANSMITTER	9.36	7.80
4469	ADDRESS ASYN RECEIVER/TRANSMITTER	13.37	11.14
4490	HEX CONTACT DE-BOUNCER	2.89	2.40
4495	HEX TO 7 SEG DECODER/DRIVER	2.70	2.25
4500	1-BIT CPU IND. CONT. UNIT	5.46	4.55
4501	TRIPLE GATE	.40	.33
4502	STROBE HEX INV-BUFFER	1.73	1.44
4503	TRI-STATE HEX BUFFER	.84	.70
4506	DUAL EXP AOI GATE	1.00	.83
4508	DUAL 4-BIT LATCH	4.53	.83
4510	BCD UP/DOWN COUNTER	1.47	1.22
4511	BCD-TO-SEVEN SEGMENT DECODER DRIVER	1.44	1.20
4512	8-CHANNEL DATA SELECTOR	1.42	1.18
4513	BCD-7 SEG LTCH/DEC/DRVR RIP-BL	1.91	1.59
4514	4-BIT LATCH 4 TO 16 LINE DECODER HI	2.91	2.43
4515	4-BIT LATCH 4 TO 16 LINE DECODER	2.91	2.43
4516	BINARY UP/DOWN COUNTER	1.59	1.32
4517	DUAL 64-BIT STATIC SHIFT REG	7.03	5.86
4518	DUAL BCO UP COUNTER	1.47	1.22
4519	4-BIT AND/OR SELECTOR	.80	.66
4520	DUAL BINARY UP COUNTER	1.47	1.22
4521	24 STG FREQ DIVIDER	3.56	2.97
4522	DIVIDE-BY-N COUNTER (BCD)	1.81	1.51
4526	DIVIDE-BY-COUNTER (BINARY)	1.81	1.51
4527	BCD RATE MULTIPLIER	1.59	1.33
4528	DUAL MONOSTABLE MULTIVIBRATOR	1.81	1.51
4529	DUAL 4-CHANNEL ANALOG DATA SELECTOR	1.96	1.63
4530	DUAL 5 IN. MAJORITY LOG-GATE	1.22	1.02
4532	8-BIT PRIOR ENCODER	3.01	2.51
4534	REAL TIME 5 DEC CNTR	9.16	7.63
4536	PROG. TIMER	4.86	4.05
4538	DUAL PREC. MONO-MLTVB	1.81	1.51
4539	DUAL 4-CHANNEL DATA SEL/MUX	1.44	1.20
4541	PROG OSC-TIMER	1.80	1.50
4543	BCD TO SEVEN SEGMENT DECODER (LCD)	2.63	2.19
4547	HI-CURRENT BCD/7 SEG DEC/DRVR	1.65	1.38
4551	QUAD 2 IN. ANALOG MUX	1.44	1.20
4553	3 DIGIT BCD CNTR	3.95	3.29
4556	DUAL BIN TO 1 OF 4 DEC (INV.)	1.15	.96
4557	1 TO 64-BIT VARIABLE SHIFT REG	3.95	3.29
4558	BCD TO 7 SEG DCDR	1.77	1.47
4560	NBCD ADDER	3.80	3.17
4566	IND TIME BASE GEN.	3.28	1.98
4568	PHASE COMPARATOR/PROG. TIMER	6.49	5.41
4569	HIGH SPEED DUAL PROG. CTR	2.48	2.07
4582	LOOK AHEAD CARRY BLOCK	1.37	1.14
4583	DUAL SCHMITT TRIG	1.69	1.41
4584	EX RPL MM74C14N	.85	.71
4585	4-BIT MAG COMP	1.80	1.50
4597	8-BIT BUS COMPAT CNTR-LATCH	3.72	3.10
4598	8-BIT BUS COMPAT ADDRS-LATCH	4.33	3.61
4599	8-BIT ADDRESSABLE LATCH	3.72	3.10
45100	4 x 4 CROSS POINT SW W/CONT MEM	4.44	3.70

6 Amps 125 VAC
7 Amps 30 VDC



DPDT STANDARD TOGGLE

CAL-ST21	(ON-NONE-ON)	$3.50	$3.25	$3.10
CAL-ST22	(ON-OFF-ON)	3.80	3.50	3.25
CAL-ST23	(MOM ON-OFF-MOM ON)	3.80	3.50	3.25
CAL-ST24	(ON-OFF-MOM ON)	3.95	3.60	3.40
CAL-ST25	(ON-NONE-MOM ON)	3.95	3.60	3.40
CAL-ST26	(ON-ON-ON)	3.95	3.60	3.40

filament transformers

High quality low voltage filament transformers. Conservatively rated. Individually boxed.

Part No.	Primary	Secondary	Rating	Sh.Wt.	Price
CAL-T631	117 V AC	6.3 V	1 AMP	.5 lbs.	$3.39
CAL-T1261	117 V AC	12.6 V	1 AMP	.5 lbs.	$4.39
CAL-T126C05	117 V AC	12.6 V CT	500 MA	.5 lbs.	$3.39
CAL-T126C3	117 V AC	12.6 V CT	3 AMP	2 lbs.	$7.59
CAL-T241	117 V AC	24 V	1 AMP	1 lb.	$4.79

VOLTAGE REGULATOR

POSITIVE

78H05	5V	5A	$8.95
309K	5V	1.5A	$2.09
317K	1.2-37V	1.5A	$4.50
323K	5V	3A	$7.70
7805CR	5V	1.5A	$2.50
7806ck	6V	1.5A	$2.50
7808CK	8V	1.5A	$2.50
7812CK	12V	1.5A	$2.50
7815CK	15V	1.5A	$2.50
7818CK	18V	1.5A	$2.50
317T	1.2-37V	1.5A	$2.95
7805CT	5V	1.5A	$1.25
7806CT	6V	1.5A	$1.25
7808CT	8V	1.5A	$1.25
7812CT	12V	1.5A	$1.25
7815CT	15V	1.5A	$1.25
7818CT	18V	1.5A	$1.25
7824CT	24V	1.4A	$1.25
78L05CP	5V	.1A	$ .49
78L08CP	8V	.1A	$ .49
78L12CP	12V	.1A	$ .49
78L15CP	15V	.1A	$ .49
78L18CP	18V	.1A	$ .49
78L24CP	24V	.1A	$ .49
LM305H	4.5V - 40V VOLT REG.		$1.89
LM723CN	2-37V VOLT REG.		$ .59
LM723CH	2-37V VOLT REG.		$1.06
MC3423PI	OVER/VOLT PROT		$1.31

CASE STYLE

K TO-3

CT TO-220

CP TO-92

H Package

N Package

NEGATIVE

7905CK	5V	1.5A	$2.50
7906CK	6V	1.5A	$3.69
7908CK	8V	1.5A	$3.69
7912CK	12V	1.5A	$2.50
7915CK	15V	1.5A	$2.50
7918CK	18V	1.5A	$3.69
7924CK	24V	1.5A	$3.69
7905CT	5V	1.5A	$1.75
7906CT	6V	1.5A	$1.75
7908CT	8V	1.5A	$1.75
7912CT	12V	1.5A	$1.75
7915CT	15V	1.5A	$1.75
7918CT	18V	1.5A	$1.75
7924CT	24V	1.5A	$1.75
79L05CP	5V	.1A	$1.10
79L12CP	12V	.1A	$1.10
79L15CP	15V	.1A	$1.10
79L18CP	18V	.1A	$1.10
79L24CP	24V	.1A	$1.10

We will be pleased to give you a quotation on your large quantity requirements.

WE ARE PROUD TO FEATURE MOTOROLA, NATIONAL, AND OTHER LEADING MANUFACTURES OF SEMICONDUCTORS.

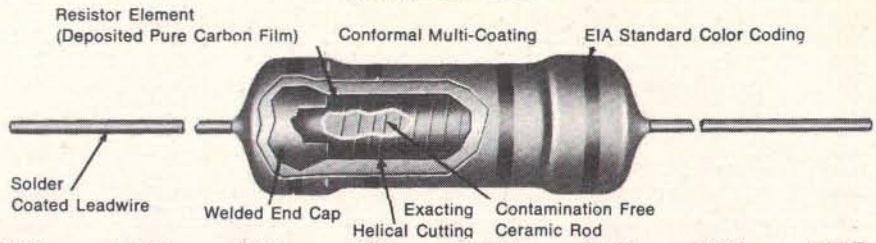
EXACT PART NUMBER OF DEVICES MAY VARY DEPENDING ON MANUFACTURE.

PRIORITY ONE ELECTRONICS

CARBON FILM
¼ WATT 5%
FIXED RESISTORS

ORDERING INSTRUCTIONS:
¼ watt resistors must be ordered in exact multiples of 4, 50(L) or 1,000(M) pcs. per value.

CONSTRUCTION:



PART #	VALUE	PART #	VALUE	PART #	VALUE	PART #	VALUE	PART #	VALUE	PART #	VALUE
RCQ-100	10 ohm	RCQ-101	100 ohm	RCQ-102	1.0 K	RCQ-103	10 K	RCQ-104	100 K	RCQ-105	1.0 M
RCQ-110	11 ohm	RCQ-111	110 ohm	RCQ-112	1.1 K	RCQ-113	11 K	RCQ-114	110 K	RCQ-115	1.1 M
RCQ-120	12 ohm	RCQ-121	120 ohm	RCQ-122	1.2 K	RCQ-123	12 K	RCQ-124	120 K	RCQ-125	1.2 M
RCQ-130	13 ohm	RCQ-131	130 ohm	RCQ-132	1.3 K	RCQ-133	13 K	RCQ-134	130 K	RCQ-135	1.3 M
RCQ-150	15 ohm	RCQ-151	150 ohm	RCQ-152	1.5 K	RCQ-153	15 K	RCQ-154	150 K	RCQ-155	1.5 M
RCQ-160	16 ohm	RCQ-161	160 ohm	RCQ-162	1.6 K	RCQ-163	16 K	RCQ-164	160 K	RCQ-165	1.6 M
RCQ-180	18 ohm	RCQ-181	180 ohm	RCQ-182	1.8 K	RCQ-183	18 K	RCQ-184	180 K	RCQ-185	1.8 M
RCQ-200	20 ohm	RCQ-201	200 ohm	RCQ-202	2.0 K	RCQ-203	20 K	RCQ-204	200 K	RCQ-205	2.0 M
RCQ-220	22 ohm	RCQ-221	220 ohm	RCQ-222	2.2 K	RCQ-223	22 K	RCQ-224	220 K	RCQ-225	2.2 M
RCQ-240	24 ohm	RCQ-241	240 ohm	RCQ-242	2.4 K	RCQ-243	24 K	RCQ-244	240 K	RCQ-245	2.4 M
RCQ-270	27 ohm	RCQ-271	270 ohm	RCQ-272	2.7 K	RCQ-273	27 K	RCQ-274	270 K	RCQ-275	2.7 M
RCQ-300	30 ohm	RCQ-301	300 ohm	RCQ-302	3.0 K	RCQ-303	30 K	RCQ-304	300 K	RCQ-305	3.0 M
RCQ-330	33 ohm	RCQ-331	330 ohm	RCQ-332	3.3 K	RCQ-333	33 K	RCQ-334	330 K	RCQ-335	3.3 M
RCQ-360	36 ohm	RCQ-361	360 ohm	RCQ-362	3.6 K	RCQ-363	36 K	RCQ-364	360 K	RCQ-365	3.6 M
RCQ-390	39 ohm	RCQ-391	390 ohm	RCQ-392	3.9 K	RCQ-393	39 K	RCQ-394	390 K	RCQ-395	3.9 M
RCQ-430	43 ohm	RCQ-431	430 ohm	RCQ-432	4.3 K	RCQ-433	43 K	RCQ-434	430 K	RCQ-435	4.3 M
RCQ-470	47 ohm	RCQ-471	470 ohm	RCQ-472	4.7 K	RCQ-473	47 K	RCQ-474	470 K	RCQ-475	4.7 M
RCQ-510	51 ohm	RCQ-511	510 ohm	RCQ-512	5.1 K	RCQ-513	51 K	RCQ-514	510 K	RCQ-515	5.1 M
RCQ-560	56 ohm	RCQ-561	560 ohm	RCQ-562	5.6 K	RCQ-563	56 K	RCQ-564	560 K	RCQ-565	5.6 M
RCQ-620	62 ohm	RCQ-621	620 ohm	RCQ-622	6.2 K	RCQ-673	67 K	RCQ-624	620 K	RCQ-625	6.2 M
RCQ-680	68 ohm	RCQ-681	680 ohm	RCQ-682	6.8 K	RCQ-683	68 K	RCQ-684	680 K	RCQ-685	6.8 M
RCQ-750	75 ohm	RCQ-751	750 ohm	RCQ-752	7.5 K	RCQ-753	75 K	RCQ-754	750 K	RCQ-755	7.5 M
RCQ-820	82 ohm	RCQ-821	820 ohm	RCQ-822	8.2 K	RCQ-823	82 K	RCQ-824	820 K	RCQ-825	8.2 M
RCQ-910	91 ohm	RCQ-911	910 ohm	RCQ-912	9.1 K	RCQ-913	91 K	RCQ-914	910 K	RCQ-915	9.1 M
										RCQ-106	10 M

TO ORDER, YOU MUST ADD A QUANTITY CODE TO THE PART

"BLANK" = PACKAGE OF 4
SHIPPING WT. 1 OZ.

29¢

"L" = PACKAGE OF 50
SHIPPING WT. 1 OZ.

1-19 .... $1.00
20+ .... $ .80

"M" = PACKAGE OF 1,000
SHIPPING WT. 1 LB.

1-4 .... $14.00
5-9 .... $13.00
10-24 .... $12.00
25+ .... Call For Price

EXAMPLE OF HOW TO ORDER

IF YOU WISH:	QTY. ORDERED	PART NUMBER	UNIT	AMOUNT
8 PCS. OF A 47 K RESISTOR	2	RCQ-473	.29	.58
150 PCS. OF A 2.2 M RESISTOR	3	RCQ-225L	1.00	3.00
2,000 PCS. OF A 390 ohm RESISTOR	2	RCQ-391M	14.00	28.00

TRANSISTORS

	POLARITY	VCE	IC(MA)	HFE	F(MHZ)	PACKAGE OF	100 +
2N697	NPN	40V	150	120	30	2/$1.18	$ .50
2N2219A	NPN	40V	800	200	300	2/$1.00	$ .40
2N2221A	NPN	30V	800	120	250	2/$1.00	$ .35
2N2222A	NPN	40V	800	300	300	3/$1.00	$ .27
2N2904A	PNP	60V	600	40	3WT	2/$1.00	$ .41
2N2906A	PNP	60V	600	40	1.8WT	2/$1.00	$ .40
2N2907A	PNP	60V	600	100	200	3/$1.00	$ .27
2N3053	NPN	40V	700	250	5WT	2/$1.40	$ .50
2N3054	NPN	55V	4A	100	75WT	$1.42	$1.23
2N3055	NPN	60V	15A	70	115WT	$ .98	$ .75
2N3904	NPN	40V	200	300	300	5/$1.00	$ .14
2N3906	PNP	40V	200	300	250	5/$1.00	$ .14
2N4400	NPN	40V	600	150	200	4/$1.00	$ .15
2N4401	NPN	40V	600	300	250	4/$1.00	$ .15
2N4402	PNP	40V	600	150	150	4/$1.00	$ .15
2N4403	PNP	40V	600	300	200	4/$1.00	$ .15
2N5400	PNP	120V	600	180	310MW	2/$1.00	$ .40
2N5401	PNP	150V	600	240	310MW	2/$1.00	$ .40
2N6028	PUT	40V	375MW	PROG	UJT	2/$1.00	$ .45
2N6282	NDL	60V	10A	1.8K	160W	$4.12	$3.55
2N6285	PDL	60V	10A	1.8K	160W	$4.95	$4.25
MPF102	NJFT	25V	10	VHF	AMP	2/$1.00	$ .45
MPF161	PJFT	40V	10	GEN	PRPS	2/1.60	$ .60
MPS2222A	PNP	40V	800	300	300	4/$1.00	$ .16
MPS2907	PNP	60V	600	100	200	4/$1.00	$ .16
MPSA13	NDL	30V	300	5K	200	2/$1.00	$ .30
MPSA63	PDL	30V	300	5K	200	2/$1.00	$ .30
MJE2955	PNP	60V	10A	90W	2	$2.98	$2.36
MJE2955T	PNP	60V	10A	75W	2	$1.19	$ .95
MJE3055	NPN	60V	10A	90W	2	$1.98	$1.60
MJE3055T	NPN	60V	10A	75W	2	$ .98	$ .90
TIP29A	NPN	60V	3A	75	30W	2/$1.40	$ .55
TIP30A	PNP	60V	3A	75	30W	2/$1.60	$ .60
TIP31A	NPN	60V	5A	50	40W	2/$1.60	$ .65
TIP32A	PNP	60V	5A	50	40W	2/$1.60	$ .65
TIP41A	NPN	60V	10A	75	65W	$.98	$ .85
TIP42C	PNP	60V	10A	75	65W	$1.49	$1.10
TIP48	NPN	300V	1A	150	40W	$1.10	$ .90
TIP49	NPN	350V	1A	150	40W	$1.20	$ .95
TIP120	NDL	60V	8A	1K	65W	$1.40	$1.15
TIP127	PDL	100V	8A	1K	65W	$1.98	$1.55
2N5060	SCR	30V	.8A			2/$1.50	$ .60
C106B1	SCR	250V	4A			2/$1.00	$ .39
T2800B	TRIAC	200V	8A			$1.49	$1.20

SIGNAL AND RECT. DIODES

	PIV	AMPS	APPLIC.	PACKAGE OF	/c 100
IN1183	50V	35A	RECT.	1/$2.26	$1.74
IN4001	50V	1A	RECT.	5/$1.00	$10.00
IN4002	100V	1A	RECT.	5/$1.00	$10.00
IN4003	200V	1A	RECT.	5/$1.00	$10.00
IN4004	400V	1A	RECT.	5/$1.00	$12.00
IN4005	600V	1A	RECT.	4/$1.00	$14.50
IN4006	800V	1A	RECT.	4/$1.00	$16.00
IN4007	1000V	1A	RECT.	4/$1.00	$18.50
IN4148	75V	10MA	SIG.	5/$1.00	$10.00
IN5400	50V	3A	RECT.	3/$1.19	$30.00
IN5402	200/V	3A	RECT.	3/$1.49	$40.00
IN5404	400V	3A	RECT.	3/$1.79	$50.00

ZENER DIODES

	Vz	WATT	IT MA	PACKAGE OF	/c PACKAGE OF 100
IN4728A	3.3V	1W	76MA	4/$1.00	$16.00
IN4729A	3.6V	1W	69MA	4/$1.00	$16.00
IN4730A	3.9V	1W	64MA	4/$1.00	$16.00
IN4731A	4.3V	1W	58MA	4/$1.00	$16.00
IN4732A	4.7V	1W	53MA	4/$1.00	$16.00
IN4733A	5.1V	1W	49MA	4/$1.00	$16.00
IN4734A	5.6V	1W	45MA	4/$1.00	$16.00
IN4735A	6.2V	1W	41MA	4/$1.00	$16.00
IN4736A	6.8V	1W	37MA	4/$1.00	$16.00
IN4737A	7.5V	1W	34MA	4/$1.00	$16.00
IN4738A	8.2V	1W	31MA	4/$1.00	$16.00
IN4739A	9.1V	1W	28MA	4/$1.00	$16.00
IN4740A	10V	1W	25MA	4/$1.00	$16.00
IN4741A	11V	1W	23MA	4/$1.00	$16.00
IN4742A	12V	1W	21MA	4/$1.00	$16.00
IN4743A	13V	1W	19MA	4/$1.00	$16.00
IN4744A	15V	1W	17MA	4/$1.00	$16.00
IN4746A	18V	1W	14MA	4/$1.00	$16.00
IN4747A	20V	1W	12.5MA	4/$1.00	$16.00
IN4751A	30V	1W	8.5MA	4/$1.00	$16.00
IN4754A	39V	1W	6.5MA	3/$1.00	$20.00
IN4757A	51V	1W	5MA	3/$1.00	$20.00
IN4759A	62V	1W	4MA	2/$1.00	$38.00
IN4761A	75V	1W	3.3MA	2/$1.00	$42.00
IN4763A	91V	1W	2.8MA	2/$1.00	$42.00
IN4764A	100V	1W	2.5MA	2/$1.00	$42.00

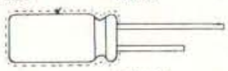
PLEASE ORDER BY PART NUMBER

ALUMINUM ELECTROLYTIC CAPACITORS

AXIAL LEAD

PART NO. VALVE/VOLTAGE	SIZE (INCHES)	PRICE PER PKG. OF:	100 PAC ADD "C" TO END OF PART #	1000 PAC ADD "M" TO END OF PART #
CA-1/50	.19 x .49	6/1.00	11.00	90.00
CA-2.2/50	.19 x .49	6/1.00	11.00	90.00
CA-3.3/50	.24 x .49	6/1.00	11.00	90.00
CA-4.7/50	.24 x .49	6/1.00	11.00	90.00
CA-10/50	.24 x .63	6/1.00	11.00	90.00
CA-22/16	.24 x .49	6/1.00	11.00	90.00
CA-22/50	.32 x .63	4/1.00	15.00	120.00
CA-33/16	.24 x .49	5/1.00	14.00	110.00
CA-33/35	.32 x .63	4/1.00	17.00	130.00
CA-47/16	.24 x .63	4/1.00	15.00	115.00
CA-47/35	.32 x .79	3/1.00	20.00	140.00
CA-100/16	.32 x .63	4/1.00	18.00	140.00
CA-100/35	.40 x .79	3/1.20	27.00	220.00
CA-220/16	.40 x .79	3/1.00	22.00	170.00
CA-220/35	.40 x 1.24	2/1.00	30.00	230.00
CA-330/16	.40 x .99	3/1.20	25.00	200.00
CA-330/35	.51 x 1.24	3/1.20	36.00	300.00
CA-470/10	.40 x .99	3/1.20	25.00	200.00
CA-470/16	.40 x 1.24	2/1.00	32.00	240.00
CA-470/35	.63 x 1.24	2/1.40	42.00	340.00
CA-1000/10	.51 x .99	2/1.00	34.00	270.00
CA-1000/16	.51 x 1.24	2/1.20	38.00	295.00
CA-1000/35	.63 x 1.58	.98	60.00	520.00
CA-2200/10	.63 x 1.24	2/1.50	49.00	390.00
CA-2200/25	.87 x 1.98	1.10	74.00	605.00
CA-3300/10	.63 x 1.58	1.00	65.00	500.00
CA-3300/25	.87 x 1.98	1.50	100.00	750.00
CA-4700/10	.71 x 1.58	1.20	80.00	600.00
CA-4700/25	.99 x 1.98	2.00	130.00	1000.00

RADIAL LEAD



PART NO. VALVE/VOLTAGE	SIZE (INCHES)	PRICE PER PKG. OF:	100 PAC ADD "C" TO END OF PART #	1000 PAC ADD "M" TO END OF PART #
CR-1/50	.20 x .45	6/1.00	8.00	65.00
CR-2.2/50	.20 x .45	6/1.00	8.00	65.00
CR-3.3/50	.20 x .45	6/1.00	8.00	65.00
CR-4.7/50	.20 x .45	6/1.00	8.00	65.00
CR-10/50	.24 x .45	6/1.00	9.00	70.00
CR-22/16	.24 x .45	6/1.00	9.00	70.00
CR-22/35	.32 x .49	6/1.00	12.00	95.00
CR-33/16	.24 x .45	6/1.00	9.00	70.00
CR-33/35	.39 x .49	6/1.00	13.00	103.00
CR-47/16	.32 x .49	6/1.00	12.00	95.00
CR-47/35	.39 x .49	5/1.00	14.00	110.00
CR-100/16	.39 x .49	5/1.00	13.00	103.00
CR-100/35	.39 x .63	4/1.00	15.00	115.00
CR-220/16	.39 x .63	4/1.00	15.00	115.00
CR-220/35	.51 x .79	3/1.00	24.00	180.00
CR-330/16	.51 x .79	3/1.00	22.00	150.00
CR-330/35	.63 x .99	2/1.00	32.00	250.00
CR-470/16	.51 x .79	3/1.00	24.00	180.00
CR-470/35	.63 x .99	2/1.20	40.00	310.00
CR-1000/10	.51 x .79	3/1.20	25.00	200.00
CR-1000/16	.63 x .99	3/1.40	31.00	230.00
CR-1000/35	.71 x 1.40	2/1.50	57.00	450.00
CR-2200/10	.63 x 1.24	2/1.20	44.00	310.00
CR-2200/25	.87 x 1.58	1.00	70.00	550.00
CR-3300/10	.63 x 1.40	2/1.60	60.00	400.00
CR-4700/10	.71 x 1.58	1.00	70.00	550.00

Volume Discount: C-packages, 1-9 None, 10-24 less 10%, 25 - less 15%. M-packages, 1-4 None, 5-9 less 10%, 10-up Call for price. Axial and Radial Capacitors may be combined in standard packages for best price. Other values/voltages available in 1000 (M) packages - Call for price.

"BLANK" = Package of ( ) **"C" = Package of 100**
"M" = Package of 1,000

Exact size may vary. The dimensions above are for reference only.

CRYSTALS

PART #/FREQ.	APPLICATION	1-9	10-24
XTL1.000MH	6800CPU, STD. CLOCK	$4.95	$4.45
XTL1.843MH	4411 BAUD RATE GEN	$4.95	$4.45
XTL2.000MH	Z80 CPU, STD. CLOCK	$4.95	$4.45
XTL3.000MH	STANDARD CLOCK FREQ.	$4.95	$4.45
XTL3.276MH	ICM7025,STOPWATCH	$4.95	$4.45
XTL3.579MH	TV COLOR BURST SM CASE	$3.95	$3.50
XTL4.000MH	Z80A CPU, STD CLOCK	$3.95	$3.50
XTL6.000MH	STANDARD CLOCK FREQ.	$3.95	$3.50
XTL10.00MH	STANDARD CLOCK FREQ.	$3.95	$3.50
XTL18.00MH	8080, 8008, 8224	$3.95	$3.50
XTL20.00MH	STANDARD CLOCK FREQ.	$3.95	$3.50

ARCO DISC Ceramic

CODE LETTER	VOLT-AGE	Dia.	Lead Spacing	Thk.	Lead Dia.
			Inches		
A	1000V	.290	.250	.156	.025
B	1000V	.385	.250	.156	.025
C	1000V	.590	.375	.156	.025
D	50V	.276	.156	.250	.025
E	50V	.315	.156	.250	.025
F	50V	.355	.156	.375	.025
G	50V	.473	.156	.375	.025
H	25V	.394	.157	.250	.025
I	25V	.484	.157	.250	.025
J	25V	.532	.157	.375	.025

ARCO DISC CERAMIC

Part #	Value	Code Letter	Price per Package of:	-C 100 pac Add -C to end of part #	-M 1000 pac Add -M to end of part #
CCD-3R3	3.3pf	A	4/29¢	$4.60	$37.00
CCD-050	5 pf	A	4/29¢	$4.60	$37.00
CCD-060	6 pf	A	4/29¢	$4.60	$37.00
CCD-6R8	6.8pf	A	4/29¢	$4.60	$37.00
CCD-7R5	7.5pf	A	4/29¢	$4.60	$37.00
CCD-080	8pf	A	4/29¢	$4.60	$37.00
CCD-100	10pf	A	4/29¢	$4.60	$37.00
CCD-120	12pf	A	4/29¢	$4.60	$37.00
CCD-150	15pf	A	4/29¢	$4.60	$37.00
CCD-180	18pf	A	4/29¢	$4.60	$37.00
CCD-200	20pf	A	4/29¢	$4.60	$37.00
CCD-220	22pf	A	4/29¢	$4.60	$37.00
CCD-240	24pf	A	4/29¢	$4.60	$37.00
CCD-250	25pf	A	4/29¢	$4.60	$37.00
CCD-270	27pf	A	4/29¢	$4.60	$37.00
CCD-300	30pf	A	4/29¢	$4.60	$37.00
CCD-330	33pf	A	4/29¢	$4.60	$37.00
CCD-390	39pf	A	4/29¢	$4.60	$37.00
CCD-470	47pf	A	4/29¢	$4.60	$37.00
CCD-500	50pf	A	4/29¢	$4.60	$37.00
CCD-510	51pf	A	4/29¢	$4.60	$37.00
CCD-560	56pf	A	4/29¢	$4.60	$37.00
CCD-680	68pf	A	4/29¢	$4.60	$37.00
CCD-750	75pf	A	4/29¢	$4.60	$37.00
CCD-820	82pf	A	4/29¢	$4.60	$37.00
CCD-910	91pf	A	4/29¢	$4.60	$37.00
CCD-101	100pf	A	4/29¢	$4.60	$37.00
CCD-121	120pf	A	4/29¢	$4.60	$37.00
CCD-131	130pf	A	4/29¢	$4.60	$37.00
CCD-151	150pf	A	4/29¢	$4.60	$37.00
CCD-181	180pf	A	4/29¢	$4.60	$37.00
CCD-201	200pf	A	4/29¢	$4.60	$37.00
CCD-221	220pf	A	4/29¢	$4.60	$37.00
CCD-241	240pf	A	4/29¢	$4.60	$37.00
CCD-251	250pf	A	4/29¢	$4.60	$37.00
CCD-271	270pf	A	4/29¢	$4.60	$37.00
CCD-301	300pf	A	4/29¢	$4.60	$37.00
CCD-331	330pf	A	4/29¢	$4.60	$37.00
CCD-351	350pf	A	4/29¢	$4.60	$37.00
CCD-361	360pf	A	4/29¢	$4.60	$37.00
CCD-391	390pf	A	4/29¢	$4.60	$37.00
CCD-401	400pf	A	4/29¢	$4.60	$37.00
CCD-471	470pf	A	4/29¢	$4.60	$37.00
CCD-501	500pf	A	4/29¢	$4.60	$37.00
CCD-511	510pf	A	4/29¢	$4.60	$37.00
CCD-561	560pf	A	4/29¢	$4.60	$37.00
CCD-601	600pf	A	4/29¢	$4.60	$37.00
CCD-681	680pf	A	4/29¢	$4.60	$37.00
CCD-751	750pf	A	4/29¢	$4.60	$37.00
CCD-821	820pf	A	4/29¢	$4.60	$37.00
CCD-911	910pf	A	4/29¢	$4.60	$37.00
CCD-102G	.001uf	A	4/29¢	$4.60	$37.00
CCD-122	.0012uf	B	3/29¢	$5.40	$43.00
CCD-132	.0013uf	B	3/29¢	$5.40	$43.00
CCD-152G	.0015uf	B	3/29¢	$5.40	$43.00
CCD-162	.0016uf	B	3/29¢	$5.40	$43.00
CCD-182	.0018uf	B	3/29¢	$5.40	$43.00
CCD-202G	.002uf	B	3/29¢	$5.40	$43.00
CCD-222G	.0022uf	B	3/29¢	$5.40	$43.00
CCD-252G	.0025uf	B	3/29¢	$5.40	$43.00
CCD-272G	.0027uf	B	3/29¢	$5.40	$43.00
CCD-302G	.003uf	B	3/29¢	$5.40	$43.00
CCD-332G	.0033uf	C	3/29¢	$6.80	$54.00
CCD-392G	.0039uf	C	3/29¢	$6.80	$54.00
CCD-402G	.004uf	C	3/29¢	$6.80	$54.00
CCD-432G	.0043uf	C	3/29¢	$6.80	$54.00
CCD-472	.0047uf	C	3/29¢	$6.80	$54.00
TCD-502Z	.005uf	D	4/29¢	$4.90	$40.00
TCD-103Z	.01uf	E	4/29¢	$4.90	$40.00
TCD-203Z	.02uf	F	4/29¢	$4.90	$40.00
TCD-253Z	.025uf	G	4/29¢	$4.90	$40.00
TCA-333Z	.033uf	H	4/29¢	$5.60	$45.00
TCA-503Z	.05uf	I	3/29¢	$6.00	$48.00
TCA-683Z	.068uf	J	2/29¢	$9.20	$74.00
TCA-104Z	.1uf	J	2/29¢	$10.60	$85.00

TO ORDER YOU MUST ADD

A QUANTITY CODE TO THE PART #

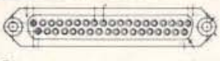
IF YOU WISH:	QTY. ORDERED	PART NUMBER	UNIT	AMOUNT
18 PCS. of 2.2 uf/50V Capacitor (Axial)	3	CA-2.2/50	1.00	3.00
200 PCS. of 100 uf/16V Capacitor (Radial) ..	2	CR-100/16C	13.00	26.00
1000 PCS. of 2,200 uf/25V Capacitor (Axial)	1	CA-2200/25M	605.00	605.00

PLEASE ORDER BY PART NUMBER

WINCHESTER ELECTRONICS
Litton

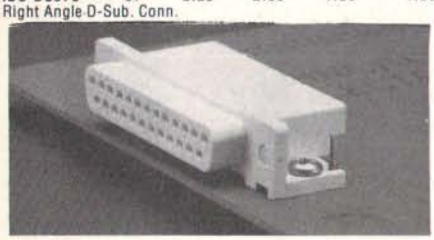
D-Subminiature Connector

P = Plug-Male
S = Socket-Female
C = Cover-Hood



PART NO.	NO. OF PINS	1-9	10-24	25-99	100-249
IDC-DE9P	9	4.20	4.00	3.60	3.20
IDC-DE9S	9	4.50	4.20	3.80	3.40
IDC-DE9C	9	1.25	1.10	1.00	
IDC-DA15P	15	4.35	4.20	3.75	3.40
IDC-DA15S	15	5.00	4.85	4.35	3.90
IDC-DA15C	15	1.40	1.25	1.10	.95
IDC-DB 25P	25	6.25	6.00	5.20	4.70
IDC-DB 25S	25	6.60	6.35	5.60	5.00
IDC-DB 25C	25	1.60	1.50	1.35	1.20
IDC-DC37P	37	8.80	8.00	7.20	6.40
IDC-DC37S	37	11.00	10.25	9.20	8.20
IDC-DC37C	37	2.25	2.00	1.80	1.60

Right Angle D-Sub. Conn.



RIGHT ANGLE D-SUBMINATURE CONNECTOR

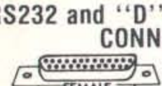
PART NO.	NO. OF PINS	1-9	10-24	25-99	100-249
IDCRADE9P	9	4.50	4.10	3.70	3.30
IDCRADE9S	9	4.70	4.30	3.90	3.50
IDCRADA15P	15	4.80	4.50	4.20	3.90
IDCRADA15S	15	5.10	4.90	4.70	4.40
IDCRADB25P	25	6.30	6.00	5.25	4.75
IDCRADB25S	25	6.75	6.40	5.50	5.00
IDCRADC37P	37	9.00	8.10	7.30	6.50
IDCRADC37S	37	11.50	10.60	9.50	8.50

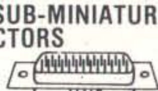
subminiature toggle switches

6 Amps 125 VAC
7 Amps 30 VDC

	SPDT STANDARD TOGGLE	PRICE 1-9	10-24	25-99
CAL-ST11	(ON-NONE-ON)	$2.60	$2.40	$2.20
CAL-ST12	(ON-OFF-ON)	2.80	2.50	2.30
CAL-ST13	(MOM ON-OFF-MOM ON)	2.80	2.50	2.30
CAL-ST14	(ON-OFF-MOM ON)	2.80	2.50	2.30
CAL-ST15	(ON-NONE-MOM ON)	2.80	2.50	2.30

RS232 and "D" SUB-MINIATURE CONNECTORS





P = Plug, Male Type - S = Socket, Female Type - C = Cover, Hood

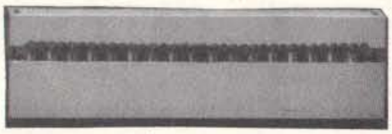
PART NO.	DESCRIPTION	PRICE 1-9	10-24	25-99
CND-DE9P	9 PIN MALE	$ 2.10	$ 1.90	$ 1.70
CND-DE9S	9 PIN FEMALE	$ 2.70	$ 2.40	$ 2.10
CND-DE9C	9 PIN COVER	$ 1.50	$ 1.25	$ 1.10
CND-DA15P	15 PIN MALE	$ 2.75	$ 2.45	$ 2.15
CND-DA15S	15 PIN FEMALE	$ 3.95	$ 3.60	$ 3.20
CND-DA15C	15 PIN COVER	$ 1.50	$ 1.30	$ 1.10
CND-DB25P	25 PIN MALE	$ 3.50	$ 3.25	$ 3.00
CND-DB25S	25 PIN FEMALE	$ 4.60	$ 4.35	$ 4.20
CND-DB51212	1 PC. GREY HOOD	$ 1.60	$ 1.45	$ 1.30
CND-P25H	2 PC. GREY HOOD	$ 1.50	$ 1.25	$ 1.10
CND-DB51226	2 PC. BLACK HOOD	$ 1.90	$ 1.65	$ 1.45
CND-DC37P	37 PIN MALE	$ 5.80	$ 5.10	$ 4.45
CND-DC37S	37 PIN FEMALE	$ 8.70	$ 7.70	$ 6.70
CND-DC37C	37 PIN COVER	$ 1.80	$ 1.55	$ 1.30
CND-DD50P	50 PIN MALE	$ 8.75	$ 7.75	$ 6.70
CND-DD50S	50 PIN FEMALE	$11.65	$10.25	$ 8.90
CND-DD50C	50 PIN COVER	$ 2.00	$ 1.80	$ 1.60
CND-D20418	HARDWARE SET 2 PR.	$ 1.00	$ 0.80	$ 0.70
CND-RS2328F	RS232, DB25P, EIA CLASS 1 CABLE 8 CON. 8 FT.	$19.95	$17.95	$15.95
CND-5730360	CENT. 700 SERIES PRINTER CONNECTOR	$ 9.00	$ 7.50	$ 6.00

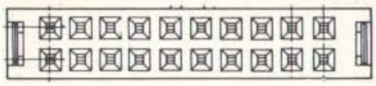
FLEX-COM

Edgecard Connector

PART NO.	NO. OF PINS	PRICE 1-9	10-24	25-99	100-249
IDC-20CE	10/20	4.35	4.15	3.75	3.30
IDC-26CE	13/26	5.00	4.75	4.30	3.80
IDC-34CE	17/34	6.00	5.70	5.10	4.50
IDC-40CE	20/40	6.90	6.50	5.80	5.25
IDC-50CE	25/50	7.25	7.00	6.30	5.40

Socket Connector





PART NO.	NO. OF PINS	PRICE 1-9	10-24	25-99	100-249
IDC-20SKT	10/20	2.75	2.50	2.25	2.00
IDC-26SKT	13/26	3.50	3.20	2.85	2.30
IDC-34SKT	17/34	4.50	4.20	3.75	3.30
IDC-40SKT	20/40	5.40	5.00	4.50	3.90
IDC-50SKT	25/50	6.50	6.00	5.40	4.75

Header Connector

Right Angle Soldertail GOLD Header

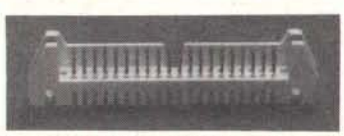
PART NO.	1-9	10-24	25-99	100-249
IDC-RAH 20ST	1.90	1.60	1.45	1.30
IDC-RAH 26ST	2.25	2.00	1.80	1.60
IDC-RAH 34ST	2.95	2.60	2.35	2.10
IDC-RAH 40ST	3.60	3.00	2.70	2.40
IDC-RAH 50ST	4.30	3.60	3.25	2.90

RIGHT ANGLE WIRE WRAP GOLD HEADER

PART NO.	1-9	10-24	25-99	100-249
IDC-RAH 20WW	4.15	3.60	3.25	2.90
IDC-RAH 26WW	5.30	4.30	3.90	3.50
IDC-RAH 34WW	5.95	5.00	4.75	4.50
IDC-RAH 40WW	7.00	6.00	5.40	4.80
IDC-RAH 50WW	7.95	6.80	6.20	5.50

STRAIGHT SOLDERTAIL GOLD HEADER

PART NO.	NO. PINS	1-9	10-24	25-99	100-249
IDCSTH20ST	10/20	1.85	1.55	1.40	1.30
IDCSTH26ST	13/26	2.20	1.95	1.75	1.55
IDCSTH34ST	17/34	2.90	2.55	2.30	2.05
IDCSTH40ST	20/40	3.55	2.95	2.65	2.40
IDCSTH50ST	25/50	4.25	3.55	3.20	2.85



STRAIGHT WIRE WRAP GOLD HEADER

PART NO.	NO. PINS	1-9	10-24	25-99	100-249
IDCSTH20WW	10/20	4.20	3.55	3.20	2.85
IDCSTH26WW	13/26	5.25	4.25	3.85	3.45
IDCSTH34WW	17/34	5.90	4.95	4.70	4.45
IDCSTH40WW	20/40	6.95	5.95	5.35	4.75
IDCSTH50WW	25/50	7.90	6.75	6.15	5.45

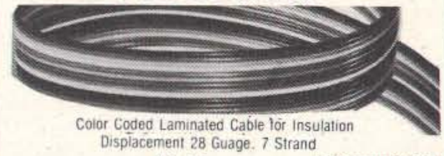
HEADER CONNECTOR EJECTOR

The IDCEJ provides an ejector and positive lock for socket connectors when used with any of the header connectors listed. Easy installation and low cost provide easy extraction, when desired, for your IDC socket interconnects. Order 2 for each connector.

PART NO.	QTY.	PRICE
IDCEJ5	pkg 5	$1.00
IDCEJC	pkg 100	$10.00

IDC System

RIBBON CABLE

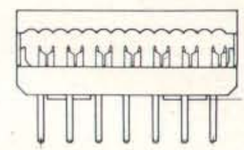


Color Coded Laminated Cable for Insulation Displacement 28 Guage. 7 Strand

PART NO.	NO. OF CONDUCTORS	PRICE PER SPOOL 10 Ft.	100 Ft.
IDC-09CC	9	3.80	30.00
IDC-14CC	14	4.75	40.00
IDC-16CC	16	5.50	45.00
IDC-20CC	20	7.00	60.00
IDC-25CC	25	8.50	72.00
IDC-26CC	26	8.50	72.00
IDC-34CC	34	11.00	100.00
IDC-40CC	40	13.00	115.00
IDC-50CC	50	16.00	145.00

GRAY LAMINATED CABLE FOR INSULATION DISPLACEMENT 28 Gauge 7 Strand

PART NO.	NO. OF CONDUCTORS	PRICE PER SPOOL 10 Ft.	100 Ft.
IDC-09GY	9	2.50	18.05
IDC-14GY	14	3.50	28.00
IDC-16GY	16	4.00	32.00
IDC-20GY	20	4.80	40.00
IDC-25GY	25	6.00	50.00
IDC-26GY	26	6.00	50.00
IDC-34GY	34	8.30	66.00
IDC-40GY	40	10.00	77.00
IDC-50GY	50	12.00	95.00



Dip Plugs

PART NO.	NO. OF PINS	1-9	10-24	25-99	100-249
IDC-14DP	14	1.50	1.40	1.25	1.10
IDC-16DP	16	1.70	1.60	1.45	1.30
IDC-24DP	24	2.50	2.20	2.00	1.80

TRANSITION CONNECTOR

PART NO.	NO. OF PINS	PRICE 1-9	10-24	25-99	100-249
IDCTRNS10	10	2.00	1.60	1.10	1.00
IDCTRNS16	16	2.20	1.80	1.30	1.10
IDCTRNS20	20	2.50	2.00	1.40	1.25
IDCTRNS26	26	3.00	2.40	1.80	1.50
IDCTRNS34	34	4.00	3.10	2.20	2.00
IDCTRNS40	40	4.50	3.50	2.50	2.25
IDCTRNS50	50	5.50	4.40	3.10	2.50

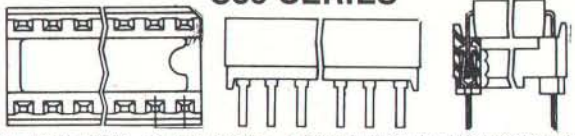
INSTALLATION/ASSEMBLY TOOLS

IDC-1080	Arbor Press with Ram for all connectors *except* D-Subminiature order platens below. 12 LBS.	$180.00
IDC-013031	Platen & Ram for D-Subminiature Connectors	$ 50.00
IDC-1083	Platen for Edgecard Connector	$ 35.00
IDC-10811	Platen for Socket Connector	$ 35.00
IDC-1096	Platen for 24 Pin Dip Plug only	$ 35.00
IDC-10871	Platen for 14 & 16 Pin Dip Plug only	$ 35.00
IDC-1084	Platen for Transition Connector SH. WT. 1 LB.	$ 35.00

PLEASE ORDER BY PART NUMBER

Texas Instruments

FACE GRIP LOW PROFILE SOLDER TAIL DIP SOCKETS C85 SERIES



- Face grip design provides maximum retention force
- Anti-wicking feature
- Redundant contact points

SPECIFICATIONS

Material:

A. Body—94V-0 polyester
B. Contact—copper nickel alloy
C. Finish—200 microinches tin alloy (min.)stripe

NOTES:

A. Operating temperature—40°C to +100°C
B. Contacts have redundant spring elements
C. Accommodates standard IC leads up to .014 thick and .025 wide
D. Contact is designed and oriented to the insulator to grasp the broad "face" of the IC lead, allowing for low insertion and high retention forces
E. Socket is designed to achieve maximum density on boards
F. Sockets may be mounted end to end on .100" centers continuous line or on .400" centers row to row
G. Socket is designed to prevent IC leads from contacting PC board
H. Closed entry feature - along with chamfered side walls - provided to facilitate automatic IC insertion and protect the IC leads against damage.
I. Strengthened socket legs allow for automatic socket insertion into PC board
J. Anti-wicking feature built into contact leg
K. Design to achieve outstanding insertion/withdrawal characteristics

PART NO.	PINS	PRICE 1-9	10-49	50-99	100-499	500-999	1,000+
TIS-08LP	08	N/A	.15	.10	.08	.07	.06
TIS-14LP	14	N/A	.18	.15	.14	.12	.11
TIS-16LP	16	N/A	.20	.18	.16	.13	.12
TIS-18LP	18	.30	.25	.22	.18	.15	.13
TIS-20LP	20	.30	.25	.23	.20	.17	.145
TIS-22LP	22	.35	.30	.25	.22	.19	.17
TIS-24LP	24	.40	.35	.30	.24	.20	.18
TIS-28LP	28	.45	.40	.35	.28	.24	.21
TIS-40LP	40	.50	.45	.42	.40	.35	.31

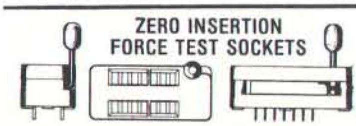
*MINIMUM ORDER $1.00 Per Line item

DIP PLUGS

PART NO.	PINS	PRICE 1-9	10-24	25-99	100-249
KNX-08DP	8	.50	.45	.43	.40
KNX-14DP	14	.65	.60	.58	.55
KNX-16DP	16	.70	.65	.62	.58
KNX-24DP	24	1.15	1.05	.90	.95
KNX-40DP	40	1.90	1.70	1.60	1.50

Socket and Dip Plug priced based on gold not exceeding $700 per oz.

ZERO INSERTION FORCE TEST SOCKETS

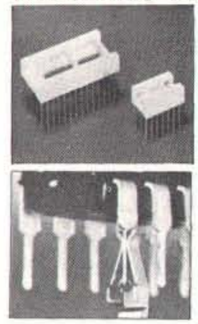


	1-9	10-24	25-99
ZIP-16DIP	$5.50	5.35	4.95
ZIP-24DIP	$7.50	7.25	6.95
ZIP-40DIP	$10.25	9.85	9.50

• Zero insertion pressure (ZIP) insertion and extraction. • Wide entry holes to accommodate maximum number of device types. • Contact exit on even .100 spacing for convenience in board mounting. • Built in "stop" for cam handle prevents "overthrow" causing plastic damage. • Top mount assy. Screws for ease of replacement of worn or damaged internal components. • General redesign of original ZIP DIP incorporating improvements designed to extend life and reliability.

ROBINSON NUGENT, INC.

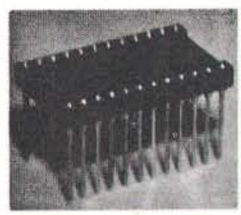
IYN SERIES GOLD 3 LEVEL WIRE WRAP SOCKETS

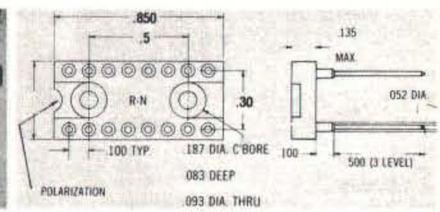


- Deep Chamfered Closed Entry Contacts
- RN Side Wipe Contact Design
- Terminal Barbs Allow Self-lock into PC Board
- Rugged Socket Body Design

PART NO.	PINS	PRICE* 1-9	10-24	25-99	100-249	250-999
RNS-08WWG	8	.50	.42	.40	.37	.33
RNS-14WWG	14	.60	.49	.47	.45	.42
RNS-16WWG	16	.65	.52	.50	.47	.44
RNS-18WWG	18	.85	.75	.70	.65	.60
RNS-20WWG	20	1.00	.90	.80	.75	.70
RNS-22WWG	22	1.25	1.15	1.10	1.05	1.00
RNS-24WWG	24	1.25	1.15	1.10	1.05	1.00
RNS-28WWG	28	1.60	1.50	1.40	1.30	1.20
RNS-40WWG	40	1.85	1.65	1.55	1.45	1.35

Socket prices based on gold not exceeding $700 per oz.





RN ICA HIGH RELIABILITY WIRE WRAP SOCKET

• 30 μ inch gold contact • Pin socket contacts for high reliability and high retention mounting holes • End and side stackable • 3-Level wrap • Low profile body with.

PART NO.	PINS	PRICE 1-9	10-24	25-99	100-249	250-999
RNH-08HRW	8	1.10	1.00	.90	.86	.77
RNH-14HRW	14	1.65	1.55	1.45	1.30	1.15
RNH-16HRW	16	1.85	1.65	1.55	1.40	1.25
RNH-18HRW	18	2.10	1.90	1.85	1.65	1.60
RNH-20HRW	20	2.45	2.10	2.00	1.80	1.70
RNH-22HRW	22	2.55	2.30	2.10	2.00	1.90
RNH-24HRW	24	2.75	2.50	2.20	2.10	2.00
RNH-28HRW	28	3.25	3.00	2.60	2.50	2.40
RNH-40HRW	40	4.50	4.25	4.00	3.60	3.40

PRECUT WIRE WRAP WIRE
PRECUT WIRE SAVES TIME AND COSTS LESS THAN WIRE ON SPOOLS

Kynar precut wire. All lengths are overall, including 1" strip on each end. Colors and lengths cannot be mixed for quantity pricing. Choose from colors **Red** (R), **Blue** (U), **Black** (B), and **Yellow** (Y).

PRECUT WIRE 100 PACK

PART NO.	LENGTH	100/Bag	PART NO.	Length	100/Bag
PGP025C*	2.5"	$1.25	PGP055C*	5.5"	1.58
PGP030C*	3.0"	1.30	PGP060C*	6.0"	1.65
PGP035C*	3.5"	1.37	PGP070C*	7.0"	1.99
PGP040C*	4.0"	1.42	PGP080C*	8.0"	2.14
PGP045C*	4.5"	1.48	PGP090C*	9.0"	2.24
PGP050C*	5.0"	1.54	PGP100C*	10.0"	2.39

PRECUT WIRE 500 PACK

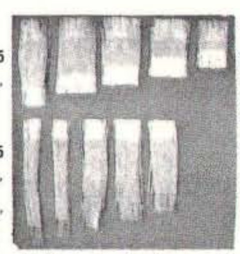
PART NO.	LENGTH	500/Bag	PART NO.	LENGTH	500/Bag
PGP025D*	2.5"	$3.58	PGP055D*	5.5"	5.38
PGP030D*	3.0"	3.86	PGP060D*	6.0"	5.66
PGP035D*	3.5"	4.15	PGP070D*	7.0"	6.76
PGP040D*	4.0"	4.44	PGP080D*	8.0"	7.38
PGP045D*	4.5"	4.74	PGP090D*	9.0"	8.11
PGP050D*	5.0"	5.04	PGP100D*	10.0"	8.71

*Specify color when ordering. **Red** (R), **Blue** (U), **Black** (B), & **Yellow** (Y).

Example: If you wish to order (2) pkg. 500, 4", Red:

2	PGP040DR	4.44	8.88

PRECUT WIRE KIT ASSORTMENTS



PGPWKI* $9.95
CONTAINS

250	3"	100	4½"
250	3½"	100	5"
100	4"	100	6"

PGPWK2* $24.95
CONTAINS

250	2½"	250	5"
500	3"	100	5½"
500	3½"	250	6"
500	4"	100	6½"
250	4½"	100	7"

PGPWK3* $32.95
CONTAINS

500	2½"	500	4½"
500	3"	500	5"
500	3½"	500	5½"
500	4"	500	6"

PGPWK4* $59.95
CONTAINS

1000	2½"	1000	4½"
1000	3"	1000	5"
1000	3½"	1000	5"
1000	4"	1000	6"

Wire kit assortments are available in the 4 colors mentioned along with a rainbow assortment. Use color code (A) for the rainbow assortment
Example: If you wish to order one wire kit 3 in blue:

1	PGPWK3U	32.95	32.95

SPOOLED 30 GAUGE KYNAR

PART NO.	SPOOL LENGTH	1-4	5-9
PGS050*	50 ft.	1.99	1.85
PGS100*	100 ft.	3.65	3.50
PGS250*	250 ft.	5.75	5.50
PGS500*	500 ft.	10.20	9.95
PGS1000*	1000 ft.	17.95	15.95

Example: If you wish to order 400 ft. of yellow:

4	PGS100Y	3.65	14.60

*Specify color when ordering, **Red** (R), **Blue** (U), **Black** (B), **Yellow** (Y), & for wire kits *only* rainbow **assortment** (A).

Texas Instruments Gold Plated Edgeboard Connectors

Standard But Not Ordinary

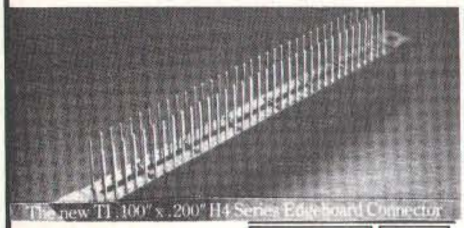
The H4 Series standard edgeboard connectors offer the best value in the edgeboard market today.

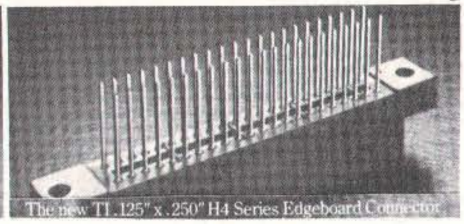
To assure reliable electrical connections, our cantilever contacts are pre-loaded for optimum normal force and bifurcated for redundancy; each contact point features from 50 (Wire Wrap*) to 75 (solder tail) microinches (minimum) of wrought gold inlay over a nickel diffusion barrier. The inlay is metallurgically bonded to a copper-nickel-tin alloy (CA 725) that is suited to both soldered and wire wrapped terminations. The dielectric contact-housing is made of glass-filled thermoplastic polyester, meeting U.L. Flammability Classification 94V-0.

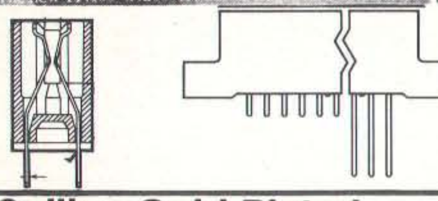
FEATURES

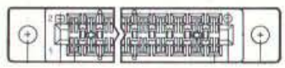
RELIABLE, COST-EFFICIENT CONTACT DESIGN
- 50 (Wire Wrap) to 75 (solder tail) microinches gold inlay over a nickel diffusion barrier (minimum thickness).
- Copper-nickel-tin CA 725 Alloy.
- Bifurcated contact points.
- Preloaded, cantilever spring design.
- Contacts are user removable.

RUGGED BODY
- Glass-filled thermoplastic polyester — Meets U.L. Flammability Classification 94V-0. — Resists common cleaning solvents.
- Solder standoff — Facilitates cleaning — Reduces solder wicking
- Between contact polarization key (snaplock for .100" & .125" centers).
- Generous chamfered card slot.
- Molded contact identification — Alphanumeric (.156" centers) — Numeric (.100" & .125" centers).
- Location ridges (bottom) and raised dots (top) mark every fifth contact position.
- Entire connector design is U.L. Approved.









ABBREVIATIONS:

ST — Solder Tail Gold
WW — Wire Wrap Gold

TI EDGE CONNECTORS

.1" Contact Centers: ST - Soldertail GOLD; WW - Wire Wrap GOLD

PART NO.	PRICE 1-9	10-24	25-99	100-249
TIC-1530-1 ST	1.60	1.45	1.30	1.10
TIC-1530-1 WW	1.70	1.55	1.35	1.15
TIC-1836-1 ST	2.00	1.80	1.60	1.40
TIC-1836-1 WW	2.10	1.90	1.65	1.45
TIC-2244-1 ST	2.25	2.00	1.75	1.50
TIC-2244-1 WW	2.40	2.15	1.90	1.60
TIC-2550-1 ST	2.50	2.25	2.00	1.65
TIC-2550-1 WW	2.70	2.45	2.15	1.80
TIC-3060-1 ST	2.95	2.60	2.30	1.90
TIC-3060-1 WW	3.20	2.85	2.55	2.20
TIC-3672-1 ST	3.30	2.95	2.60	2.30
TIC-3672-1 WW	3.90	3.50	3.10	2.60
TIC-4080-1 ST	3.60	3.25	2.85	2.40
TIC-4080-1 WW	4.40	4.00	3.60	3.00
TIC-4386-1 ST	3.90	3.50	3.10	2.60
TIC-4386-1 WW	4.65	4.15	3.70	3.10
TIC-50100-1 ST	4.50	4.05	3.60	3.00
TIC-50100-1 WW	5.40	4.90	4.30	3.60

.125" Contact Centers: STG - Soldertail GOLD; WWG - Wire Wrap GOLD

PART NO.	PRICE 1-9	10-24	25-99	100-249
TIC-2244-2 ST	2.30	2.10	1.85	1.50
TIC-2244-2 WW	2.60	2.35	2.10	1.75
TIC-2856-2 ST	2.80	2.55	2.25	1.85
TIC-2856-2 WW	3.20	2.90	2.55	2.15
TIC-3060-2 ST	2.90	2.60	2.30	1.95
TIC-3060-2 WW	3.40	3.05	2.70	2.25
TIC-3162-2 ST	3.05	2.75	2.45	2.05
TIC-3162-2 WW	3.50	3.15	2.80	2.35
TIC-3672-2 ST	3.45	3.10	2.75	2.30
TIC-3672-2 WW	4.00	3.60	3.20	2.68
TIC-4080-2 ST	3.80	3.45	3.05	2.55
TIC-4080-2 WW	4.45	4.00	3.55	2.95
TIC-4488-2 ST	4.20	3.80	3.35	2.80
TIC-4488-2 WW	4.85	4.35	3.90	3.20
TI-S100 ST	**3.20**	**2.90**	**2.50**	**2.20**
TI-S100 WW	**4.00**	**3.75**	**3.50**	**3.25**

Sullins Gold Plated Edgeboard Connectors

E — Eyelet Gold S — Solder Tail Gold
W — Wire Wrap Gold

Sullins Electonics was the first manufacturer to pioneer the use of Valox,* a polyester, as an insulator material. The quality and performance of this material has been a prime reason for the outstanding success of this connector line. Sullins connectors are unique in being recognized by Underwiters Laboratories. This is especially important to customers fabricating UL approved devices.

Materials & Characteristics

Insulator: Glass filled thermoplastic polyester, color: Blue.
Insulation Resistance: 5000 megoms.
Solvent Resistance: Perchloroethylene, Freon 113, Freon 11, Trichloroethylene.
Contacts: Phosphor Bronze.
Operating Voltage: .100 (2.54) .125 (3.17) .150 (3.81) .156 (3.96) contact centers
(At sea level) 600 VDC 800 VDC 1500 VDC 1800 VDC
Current Rating: 5 amperes.
Voltage Drop: 30 MV at rated current.
Contact Resistance: 10 milliohms maximum.
Operating Temperature: -65°C to +125°C
Board Thickness Accommodated: .062 inch (1.57mm)
Board Insertion: 2 to 16 ozs per contact pair using .062 (1.57mm) steel test blade.

.156" CONTACT CENTER CONNECTORS

CAT. PART NO.	APPLICATION	1-9	10-24	25-99
SUL-612S5	PET, MA1003	1.95	1.80	1.55
SUL-612E5	PET, MA1003	2.30	2.00	1.75
SUL-1020E5		2.00	1.77	1.53
SUL-1224E5	COMMODORE PET	3.60	3.20	2.80
SUL-1224S5	COMMODORE PET	3.30	2.90	2.50
SUL-1530E5	VECTOR PLUGBOARDS, GRI KEYBOARDS	3.30	2.90	2.50
SUL-1530S5	VECTOR PLUGBOARDS, GRI KEYBOARDS	3.50	3.05	2.65
SUL-1530W5	VECTOR PLUGBOARDS, GRI KEYBOARDS	2.40	2.10	1.85
SUL-1836E5		3.70	3.25	2.80
SUL-1836S5		3.40	3.00	2.60
SUL-2244E5	VCT-3662, VCT-3682, AIM-65	3.90	3.45	3.00
SUL-2244S5	SYM, VCT-3677, KIM	4.10	3.60	3.15
SUL-2244W5	VECTOR PLUGBOARDS, SYM, KIM, AIM-65	4.20	3.70	3.20
SUL-2550E5		5.30	4.65	4.05
SUL-3672E5		8.30	7.30	6.35
SUL-3672S5		7.25	6.40	5.55
SUL-3672W5		7.40	6.55	5.65
SUL-4386E5	MOT6800, INTEL MULTIBUSS	8.25	7.30	6.30
SUL-4386S5	NSC PACER, VCT-4608(1)	8.25	7.30	6.30
SUL-4386W5	VCT-4611(1)(2), INTEL MULTIBUSS	8.80	7.75	6.75

.125" CONTACT CENTER CONNECTORS

CAT. PART NO.	APPLICATION	1-9	10-24	25-99
SUL-2856W2	PROLOG STD BUSS, VCT-4610(1)(2)	6.60	5.80	5.05
SUL-2856S2	PROLOG STD BUSS, VCT-4610(1)(2)	5.50	4.85	4.20
SUL-3672W2		6.30	5.55	4.80
SUL-4080W2	VCT-4380 PLUGBOARD	7.00	6.20	5.35
SUL-S100ALT	.140" SPACED ROWS FOR ALTAIR	7.80	6.90	6.00
SUL-S100SEG	S-100 SOLDER EYELET	7.95	7.60	6.60
SUL-S100STG	.250" SPACED ROWS, IMSAI, VCT-8803, CROMEMCO	5.95	5.40	4.70
SUL-S100WWG	S-100 WIRE WRAP	6.50	5.75	4.95
SUL-CG1	IMSAI STYLE CARD GUIDE		1.00 Pkg of 5	
SUL-CG1/C	IMSAI STYLE CARD GUIDE		15.00 Pkg of 100	

.1" CONTACT CENTER CONNECTORS

CAT. PART NO.	APPLICATION	1-9	10-24	25-99
SUL-1020E1		2.90	2.55	2.20
SUL-1020W1		2.80	2.45	2.15
SUL-1326E1	VCT-4608, SDSSBC-100, IMSAI M10, S10	3.00	2.65	2.30
SUL-1530E1		3.75	3.30	2.90
SUL-1530S1		3.60	3.20	2.80
SUL-1530W1		3.75	3.30	2.90
SUL-2040E1	TRS-80, VCT-4609 PLUGBOARD	4.20	3.70	3.20
SUL-2040S1	TRS-80, VCT-4609 PLUGBOARD	3.90	3.45	3.00
SUL-2040W1	TRS-80, VCT-4609 PLUGBOARD	4.10	3.60	3.15
SUL-2244E1	VECTOR PLUGBOARDS	5.55	4.90	4.25
SUL-2244S1	VECTOR PLUGBOARDS	4.50	3.95	3.45
SUL-2244W1	VECTOR PLUGBOARDS	5.55	4.90	4.25
SUL-2550E1	SDS VERSAFLOPPY, INTEL MULTIBUSS	6.10	5.40	4.70
SUL-2550S1	SDS VERSAFLOPPY, APPLE II	5.95	5.20	4.55
SUL-2550W1	VECTOR 4609, IMSAI P10	6.00	5.30	4.60
SUL-3060E1	INTEL MULTIBUSS, VCT-4608 (1)	6.60	5.85	5.10
SUL-3060S1	INTEL MULTIBUSS, VCT-4608 (1)	6.35	5.60	4.90
SUL-3060W1	INTEL MULTIBUSS, VCT-4608 (1)	6.20	5.50	4.75
SUL-3672E1	VCT-3719(1)(4), VCT-4493(1)	8.00	7.10	6.10
SUL-3672S1	VCT-4494(1), VCT-3719(1)(4)	7.40	6.55	5.65
SUL-3672W1	VCT-4493(1), VCT-4494(1)	7.95	7.00	6.10
SUL-4080E1	COMMODORE PET	7.40	6.55	5.70
SUL-4080S1	COMMODORE PET	7.40	6.55	5.70
SUL-4080W1	COMMODORE PET	8.35	7.40	6.40
SUL-4386E1	COSMAC ELF	7.80	6.90	6.00
SUL-4386S1	COSMAC ELF	9.30	8.25	7.15
SUL-4386W1	COSMAC ELF	9.60	8.50	7.35
SUL-50100E1	ELF PRODUCTS	9.40	8.30	7.20
SUL-50100S1	ELF PRODUCTS	9.45	8.35	7.25
SUL-50100W1	ELF PRODUCTS	8.50	7.50	6.50

GOLD S-100 CONNECTORS

PRIORITY ONE distributes the TI S-100 Card Edge Connectors at tremendous volume for prices others only wish they could duplicate.

SOLDER TAIL PRICE Part No.	1-9	10-24	25-99	100-249
TI-S100 STG	3.20	2.90	2.50	2.20

WIRE WRAP PRICE Part No.	1-9	10-24	25-99	100-249
TI-S100 WWG	4.00	3.75	3.50	3.25

Connector pricing based on gold not exceeding $700.00 per oz.

 PLEASE ORDER BY PART NUMBER

PRIORITY ONE ELECTRONICS

WIRE WRAPPING TOOLS AND WIRE

"HOBBY" WIRE WRAPPING TOOL BATTERY POWERED

For .025" (0,63mm) sq. post "MODIFIED" wrap, positive indexing anti-overwrapping device.

OKM-BW2630	TOOL	$19.95
OKM-BC1	BATTERIES AND CHARGER	$11.00
OKMBT-30	BIT FOR AWG 30	$3.95
OKMBT-2628	BIT FOR AWG 26-28	$7.95

USE "C" SIZE NI CAD BATTERIES NOT INCLUDED

HOBBY WRAP TOOLS

Wire-wrapping, stripping, unwrapping tool for AWG 30 on .025 (0.63mm) Square Post

OKM-WSU-30M	MODIFIED WRAP	$7.95

TRI-COLOR DISPENSER

- 3 Rolls of Wire in one dispenser.
- 3 Colors: Blue, White, Red. 50 ft. of each color.
- AWG 30 (0,25mm) KYNAR* Insulated Wire.
- Built-in Plunger cuts wire to desired length.
- Built-in Stripper strips 1" of insulation.
- Refillable (for refills, see below).

OKM-WD-30-TRI	TRI-COLOR DISPENSER	$7.95

TRI-COLOR DISPENSER REPLACEMENT ROLLS

- AWG 30 (0,25mm) KYNAR* Insulated Wire.
- 3 Colors: Blue, White, Red, 50 ft. each color.
- Silver plated, solid conductor, easy stripping.

OKM-R-30-TRI	REPLACEMENT ROLLS	$5.95

DIP IC INSERTION TOOLS WITH PIN STRAIGHTNER

Narrow profile permits tool to work on densely spaced patterns, while unique insertion mechanism assures accuracy as well as excellent "feel". Finally, the tool includes a remarkable pin straightener built into the handle. Simply insert the IC, rock it on the straightening saddle, and push down on the tool. An automatic ejector delivers the IC ready to be placed in the insertion end for installation in your board or socket.

OKM-INS-1416	14-16 PIN DIP/IC INSERTER	$3.49

Ground strap may be easily attached for highly sensitive MOS & CMOS IC's. Durable chrome plated ABS construction features precision parts for long life and easy one-hand operation. (Ground strap not included.)

OKM-MOS-1416	14-16 PIN, MOS CMOS SAFE INSERTER	$7.95
OKM-MOS-2428	24-28 PIN, MOS CMOS SAFE INSERTER	$7.95

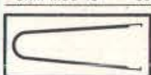
36-40 PIN CMOS-SAFE IC INSERTION TOOL

Unique new insertion tool. Also aligns bent-out pins. A twist of the handle compresses the pins to proper .600 inch spacing and locks the IC into the tool. Then simply place the tool on the socket and depress the plunger for instant and accurate insertion. Features heavy chrome plating throughout for reliable static dissipation. Includes terminal lug for attachment of ground strap.

GROUND STRAP NOT INCLUDED

OKM-MOS-40	36-40 PIN CMOS SAFE INSERTION TOOL	$7.95

DIP IC EXTRACTOR TOOL



The EX-1 Extractor is ideally suited for hobbyist or lab engineer. Featuring one piece spring steel construction. It will extract all LSI, MSI and SSI devices of from 8 to 24 pins.

OKM-EX-1	EXTRACTOR TOOL	$1.49

WK-7 IC INSERTION

OKM-WK-7	COMPLETE IC INSERTER/EXTRACTOR KIT	$29.95
	INDIVIDUAL COMPONENTS	
OKM-MOS-1416	14-16 PIN MOS CMOS SAFE INSERTER	$7.95
OKM-MOS-2428	24-28 PIN MOS CMOS SAFE INSERTER	$7.95
OKM-MOS-40	36-40 PIN MOS CMOS SAFE INSERTER	$7.95
OKM-EX-1	14-16 PIN EXTRACTOR TOOL	$1.49
OKM-EX-2	24-40 PIN CMOS SAFE EXTRACTOR TOOL	$7.95

WIRE DISPENSER

- With 50 ft. Roll of AWG 30 KYNAR* wire-wrapping wire.
- Built-in Plunger cuts wire to desired length.
- Built-in Stripper strips 1" of insulation.
- Refillable (For refills, see below)

OKM-WD-30-B	BLUE WIRE	$4.95
OKM-WD-30-Y	YELLOW WIRE	$4.95
OKM-WD-30-W	WHITE WIRE	$4.95
OKM-WD-30-R	RED WIRE	$4.95

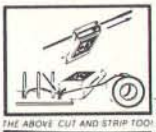
DISPENSER REPLACEMENT ROLLS

Wire for wire-wrapping AWG-30 (0,25mm) KYNAR* wire, 50 ft. roll, silver plated, solid conductor, easy stripping.

OKM-R-30B-0050	30-AWG BLUE 50 FT. ROLL	$2.98
OKM-R-30Y-0050	30-AWG YELLOW50 FT. ROLL	$2.98
OKM-R-30Y-0050	30-AWG WHITE 50 FT. ROLL	$2.98
OKM-R-30R-0050	30-AWG RED 50 FT. ROLL	$2.98

*KYNAR-PENNWALT

"CLIP AND STRIP" TOOL

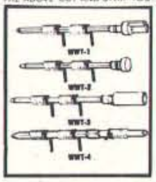


A unique new design for stripping 1" insulation from 30AWG wire. Insert wire, squeeze tool closed to cut off excess wire, pull wire through stripping slot to remove insulation. Handy pocket size, only 1¾" x 1" x ⅜". Shipping Weight 4 oz.

OKM-CAS-130	CLIP AND STRIP	$1.98

THE ABOVE CUT AND STRIP TOOL IS NOT APPLICABLE FOR MYLENE OR TEFLON INSULATION

TERMINALS

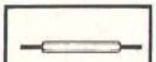


- .025" (0,63mm) Square Post.
- 3 Level Wire-Wrapping.
- Gold Plated.

25 PER PACKAGE

OKM-WWT-1	SLOTTED TERMINAL	$4.98
OKM-WWT-2	SINGLE SIDED TERMINAL	$2.98
OKM-WWT-3	IC SOCKET TERMINAL	$4.98
OJM-WWT-4	DOUBLE SIDED TERMINAL	$1.98

TERMINAL INSERTING TOOL



For inserting WWT-1, WWT-2, WWT-3, and WWT-4 Terminals into .040" (1,0 mm) Dia. Holes.

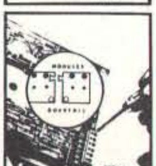
OKM-INS-1	INSERTING TOOL	$2.49

P.C.B. TERMINAL STRIPS

The TS strips provide positive screw activated clamping action, accommodate wire sizes 14-30 AWG (1.8 - 0,25mm). Pins are solder plated copper, .042 inch (1mm) diameter, on .200 inch (5mm) centers.

OKM-TS-4	4-POLE	$1.69
OKM-TS-8	8-POLE	$2.59
OKM-TS-12	12-POLE	$3.49

MODULAR TERMINAL STRIPS

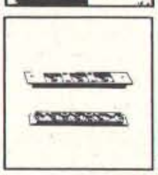


For versatile, economical connections especially in bread-boarding and short runs. Two circuit modules are dovetailed together in various combinations to form printed circuit board terminals with the desired number of circuits. Making stable, low-resistance connections with only a screwdriver, the space-saving terminals take conductors from 26 through 16 AWG conforming to .20 inch (5,08mm) hole spacing on boards up to .126 inch (3,20mm) thick.

OKM-TS-6MD	2-POLE	$1.79

(3 per Package)

PC CARD GUIDES

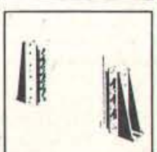


TR-1 consists of 2 guides precision molded with unique spring finger action that dampens shock and vibration, yet permits smooth insertion or extraction. Guides accommodate any card thickness from .040-.100 inches.

QUANTITY — ONE PAIR (2 PCS.)

OKM-TR-1	CARD GUIDES	$1.89

PC CARD GUIDES & BRACKETS



TRS-2 kit includes 2 TR-1 guides plus 2 mounting brackets. Support brackets feature unique stabilizing post that permits secure mounting with only 1 screw.

QUANTITY — ONE SET (4 PCS.)

OKM-TRS-2	GUIDES & BRACKETS	$3.79

BW928 INDUSTRIAL WRAPPING TOOL

GREAT FOR PRODUCTION!

- Accepts Industrial Bits & Sleeves (Gardner Denver or equivalent)
- Auto-Indexing
- Modified Wrap
- Back-Force available (Recommended for #30)

Part No.	Description	Price
OKM-BW928	Tool	$49.95
OKM-BW928BF	Tool (with Backforce)	52.95
OKM-BT30I	#30 Bit & Sleeve	29.50
OKM-BC1	Batteries & Charger	11.00

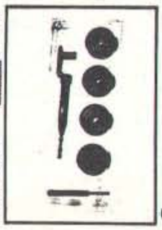
JUST WRAP REPLACEMENT ROLLS

OKMR-JW-B	BLUE WIRE	50 ft. Roll	$2.98
OKMR-JW-W	WHITE WIRE	50 ft. Roll	$2.98
OKMR-JW-Y	YELLOW WIRE	50 ft. Roll	$2.98
OKMR-JW-R	RED WIRE	50 ft. Roll	$2.98

UNWRAP TOOL FOR JUST WRAP

OKM-JUW-1	UNWRAPPING TOOL	$3.49

JUST WRAP KIT



CONTAINS:

- JUST WRAP Tool
- Roll of Blue Wire, 50 ft.
- Roll of White Wire, 50 ft.
- Roll of Yellow Wire, 50 ft.
- Roll of Red Wire, 50 ft.
- Unwrapping Tool

OKM-JWK-6	JUST WRAP KIT	$24.95

PRB-1 DIGITAL LOGIC PROBE

Shipping Weight 4 oz.

Compatible with DTL, TTL, CMOS, MOS and Microprocessors using a 4 to 15V power supply. Thresholds automatically programmed. Automatic resetting memory. No adjustment required. Visual indication of logic levels, using LED's to show high, low, bad level or open circuit logic and pulses. Highly sophisticated, shirt pocket portable (protective tip cap and removable coil cord).

- DC to > 50 MHZ
- 10 Nsec. pulse response
- 120 K Ω impedance
- Automatic pulse stretching to 50 Msec.
- Automatic resetting memory
- Open circuit detection
- Automatic threshold resetting
- Compatible with all logic families 4-15 VDC
- Range extended to 15-25 VDC with optional PA-1 adapter
- Supply O.V.P. to ± 70 VDC
- No switches/no calibration

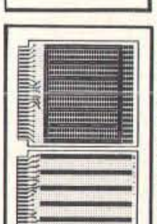
OKM-PRB-1	DIGITAL LOGIC PROBE	$36.95

VACUUM VISE

Unique vacuum based light duty vise for precision handling of small components and assemblies. Rugged ABS construction, 1½" (38mm) wide jaws, 1¼" (32mm) travel for maximum versatility. Also features screw lugs for permanent installation. (mounting screws included)

OKM-VV1	VACUUM VISE	$3.49

PC BOARD



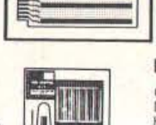
The 4 x 4.5 x 1/16 inch board is made of glass coated EPOXY Laminate and features solder coated 1 oz. copper pads. The board has provision for a 22/44 two sided edge connector, with contacts on standard .156 spacing. Edge contacts are non-dedicated for maximum flexibility.

The board contains a matrix of .040 in. diameter holes on .100 inch centers. The component side contains 76 two hole pads that can accommodate any DIP size from 6-40 pins, as well as discrete components. Typical density is 18 of 14-Pin DIP's. Components may be soldered directly to the board or intermediate sockets may be used for soldering or wire-wrapping.

Two independent bus systems are provided for voltage and ground on both sides of the board. In addition, the component side contains 14 individual busses running the full length of the board for complete wiring flexibility. These busses enable access from edge contacts to distant components. These busses can also serve to augment the voltage or ground busses, and may be cut to length for particular applications.

OKM-H-PCB-1	HOBBY BOARD	$4.99

WIRE WRAPPING KIT WK-4



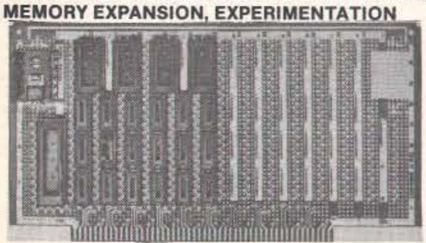
Contains: Hobby Wrap Tool WSU-30M, Wire Dispenser WD-30-B, (2) 14 DIP's, (2) 16 DIP's, Hobby Board H-PCB-1, DIP IC Insertion Tool INS-1416, DIP IC Extractor Tool EX-1 and PC Edge Connector CON-1

OKM-WK-4B(BLUE)	WIRE-WRAPPING KIT	$25.99

CA, AK, HI, CALL (213) 894-8171

Vector S-100 PRODUCTS

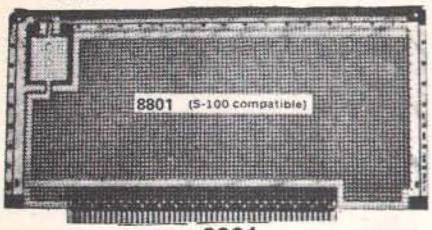
COMPATIBLE PLUGBOARDS FOR INTERFACE, MEMORY EXPANSION, EXPERIMENTATION



VCT- 8800V

Universal Microcomputer/processor plugboard, use with S-100 bus. Complete with heat sink & Hardware. 5.3" x 10" x 1/16."

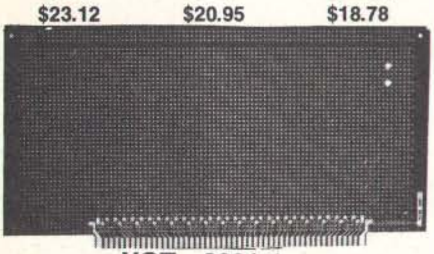
1 - 4	5 - 9	10 - 24
$22.48	$20.37	$18.26



VCT- 8801

Individual tinned square pads surround most holes. Ideal for mounting components by "tack" soldering. Top of board pod free for mounting I/O connectors.

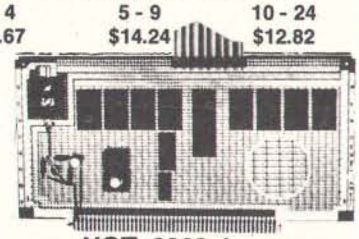
1 - 4	5 - 9	10 - 24
$23.12	$20.95	$18.78



VCT- 8801-1

Plain no etched circuitry except contacts. Produces maximum flexibility.

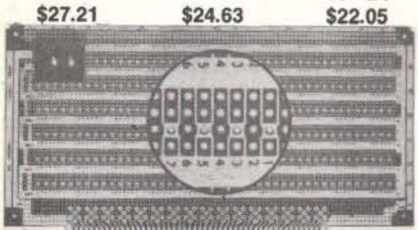
1 - 4	5 - 9	10 - 24
$15.67	$14.24	$12.82



VCT- 8802-1

Pad per 2 holes. Two-hole pads allow tack soldering of socket, plus second hole for component leads.

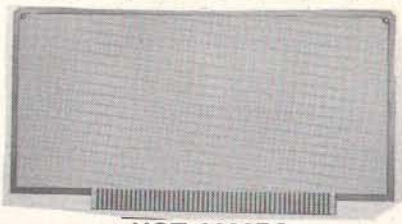
1 - 4	5 - 9	10 - 24
$27.21	$24.63	$22.05



VCT- 8804

"ANY DIP" has full power and ground planes back to back. Boards accommodates .3, .4, .6, .9" Dips.

1 - 4	5 - 9	10 - 24
$24.67	$22.34	$20.02



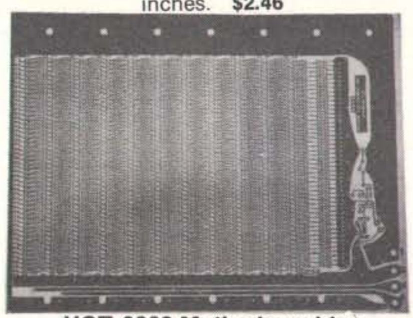
VCT-8800R2

- Make your own custom S-100-Bus circuit board.
- Just expose, develop and etch in lab or home.
- No camera or dark room needed.
- Coated both sides with copper and + Positive Photoresist.
- S-100-Bus card size: 5.3" x 10" x .062"
- Gold plated 50/100 contacts on .125" centers, continuous into copper fields both sides.
- Complete step-by-step instructions.
- Expose with bright sun or G.E. RS 275 watt suntan lamp.
- Two layout papers: 1 clear, 1 with 0.1" grid.

VCT-8800R2

1 - 9	10 - 49	50 - 99
$22.42	$20.32	$18.22

VCT-0088-21-45 developer for + Positive Photoresist, 6 oz. concentrate makes 30 fluid ounces, develops 2400 square inches. **$2.46**



VCT-8803 Motherboard for S-100 Bus Microcomputer

Mounts 11 receptacles with 100 contacts or 10 receptacles plus interconnections to smaller boards for expansion. Connectors mount with tabs protruding through .038 inch (1 mm) diameter holes in rows spaced .250 inch (6.4 mm) on each connector position and 0.75 inch (19 mm) between connector positions. Includes etched circuit and instructions for active or passive terminations plus 12 tantalum capacitors for +5, +12, -12 volt buses, and spacers for mounting in Vector VP1 or VP2 case. G-10 epoxy glass board with 2 ounce copper, solder plated circuitry plus solder mask to avoid accidental short circuits. Large buses: +5V and GND (10 amps), +12V or 16V (7 amps). Current ratings are per MIL-STD-275 with 10° rise.

Shipping weight 2 pounds (.9kg) **$29.50**

VCT-3690-12 Card Extender

Card Extender has 100 contacts — 50 per side on .125 centers. Attached connector is compatible with S-100 Bus Systems.

1 - 4	5 - 24
$26.64	$24.18

VCT-3690 6.5" 22/44 pin .156 ctrs. Extender **$15.66**
VCT-3690-4 7.5" 36/72 pin .1 ctrs. Extender **$22.76**
VCT-3690-6 11" 22/44 pin .156 ctrs. Extender **$18.80**

21 pairs of nylon guides furnished for snap-in mounting on .25" multiples

VCT-CCK100 $49.80 — Shipping Wt. 10 lbs.

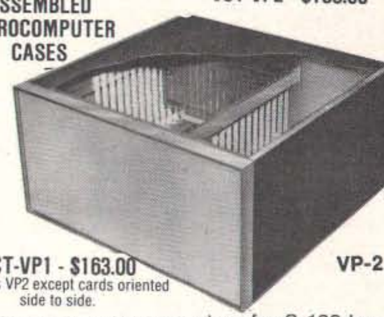
RACH MOUNTABLE CAGE

Especially designed to accommodate S100 size Plugboards, Motorola Exorcisor,™ and Micromodule™ Plugboards. Cage has .081" thick anodized aluminum side walls. Will accommodate Plugboards 4.0" to 8.5" long and 10.0" to 11.5" wide by 1/16" thick. Cages assemble quickly.

*™Registered Motorola trademark

VECTOR-PAK ASSEMBLED MICROCOMPUTER CASES

VCT-VP2 - $159.00



VCT-VP1 - $163.00
Same as VP2 except cards oriented side to side.

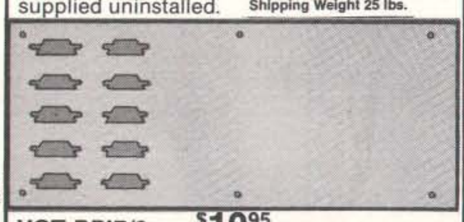
VP-2

Adjustable packaging system for S-100 bus microcomputers, compatible with Altair 8800 and IMSAI 8080 size cards.

- Smart looking, deluxe cases unmarred by unsightly screws or fasteners.
- Finished in dark blue textured vinyl.
- Instantly accessible interiors with slip out covers.
- Removable recessed rear and front panels.
- Fully adjustable interior mounting systems for any card or card spacing within size limitations. No cutting or drilling necessary.
- Perforated bottom cover for cooler operation.

DESCRIPTION

Assembled case with perforated bottom cover. Installed mounting struts for card guides and receptacles or motherboard. Cards top loaded, spanning front to back. Card guide (12 pair) and chassis plate supplied uninstalled. **Shipping Weight 25 lbs.**



VCT-BPI7/9 $10.95

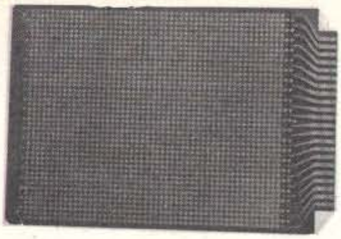
INPUT/OUTPUT CONNECTOR REAR PANEL

- Ten connector cutouts for ITT Cannon DB25S Type 25 pin connector (connectors furnished by user).
- Panel may be installed with cutouts on either right or left side.
- Interchangeable with standard rear panel.
- Connectors may be slipped through the panel for ease of assembly or disassembly.

PLEASE ORDER BY PART NUMBER

PRIORITY ONE ELECTRONICS

Vector

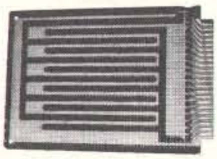


VCT-3662	6.5" x 4.5"	**$ 8.69**
VCT-3662-2	9.6" x 4.5"	**$10.85**

P pattern plugboards for IC's Epoxy Glasss 1/16" 44 pin. con. spaced .156

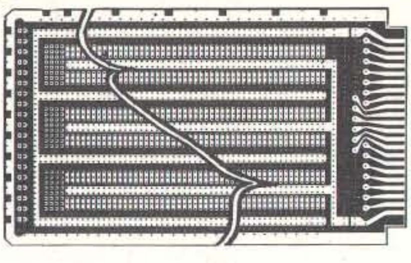
VCT-3719-1 **$9.28**
Same as 3662 except 36/72 con. on .1 centers

VCT-3719-4 **$11.73**
Same as 3662-2 except 36/72 con. on .1 centers



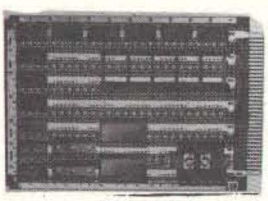
VCT-3682	9.6" x 4.5"	**$13.63**
VCT-3682-2	6.5" x 4.5"	**$11.04**

Hi-Density Dual In-Line Plugboard for Wire Wrap with Power and Grd. Bus Epoxy Glass 1/16" 44 pin con. spaced .156



VCT-3677	9.6" x 4.5"	**$13.33**
VCT-3677-2	6.5" x 4.5"	**$10.68**

Gen. Purpose DIP Boards with Bus Pattern for Solder or Wire Wrap Epoxy Glass 1/16" 44 pin con. spaced .156



VCT-4493-1

4.5" x 9.6" Universal pattern for any .3," .4," .6," .9," spaced DIPS. Holds 63 Dips. Accommodates additional I/O connectors 36/72 con. on .1 centers.

1-4	5-9	10-24
$23.83	$21.55	$19.27

VCT-4494-1

Same as 4493 except 22/44 con. on .156 centers.

1-4	5-9	10-24
$20.68	$18.67	$16.67

VCT-4493

4.5" x 6.5" Universal pattern for any .3," .4," .6," .9," spaced DIPS. Accommodates additional I/O connectors 36/72 con. on .1 centers.

1-4	5-9	10-24
$19.37	$17.59	$15.80

VCT-4494

Same as 4493 except 22/44 con. on .156 centers.

1-4	5-9	10-24
$16.33	$14.79	$13.25

VCT-HA9
Pkg. of 4 **$1.26**
Ejector Card with Roll Pin

VCT-HA9C
Pkg. of 100 **$26.25**

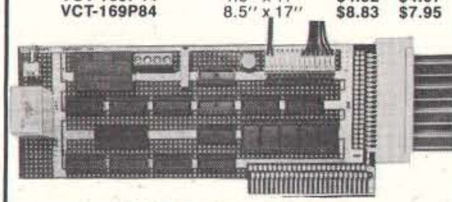
WIRE WRAP POSTS (see next page)

Vector catalog is available to qualified industrial and institutional customers.

1/16 Vector BOARD

.042 dia. holes on 0.1 spacing for IC's

PART NO.	SIZE	PRICE 1-9	10-19
Phenolic			
VCT-64P44-X	4.5" x 6.5"	**$1.56**	**$1.40**
VCT-169P44-X	4.7" x 17"	**$3.69**	**$3.32**
Epoxy Glass			
VCT-64P44	4.5" x 6.5"	**$1.79**	**$1.61**
VCT-84P44	4.5" x 8.5"	**$2.21**	**$1.99**
VCT-169P44	4.5" x 17"	**$4.52**	**$4.07**
VCT-169P84	8.5" x 17"	**$8.83**	**$7.95**



APPLE PLUGBOARD

Vector 4609 Peripheral Interface Plugboard for construction of custom circuits. Plug compatible with Apple II, Commodore PET and Super Kim microcomputers.

VCT-4609

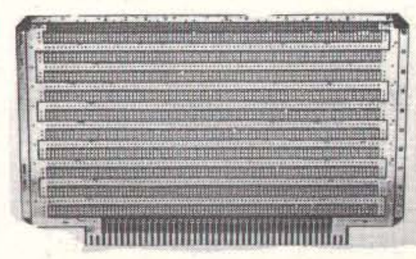
1-4	5-9	10-24
$20.74	$18.79	$16.84

VCT-4607

DEC, LSI-11, PDP8, PDP11, Heath H-11, P Pattern Epoxy Glass, Plug Board 8.43" x 5.187" Dual 36 pin DEC/HEATH Connectors.

1-4	5-9	10-24
$16.26	$14.74	$13.21

MOTOROLA EXORCISER PROTOTYPING BOARDS



VCT-4611

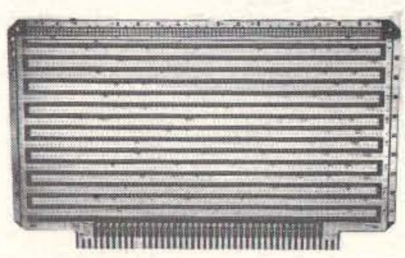
3 hole pads interspersed with power busses (shown above).

1-4	5-9	10-24
$29.95	$26.96	$23.96

VCT-4611-1

Bare board except with edge connector. No power bussing.

1-4	5-9	10-24
$19.95	$17.96	$15.96



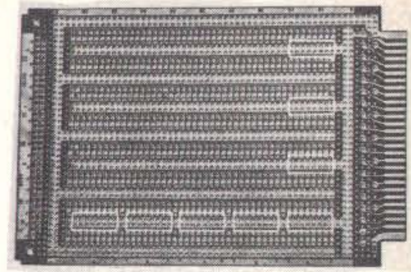
VCT-4611-2

Has only interspersed power busses.

1-4	5-9	10-24
$29.95	$26.96	$23.96

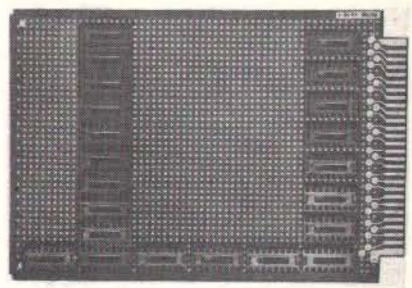
UNIVERSAL MICROCOMPUTER PLUGBOARDS

For STD Bus, Pro-Log Microprocessors and General Use. Size: 4.5" x 6.5" x .052" 28/56 contacts on .125" centers.



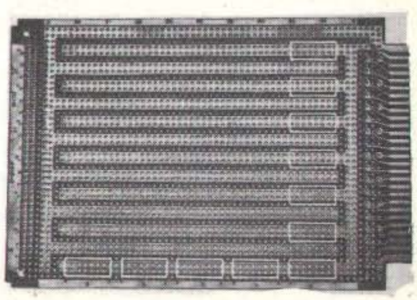
VCT-4610 for soldering or wire wrapping. Mounts 20 16 pin DIP ICs.

1-4	5-9	10-24
$18.95	$17.06	$15.16



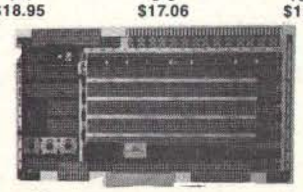
VCT-4610-1 for soldering or wire wrapping. Mounts 59 16 pin DIP ICs.

1-4	5-9	10-24
$15.95	$14.36	$12.76



VCT-4610-2 wire wrapping board. Mounts 35 16 pin DIP ICs.

1-4	5-9	10-24
$18.95	$17.06	$15.16



VCT-4608

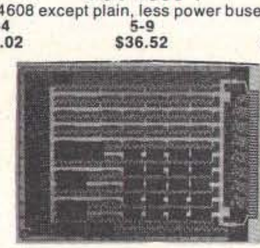
Form and size compatible with INTEL SBL80 Series and NATIONAL BLC 80 Series microcomputer boards. Power and Ground buses on both sides.

1-4	5-9	10-24
$59.23	$53.67	$48.11

VCT-4608-1

Same as 4608 except plain, less power buses.

1-4	5-9	10-24
$40.02	$36.52	$32.92



VCT-4350

Large Microprocessor development plugboard with Zig-Zag Buses 7" x 9.6" Holds 77 DIPs, 80 pin con. spaced .125.

1-4	5-9	10-24
$22.80	$20.69	$18.58

VISIT OUR RETAIL STORE

Vector

SLIT-N-WRAP T.M.

Slit-N-Wrap is easy to use and is up to four times faster than ordinary manual wrapping because there is no wire measuring or pre-stripping required. Slit-N-Wrap tools have a patented action which slits wire insulation while the tools is wrapping wire on .025" square posts. Connections are just as reliable as with conventional wrapping tools. Tests show Slit-N-Wrapped connections exceed Mil Spec requirements for pull-off, are low resistance, and are gas tight. All tools and bits are guaranteed to provide at least 10,000 reliable wraps before bit replacement is required.

Slit-N-Wrap tools with Tefzel insulated wire

P184

Manual wrapping kit with knurled aluminum shaft, replaceable hardened steel bit, and 2 rolls of Tefzel wire.

VCT-P184 *1 lb.* **$30.00**

P184-4T1

Motorized Slit-N-Wrap kit, complete with rechargeable NiCad batteries and charger.

VCT-P184-4T *3 lbs.* **$105.00**

AC powered Slit-N-Wrap with pistol grip and trigger for industrial and production use.

VCT-P184-4T1 *3 lbs.* **$105.00**

Hardened steel replacement bit for P184 series.

VCT-P184A *1 lb.* **$16.95**

Tefzel insulate silver plated copper 28 gauge wire for P184 series Slit-N-Wrap tools (2 rolls per package).

VCT-W28-6A	*Green, 0.5 lbs.*	**$5.39**
VCT-W28-6B	*Red, 0.5 lbs.*	**$5.39**
VCT-W28-6E	*White, 0.5 lbs.*	**$5.39**
VCT-W28-6F	*Yellow, 0.5 lbs.*	**$5.39**

P180 Series Tools Using Polyurethane Nylon Coated Wire. Polyurethane nylon coated copper wire is used in all P180 series tools. The small diameter of this insulated 28 gage wire permits two 7-turn wraps on a 0.025 inch (.64 mm) square post occupying only .21 inch (5.3 mm) of post length. Soldering is not required on rectangular posts, but if wrapped on round or irregular posts it may be soldered using a 750°F (399°C) iron which melts the insulation as solder flow occurs.

Manual wrapping kit with 2 rolls of Polyurethane wire.

VCT-P180 *1 lb.* **$25.00**

Motorized Slit-N-Wrap kit, complete with rechargeable NiCad batteries and charger.

VCT-P160-4T *3 lbs.* **$99.50**

AC powered Slit-N-Wrap with pistol grip and trigger for industrial and production use.

VCT-P160-4T1 *3 lbs.* **$99.50**

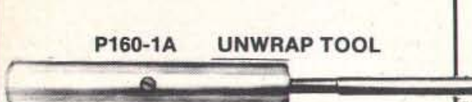
Hardened steel replacement bit for P180 and P160 series.

VCT-P180A *1 lb.* **$14.25**

Polyurethane nylon insulated copper 28 guage wire for P180 and P160 series Slit-N-Wrap tools (3 rolls per package).

VCT-W28-2A	*Green, 0.5 lbs.*	**$2.89**
VCT-W28-2B	*Red, 0.5 lbs.*	**$2.89**
VCT-W28-2C	*Clear, 0.5 lbs.*	**$2.89**
VCT-W28-2D	*Blue, 0.5 lbs.*	**$2.89**

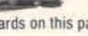
P160-1A UNWRAP TOOL



Unwrapping bit is recessed far enough into the sleeve (spring loaded) to permit unwrapping more than the maximum amount of turns on a 3 wrap post for 26-30 gage wire.

VCT-P160-1B **$12.50**

MICRO CLIP



Vector

For .042" dia. holes, (all boards on this page)

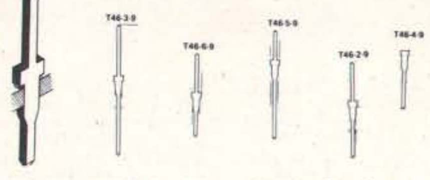
VCT-T42-1/C	**Pkg. of 100**	**$ 1.54**
VCT-T42-1/M	**Pkg. of 1000**	**$ 11.28**
VCT-P149 Hand installing tool		**$ 3.23**

WRAP POST

For .042" dia. holes, (all boards on this page). Bifurcated contacts for soldering components above board and .025" sq. wrap post below board.

T44 will accept a .021" max. dia. lead in bifurcated end, 3 level wrap post.		
VCT-T44/C	**Pkg. of 100**	**$ 2.34**
VCT-T44/M	**Pkg. of 1000**	**$14.35**
A13 Hand installing tool		**$ 4.19**
T68 will accept a .032" max. dia. lead in bifurcated end, 3 level wrap post.		
VCT-T68/C	**Pkg. of 100**	**$ 2.67**
VCT-T68/M	**Pkg of 1000**	**$16.40**
VCT-A13-1 Hand installing tool		**$ 4.15**
T68A will accept a .032" max. dia. lead in bifurcated end, 2 level wrap post.		
VCT-T68A/C	**Pkg. of 100**	**$ 2.57**
VCT-T68A/M	**Pkg. of 1000**	**$14.50**
VCT-A13-1 Hand installing tool		**$ 4.15**

T46 SERIES WRAP POST



Feed-thru wrap post fits plated-thru or plain holes. Sharp Corners on these .025" sq. wrap post bite into wrapped wires for perfect connections.

VCT-T46-2-9/C	**Pkg. 100**	**$ 2.72**
VCT-T46-2-9/M	**Pkg. 1000**	**$20.46**
VCT-T46-3-9/C	**Pkg. 100**	**$ 2.74**
VCT-T46-3-9/M	**Pkg. 1000**	**$20.90**
VCT-T46-4-9/C	**Pkg. 100**	**$ 2.64**
VCT-T46-4-9/M	**Pkg. 1000**	**$16.91**
VCT-T46-5-9/C	**Pkg. 100**	**$ 3.58**
VCT-T46-5-9/M	**Pkg. 1000**	**$28.93**
VCT-T46-6-9/C	**Pkg. 100**	**$ 4.99**
VCT-T46-6-9/M	**Pkg. 1000**	**$42.96**
VCT-P133B Hand installing tool		**$ 3.03**

T107 Bus Strip **T112-1 Bus Link** **T112-2 Bus Link**

T107 Bus Strip

Used for supply busses on Vector board. Holes are .1" Centers 13" long.

VCT-T107	**Pkg. 10**	**$ 4.00**
VCT-T107/C	**Pkg. 100**	**$38.06**

T112-1 Bus Link

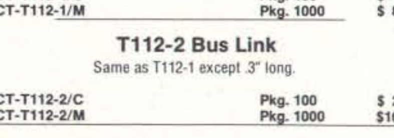
Fits over WW pin and connects pin to bus .2" long.

VCT-T112-1/C	**Pkg. 100**	**$ 1.93**
VCT-T112-1/M	**Pkg. 1000**	**$ 8.14**

T112-2 Bus Link

Same as T112-1 except .3" long.

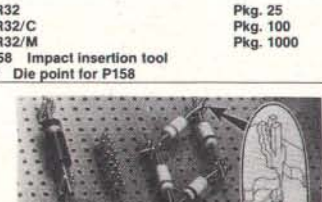
VCT-T112-2/C	**Pkg. 100**	**$ 2.15**
VCT-T112-2/M	**Pkg. 1000**	**$10.95**



SOCKET PINS

Gold plated, machined socket pin for .042" dia. holes. (All boards on this page.) With 3 level wrap post.

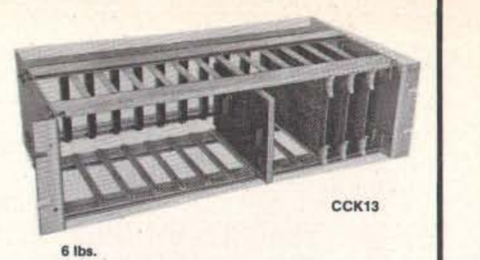
VCT-R32	**Pkg. 25**	**$ 5.75**
VCT-R32/C	**Pkg. 100**	**$ 21.63**
VCT-R32/M	**Pkg. 1000**	**$202.95**
VCT-P158 Impact insertion tool		**$ 20.95**
VCT-D9 Die point for P158		**$ 4.05**



TRIFURCATED KLIP WRAP POST

For .010" to .040" diameter leads above board and .025" sq. 3 level wrap post below board for .042" dia. holes. (All boards on this page.)

VCT-T49/C	**Pkg. 100**	**$ 3.76**
VCT-T49/M	**Pkg. 1000**	**$22.70**
VCT-P156 Hand installing tool		**$ 3.52**



CCK13

6 lbs.

VECTOR-PAK CARD CAGES

19" rack mounting cages are supplied completely assembled, ready for connectors. Models listed accommodate 1/16" thick cards 4.5" maximum to 3" minimum width x 6.5" long. Heavy extruded aluminum cross members (T-Struts) provide strength and easy, infinitely variable connector spacing. 21 pairs of 4-40 connector mounting nuts are furnished installed.

Vector VCT-CCK-3 Card Cage — Has 108 grooves 0.075" wide for 1/16" circuit cards in extruded aluminum plates mounted top and bottom. Vertically and horizontally adjustable rear cross members have 21 pairs of 4-40 nuts in captive grooves for easy connector mounting. Net Each........ **$47.94**

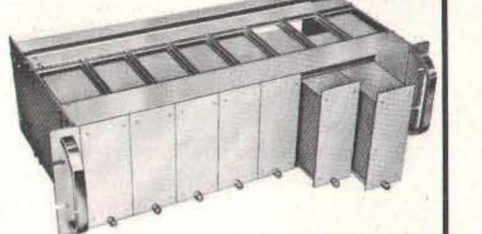
Vector VCT-CCK-13 Card Cage — 19" wide x 5¼" high x 8.9" deep. Has 21 pairs of riveted anodized aluminum card guides. Net Each........ **$50.00**

Vector VCT-CCK13P Card Cage — Same as No. CCK13, except with riveted ABS plastic guides. Net Each **$50.00**

Vector VCT-CCK-14P Card Cages — 19" wide x 5¼" high x 12" deep. Has 21 pairs of riveted card ABS plastic card guides on 0.75" centers. Net Each........ **$57.63**

Vector VCT-CCK13S — Same as CCK-13, except for snap in card guides. Comes in kit form. Net Each **$41.00**

Vector



10 lbs.

The Vector Pak system is a coordinated packaging system which provides modular cases and cages for nearly all Vector Plugboards and many industry-standard plug-in boards. Adjustable rear struts mount almost any PC connector without hole drilling or special brackets. Other sizes and cage parts are available separately — write for catalog.

Vector VCT-CMA3A-20 Cage — 5¼" h. x 8.9" deep fits 19" racks. Holds eight 2" wide x 6½" long x 4.6" high modules (Vector No. EFP204A66). Net Each **$62.56**

Vector VCT-CMA4A-20 Cage — 5¼" h. x 12" deep fits 19" racks. Holds eight 2" wide x 9.6" long x 4.6" high modules (Vector No. EFP204A96). Net Each **$63.99**

Vector VCT-CMA3A-16 Cage — Same as No. CMA3A-20, except holds ten 1.6" wide x 6.6" long x 4.6" high modules (Vector No. EFP164A66). Net Each **$67.91**

2 lbs.

EFP MODULES

All-aluminum modules are anodized finished and have 5¼" high front panels with thumb screw, solid rear panels for connector mounting or circuit board slotting, rear sliding side covers, plus inner multiple-grooved top and bottom rails to hold 1/16" thick circuit boards without special bracketing.

Vector VCT-EFP164A66 Case — Has 6 circuit board grooves spaced 0.150" apart on top and bottom rails, holds circuit boards 4.5" wide x 6.5" maximum length inside. Net Each........ **$9.38**

Vector VCT-EFP204A66 Case — Same as No. EFP164A66, except has 9 circuit board grooves. Net Each **$9.65**

Vector VCT-EFP204A97 Case — Same as No. EFP164A66, except holds circuit boards 4.5" wide x 9.6" maximum length inside. Net Each **$11.07**

CALL FOR VECTOR ITEMS NOT LISTED

PRIORITY ONE ELECTRONICS

E·Z CIRCUIT™ by Bishop Graphics, Inc.

BLACK PRECISION SLIT PC ARTWORK TAPE

18 Yds./Roll

PRICE PER PACKAGE ... $1.15

Tape Size in	Tape Size mm	Cat No.†
.015	0,38	BEZ010151
.020	0,51	BEZ010201
.026	0,66	BEZ010261
.031	0,79	BEZ010311
.040	1,02	BEZ010401
.050	1,27	BEZ010501
.062	1,57	BEZ010621
.080	2,03	BEZ010801
.093	2,36	BEZ010931
.100	2.54	BEZ011001
.125	3,18	BEZ011251
.200	5,08	BEZ012001
.250	6,35	BEZ012501

Donut Pads

Price per Package $1.25

OD In.	OD mm	ID In.	ID mm	Cat. No.†	Qty./Pkg.
.080	2,03	.031	0,79	BEZD216	80
.100	2,54	.031	0,79	BEZD101	64
.120	3,05	.031	0,79	BEZD239	64
.125	3,18	.031	0,79	BEZD102	128
.150	3,81	.031	0,79	BEZD144	112
.160	4,06	.040	1,02	BEZD247	112
.187	4,75	.031	0,79	BEZD138	120
.200	5,08	.031	0,79	BEZD139	120
.250	6,35	.062	1,57	BEZD109	120
.300	7,62	.062	1,57	BEZD111	120

Combination Pack

Tees

90° Elbows

Universal Corners

PRICE PER PACKAGE ... $1.95

Line Width (LW) In.	Line Width (LW) mm	Cat. No.†	Qty. Per Pkg. Tees	Qty. Per Pkg. Elbows	Qty. Per Pkg. Univ. Corners
040	1.02	BEZK101	40	42	21
050	1.27	BEZK102	40	42	20
062	1.57	BEZK103	40	42	20
100	2.54	BEZK104	40	40	12
039	1.0	BEZK105	40	42	21
079	2.0	BEZK106	40	42	12

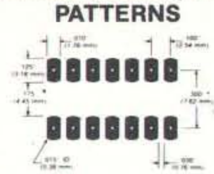
ARTWORK TARGETS

30 per package $1.95

Catalog Number BEZ4020

Catalog Number BEZ4038

PRESSURE SENSITIVE DUAL IN-LINE (DIP) PATTERNS

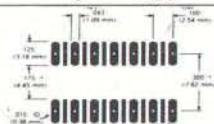


Cut Pad

- Basic large terminal area
- Most commonly used pattern

PRICE PER PACKAGE ... $1.95

No. of Leads	Qty/Pkg.†	Scale	Catalog Number
14	32 28	1X 2X	BEZ6014 BEZ6038
16	32 28	1X 2X	BEZ6004 BEZ6109
18	30 26	1X 2X	BEZ6900 BEZ6901
24	12 12	1X 2X	BEZ6535 BEZ6536
28	12 12	1X 2X	BEZ6903 BEZ6904
40	8 8	1X 2X	BEZ6906 BEZ6907

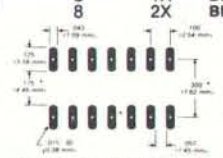


Cut Pad With Conductors

- Use where conductor traces must be routed between pads

PRICE PER PACKAGE ... $1.95

No. of Leads	Qty/Pkg.†	Scale	Catalog Number
14	32 28	1X 2X	BEZ6760 BEZ6761
16	32 28	1X 2X	BEZ6763 BEZ6764
40	8 8	1X 2X	BEZ6984 BEZ6985



Narrow Cut Pad

- Use where a longer terminal area is desired. Can be used with conductors between terminals.

PRICE PER PACKAGE ... $1.95

No. of Leads	Qty/Pkg.†	Scale	Catalog Number
14	32 28	1X 2X	BEZ6013 BEZ6071
16	32 28	1X 2X	BEZ6453 BEZ6244
40	8 8	1X 2X	BEZ6987 BEZ6988

PRINTED CIRCUIT "TO" STYLE PATTERNS

Price per package ... $1.95

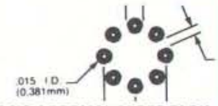
TO-18 PATTERNS

1X SCALE, CAT. NO.† BEZ6003, 36 PER PACK
2X SCALE, CAT. NO. BEZ6048, 36 PER PACK

3-LEAD TO-5 PATTERNS

1X SCALE, CAT. NO.† BEZ6029, 36 PER PACK

8-LEAD TO-5 PATTERNS

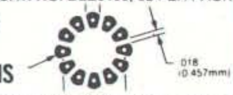


1X SCALE, CAT. NO.† BEZ6021, 36 PER PACK
2X SCALE, CAT. NO. BEZ6197, 32 PER PACK

10-LEAD TO-5 PATTERNS

1X SCALE, CAT. NO.† BEZ6022, 36 PER PACK
2X SCALE, CAT. NO. BEZ6198, 32 PER PACK

12-LEAD TO-5 PATTERNS



1X SCALE, CAT. NO.† BEZ6023, 36 PER PACK
2X SCALE, CAT. NO. BEZ6203, 32 PER PACK

BASIC PRINTED CIRCUIT SEQUENTIAL REFERENCE DESIGNATION KIT

Set Sequence .125" High (3,17 mm)	No. of Sets Per Kit
C1 Thru C20	1
CR1 Thru CR10	1
U1 Thru U20	1
R1 Thru R40	1
Q1 Thru Q20	1
Catalog Number	BEZ80051 $1.95

SEQUENTIAL ALPHABET KIT

Letter Characters .125" High (3,17 mm)	No. of Each Letter Per Set	No. of Sets Per Kit	Cat. No.†
A,E,I,O,R,T	4	2	BEZ80451 $1.95
C,L,N,S,U	3		
D,G,H,M,P,Y	2		
B,F,J,K,Q,V,W,X,Z	1		

SEQUENTIAL NUMBERS KIT

(120 Characters Per Kit)

Set Numbers .125" High (3,17 mm)	No. of Sets Per Kit
01234	12
56789	12
Catalog Number	BEZ80551 $1.95

X-ACTO KNIFE & BLADES

Cat. No. EZ3101 (X-Acto No. 1) $1.50

Cat. No.† BEZ3110 (X-Acto No. 16) 5 blade pack $1.30

Cat. No.† BEZ3108 (X-Acto No. 11) 5 blade pack $1.30

8X MAGNIFIER

- Check PC Artwork & Boards

CAT. NO. BEZ3520 .... $7.95

DRAFTING AIDS

POLYESTER ACCUFILM CIF®

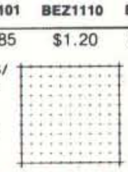
(Clear Inking Film) the ideal film for creating precision PC artwork masters.

- Excellent surface adhesion
- Dimensional stability
- Protective anti-static coating

QUANTITY: 2 SHEETS PER PACKAGE

Sheet Size	8½ x 11 (21,6 x 27,9 cm)	11 X 17 (27,9 x 43,2 cm)	17 x 22 (43,2 x 55.9 cm)
Catalog No.†	BEZ1101	BEZ1110	BEZ1116
Price	$ .85	$1.20	$1.95

10 x 10 LINES/IN. (.100" — 2,54 mm) HEAVY ACCENT ON 10TH LINE



Polyester VALU-GRIDS

Always use a background grid during printed circuit artwork and layout preparation to insure maximum accuracy on the final circuit board. Using a grid system facilitates the placement of components, terminal areas (donut pads) and conductor traces on the layout. Most multiple lead components, such as dual in-line packages (DIPS) conform to standard grid increments.

Sheet Size	Catalog Number	Price Per Sheet
8½" x 11" (21.6 x 27.9 cm)	BEZ10223	$ .95
11" x 17" (27.9 x 43.2 cm)	BEZ10224	1.55
17" x 22" (43.2 x 55.9 cm)	BEZ10225	2.25

ACCUPRINT DRAFTING and TRACING VELLUM PADS

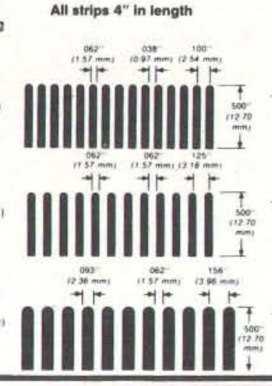
Use these handy sheets of Bishop's ACCUPRINT drafting vellum for preliminary printed circuit artwork sketches and layouts. **50-Sheet Pad**

SAME GRID PATTERN AS ABOVE

Sheet Size	Plain (Ungridded) 16# .0027" Thick (0.07 mm)	10 x 10 GRID Dropout Blue 16# .0027" Thick (0.07 mm)
8½" x 11" (21,6 x 27,9 cm)	-------	BEZ20207 $ 6.45
11" x 17" (27,9 x 43,2 cm)	BEZ20211 $ 9.95	BEZ20208 $11.40

INSERTION-TYPE CONNECTOR PATTERNS

All strips 4" in length

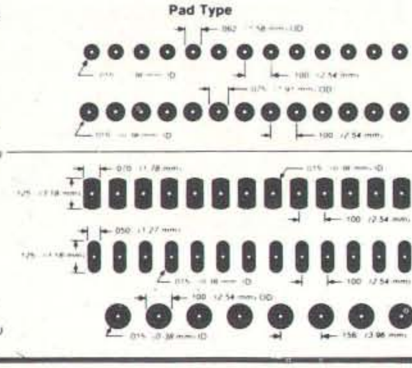


Center to Center Spacing	Scale	Contacts/Strip	Strips per Pack/Catalog Number	Strips/Pack	Center to Center Spacing
.100" (2.54 mm)	1X	44	2 BEZ6805 $1.95	2	.100" (2.54 mm)
	2X	22	5 BEZ6809 $1.95	5	
.125" (3.175 mm)	1X	35	2 BEZ6704 $1.95	2	
	2X	17	4 BEZ6716 $1.95	4	
.156" (3.962 mm)	1X	28	3 BEZ6708 $1.95	3	.156" (3.96 mm)
	2X	14	5 BEZ6720 $1.95	5	

MULTI-PURPOSE PRESPACED PADS

Use to speed tape-up for rows of axial lead component pads or add DIP sizes

Pad Type



Scale	Scale/Pads Per Strip	Strips Per Pack	Catalog Number
1X	40	6	BEZ5000 $1.95
2X	20	7	BEZ5001 $1.95
1X	1X 40	6	BEZ5003 $1.95
2X	2X 20	7	BEZ5004 $1.95
1X	1X 40	6	BEZ5017 $1.95
2X	2X 20	7	BEZ5018 $1.95
1X	1X 40	6	BEZ5020 $1.95
2X	2X 20	7	BEZ5021 $1.95
2X	2X 13	7	BEZ5015 $1.95
1X	1X 25	6	BEZ5014 $1.95

PLEASE BE SURE TO ORDER BY PART NUMBER ONLY!

NEW! E-Z BUS™ MICROPROCESSOR

Your Direct Route To Efficient, Creative and

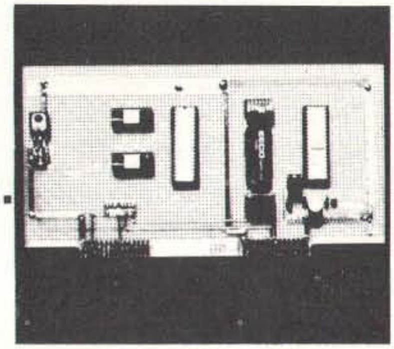
Cut The High Cost Of Creativity! Combine E-Z CIRCUIT's COPPER ELECTRONIC PACKAGING & PROTOTYPING SYSTEM With E-Z CIRCUIT's New E-Z BUS™ CARDS And Plug Unlimited Flexibility Into Your Microprocessor System — At A Remarkably LOW COST!

It's As E-Z As...

1.

2.

3.



E-Z CIRCUIT's Unique New Pressure-Sensitive COPPER Printed Circuit Design System Lets You Create Your Own Printed Circuit Boards Quickly, Easily and Professionally .... Without Artwork, Photography, Or Etching!

E-Z CIRCUIT's Unique Copper PC Design System is an aerospace-proven concept in circuit packaging that lets you construct prototypes and limited quantity production printed circuit boards directly from engineering sketches and logic flow diagrams. Using this system, you can make production quality PC boards without artwork, photography, screening, etching, plating or any other production problems and time delays.

The flexibility and simplicity of E-Z CIRCUIT's Copper Printed Circuit Design System reduces construction time, facilitates problem solving, speeds design changes and insures a highly reliable, technically precise PC board prototype or microprocessor interface.

NEW! E-Z BUS™ CARDS

Plain Microprocessor Plugboards Designed To Permit Maximum Creativity In Program Experimentation, Interface Design, And Memory Expansion

Want to build your own microprocessor interface without the hassle of modifying standard hardware? E-Z CIRCUIT's plain, pre-shaped, pre-drilled microprocessor plugboards free your design of unnecessary contacts, etching, and other extras that cost you money, yet serve no purpose in your design! Using E-Z CIRCUIT's new Bus Cards as a base, you build the precise microprocessor design, modem control, motor control or memory expansion you require, with cost, composition, quality and reliability completely under your control.

We have an E-Z BUS™ Card specially designed to plug into your state-of-the-art microprocessor system.

- **Mount any combination of E-Z CIRCUIT™ pressure-sensitive copper dual-in-lines (DIPS), and E-Z CIRCUIT wrap post sockets plus transistors, resistors, capacitors, E/M or SS relays, terminals and switches.**
- **Plain, non-etched surface allows unrestricted component placement and maximum flexibility for the implementation of your individual design**
- **.042" diameter holes on 0.1" grid centers**
- **Precision pre-shaped for total compatability with microprocessor units specified**
- **Ideal for use with E-Z CIRCUIT's pressure-sensitive Copper PC Design System or for soldering and wire wrapping applications using E-Z CIRCUIT's standard hardware and accessories**

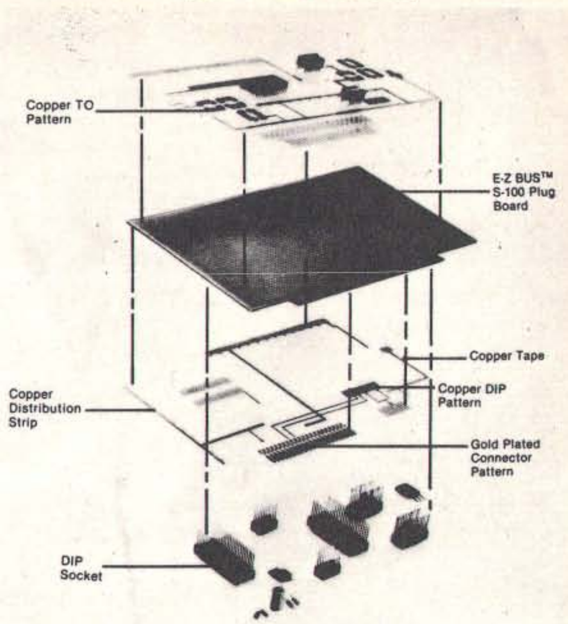
PLEASE ORDER BY PART NUMBER

DESIGN SYSTEM

Successful Interface Design

E-Z CIRCUIT's Comprehensive COPPER ELECTRONIC PACKAGING & PROTOTYPING SYSTEM Contains Everything You Need . . .

(Exploded view of E-Z BUS SYSTEM)



COPPER POWER & GROUND DISTRIBUTION STRIPS

SINGLE PAD DISTRIBUTION STRIP
Order No. BEZ1321 Pkg. of 3 Strips $1.95

SINGLE PAD DISTRIBUTION STRIP
Order No. BEZ1323 Pkg. of 3 Strips $1.95

SINGLE ROW DISTRIBUTION STRIP
Order No. BEZ1325 Pkg. of 6 Strips $1.95

DOUBLE ROW DISTRIBUTION STRIP
Order No. BEZ1326 Pkg. of 3 Strips $1.95

DUAL IN-LINE PACKAGE (DIP) PATTERNS

FOUR PAD (DIP) PATTERN
Accommodates 8, 10, 14, 16, 18, or 20-lead Dual In-Line Devices.
Description: 56 dual rows of four pads each on .100" (2,54 mm) centers. Extra terminal areas accommodate 3 additional terminations for each DIP lead.
Catalog Number BEZ1205
Pkg. of 1 Pattern . . . $1.95

TRIPLE PAD (DIP) PATTERN
Accommodates 8, 10, 14, 16, 18, or 20-lead Dual In-Line Devices.
Description: 56 dual rows of three pads each on .100" (2,54 mm) centers. Extra terminal areas accommodate 2 additional terminations for each DIP lead.
Catalog Number BEZ1206
Pkg. of 1 Pattern . . . $1.95

DOUBLE PAD (DIP) PATTERN
Accommodates 8, 10, 14, 16, 18, or 20-lead Dual In-Line Devices.
Description: 56 dual rows of two pads each on .100" (2,54 mm) centers. Extra terminal areas accommodate 1 additional termination for each DIP lead.
Catalog Number BEZ1223
Pkg. of 2 Patterns . . . $1.95

COPPER CONNECTOR PATTERNS

The combination of a component side pattern and a matching circuit side pattern allows 44 separate connections.
Includes One Connector Pattern Each For Circuit & Component Sides: 22 positions, @ .156" (3,96 mm) Centers
Order No. BEZ1365
1 Set Per Pack . . . $2.90

CONNECTOR CONFIGURATION
The combination of a component side pattern and a matching circuit side pattern allows 44 separate connections.
Includes one connector pattern each for Circuit & Component Sides: 22 positions, @ .156" (3,96 mm) Centers
Order No. BEZ2369
1 Set Per Pack . . . $3.80

PLUG-IN CONNECTOR CONFIGURATIONS
Connector strips are nickel and gold plated over copper

UN-DRILLED Off-Grid Etched Holes
COMPACT CONNECTOR CONFIGURATION

ORDER NO.	PIN CTRS. "A"	NO. PINS	STRIPS PER PKG.	PRICE PER PKG.
BEZ2371	.125"	15	2	$2.40

COPPER PRESPACED DONUT PADS
31 Prespaced Donut Pads/Strip
Order No. BEZ1340
Pkg. of 6 Strips . . . $1.95

COPPER DISCRETE COMPONENT STRIP
Allows mounting of 16 components
.500" Row Center Spacing
.200" Component Spacing
Order No. BEZ1301
1 Strip Per Pack . . . $1.95

COPPER PRESPACED CONDUCTOR STRIP
Order No. BEZ1332
Package of 10 Conductors (1 strip) . . . $1.95

COPPER TEST POINT STRIP
Cut for number of test points required.
Order No. BEZ1381
2 Strips Per Pack . . . $1.95

COPPER TO-5 Style Case PATTERNS

TO-5, 4 Lead
.200" Lead Dia. Spacing
Order No. BEZ1022
8/pkg. . . . $1.95

TO-5, 3-Lead,
.200" Lead Dia. Spacing
Order No. BEZ1021
8/pkg. . . . $1.95

TO-5, 12-Lead,
Spread Dimensions
Order No. BEZ1026
6/pkg. . . . $1.95

TO-5, 10-Lead,
Spread Dimensions
Order No. BEZ1025
6/pkg. . . . $1.95

TO-5, 8-Lead,
Spread Dimensions
Order No. BEZ1024
6/pkg. . . . $1.95

TO-5, 6-Lead,
Dual Transistor Spread Dimensions
Order No. BEZ1023
6/pkg. . . . $1.95

COPPER POWER TRANSISTOR PATTERNS

TO-3 PACKAGE FLANGE MOUNTING
Order No. BEZ11404
5 Patterns/pkg. . . . $1.95

RECTIFIER, SCR TRIAC (Stud Mounting Packages)
Order No. BEZ1405
6 Patterns/pkg. . . . $1.95

SCR & TRANSISTOR FLAT PLASTIC PACKAGE (Thru Hole Mtg.)
Order No. BEZ1406
8 Patterns/pkg. . . . $1.95

COPPER 90° ELBOW

LINE WIDTH	ORDER NO.	QTY.	PRICE
.050" (1,27 mm)	BEZ121	36	$1.95
.100" (2,54 mm)	BEZ124	26	1.95

COPPER DONUT PADS
(Round Terminal Areas)
$1.50 Per Package

(O.D.) Outside Diameter		(I.D.) Inside Diameter		Order No.
Decimal	Metric	Decimal	Metric	
.250"	6,35 mm	.040"	1,02 mm	BEZ7204
.125"	3,18 mm	.040"	1,02 mm	BEZ7202
.100"	2,54 mm	.040"	1,02 mm	BEZ7201

COPPER TAPES

DECIMAL SIZE	METRIC SIZE	ORDER NO.	PRICE PER PKG.
.015	0,38 mm	BEZ300151	$1.00
.031	0,79 mm	BEZ300311	1.00
.050	1,27 mm	BEZ300501	1.00
.100	2,54 mm	BEZ301001	1.00

INSULATIVE TAPE

.100" (2,54 mm) Width . . .
Order No. BEZ311001
.200" (5,08 mm) Width . . .
Order No. BEZ312001
QUANTITY: One 10 Foot Roll Per Package
PRICE: $1.00 Per Package

CUT 'n' PEEL COPPER SHEETS

Ideal for use as power and ground planes.
Order No. BEZ7251, 5" x 6", 3 Sheets Per Pack . . . $1.95

CA, AK, HI, CALL (213) 894-8171

E-Z-HOOK

Model X100W's mini size and Finger-eze Hypo-Action permit direct multiple hookups to one pin, even with wire wraps. Connects vertically or horizontally.

IMPORTANT ORDERING INFORMATION

Most Items Available in 10 Retma Colors:

BK - BLACK	**BR** - BROWN	**RD** - RED
OR - ORANGE	**YE** - YELLOW	**GN** - GREEN
BU - BLUE	**VT** - VIOLET	**GY** - GREY
WH - WHITE	**S** - ONE EACH OF ALL 10 COLORS	

COLOR: Must be replaced with one of the above **ABBREVIATIONS.**

X100W MINI HOOK

2.25"

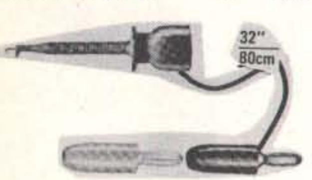
The X100W Mini-Hook combines all the proven features that have made all E-Z-Hook products the most used Test and Trouble Shooting Aids available. The concave Plunger configuration and built-in Washer provide tireless Finger-eze Hypo-Action for fast, safe, short-free testing. Hook is large enough to span most component leads, yet small enough to get into tight places. Tough, for continued use production testing, yet so gentle, it will not damage delicate components. Insulated to a single contact point for true readings.

EZH-X100W - COLOR .... **$.98**
EZH-X100W-S - 1 ea. 10 colors **$9.50**

JUMPER—MINI-HOOK TO MINI-HOOK

SPECIFY LENGTH

Part Number	Wire Lgth. Inches	PRICE
EZH204-12-COLOR	12	$ 2.00
EZH204-12-S 1 ea. 10 colors	12	$19.50
EZH204-24-COLOR	24	$ 2.00
EZH204-24-S 1 ea. 10 colors	24	$19.50
EZH204-36-COLOR	36	$ 2.00
EZH204-36-S 1 ea. 10 colors	36	$19.50

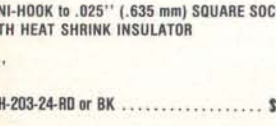


32" 80cm

MINI-HOOK TO STACKING BANANA PLUG
32"/80cm

EZH-201W-RD or BK $1.95

MINI-HOOK TO MINIATURE BANANA PLUG
EZH-CZIW-RD or BK $2.25



MINI-HOOK to .025" (.635 mm) SQUARE SOCKET WITH HEAT SHRINK INSULATOR

24"

EZH-203-24-RD or BK $1.95

E—Z—LINK
0.25" SQUARE SOCKET CONTINUOUS JUMPERS

E-Z-Link Continuous Jumpers were designed to facilitate pre-testing back-panel layouts before final wire wrapping. Consisting of insulated .025" (.635mm) Square Socket Connectors evenly spaced on 3" centers, E-Z-Links individually snap over standard wire wrapping pins to form any desired network. Eliminate costly in-plant measuring, stripping and crimping

EZH-L3025-BL or RD-pkg. of 25 .... $19.95

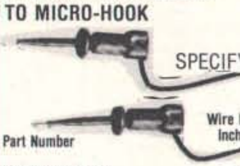
XM MICRO HOOK

1.75"

The XM Micro Hook is designed for difficult IC test connections. Light weight (less than 1 gram) and Finger-eze Hypo Action permit direct hook up to delicate wires where weight and leverage may damage component.

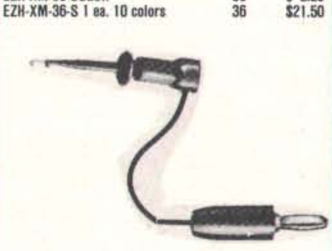
EZH-XM - COLOR .... **$.99**
EZH-XM-S - 1 ea. 10 colors **$9.60**

JUMPER—MICRO-HOOK TO MICRO-HOOK



SPECIFY LENGTH

Part Number	Wire Lgth. Inches	PRICE
EZH-XM-12-COLOR	12	$ 2.25
EZH-XM-12-S 1 ea. 10 colors	12	$21.50
EZH-XM-24-COLOR	24	$ 2.25
EZH-XM-24-S 1 ea. 10 colors	24	$21.50
EZH-XM-36-COLOR	36	$ 2.25
EZH-XM-36-S 1 ea. 10 colors	36	$21.50



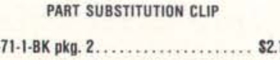
MICRO-HOOK TO STANDARD BANANA PLUG
32"/80cm

EZH-BXM-RD or BK $1.95

TEST CABLE WALL BRACKET

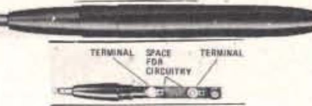
Deep Slots hold Test Cables for Easy Selection. Half slot on each end end permits-mounting of two or more units side by side with no loss of space between racks.

EZH-WB6 - $3.00



PART SUBSTITUTION CLIP

EZH-71-1-BK pkg. 2 .... $2.10



SLIM-LINE TEST CONNECTOR

Slim Line Probe with lightweight construction. Screw on cover for ready access to repair or replace internal circuitry or connections.

EZH-54-1-RD or BK $1.95

XPL PROBE CABLES

Interior Circuit Area capable of holding a wide variety of components.

EZH-406IXPL $10.95

X100W GROUND

BNC MALE TO BNC MALE, WEATHERPROOF, WITH STRAIN RELIEF BOOTS

SPECIFY LENGTH

Cable:RG58C/U

Part Number	Wire Length Inches	PRICE
EZH-1026-24	24	$6.50
EZH-1026-36	36	$6.50
EZH-1026-48	48	$6.50

BNC FEMALE TO BNC

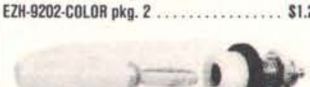
EZH-9220 .... $2.00

BNC FEMALE TO UHF MALE ADAPTER

EZH-9001 .... $3.60

STANDARD BANANA PLUG

EZH-9202-COLOR pkg. 2 .... $1.20



PANEL MOUNT STANDARD BANANA JACK

EZH-9217-COLOR pkg. 4 .... $2.00

BNC FEMALE TO STANDARD DOUBLE BANANA PLUG ADAPTOR

EZH-9225 .... $3.75

BNC FEMALE PANEL RECEPTACLE

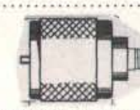
EZH-9230 .... $1.20

BNC MALE CONNECTOR FOR RG58C/U CABLE

EZH-8901 .... $1.75

BNC T ADAPTOR - 2 FEMALE AND 1 MALE CONNECTORS

EZH-9238 .... $4.95

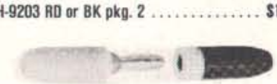


UHF MALE CONNECTOR FOR RG58C/U CABLE

EZH-8911 .... $1.50

STACKING BANANA PLUG

EZH-9203 RD or BK pkg. 2 .... $1.50



STANDARD BANANA JACK

EZH-9210 RD or BK pkg. 2 .... $1.50

STACKING DOUBLE BANANA PLUG

EZH-DB750 .... $2.10

ALLIGATOR CLIP LEAD SETS

10 color coded mini clip leads 15 inches long. 2 each red, green, yellow, black, and white.
CAL-ACL-1015 $2.19

10 color coded standard clip leads 15 inches long. 2 each red, green, yellow, black, and white.
CAL-ACL-1015H $2.69

PLEASE ORDER BY PART NUMBER

PRIORITY ONE ELECTRONICS

NEW

introducing the great *new* "Super-Grip II" test clip from A P Products

22 new models for troubleshooting DIP's safely and quickly

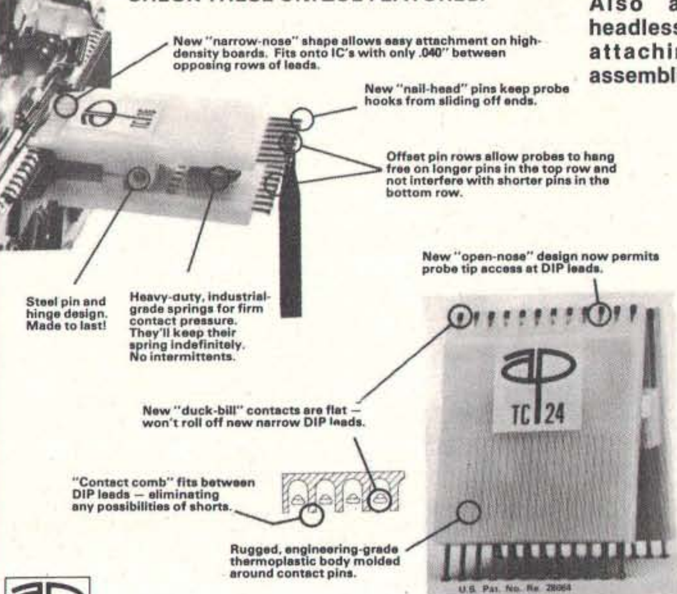
When we invented the original A P Super-Grip test clip in 1967, we thought it was perfect. . . *but look at the improvements we've made!*

In the process, we've retained every invaluable design feature that assures ultra-reliable, non-shorting electrical connections with positive clamping action.

And here are *more* bonus features:

- **Proven Alloy 770 contacts for optimum wiping action.**
- **New one-piece body for each DIP size. TC-14 fits 14-pin DIP, etc.**
- **Simplifies prototype and production testing, field service work, and Quality Control inspection.**

CHECK THESE UNIQUE FEATURES:



Also available with long, headless, test lead pins for attaching AP jumper cable assemblies... Both rows of contacts are the same length (not offset) and do not have "nail-heads".

STANDARD TEST CLIP

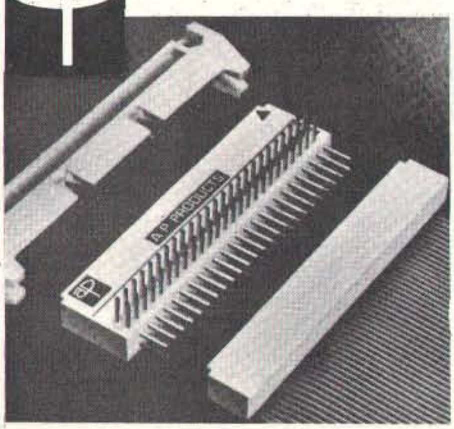
PART NUMBER	MODEL NUMBER	ROW TO ROW SPACING	PRICING SCHEDULE 1-49	50-99	100-249	250-499
APP-923695	TC-8	.3 IN.	$ 7.35	$ 6.90	$ 6.60	$ 6.30
APP-923698	TC-14	.3 IN.	$ 4.50	$ 4.07	$ 3.82	$ 3.69
APP-923700	TC-16	.3 IN.	$ 4.75	$ 4.28	$ 4.04	$ 3.90
APP-923702	TC-16LSI	.5/.6 IN.	$ 8.95	$ 8.40	$ 8.05	$ 7.70
APP-923703	TC-18	.3 IN.	$10.00	$ 9.40	$ 9.00	$ 8.60
APP-923704	TC-20	.3 IN.	$11.55	$10.85	$10.40	$ 9.95
APP-923705	TC-22	.4 IN.	$12.95	$12.17	$11.67	$11.17
APP-923714	TC-24	.5/.6 IN.	$13.85	$12.47	$11.77	$11.36
APP-923718	TC-28	.5/.6 IN.	$15.25	$14.35	$13.75	$13.10
APP-923720	TC-36	.5/.6 IN.	$19.95	$18.75	$17.95	$17.15
APP-923722	TC-40	.5/.6 IN.	$21.00	$19.75	$18.90	$18.05

HEADLESS TEST CLIP

PART NUMBER	MODEL NUMBER	ROW TO ROW SPACING	PRICING SCHEDULE 1-49	50-99	100-249	250-499
APP-923690-8	LTC-8	.3 IN.	$ 7.35	$ 6.90	$ 6.60	$ 6.30
APP-923690-14	LTC-14	.3 IN.	$ 4.50	$ 4.07	$ 3.82	$ 3.69
APP-923690-16	LTC-16	.3 IN.	$ 4.75	$ 4.28	$ 4.04	$ 3.90
APP-923690-16L	LTC-16LSI	.5/.6 IN.	$ 8.95	$ 8.40	$ 8.05	$ 7.70
APP-923690-18	LTC-18	.3 IN.	$10.00	$ 9.40	$ 9.00	$ 8.60
APP-923690-20	LTC-20	.3 IN.	$11.55	$10.85	$10.40	$ 9.95
APP-923690-22	LTC-22	.4 IN.	$12.95	$12.17	$11.67	$11.17
APP-923690-24	LTC-24	.5/.6 IN.	$13.95	$12.47	$11.77	$11.36
APP-923690-28	LTC-28	.5/.6 IN.	$15.25	$14.35	$13.75	$13.10
APP-923690-36	LTC-36	.5/.6 IN.	$19.95	$18.75	$17.95	$17.15
APP-923690-40	LTC-40	.5/.6 IN.	$21.00	$19.75	$18.90	$18.05

another "Faster & Easier" innovation from
A P PRODUCTS INCORPORATED

INTRA-CONNECTOR



PROVIDES FULL ACCESS TO LINES . . . SAVES VALUABLE TIME TESTING FLAT RIBBON CABLE SYSTEMS

- **Permits quick testing of previously unprobeable circuits.**
- **Provides both straight-in and right-angle functions.**
- **Mates with standard .10"x.10" dual-row connectors.**

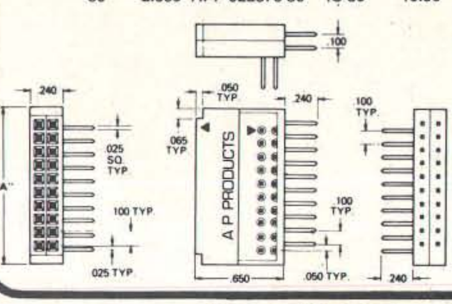
Patent Pending

No. of Contacts	Dim. "A"	Part Number	Model No.	Price Each
20	1.160	APP-922576-20	IC-20	$6.00
26	1.460	APP-922576-26	IC-26	6.90
34	1.860	APP-922576-3	IC-34	8.10
40	2.160	APP-922576-4C	IC-40	9.00
50	2.660	APP-922576-50	IC-50	10.50

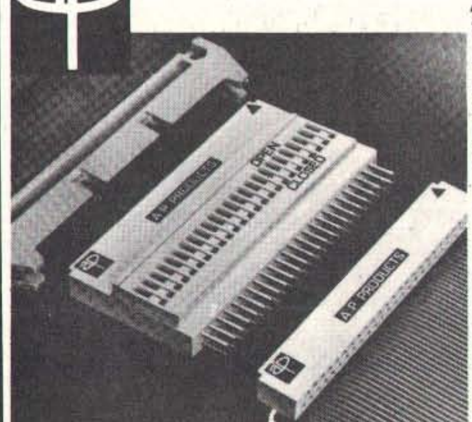
The A P Intra-Connector allows immediate access to previously inaccessible lines. In use, the connector is interjected between mating system connectors to provide external pin contacts that can be probed individually or connected to another cable assembly; pins also can be used to facilitate daisy chaining from a single connector cable end.

Two Intra-Connectors used in conjunction with the A P Intra-Switch form a complete test assembly for probing signals under no load and full load conditions.

Contacts are non-corrosive alloy 770. Body is glass-filled polyester.



INTRA-SWITCH



ALLOWS ANY LINE TO BE OPENED OR CLOSED . . . ANOTHER TIME SAVER IN TESTING FLAT RIBBON CABLE SYSTEMS

- **Permits instant line-by-line switching for diagnostic or QA testing.**
- **Switches actuated with pencil or probe tip.**
- **Mates with standard .10"x.10" dual-row connectors.**

Patent Pending

No. of Contacts	Dim. "A"	Part Number	Model No.	Price Each
20	1.160	APP-922578-2C	IS-20	$12.00
26	1.460	APP-922578-2E	IS-26	13.80
34	1.860	APP-922578-34	IS-34	16.20
40	2.160	APP-922578-40	IS-40	18.00
50	2.660	APP-922578-50	IS-50	21.00

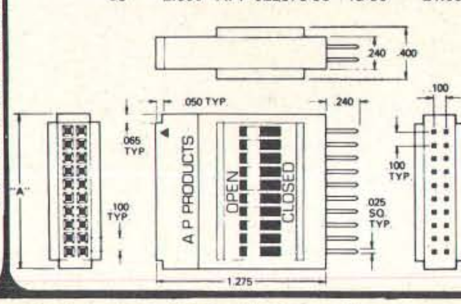
The A P Intra-Switch allows opening and closing any number of lines, individually, at system interconnection points. Applications include switching command signals to control boards as well as switching I/O signals to or from test equipment and for programming optional preset logic functions.

Design features include: low profile for use in confined areas, switch buttons recessed in face of covers to eliminate accidental switching, and position "1" identification.

Wiping action and spherical detents maximize contact integrity.

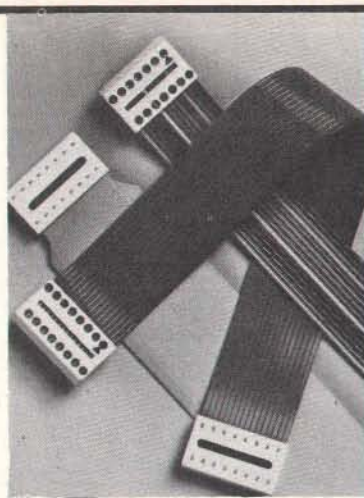
An Intra-switch used in conjunction with two A P Intra-Connectors form a complete test assembly for probing signals under no load and full load conditions.

Contacts are non-corrosive alloy 770. Body is glass-filled polyester.



PLEASE ORDER BY PART NUMBER

DIP JUMPERS



FLAT RIBBON CABLE ASSEMBLIES WITH DIP CONNECTORS

- **Available with 14, 16, 24 and 40 contacts.**
- **Mate with standard IC sockets.**
- **Fully assembled and tested.**
- **Integral molded-on strain relief.**
- **Discrete test points for line-by-line probeability.**

CALL FOR QTY. PRICING

A P DIP Jumpers are the low-cost, high-quality solution for jumpering within a PC board; interconnecting between PC boards, backplanes and motherboards; interfacing Input/Output signals; and more.

All assemblies use rainbow cable. Standard lengths are 6, 12, 18, 24 and 36 inches.

SINGLE-ENDED DIP JUMPERS

No. Contacts	Length 36"
14	APP-924102-36 $3.49
16	APP-924112-36 $3.69
24	APP-924122-36 $5.89
40	APP-924132-36 $10.09

DOUBLE-ENDED DIP JUMPERS

No. Contacts	Length 6"	Length 12"	Length 18"	Length 24"	Length 36"
14	APP-924106-6 $3.59	APP-924106-12 $3.89	APP-924106-18 $4.19	APP-924106-24 $4.49	APP-924106-36 $5.09
16	APP-924116-6 $3.99	APP-924116-12 $4.29	APP-924116-18 $4.59	APP-924116-24 $4.89	APP-924116-36 $5.49
24	APP-924126-6 $6.19	APP-924126-12 $6.69	APP-924126-18 $7.19	APP-924126-24 $7.69	APP-924126-36 $8.69
40	APP-924136-6 $10.29	APP-924136-12 $11.19	APP-924136-18 $12.09	APP-924136-24 $12.99	APP-924136-36 $14.79

A P PRODUCTS INCORPORATED
FLAT RIBBON CABLE ASSEMBLIES

- Choice of 3 types of end connectors molded on and factory tested.
- Daisy chain and single-end also available.
- 5 popular sizes to choose from: 20, 26, 34, 40 and 50 contacts, each with line-by-line probe access holes.
- Choice of 2 cable types and 5 lengths.

FLAT RIBBON CABLE

Stranded, 28 AWG with laminated PVC insulation.

"Electric Pink" cable has red stripe on one edge for orientation. Used only on double-end and daisy chain assemblies.

"Rainbow" cable is coded in standard 10-color sequence on front. Serpentine striping on back aids in identifying wire number and wire group during tear down separation for discrete wire terminations. Used only on single-end jumpers.

CARD-EDGE JUMPERS

Mates with double-sided 1/16" PC board up to 2.050" wide with contact fingers on .100" centers. Probe access holes in back.

CARD-EDGE JUMPERS

No. Contacts	DOUBLE END Electric Pink 6"	SINGLE END Rainbow 36"	DAISY CHAIN (3 connectors) Electric Pink 6"
20	APP-924052-06-R $8.89	APP-924062-36-R $6.49	APP-924092-06-R $13.29
26	APP-924053-06-R $9.99	APP-924063-36-R $7.59	APP-924093-06-R $14.89
34	APP-924054-06-R $12.39	APP-924064-36-R $9.59	APP-924094-06-R $18.49
40	APP-924055-06-R $14.49	APP-924065-36-R $11.19	APP-924095-06-R $21.69
50	APP-924056-06-R $15.69	APP-924066-36-R $12.79	APP-924096-06-R $23.49

SOCKET JUMPERS

Mates with .025" square or dia. posts spaced on patterns of .100" centers. Probe access holes in back.

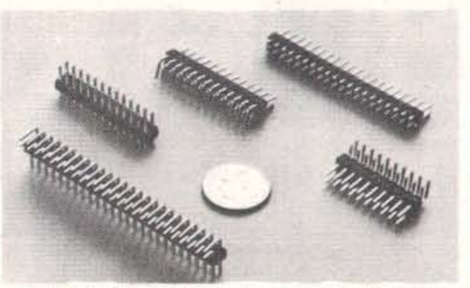
SOCKET JUMPERS

No. Contacts	DOUBLE END JUMPER ASSEMBLIES Electric Pink Cable 6"	18"	36"	SINGLE END Rainbow 36"	DAISY CHAIN (3 connectors) Electric Pink 6"
20	APP-924002-06-R $4.99	APP-924002-18-R $5.69	APP-924002-36-R $6.59	APP-924012-36-R $4.89	APP-924072-06-R $7.39
26	APP-924003-06-R $6.49	APP-924003-18-R $7.29	APP-924003-36-R $8.59	APP-924013-36-R $6.29	APP-924073-06-R $9.69
34	APP-924004-06-R $8.49	APP-924004-18-R $9.59	APP-924004-36-R $11.19	APP-924014-36-R $8.29	APP-924074-06-R $12.69
40	APP-924005-06-R $9.99	APP-924005-18-R $11.59	APP-924005-36-R $12.99	APP-924015-36-R $9.69	APP-924075-06-R $14.89
50	APP-924006-06-R $12.39	APP-924006-18-R $13.99	APP-924006-36-R $16.29	APP-924016-36-R $11.99	APP-924076-06-R $18.49

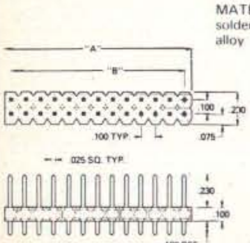
double-row JUMPER HEADERS

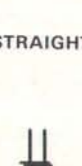
Ideal mates for "GREAT JUMPERS"

- Solder to PC boards for instant plug-in access via socket-connector jumpers
- .025" square posts are molded into plastic header strip on a .10" x .10" matrix
- Choice of straight or right angle configurations



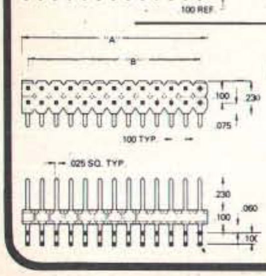
MATERIALS: Dielectric is thermoplastic polyester, unaffected by wave soldering or board cleaning solvents. Posts are fabricated from copper alloy





STRAIGHT

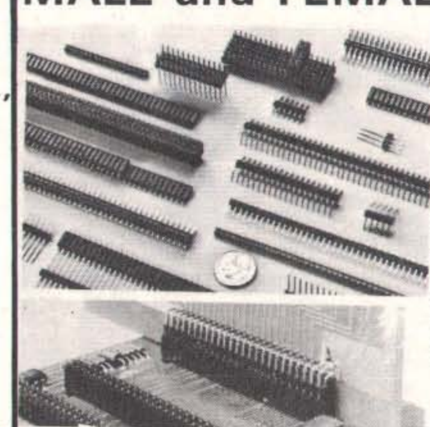
No. Posts	Dim. "A"	Dim. "B"	Part Number	Price 2 sets
20	1.0	0.9	APP-923862-R	$1.59
26	1.3	1.2	APP-923863-R	$1.89
34	1.7	1.6	APP-923864-R	$2.39
40	2.0	1.9	APP-923865-R	$2.79
50	2.5	2.4	APP-923866-R	$3.39



RIGHT-ANGLE

No. Posts	Dim. "A"	Dim. "B"	Part Number	Price 2 sets
20	1.0	0.9	APP-923872-R	$1.89
26	1.3	1.2	APP-923873-R	$2.39
34	1.7	1.6	APP-923874-R	$2.99
40	2.0	1.9	APP-923875-R	$3.49
50	2.5	2.4	APP-923876-R	$4.29

MALE and FEMALE HEADERS

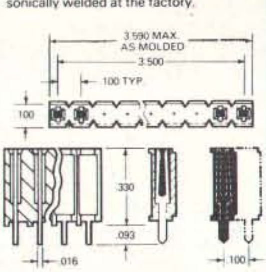


Typical application: board-to-board interconnection.

FEMALE HEADERS

- **Mate with matrices of .025" square or round posts on .100" centers**
- **Ideal as single and dual-row cable connectors for back panels and patchboard matrices**
- **36 "tuning fork" contacts per row in rugged nylon header strip**
- **May be "cut-to-length" for shorter rows of contacts**
- **Single and dual-row strips available**

Female A P Headers are stackable — to maintain .100-inch row-to-row spacing. Solder tails are sized for PC board mounting or cable attachment. Built-in stand-offs facilitate wave soldering and board cleaning. Dual-row headers are ultrasonically welded at the factory.



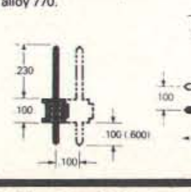
MALE HEADERS—STRAIGHT & RIGHT ANGLE

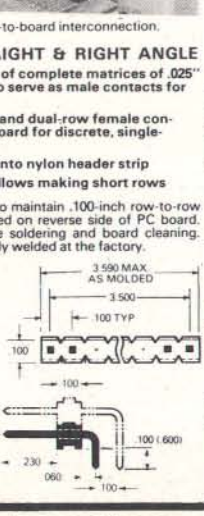
- **For economical attachment of complete matrices of .025" square posts to PC boards to serve as male contacts for interconnection systems**
- **Ideal for mating with single and dual-row female connectors; also use as patch-board for discrete, single-position connections**
- **36 posts per row — molded into nylon header strip**
- **"Break-to-length" feature allows making short rows**

Male A P Headers are stackable to maintain .100-inch row-to-row spacing. All may be wire-wrapped on reverse side of PC board. Built-in stand-offs facilitate wave soldering and board cleaning. Dual-row headers are ultrasonically welded at the factory.

MATERIALS

DIELECTRIC: Thermoplastic polyester.
POSTS: Full-hard copper alloy 770.





Female	Rows	Part No.	Price Ea.
	1	APP-929974	$1.49
	2	APP-929975	$3.09

MODELS for wire wrapping

Male (.600)	Rows	Part No.	Price Ea.
Straight	1	APP-929834-05	$2.29
Straight	2	APP-929836-05	$4.59
Rt. angle	1	APP-929835-03	$2.09
Rt. angle	2	APP-929838-03	$4.59

Tail length shown as (.600) in drawings.

Male (.100)	Rows	Part No.	Price Ea.
Straight	1	APP-929834-01	$1.09
Straight	2	APP-929836-01	$2.29
Rt. angle	1	APP-929835-01	$1.29
Rt. angle	2	APP-929838-01	$2.99

PRIORITY ONE ELECTRONICS

Introducing POWERACE

Use a POWERACE for faster and easier prototyping of all types of electronic circuits

- 1680 solderless, plug-in tie points...will hold up to 18 14-pin DIP's.
- Breadboard elements accept all DIP sizes...including RTL, DTL, TTL and CMOS devices, TO-5's and discretes with leads up to .032" dia.
- All connections to/from switches, indicators, power supplies and meters are made via solderless, plug-in, tie-point blocks on control panels.
- Interconnect with any solid 20 to 30 AWG wire.
- Breadboard elements are mounted on ground planes...ideal for high-frequency and high-speed/low-noise circuits.
- Short-circuit-proof fused power supplies.
- Operate on 110 to 130 VAC at 60 Hz.
- Space-age compact styling and high-grade components permit convenient, organized and quick prototyping. SHIPPING WEIGHT - 4 LBS.

POWERACE 103	POWERACE 101	POWERACE 102
APP-923103	APP-923101	APP-923102
$124.95	**$79.95**	**$124.95**
Triple-output power supply for prototyping both linear and digital circuits.	*The general purpose model for prototyping all types of circuits.*	*The complete digital prototyping lab with FREE logic probe built in!*

All three of these brand new POWERACE models offer a new dimension in convenience for fast, solderless, circuit building and testing.

In addition to built in power supplies, each model incorporates two of the famous AP Products Super Strip universal plug-in breadboards. (See Super Strip page for complete details.) Combined, they provide 16 distribution buses of 25 tie points per bus. These may be jumpered in groups as desired and used for voltage and ground distribution, reset lines, clock lines, shift command, etc. The remaining 1280 tie points are for plugging in circuit components and jumper wires.

As a bonus, a free logic probe is incorporated into model 102.

POWERACE 101

POWER SUPPLY is required and adjustable from +5 to +15 VDC at 600 mA. Ripple/noise is ≤10 mV at full load. Line and load regulation is ≤3%.

METER is built in 0-15 VDC. Inputs are accessible at tie-point blocks on control panel which allows monitoring of power supply or circuits. Meter accuracy is 5% of full scale.

POWERACE 103

TRIPLE-OUTPUT POWER SUPPLY has outputs of +5 VDC at 750 mA; +15 VDC at 250 mA; and −15 VDC at 250 mA. Ripple/noise is ≤10 mV at full load for all outputs. Line and load regulation is ≤1% for all outputs. ±15-volt outputs track.

METER is built in 15-0-15 VDC. Input is accessible at tie-point blocks on control panel which allows monitoring of power supply or circuits. Meter accuracy is 5% of full scale.

TWO LOGIC INDICATORS (LED's) have buffered inputs that require 1 microamp max.

TWO LOGIC SWITCHES, momentary, with debounce circuitry. Both Q and Q outputs can sink 15 mA, and source 5 mA.

TWO DATA SWITCHES with logic 1 and logic 0 outputs have unlimited sinking capabilities and can source 10 mA.

POWERACE 102

POWER SUPPLY is regulated +5 VDC at 1 amp. Ripple/noise is ≤10 mV at full load. Line and load regulation is ≤1%.

PULSE DETECTION WITH MEMORY is built-in. Will detect positive or negative going pulses as short as 10 nanoseconds. Memory is reset by momentary switch on control panel.

THREE LOGIC INDICATORS (LED's) have buffered inputs that require 1 microamp max.

FREE LOGIC PROBE: the above pulse detection with memory plus logic indicator features constitute a free, built-in logic probe!

TWO LOGIC SWITCHES, momentary, with debounce circuitry. Both Q and Q outputs can sink 15 mA, and source 5 mA.

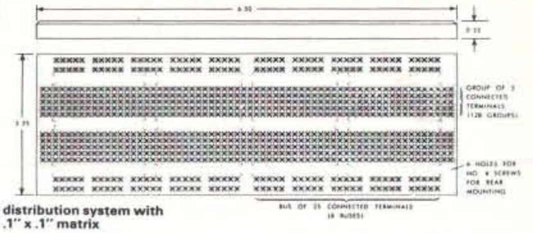
FOUR DATA SWITCHES with logic 1 or logic 0 outputs have unlimited sinking capabilities and can source 10 mA.

CLOCK GENERATOR has the following frequencies available: 1 Hz, 10 Hz, 100 Hz, 1 KHz, 10 KHz and 100 KHz with a 50% duty cycle. C output will sink 15 mA and source 5 mA. C output will sink or source 50 mA.

ONE-SHOT PULSE GENERATOR has output of 7 ms pulse, positive or negative going. Both Q and Q outputs can sink 15 mA and source 400 microamps.

SUPER-STRIPS

UNIVERSAL BREADBOARDING ELEMENTS WITH SOLDERLESS PLUG-IN TIE POINTS



- Combine distribution system with universal .1" x .1" matrix
- 840 solderless, plug-in tie-points
- Accommodates up to 9 14-pin DIPs
- Compatible with all DIP's and discretes with lead diameters to .032"
- Require no special patch cords

The A P Super-Strips combine a power/signal distribution system with a matrix of 128 terminals, each with 5 tie points. The distribution system consists of eight buses, each individual bus consisting of a line of 25 tie points. All tie points are the solderless, plug-in type of the same design used on A P Terminal Strips and A P Distribution Strips.

The Super Strip will accept all DIP's, TO-5's and discrete components with lead diameters up to .032 inches. As many as nine 14-pin DIP's can be accommodated. Any solid wire up to No. 20 A.W.G. can be used for interconnections.

Super-Strips may be permanently mounted with the integral non-shorting instant-mounting backing, or for quick removal, they may be mounted with screws (supplied) on panels up to 1/8" thick. Hardware and mounting templates are provided with every strip. Body material is acetal copolymer.

SS-2 (Alloy 770 terminals) *APA-923252* **$17.00**
SS-1 (Gold-plated terminals) *APA-923748* **$29.95**

TERMINAL and DISTRIBUTION STRIPS

BREADBOARDING BUILDING BLOCKS WITH UNIVERSAL MATRICES OF SOLDERLESS PLUG-IN TIE-POINTS

Model 264L Terminal Strip, 128 five-tie-point terminals, APP-923261 ...... **$12.50**

Model 212R Distribution Strip, 24 four-tie-point terminals, APP-923277 .... **$2.50**

Model 248L Terminal Strip, 96 five-tie-point terminals, APP-923265 ...... **$10.00**

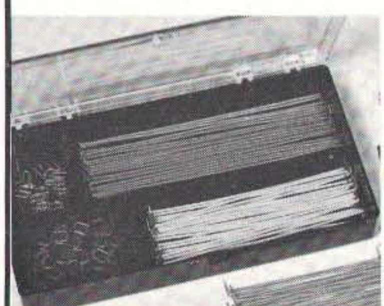
Model 209R Distribution Strip, 18 four-tie-point terminals, APP-923281 .... **$2.25**

Model 264R Terminal Strip, 128 four-tie-point terminals, APP-923289 ...... **$10.00**

Model 154R Terminal Strip, 54 four-tie-point terminals, APP-923291 .... **$6.00**

Model 606R Distribution Strip, 36 four-tie-point terminals, APP-923293 .... **$3.50**

NOTE: THE HEIGHT OF ALL STRIPS IS .32 INCHES



JUMPER WIRE KIT

Each kit contains 350 wires cut to 14 different lengths from 0.1" to 5.0."

Each wire is stripped and the leads are bent 90° for easy insertion.

Wire length is classified by color coding.

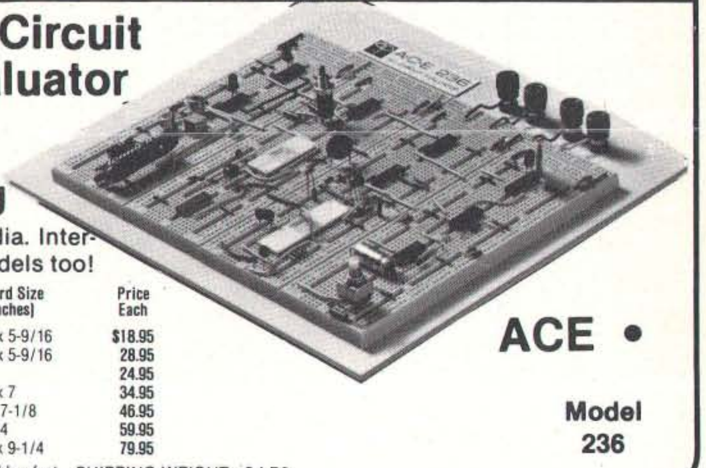
All wire is solid tinned 22 gauge with PVC insulation.

The wires come packed in a convenient plastic box.

APP-92335I JK-1...$10.95

for fast, solderless, plug-in circuit building and testing

All-Circuit Evaluator



Just plug in any components with leads to .032" dia. Interconnect with solid wire up to 20 ga. Assembled models too!

Part No.	ACE Model No.	Tie Points	DIP Capacity	No. Buses	No. Posts	Board Size (Inches)	Price Each
APP-923333	200-K (kit)	728	8 (16's)	2	2	4-9/16 x 5-9/16	$18.95
APP-923332	208 (assem.)	872	8 (16's)	8	2	4-9/16 x 5-9/16	28.95
APP-923334	201-K (kit)	1032	12 (14's)	2	2	16 x 7	24.95
APP-923331	212 (assem.)	1224	12 (14's)	8	2	4-9/16 x 7	34.95
APP-923326	218 (assem.)	1760	18 (14's)	10	2	6-1/2 x 7-1/8	46.95
APP-923325	227 (assem.)	2712	27 (14's)	28	4	8 x 9-1/4	59.95
APP-923324	236 (assem.)	3648	36 (14's)	36	4	10-1/4 x 9-1/4	79.95

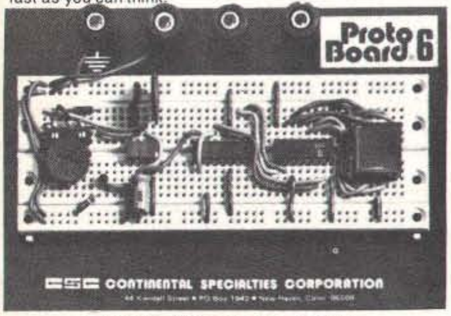
Gold-anodized aluminum base/ground; non-corrosive nickel-silver terminals: 4 rubber feet. SHIPPING WEIGHT - 2 LBS.

ACE • Model 236

CA, AK, HI, CALL (213) 894-8171

PROTO-BOARD® UNITS

All the speed and convenience of QT sockets and Bus Strips plus backplanes and binding posts in both kits and preassembled units. Assemble, test and modify circuits as fast as you can think.



PROTO-BOARD PB-6 KIT— HOLDS 10, 14 PIN IC'S

Contains one preassembled QT-47S socket, two preassembled QT-47B bus strips, four binding posts, metal ground/base plate, non-marring feet and all hardware. Ten minute assembly time. **Size:** 6'' l. x 4'' w. x 1.4'' h. **Weight:** 7 ozs.

GSCPB6 .... $19.95

PROTO-BOARD PB-100 KIT— HOLDS 10, 14 PIN IC'S

Contains two preassembled QT-35S sockets, one preassembled QT-35B bus strip, two binding posts, non-metallic base plate, non-marring feet and all hardware. Ten minute assembly time. **Size:** 4.5'' w. x 6'' l. x 1.4'' h. **Weight:** 7.5 ozs.

GSCPB100—Complete $21.95

PROTO-BOARD PB-101— HOLDS 10, 14 PIN IC'S

Fully assembled breadboard contains two QT-35S sockets and four QT-35B bus strips mounted on metal ground/base plate with non-marring feet. Excellent for audio and small digital projects. **Size:** 6.0'' l. x 4.5'' w. x 1.4'' h. **Weight:** 9 ozs.

GSCPB101 .... $25.00

PROTO-BOARD PB-102— HOLDS 12, 14 PIN IC'S

Fully assembled breadboard contains two QT-47S sockets, three QT-47B bus strips and one QT-35B bus strip on a metal ground/base plate with non-marring feet. Excellent for intermediate digital needs **Size:** 7.4'' l. x 4.5'' w. x 1.4'' h. **Weight:** 10 ozs.

GSCPB102 .... $30.00

PROTO-BOARD PB-103— HOLDS 24, 14 PIN IC'S

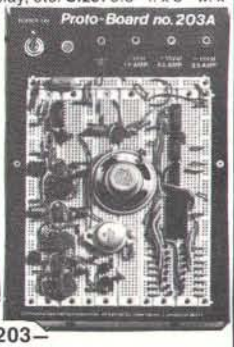
Fully assembled breadboard contains three QT-59S sockets, four QT-59B and one QT-47B bus strip, four binding posts on a metal ground/base plate with non-marring feet. Build calculators, interfaces, networks, etc. **Size:** 9'' l. x 6'' w. x 1.4'' h. **Weight:** 21 ozs.

GSCPB103 .... $50.00

PROTO-BOARD PB-104— HOLDS 32, 14 PIN IC'S

Fully assembled breadboard contains four QT-59S sockets, seven QT-59B bus strips and four binding posts on a metal ground/base plate with non-marring feet. Build a CPU, encoder, complex display, etc. **Size:** 9.8'' l. x 8'' w. x 1.4'' h. **Weight:** 29 ozs.

GSCPB104 .... $66.00



PROTO-BOARD PB-203— HOLDS 24, 14 PIN IC'S

Fully assembled breadboard contains built-in, short-proof, fused, 5 VDC at 1 amp, regulated power supply, in addition to three QT-59S sockets, four QT-59B bus strips, one QT-47B bus strip and four binding posts. Capacity for most digital and many analog projects. **Size:** 9.75'' l. x 6.6'' w. x 3.25'' h. **Weight:** 5 lbs.

GSCPB203 .... $105.00

PROTO-BOARD PB-203A

Provides all the features of Proto-Board PB-203 with additional +15 and −15 VDC at 0.5 Amp power supplies with internally adjustable output voltages. **Size:** Same as PB-203. **Weight:** 5.5 lbs.

GSCPB203A $160.00

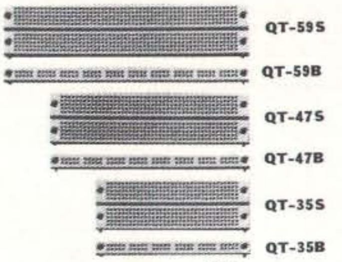
PROTO-BOARD KIT PB-203AK

Kit version of Model PB-203A. Kit contains all components of model PB-203A plus solder, hook-up wire and easy-to-follow instructions. **Weight:** 5.5 lbs.

GSCPB203AK $136.00

QUICK TEST SOCKETS AND BUS STRIPS

Universal breadboarding system eliminates the soldering iron. QT sockets provide terminals, each consisting of five pre-connected tie-points, running across the width of the socket. QT bus strips consist of two rows of connected tie-points (in groups of five) which run the length of the bus strip. All QT sockets and bus strips are molded of high-temperature plastic so that leads and wires can be soldered while plugged in. Specially engineered, large-aperture holes guide leads into contacts easily and effortlessly. Contacts are pre-stressed, spring-loaded, non-corrosive, nickel silver alloy. Average contact resistance is 0.5 milliohm initially; 0.4 milliohm after use. Any diameter lead from .015'' to .032'' may be used. All QT units are .33'' thick; all sockets are 1.32'' wide; all bus strips are .36'' wide. Units mount with 4-40 flat-head screws from the front or 6-32 self-tapping screws from the rear. QT units feature unique snap/lock mechanism which mates units together in seconds to expand or contract a breadboard system at will.



CSC No.	Length Ins.	Mtg. Dimen., Ins.	Price
	QT SOCKETS		
GSC-QT-59S	6.5	6.2 x 0.7	$12.95
GSC-QT-47S	5.3	5.0 x 0.7	9.75
GSC-QT-35S	4.1	3.8 x 0.7	7.25
	QT BUS STRIPS		
GSC-QT-59B	6.5	6.2 x 0.7	2.75
GSC-QT-47B	5.3	5.0 x 0.7	2.50
GSC-QT-35B	4.1	3.8 x 0.7	2.20

EXPERIMENTOR BREADBOARDING SOCKETS

Solderless breadboarding sockets accept DIP's, transistors, LED's, resistors, capacitors, and most all types of discrete components, as well as #22-30 solid hookup wire. An interlocking system permits boards to be snapped together for optimum configuration for any circuit. Sockets are molded of durable, abrasion resistant material and feature prestressed, nickel-silver contacts. A vinyl plastic backing prevents shorts when sockets are mounted on metallic surfaces. Tie-points are alphanumerically identified for faster wiring and circuit tracing. Sockets can be used loose or fastened to a mounting surface.

EXPERIMENTOR EXP-300—With 0.3'' center channel spacing to fit the smaller DIP's. Size, ⅜'' d. x 2.1'' w. x 6'' lg.
GSCEXP300 .... $12.00

EXPERIMENTOR EXP-350—With 0.3'' center channel spacing to fit the smaller DIP's. Size ⅜'' d. x 2.1'' w. x 3.6'' lg.
GSCEXP350 .... 6.75

EXPERIMENTOR EXP-325—With 0.3'' center channel spacing to fit the smaller DIP's. Size ⅜'' d. x 2.1'' w. x 1.86'' lg.
GSCEXP-325 .... 3.50

EXPERIMENTOR EXP-600—With 0.6'' center channel spacing to fit the larger DIP's. Size ⅜'' d. x 2.4'' w. x 6'' lg.
GSCEXP600 .... 14.75

EXPERIMENTOR EXP-650—With 0.6'' center channel spacing to fit the larger DIP's. Size ⅜'' d. x 2.4'' w. x 3.6'' lg.
GSCEXP650 .... 8.75

EXPERIMENTOR EXP-4B—With 40-point bus strips. Size, ⅜'' d. x 1.0'' w. x 6.0'' lg.
GSCEXP4B 4.75

EXPERIMENTOR SCRATCHBOARD™ WORKPADS

Start breadboarding even before parts are assembled. Or, sketch a working breadboard and save it for later. Preprinted pads of paper with a light blue breadboard hole pattern give you a head start.

EXPERIMENTOR MATCHBOARD™

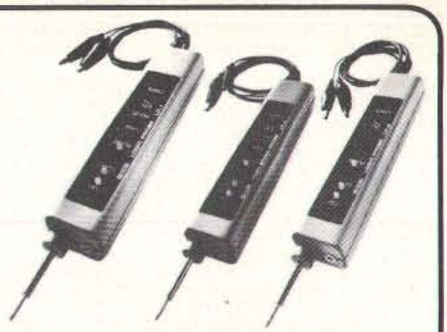
Go from breadboard to finished PC board non-stop. Predrilled, pre-etched, copper-clad PC boards speed the work. The component side is silk-screened with alphanumeric index.

EXP-300P—One Matchboard. Net Each $2.70

EXP-302—Three 50-sheet Scratchboard workpads. Net each $3.30

EXP-303—Two Matchboards and one EXP-300 Breadboard. Net Each $16.00

EXP-304—Two Matchboards, one EXP-300 Breadboard and one Scratchboard. Net Each 17.00



LOGIC PROBES

CSC logic probes are the ultimate tools for digital design and testing. These hand-held units provide an instant overview of circuit conditions. Simple to use; just clip power leads to circuit's power supply, set logic family switch to TTL/DTL or CMOS/HTL. Touch probe to test node. Trace logic events and pulses through digital circuits. Even stretch and latch for easy pulse detection. Instant recognition of high, low or invalid levels, open circuits and nodes. Simple, dual-level detector LED's tell it quickly, correctly. HI (Logic "1"); LO (Logic "0"). Also incorporates blinking pulse detector, e.g., HI and LO LEDs blink on or off, tracking "1" or "0" states at square wave frequencies up to 1.5 MHz. Pulse LED blinks during pulse transition. Choice of three models to meet individual requirements; budget, project and speed of logic circuits.

LP-1 LOGIC PROBE—Hand-held logic probe provides instant reading of logic levels for TTL, DTL, HTL or CMOS. **Input Impedance:** 100,000 Ohms. **Min. Detectable Pulse:** 50 ns. **Max. Input Signal (Frequency):** 10 MHz. **Pulse Detector (LED):** High speed train or single event. **Pulse Memory:** Pulse or level transition detected and stored. **GSCLP1** $50.00

LP-2 LOGIC PROBE—Economy version of Model LP-1. Safer than a voltmeter. More accurate than a scope. **Input Impedance:** 300,000 Ohms. **Min. Detectable Pulse:** 300 ns. **Max. Input Signal (Frequency):** 1.5 MHz. **Pulse Detector (LED):** High speed train or single event. **Pulse Memory:** none. **GSCLP2** $28.00

GSLP3—High speed logic probe. Captures pulses as short as 10 ns. **Input Impedance:** 500,000 Ohms. **Minimum Detectable Pulse:** 6 ns. **Max. Input Signal (Frequency):** 60 MHz. **Pulse Detector (LED):** High speed train or single event. **Pulse Memory:** Pulse or level transition detected and stored. **GSCLP3** $77.00

DIGITAL PULSER

The ultimate in speed and ease of operation. Simply connect clip leads to positive and negative power, then touch DP-1's probe to a circuit node; automatic polarity sensor detects circuit's high or low condition. Depress the pushbutton and trigger an opposite polarity pulse into the circuit. Fast, troubleshooting includes injecting signals at key points in TTL, DTL, CMOS or other popular circuits. Test with single pulse or 100 pulses per second via built-in dual control pushbutton button selects single shot or continuous modes. LED indicator monitors operating modes by flashing once for single pulse or continuously for a pulse train. Completely automatic, probe-size lab/field pulse generator for any family of digital circuits. **Output:** Tri-state. **Polarity:** Pulse-sensing auto-polarity. **Sync and Source:** 100 mA. **Pulse Train:** 100 pps. **LED Indicator:** Flashes for single pulse; stays lit for pulse train.

GSCDPI $83.00

LOGIC MONITOR

Trace signals through all types of digital circuits. Unit clips over any DIP IC up to 16 pins. Each of its 16 contacts connects to a single-bit level detector that drives a high-intensity, numbered LED readout activated when the applied voltage exceeds a fixed 2 V threshold. Logic "1" turns LED on; logic "0" keeps LED off. A power-seeking gate networks automatically locates supply leads and feeds them to the LM-1's internal circuitry. Saves minutes, even hours in design troubleshooting, debugging of equipment. **Voltage Threshold:** 2 V ±0.2 V. **Input Impedance:** 100,000 Ohms. **Input Voltage Range:** 4-15 V max. across any two or more inputs. **Current Drain:** 200 mA at 10 V. **Size:** 4'' l. x 2'' w. x 1.75'' d. when open. **Weight:** 3 ozs.

GSCLMI $60.00

PLEASE ORDER BY PART NUMBER

PRIORITY ONE ELECTRONICS

LOGICAL ANALYSIS KITS

The increasing use and complexity of digital logic has created the need for portable and compact test equipment. The Logical Analysis Kits contain design/test/troubleshooting instruments that detect and locate logic problems, as well as component or mechanical failures, down to a specific IC pin. The Logic Pulser (the source) and the Logic Probe or Logic Monitor (detectors) instantly provide static and dynamic logic state analyses. These portable compact units save time in all phases of digital work.

GSC Model LTC-1 Logical Analysis Kit—Complete with LP-1 logic Probe, DP-1 Logic Pulser, LM-1 Logic Monitor, wiring accessories, manuals and molded case. . **$220.00**
GSC Model LTC-2 Logical Analysis Kit—For high-speed and memory analysis. Same as Model LTC-1, except substitutes LP-3 High-Speed Logic Probe **$250.00**

MAX100 100MHZ FREQUENCY COUNTER

Specifications.
Frequency Range: 20 Hz to 100 MHz guaranteed; 110 MHz typical. **Gate Time:** 1 sec. **Resolution:** 1 Hz. **Accuracy:** ±1 count ± time base error. **Input Impedance:** 1 Magohm shunted by 56pF. **Coupling:** AC. **Sine Wave Sensitivity:** 30 mV RMS at 50 MHz. **Internal Time Base Frequency:** 3.579545 MHz crystal oscillator. **Stability:** ±3 ppm at 25°C. **Temperature Stability:** Better than 0.2 ppm/°C, 0-50°C Max. **Aging:** 10 ppm/year. **Display:** Eight 0.6" LED digits. **Lead-Zero Blanking:** Decimal point appears between 6th and 7th digit when input exceeds 1 MHz. **Overflow:** With signals over 99,999,999 Hz, most significant (left hand) digit flashes, allowing readings in excess of 100 MHz. **Display Update:** 1/6 sec. plus 1 sec. gate time. **Low Battery Indicator:** When battery supply falls below 6.6 VDC, all digits flash at 1 Hz. **Power Required:** Internal, 6 "AA" cells; external, 110 or 220 VAC charger/eliminator, auto cigarette lighter adapter or 7.2-10 VDC external supply. **Battery Charging:** 12-14 hrs. **Size:** 1.75"h. x 5.63" w. x 7.75" d. **Weight:** Less than 1.5 lbs. with batteries

CSC Model MAX-100 Frequency Counter— $149.00

MAX-100 ACCESSORIES

GSC 100-CA1—100 VAC Charger/eliminator **12.45**
GSC 100-IPC—Input cable with clip leads **7.45**
GSC Model PS-500 Prescaler—500 MHz 10:1 ....**70.00**
GSC 100-CC—Carrying case **12.45**

MAX-50 FREQUENCY COUNTER

A mini-sized, hand-held frequency counter featuring the latest LSI circuitry and advanced engineering provides accurate frequency measurement and operating simplicity. Can be used to check AM, CB. Business Radio, audio, ultrasonics and many others. **Frequency Range:** 100 Hz to 50 MHz. **Input Impedance:** More than 1 Megohm, diode protected. **Input Connector:** Miniature phone jack. **Input Coupling:** AC. **Sensitivity:** 30 mV from 100 Hz to 30 MHz; 100 mV from 30 MHz to 50 MHz. **Maximum Input:** 200 V p-p to 1 kHz; 75 V p-p from 1 kHz to 10 MHz; 50 V p-p to 50 MHz. **Time Base Accuracy:** ± 3 ppm at 25°C. **Temperature Stability:** Better than 0.2 ppm/°C from 0° C to 50° C. **Display:** Six 0.1" magnified LEDs with anti-glare window. **Zero Blanking:** All zeros to the left of the first significant digit are blanked; decimal points light automatically. **Display Update:** 6 per-second. **Power Requirements:** One 9 V alkaline battery or battery eliminator. **Size:** 3" h. x 6" w. x 1.5" d. **Weight:** 8 ozs.

GSC MAX-50—Frequency Counter **$77.00**
GSC MMAC2 AC Adapter—For 110 VAC **12.45**
GSC MMA4 Mini Antenna— **4.95**
GSC MM-IPC Input Cable Clip Leads— **4.95**
GSC MMC5 Carrying Case— **7.45**

MAX-550 FREQUENCY COUNTER
550 MHz VERSION OF ABOVE
GSC MAX-550— **$165.00**

6001 FREQUENCY COUNTER

Model 6001 bench-top frequency counter is designed for applications from audio to UHF, in communications, data processing, process control. RF and digital design, multiplex, etc. Exceptional flexibility in general-purpose lab and test-bench applications. Extremely accurate measurements from 50 Hz to 650 MHz. **Inputs (Front Panel):** Two inputs provided through front-panel BNC connectors. "A" input is for signal frequencies from 5 Hz to 100 MHz; 1 Megohm +25 pF input impedance; a low-pass filter provides 3 dB/octave rolloff at 50 KHz. "B" input is for signal frequencies from 40 to 650 MHz; 50 Ohms input impedance; fuse protected. **Gate Times:** Three pushbutton-selectable gate times (0.1, 1.0 and 10 sec.) provide resolutions of 10, 1 and 0.1 Hz respectively; a front-panel LED indicates gate-open condition. **Timebase:** A precision 10 MHz crystal oven oscillator (0.5 ppm, 0-50° C) provides internal reference, or an external reference may be selected by a rear-panel switch. The oven oscillator output is buffered and made available at a rear-panel BNC connector. A second rear-panel BNC connector provides the input connection for an external timebase reference signal. Use of an external timebase other than 10 MHz permits the 6001 to operate in a scaling (also called rescaling) mode, in which the output is presented in units other than Hz. This permits the 6001 to be used as a directly-duplicating digital display in a number of applications, including transducer translation, flow monitoring, tachometry, etc. **Display:** 8-digit, 7-segments, 0.43" LED display features zero blanking. Decimal point indicates frequency in MHz. A contrast enhancement filter assures legibility, even in high ambient light conditions. Discrete front-panel LEDs provide Oven Ready, Overflow and Gate Open indications. In addition, the leftmost digit (of the 8-digit display) flashes to indicate counter overflow. **Controls:** Power, Gate Time Select, A/B Input Select, Low Pass Filter In/Out, Internal/External Time Base (rear-panel). **Power Required:** 105-135 VAC, 57-63 Hz, 10 VA max; 215-250 VAC, 50-60 Hz version available. **Operating Temperature:** 0-50° C. **Size:** 3" h. x 10" w. x 7" d. **Weight:** 3 lbs.

Frequency Counter—Net Each **$385.00**
Input Fuse Kit—Includes two miniature 1/10 amp fuses (for B input protection). Net per Kit **$7.50**

5001 UNIVERSAL COUNTER-TIMER

Designed for electronic measurements and display of frequency, period, interval and counted events. Unique full input signal conditioning on both channels, including attenuators, slope selection and variable trigger level. Variable delay between measurements. **Frequency:** Up to 10 MHz in four ranges. **Selectable Gate Times:** .01, 0.1, 1.0 or 10 seconds. Display indicates frequency (in KHz) at A input. **Period:** Measures period of signal at A input, 400 nsec. to 10 sec.; measures signal cycle or averages over 10, 100 or 1,000 cycles. Display indicates time period. **Frequency Ratio:** Counts number of cycles occurring at A input (to 10 MHz) during one cycle at B input (to 2 MHz), or averages over 10, 100 or 1,000 cycles at B input. Useful for scaling measurements. Display indicates ratio F_A/F_B. **Time Interval:** Measures time between given signal edge occurring at A input (starts measurement) and given edge occurring at B input (ends measurement), from 200 nsec. to 10 sec. May average over 10, 100 or 1,000 intervals, or measure single interval. **Event Count:** Counts up to 99,999,999 events at up to 10 MHz. "Run" pushbutton enables counting with running count continuous displayed: when "hold" button is pushed displayed count is frozen while counting continues; returns to continuous display when the "Run" button is pushed again. Third ("Reset") button resets count. **Delay:** Variable control causes 75 msec. to 7.5 sec. delay between measurement cycles to facilitate viewing or recording of displayed readings. Detent position freezes display indefinitely following next measurement cycle. **Full Signal Conditioning On Both Inputs:** Both inputs incorporate x1/x10/x100 selectable attenuator. +/— slope selector and variable trigger level control. Both inputs are 1 Megohm at 25 pf, DC coupled. **Display:** Bright, 8-digit, 7-segment, 0.43" LED display, drive for high visibility. Decimal point position gives frequency measurements in KHz, time measurements in micro-seconds. Discrete LED indicators show overflow (when count exceeds 99,999,999) and gate-open conditions. **Operating Temperature:** 0-50° C. **Power Required:** 105-135 VAC, 57-63 Hz, 10 VA max.; 215-250 VAC, 50-60 Hz version available. **Size:** 3' h. x 10" w. x 7" d. **Weight:** 3 lbs.

GSC5001 Universal Counter-Timer—Net Each .**$360.00**

2001 FUNCTION GENERATOR

Signal generator with advanced IC circuitry produces stable, low-distortion sine waves (less than 2% THD), fast, rise-and-fall-time square waves (less than 100 nsec.), high-linearity triangle waves (better than 1%) and TTL square waves with rise and fall time less than 25 nsec. Frequency is accurate, calibrated ±5% and sweepable up to 100:1. A voltage-controlled oscillator allows generator's frequency to be remotely shifted or swept by an AC or a DC voltage fed into the "Sweep In" jacks. A DC voltage provides a directly proportional shift in frequency, while an AC voltage provides a frequency-modulated sweep. Two shortproof, 600-Ohm outputs are adjustable from 1 mV to 100 mV p-p, open circuit and 100 mV to 10 V p-p, open circuit, with better than ±.5 dB flatness. Variable DC Offset control (push-button selectable) provides controlled, variable shifting of output waveform's center line above or below zero. **Frequency Range:** 1 Hz to 100 KHz in 5 overlapping decade ranges, pushbutton selectable, with a 10:1, 50-increment vernier dial; 1-10 Hz. 10-100 Hz. 100-1000 Hz. 1-10 KHz, 10-100 KHz. **Dial Accuracy:** ±5% of dial setting; calibrated at 10 Hz, 100 Hz, 1 KHz and 10KHz. **Sine Wave Distortion:** Less than 2% THD over frequency range. **Triangle Wave Linearity:** Better than 1% over frequency range. **Square Wave Rise and Fall Times:** Less than 100 nanosec. with 600 Ohms, 20 pF termination. **Time Symmetry:** Less than ±2%; TTL square wave output with rise and fall times less than 25 nanosec. **Sweep Range:** Maximum 100:1; maximum linear range, 10:1 at any dial setting. **Sweep Input:** 0 to ±10 Volts. **Input Impedance:** 30K Ohms. **Main Output:** Sine, square and triangle waveforms, pushbutton selectable; Hi Level, 0.1-10 V p-p open ckt, .05-5 V p-p into 600 Ohms; Lo Level (-40 dB), 1-100 mV, open ckt. .5-50 mV into 600 Ohms. **Amplitude Control:** Variable; greater than 40 dB range. **Amplitude Flatness:** Less than ±0.5 dB. **DC Offset Control:** Variable ±5 V into open ckt.; pushbutton in/out switch. **Max. DC Offset: (AC + DC) components before clipping):** Hi output, ± 10 V max.; Lo output, ±1 V max. **TTL Square Wave Output:** 10 TTL loads; rise and fall time, less than 25 nsec. **Power Required:** 105-125 VAC, 50/60 Hz; 6 Watts; 220-240 VAC, 50/60 Hz optional. **Operating Temperature:** 0° to 50° C (calibrated at 25° C ±5%). **Size:** 10" w. x 3" h. x 7" d. **Weight:** 2.2 lbs.

GSC2001 Function Generator—Net Each **$186.00**

4001 DIGITAL PULSE GENERATOR

A precision digital pulse generator that combines compact size with outstanding performance. Symmetrical and asymmetrical pulses over a wide range of frequencies, duty cycles and amplitudes. Fast rise and fall times; less than 30 nsec. Independent pulse width and spacing controls. Continuous/manual one-shot operation. External triggering; DC to 10 MHz. Synchronous output gating. Square wave and complementary output. In the Gate mode, output is synchronized with leading edge of input gate signal; last output pulse is always completed, even in absence of gate signal. **Frequency Range:** 0.5 Hz to 5 MHz. **Pulse Width and Spacing Controls:** 100 nsec. to 1 sec. in 7 overlapping decade ranges. **Variable Width and Spacing Controls:** Concentric, single-turn verniers provide continuous adjustment between ranges; pulse spacing controls not active during Trigger. Gate and One-Shot modes. **Duty Cycle:** 10' to 1 range, continuously adjustable, 0.5 Hz to 5 MHz. **Accuracy:** ±5% of control settings; calibrated at min. and max. of vernier settings. **Pulse Jitter:** Less than .1% ±50 ps. **Run Mode:** 0.5 Hz to 5 MHz frequency selectable through pulse width and spacing controls. **Trigger Mode:** DC to approx. 10 MHz, from external source. **Gate Mode:** Generator starts synchronously with leading edge of gate signal. **One-Shot Mode:** Momentary pushbutton for single-pulse operation; pulse occurs each time button is depressed. **Square Wave:** Pushbutton provides square wave at output. **Complement:** COMPL pushbutton inverts output signal without losing sync time reference. **TRIG/GATE Input Requirements:** TTL compatible; sine waves, 4 V p-p pulses, 2 V peak, greater than 40 nsec. wide; input impedance, approx. 400 Ohms, DC coupled; max. input level, ±10 V. **VAR OUT:** Amplitude, 0.1-10 V into open circuit, adjustable by single-turn vernier; rise/fall time, less than 30 nsec.; impedance, constant 50 Ohms. **TTL OUT:** Fan out, 40 TTL loads; sink, 64 mA at 0.8 V max.; rise/fall time, less than 20 nsec. **SYNC OUT:** Amplitude, 2.4 V min. fan out, 10 TTL loads; sink, 64 mA at 0.8 V max.; rise/fall time, less than 20 nsec.; pulse width, greater than 20 nsec.; sync pulse lead time, greater than 20 nsec. **Operating Temperature:** 0° to 50° C (calibrated at 25° C ±5° C). **Power Required:** 105-125 VAC, 50/60 Hz, 6 Watts; 220-240 VAC, 50/60 Hz optional. **Size:** 10" w. x 3" h. x 7" d. **Weight:** 2.2 lbs.

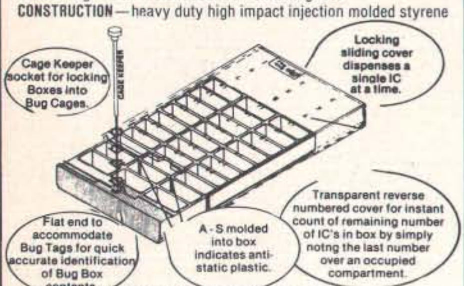
GSC4001 Digital Pulse Generator—Net Each ..**$235.00**

PRIORITY ONE ELECTRONICS

THE BUG BOX

The heart of the system is the patented III Bug Box which has 30 compartments measuring 1" x .375" x 5" deep. Each Bug Box will store 60 eight-pin DIPs, 30 fourteen-pin DIPs, 30 sixteen-pin DIPs or 30 other small components such as transistors, diodes, resistor networks, etc. The transparent cover has a stairstep design that allows the user to empty one compartment at a time. A numbering system on the cover gives an instant count of remaining ICs.

CONSTRUCTION — heavy duty high impact injection molded styrene



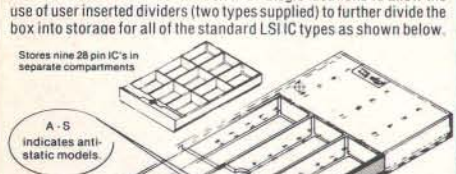
THE STANDARD BUG BOXES ARE BLUE

					Sh.Wt.
III-BGX-001	Bug Box Single Pack	$ 1.98	III-BGX-001A	$ 2.98	2 oz.
III-BGX-003	Bug Box 3 Pack	$ 5.49	III-BGX-003A	$ 8.00	6 oz.
III-BGX-010	Bug Box 10 Pack	$ 17.49	III-BGX-010A	$ 26.00	2 lbs.
III-BGX-250	Bug Box 250 Pack	$399.95	III-BGX-250A	$599.95	20 lbs.

"A" Indicates Anti-Static Plastic Construction

LSI BUG BOX (Big Bug Box)

The LSI Bug Box is designed to store large IC's and provides separate compartment storage for 8, 10, 12, 14, 16, 18, 20, 22, 24, 28 and/or 40 pin IC's. The basic box is permanently divided into three permanent compartments measuring 1 x 4.15 x .5 inches deep. Divider guides are molded into the bottom of the box in strategic locations to allow the use of user inserted dividers (two types supplied) to further divide the box into storage for all of the standard LSI IC types as shown below.



III-BLX-001	LSI Bug Box Single Pack	$ 2.96	III-BLX-001A	$ 3.98
III-BLX-003	LSI Bug Box 3 Pack	$ 8.29	III-BLX-003A	$ 10.95
III-BLX-010	LSI Bug Box 10 Pack	$ 26.49	III-BLX-010A	$ 34.95
III-BLX-250	LSI Bug Box 250 Pack	$599.95	III-BLX-250A	$799.95

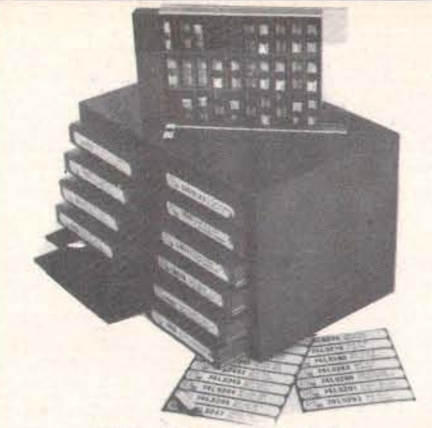
"A" Indicates Anti-Static Plastic Construction

III-BDV-012	2 sets of 12 extra LSI Dividers	$1.49	1 oz.

BUG TRAYS

Bug Trays offer storage for larger components or tools. Available in three styles. Bug Tray Open has a single open compartment measuring 3.05" wide x 4.6" long x .6" deep. Bug Tray Vertical has five storage areas measuring .5" wide x 4.6" long. Bug Tray Horizontal has eight areas measuring .4" wide x 3.95" long.

			Sh.Wt.
III-BTV-001	Bug Tray Vertical, Single	$ 1.49	1 oz.
III-BTV-010	Bug Tray Vertical, 10 Pack	$12.98	10 oz.
III-BTH-001	Bug Tray Horizontal, Single	$ 1.49	1 oz.
III-BTH-010	Bug Tray Horizontal, 10 Pack	$12.98	10 oz.
III-BTO-001	Bug Tray Open, Single	$ 1.49	1 oz.
III-BTO-010	Bug Tray Open, 10 Pack	$12.98	10 oz.
III-BTX-003	Bug Tray Mixer, 3 Pack	$ 3.98	3 oz.



THE BUG BOX SYSTEM

The Bug Box System provides a complete storage/protection/identification and retrieval capability for integrated circuits or other small components or tools. The system is totally modular and can be as small as a single Bug Box in a field engineer's tool kit or as large as the complete storage/indexing and retrieval for the thousands of different ICs and semiconductors used in a major research and development laboratory.

BUG CAGE

The Bug Cage will store either a Bug Box or a Bug Tray in any of its 12 storage locations, and its modular design allows it to be attached to other Bug Cages, either vertically or horizontally, to build storage libraries of any size. The ends of the Bug Boxes or Bug Trays are exposed so that Bug Tag identification labels, on the ends, are visible.

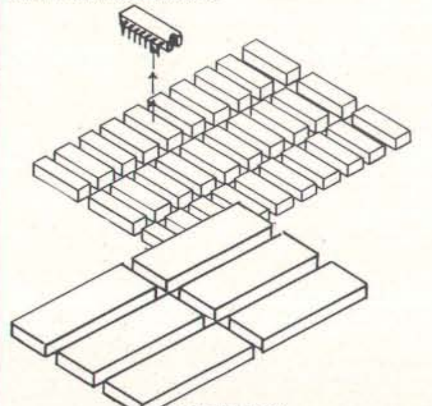
			Sh.Wt.
III-BGC-001	Bug Cage, Single	$ 10.95	2 lbs.
III-BGC-012	Bug Cage, 12 Pack	$114.95	15 lbs.

CAGE KEEPERS

Cage Keepers pin a column or Bug Boxes into the storage compartment of a Bug Cage for protection against casual use or theft and for transportation. Keepers are available in a 5" model for a single cage and a 10" model for cages stacked two high.

			Sh.Wt.
III-CKP-005	5" Cage Keepers, Pkg. of 2	$3.98	3 oz.
III-CKP-010	10" Cage Keepers, Pkg. of 2	$4.98	15 oz.

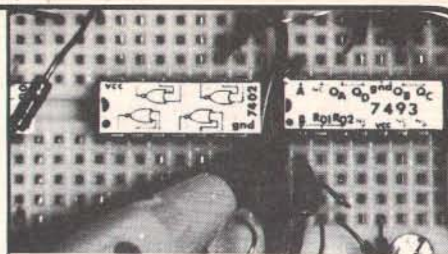
Colors: *The standard Bug Boxes and Cages are blue; yellow, red and white are available at extra cost.*



BUG RUGS

Bug Rugs provide the necessary static discharge for CMOS MOSFET devices. Bug Rugs are made of high carbon content foam which will effectively short all of the pins together when they are inserted into the foam. Die cut to the exact dimensions of the Bug Box compartments. Each package contains 30 Bug Rugs to line the 30 compartments of a Bug Box.

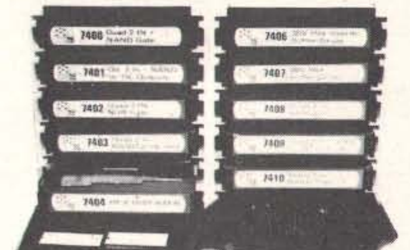
			Sh.Wt.
III-BGR-030	Bug Rug, Pkg. of 30	$ 1.98	1 oz.
III-BGR-300	Bug Rug, Pkg. of 300	$14.98	4 oz.
III-BBR-036	LSI Bug Rug, Pkg. of 6	$ 1.98	1 oz.
III-BBR-060	LSI Bug Rug, Pkg. of 60	$14.98	4 oz.



BACK PACKS

Each Back Pack label is imprinted with the exact internal logic of a specific IC as well as the pinout of this logic to the pins of the IC. When Back Packs are affixed to the IC and the IC is in the circuit, the user can trace logic connections without needing data sheets or counting pins. Each Back Pack family packet contains 532 labels and each is imprinted with the logic and pinout of the most popular ICs within that logic family. Labels are die-cut and are self-adhesive to stick to the IC. Several blank labels are included in each family packet for special labels.

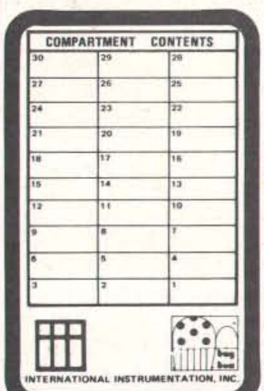
			Sh.Wt.
III-BPT-012	TTL Family Back Packs	$ 5.98	2 oz.
III-BPC-012	CMOS Family Back Packs	$ 6.98	2 oz.
III-BPM-012	Combination of TTL/CMOS Packs	$10.98	4 oz.
III-BPU-012	Over 700 Back Pack Labels covering the most popular CPU's, RAMS, UARTS, Clocks, PIA, PROMS, Drives, and much more	$ 9.95	3 oz.



BUG TAGS

Bug Tags are self-adhesive labels, die-cut to the exact dimensions of the front edge of a Bug Box. They are pre-printed with the numbers and descriptions of the ICs in the most popular logic families. They are packaged by logic families and each Bug Tag package contains 200 or more labels.

			Sh.Wt.
III-BTT-200	TTL Bug Tag Family	$ 3.98	2 oz.
III-BTC-200	CMOS Bug Tag Family	$ 3.98	2 oz.
III-BTK-200	LS Shottky Bug Tag Family	$ 3.98	2 oz.
III-BTS-200	LINEAR Bug Tag Family	$ 3.98	2 oz.
III-BTM-800	Contains ALL 800 labels above	$13.95	8 oz.
III-BTU-300	MICROPROCESSOR Bug Tags	$ 5.98	3 oz.
III-BTR-300	RESISTOR 300 labels (900 valves)	$ 5.98	3 oz.
III-BTP-300	CAPACITOR 300 labels (900 valves)	$ 5.98	3 oz.
III-BTZ-200	DIODES & ZENERS, Bug Tag Family	$ 3.98	2 oz.
III-BTD-200	DIP RESISTOR, Bug Tag Family	$ 3.98	2 oz.



III-BTF-012 $ 2.98 1 oz.

Field Engineering Kit Bug Tags (rear compartment labels) are used whenever more than one type of IC (or other component) is to be stored in a single box. The Bug Tag FE Kit contains 12 labels designed to mount on the rear cover of a Bug Box for user written or typed identification of different contents for each of the 30 compartments in the box.

PROTO-PAK

Proto-Pak's provide the basic IC set for general CMOS and/or TTL prototyping or troubleshooting. Proto-Pak includes 30 different IC's, Back Pack labels for each, a Bug Box to house them in, and Bug Tags to locate them easily.

			Sh.Wt.
III-PPT-030	TTL PROTO-PAK	$39.95	4 oz.
III-PPC-030	CMOS PROTO-PAK	$49.95	4 oz.

MICRO PROCESSOR BASED DIGITAL CAPACITANCE METER.

THE STANDARD AGAINST WHICH ALL OTHERS WILL BE MEASURED

FEATURES & FUNCTION

- .1% accuracy
- .1pF-100F range
- .1pF resolution
- Full 4 digit custom LCD display
- Temp. oven
- Auto Ranging
- Auto Zero Mapping
- Leading zero blanking

μC PROBE®

III-CP3001

$299.95

- **Microprocessor based**
- **Programmed for field upgrades & future expansion**

- Limits comparator (up to 16 simultaneous sets of limits)
- DA (dielectric absorption) Measurement
- Leakage measurement
- Four level 16 keypad for user interface & control
- Battery (auto or marine) or AC operation
- 6502 Microprocessor based
- ROM (2K or more depending upon options)
- RAM (1K or more depending upon options)
- BCD output for printer or other interface
- Single cycle or continuous operating modes
- Programmed pitch and duration audio for eyes off limits comparison testing & keyboard entry
- Limits independent of range—any limit can be from .1 pF to 1 F.
- 25 turn trimmers (internal) for range calibration and temp. setting
- Binary search auto ranging
- Avg. auto search/measure and display time = 0.1 sec. for values to 100 ufd. max. =0.279 sec.
- Direct measurements of cap values to 1 F.
- Dual input C_X terminals for computer derived measurements of caps to 100 F.
- Dual regulated power supplies
- Limits entry by keypad or displayed or stored values and computer derived + or − %'ages
- Nested multiple limits for simultaneous sorting of caps into increasing tolerance groups.
- Arithmetic average
- Statistical mean
- Standard deviation
- Statistical sampling function to determine probability of incoming batch within limits by measuring only a random sample of the batch
- Running mean calculation
- Ignore first x readings function
- Display average of x readings function

The u C-PROBE—offers advanced capacitance testing functions and statistical capabilities that are not available on any other capacitance meter at any price. The u C-Probe uses the powerful 6502 microprocessor for control and stored program functions which are simply not feasible with conventional instruments. Full auto ranging from .1 pF to 1 Farad is standard as is full auto ZERO. This automatically zeroes the effects of stray or lead capacitance—Rather than the cumbersome and time consuming zero control tweaking required with every other meter presently sold, the u C-Probe samples the offset stray or lead capacitance for each of its internal ranges and stores the result in random access memory. It then subtracts these zero offset values from each reading before display or use in other calculations. Zero mapping automatically occurs at turn-on, after the reset button is pressed or it may be initiated at any time by pressing key 1D. An exclusive audio output allows "eyes off" sorting of capacitors and gives audio feedback for other C-Meter functions and key entry. The u C-Probe's .1% accuracy and .1 pF resolution place it in the post doctorate class of capacitance meters.
Size: 8½ x 9 x 3 inches (excluding tilt stand/handle). Weight: 3½ lbs. Made in U.S.A. **III-CP3001 $299.95**

QC-NOTCH—Adds Limits comparator capability to basic unit—allows user setting of up to 16 simultaneous sets of independent limits—i.e., lower limit could be .1 pF and upper limit 1 Farad if required. Limits indication by LCD displayed "up arrow" and "high" for over upper limit, "down arrow" and "low" for under lower limit and bar with "OK" for "in range." Also displays L-01 through L-16 to show which multiple limit the capacitor fell into in multiple limits testing. Provides audio signals to denote High, Low or coded signal for the exact Limits range for multiple limits testing.
Size: Internal to main unit. Weight: 1 oz. additional weight. Installed when ordered with basic unit. Plug-in ROM-user installable. **III-QCN002 $149.95**

STATISTICS option—Provides extensive statistical monitoring in QC and other applications. Provides statistical mean, running average, standard deviation and ignore first readings capabilities as well as random sample testing rather than total batch testing for incoming acceptance/rejection of large shipments of capacitors.
Size: Internal to main unit. Weight: 1 oz. additional weight. Installed when ordered with basic unit. Plug-in ROM-user installable. **III-STT001 $149.95**

TEMP OVEN option—Houses XTAL time base and critical C-measurement components in a closely controlled (1°C) temperature oven.
Size: Internal to main unit. Weight: 4 oz. additional. Installed when ordered with basic unit. Factory ONLY installation. **III-XVN001 $79.95**

EXTENDED MSM RANGE—Extends the basic measurement functions to DA and Leakage + extends upper range to several hundred Farad.
Size: Internal to main unit. Weight: 1 oz. additional. Installed when ordered with basic unit. Factory ONLY installation. **III-EMR001 $99.95**

BCD OUTPUT—Provides an output port from the 6502 microprocessor with BCD information and handshaking signal lines to interface with printers/recorders or other equipment.
Size: Internal to main unit but including rear panel connector. Weight: 2 oz. additional. Installed when ordered with basic unit. Factory ONLY installation. **III-BCD001 $99.95**

AC ADAPTOR—Allows AC operation of u C-Probe. UL listed unit operated on 115 VAC and plugs into AC Adaptor jack on rear panel.
Size: 2 x 2 x 2 inches. Weight: 1 lb. **III-MAC800 $19.95**

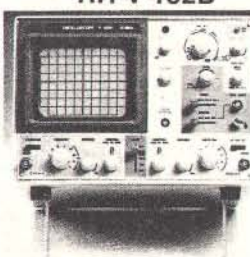
For your convenience, we recommend that you order factory options with order for basic unit.

HITACHI
Hitachi Denshi America, Ltd.

Single and dual trace, 15 and 30 MHz. All four high sensitivity Hitachi oscilloscopes are built to demanding Hitachi quality standards and are backed by a 2-year warranty. They're able to measure signals as low as 1mV/division (with X5 vertical magnifier). It's a specification you won't find on any other 15 or 30 MHz scopes. Plus: Z-axis modulation, trace rotation, front panel X-Y operation for all four scope models, and X10 sweep magnification. And, 30 MHz oscilloscopes offer internal signal delay lines. For ease of operation, functionally-related controls are grouped into three blocks on the color coded front panel. Now here's the clincher: For what you'd expect to pay more, you actually pay less. Check our scopes before you decide. All scopes come complete with probes.

Hitachi...The measure of quality.

List Price $735.00
Our Price **$650.00**
HIT-V-152B



List Price $995.00
Our Price **$859.00**
HIT-V-302B

	HIT-V-152B	HIT-V-302B
• CRT	130BUB31 (5-inch, round shape)	130BTB31 (5-inch, round shape)
Display area	8x10div (1div=9.5mm)	8x10div (1div=9.5mm)
Acceleration potential	Approx. 2kV	Approx. 4kV
Intensity modulation	Over 5Vp-p	Over 5Vp-p
• Vertical deflection		
Sensitivity and bandwidth	5mV/div~5V/div ±5%, DC~15MHz, −3dB 1mV/div~1V/div ±6%, DC~5MHzTyp, −3dB (Using x5 amplifier)	5mV/div~5V/div ±5%, DC~30MHz, −3dB 1mV/div~1V/div ±6%, DC~5MHz −3dB (Using x5 amplifier) Typ,
Rise time	24ns	12ns
Non-distorted Max. amplitude	More than 4 div at 15MHz	More than 4 div at 30MHz
Signal delay line	—	Permits viewing leading edge of displayed waveform
Input R and C	Direct 1M ohm, approx. 30pF	Direct 1M ohm, approx. 30pF
Maximum input voltage	600Vp-p or 300V (DC+AC peak)	600Vp-p or 300V (DC+AC peak)
Display mode	CH1, CH2, DUAL, ADD, DIFF	CH1, CH2, DUAL, ADD, DIFF
X-Y operation	DC~500kHz, 5mV/div~5V/div Phase difference DC~10kHz 3°	DC~500kHz, 5mV/div~5V/div Phase difference DC~10kHz 3°
• Horizontal deflection		
Sweep mode	AUTO, NORM, TV (+), TV (−)	AUTO, NORM, TV (+), TV (−)
TV synchronization	TV sync-separator circuit	TV sync-separator circuit
Internal	Over 1div (V sync-signal)	Over 1div (V sync-signal)
External	Over 1Vp-p (V sync-signal)	Over 1Vp-p (V sync-signal)
Trigger sensitivity	Frequency / Internal / External 20Hz~2MHz / 0.5div / 200mV 2~15MHz / 1.5div / 800mV	Frequency / Internal / External 20Hz~5MHz / 0.5div / 200mV 5~30MHz / 1.5div / 800mV
Trigger slope	±	±
Sweep time	0.2μs/div~0.2s/div ±5%, 19 calibrated steps	0.2μs/div~0.2s/div ±5%, 19 calibrated steps
Sweep-time magnifier	10 times (±7%)	10 times (±7%)
Max. sweep rate	100ns/div	100ns/div
• Amplitude calibrator		
Waveform	1kHz ±10% Typ, Square wave	1kHz ±10% Typ, Square wave
Voltage	0.5V ±3%	0.5V ±3%
• Power requirements	100V (120/220/240V) ±10% 50/60Hz, 40W	100V (120/220/240V) ±10% 50/60Hz, 40W
• Dimensions	Approx. 275(W)x190(H)x400(D)mm	Approx. 275(W)x190(H)x400(D)mm
• Weight	Approx. 8.5kg	Approx. 8.5kg
• Ambient operation temperature	0~+40°C	0~+40°C
• Probe	AT-10AB1.5 2	AT-10AB1.5 2

HIT-V151B (Single trace version of **HIT-V152B**)

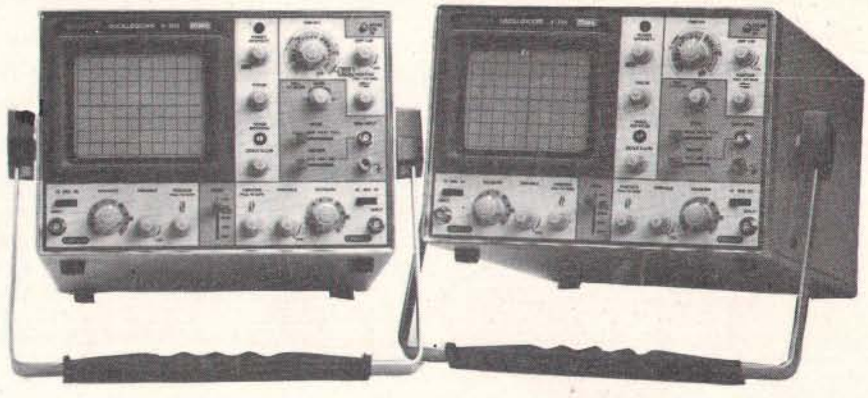
List Price $570.00 — Our Price **$525.00**

WE ACCEPT VISA, and MASTER-CHARGE

Hitachi...The measure of quality.

HITACHI
Hitachi Denshi, Ltd.

- Dynamic range 8 div.
- TV sync-separator circuit
- Built-in signal delay line (V-352)
- X-Y operation
- Sweep-time magnifier (10 times)
- Trace rotation system
- Fine-adjusting, click-positioning function
- Economically priced 20MHz dual trace oscilloscope
- Square CRT with internal graticule (illuminated scale)
- High-accuracy voltage axis and time axis set at ±3% (certified at 10° to 35°C)
- High-sensitivity 1mV/div.
- Low drift
- 2 Year Warranty



HIT-V202
20MHz DUAL TRACE
LIST PRICE: $850.00
OUR PRICE: $798.00

HIT-V352
35MHz DUAL TRACE WITH DELAY
LIST PRICE: $1150.00
OUR PRICE: $998.00

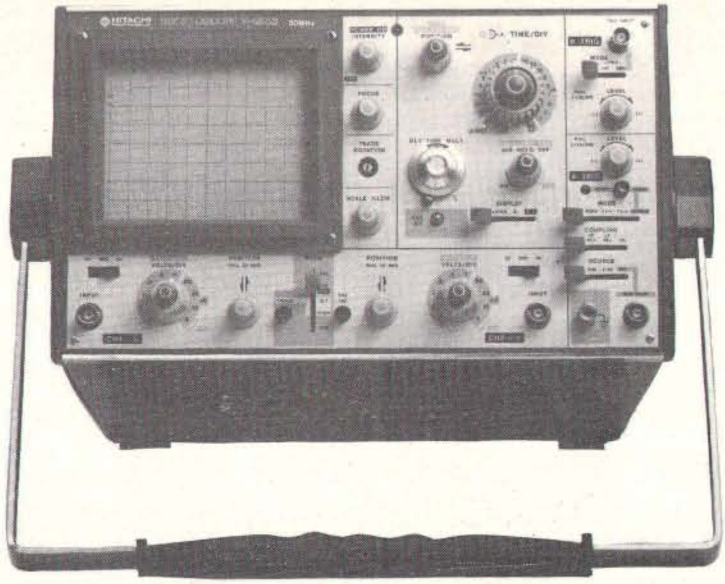
SPECIFICATIONS:	HIT-V202	HIT-V352
• CRT	140BMB31R (5.5-inch square with internal graticule, illuminated scale)	140DFB31 (5.5-inch square with internal graticule, illuminated scale)
Display area	8x10div (1div=9.4mm)	8x10div (1div=9.4mm)
Acceleration potential	Approx. 2kV	Approx. 5.2kV
Intensity modulation	Voltage: 5Vp-p or more Effective band-width: DC to 2MHz Input impedance: 47kΩ (type) Max. input voltage 300V (DC + AC peak)	Voltage: 5Vp-p or more Effective band-width: DC to 2MHz Input impedance: 47kΩ (typ) Max. input voltage: 300V (DC + AC peak)
• Vertical deflection		
Sensitivity	5mV/div to 5V/div ±3%, 9-step changeover (When using x5 amplifier) 1mV/div to 1V/div ±5% Continuously variable when using x2.5 amplifier with fine adjustment (provided with click-positioning function)	5mV/div to 5V/div ±3%, 9-step changeover (When using x5 amplifier) 1mV/div to 1V/div ±5% Continuously variable when using x2.5 amplifier with fine adjustment (provided with click-positioning function)
Bandwidth	DC to 20MHz, −3dB (at 8div) (When using x5 amplifier) DC to 7MHz, −3dB (at 8div)	DC to 35MHz, −3dB (at 8div) (When using x5 amplifier) DC to 7MHz, −3dB (at 8div)
Rise time	17.5ns, (for x5) 50ns	10ns, (for x5) 50ns
Signal delay line		Permits viewing leading edge of displayed waveform
Max. input voltage	500Vp-p or 300V (DC + AC peak, at 1kHz)	500Vp-p or 300V (DC + AC peak, at 1kHz)
Input connection	AG, GND, DC	AG, GND, DC
Input impedance	Direct 1M ohm, approx. 30pF	Direct 1M ohm, approx. 30pF
Operating modes	CH1, CH2, DUAL, ADD, DIFF	CH1, CH2, DUAL, ADD, DIFF
X-Y operation	CH1: X axis, CH2: Y axis	CH1: X axis, CH2: Y axis
Sensitivity	5mV/div to 5V/div (when using x5 amplifier: 1mV/div)	5mV/div to 5V/div (when using x5 amplifier: 1mV/div)
Phase difference	DC to 50kHz within 3°	DC to 50kHz within 3°
X bandwidth	DC to 500kHz, −3dB	DC to 500kHz, −3dB
Dynamic range	8div or more	8div or more
• Horizontal deflection		
Sweep mode	AUTO, NORM, TV (+), TV (−)	AUTO, NORM, TV (+), TV (−)
Sync signals	CH1, CH2, LINE, EXT	CH1, CH2, LINE, EXT
Sync connection	AC	AC
TV synchronization	TV sync-separator circuit	TV sync-separator circuit
Internal	1div or more (V sync-signal)	1div or more (V sync-signal)
External	1Vp-p or more (V sync-signal)	1Vp-p or more (V sync-signal)
Trigger sensitivity	Frequency / Internal / External: 20Hz to 2MHz — 0.5div — 200mV 2 to 20MHz — 1.5div — 800mV	Frequency / Internal / External: 20Hz to 5MHz — 0.5div — 200mV 5 to 35MHz — 1.5div — 800mV
AUTO low bandwidth	20Hz	20Hz
Trigger polarity	±	±
External sync input	Input impedance: approx. 1M ohm, 30pF or less Max. input voltage: 300V (DC + AC peak, at 1kHz)	Input impedance: approx. 1M ohm, 30pF or less Max. input voltage: 300V (DC + AC peak, at 1kHz)
Sweep time	0.2μs/div to 0.2s/div ±3%, 19 calibrated steps Continuously variable when using x2.5 amplifier with fine adjustment (provided with click-positioning function)	0.2μs/div to 0.2s/div ±3%, 19 calibrated steps Continuously variable when using x2.5 amplifier with fine adjustment (provided with click-positioning function)
Sweep time magnifier	10 times (±5%)	10 times (±5%)
Max. sweep rate	100ns/div (20ns/div or 50ns/div, not calibrated) Linearity: within 3% or ±5% when using x10	20ns/div Linearity: within 3% or ±5% when using x10
• Amplitude calibrator		
Waveform	Approx. 1kHz ±10% (typ), square wave	Approx. 1kHz ±10% (typ), square wave
Voltage	0.5V ±3%	0.5V ±3%
• Power requirements	100/120/220/240V ±10% 50 to 60Hz, approx. 45W	100/120/220/240V ±10% 50 to 60Hz, approx. 45W
• Weight	Approx. 8.5kg	Approx. 8.5kg
• Temperature		
Ambient operation temperature	0 to +40°C	0 to +40°C
Ambient temperature for guaranteeing specifications	+10 to +35°C	+10 to +35°C
• MTBF	20,000 hours (target value)	20,000 hours (target value)
• Accessories	Probe AT-10AF1.5 (10:1/1:1) .... 2 Power cable .... 1 Operation manual 1	Probe AT-10AF1.5 (10:1/1:1) .... 2 Power cable .... 1 Operation manual 1

WE ACCEPT VISA, and MASTER-CHARGE

PRIORITY ONE ELECTRONICS

HITACHI
Hitachi Denshi, Ltd.

The Hitachi Alternative



V-550B 50 MHz Dual Trace Delayed Sweep Oscilloscope

Until now, if you wanted a 50 MHz dual trace oscilloscope of uncompromising quality, there was only one choice. Now there's a second . . . an outstanding new delayed sweep scope with an established name—the Hitachi V-550B.

The V-550B offers all the capabilities you expect from a lab grade oscilloscope—trigger view, a bright 6'' square CRT, and a maximum sweep rate of 5 ns/div.—plus a few features you might not expect, such as 1 mV/div. sensitivity (10 MHz), automatic focus correction, and a built-in TV sync separator circuit.

The cost? Remarkably reasonable, especially when you compare it to the other leading 50 MHz scope. It's a price breakthrough made possible by using up-to-date production techniques and a design backed by over 20 years of oscilloscope experience.

A REMARKABLE VALUE THE HIT-V550B *AT* $1745.00 (Probes Included)

SHPG. WT. 29 LBS.

A New C.R.T. Development
The large square C.R.T. of V-550B is most advantageous not only for dual trace operations, but also for third channel display of triggering signals. The diameter of the screen is 6" and the tube has an internal graticule. Care has been given to such details as graticule divisions in red which provide greater contrast in photography of data.
In the vertical axis, supplementary graticule markings of 0, 10, 90, and 100% are provided, which facilitates reading of rise time of pulse waves. Since these supplementary markings are in dots they do not interfere with ordinary measurements.
The use of an improved phosphor makes the new V-550B 10 kV tube as bright as old type 15 kV C.R.T.'s.

High Sensitivity: 1 mV/div. (10 MHz)
Sensitivity of 1 mV/div is useful for research and development in such applications as medical and biological experiments where signals are weak.

Automatic Focus Feature Eliminates Lag.
Loss of focus tends to occur when brightness or sweep range is altered. Automatic focus solves this problem, eliminating the necessity for adjustment each time.

Trace rotation system for easily adjusting bright-line inclination caused by terrestrial magnetism.
When bright lines are inclined by the influence of terrestrial magnetism, this rotation system promptly compensates and corrects lines to the proper locations, rendering accurate observation constantly feasible.

Third Channel Display (Trigger View)
In addition to CH1 and CH2, CH3 can be observed. Internal triggered signals and external triggered signals can be displayed as a third trace. This feature allows time comparisons to be made between external trigger signals and displayed waveforms.

X-Y Operation Convenient for Observation of Two Types of Waves.

Delayed sweep permits 1,000 X Magnification
One of the qualifications for high performance oscilloscopes is that they be equipped with delayed sweep. Needless to say, the V-550B possesses delayed sweep, which permits magnification of any desired portion of the wave up to a thousand times.

Variable Hold-off Circuitry Facilitates Pulse Measurement
The trigger hold-off circuitry is a variable hold-off circuitry specially developed for the V-550B, permitting stable triggering on complex waveforms.

Single Sweep
The single sweep mechanism is indispensable for studying signals produced in research involving vibration, impact, explosion, and voice.

10 X Sweep Magnification Facilitates Precision Measurement

Delayed Sweep Jitter Held at Below 1/20,000

SPECIFICATIONS
C.R.T.
Type
Hitachi 150BCB31 rectangular mesh type tube with 10kV acceleration potential and metal backed phosphor.
Screen Type
P31 (GH) phosphor standard.
Useful Screen Area
8 x 10 div (1 div = 1 cm).
Graticule
Internal graticule with centimeter divisions and 2mm subdivisions along the central axis 10% and 90% lines are indicated. Illumination continuously variable.
Z-axis Input
DC-coupled positive-going signal decreases intensity: 5 Vp-p signal causes noticeable modulation at normal intensity: DC to 3.5 MHz.
VERTICAL DEFLECTION (2 Identical Channels)
Bandwidth and Rise Time
DC to at least 50 MHz and rise time 7.0 ns or less. DC to at least 10 MHz and rise time 36 ns or less as magnifier extends. Lower—3dB point, AC coupling 10 HZ or less. 10x probe: 1 Hz or less.
Deflection Factor
5mV/div to 5V/div in 10 calibrated steps, 1-2-5 sequence. Uncalibrated continuous control between steps 1:<2.5. x5 magnifier extends min. deflection rate to 1mV/div.
Accuracy
±3% (+10 to +35°C)
±5% (0 to +50°C)
Additional error for magnifier ±2%
Display Modes
CH1, CH2 (normal or invert) Alternate, chopped (250kHz rate), Added.
Input Impedance
1M ohm ±2% in parallel with 30pF approx.
Maximum Input Voltage
250V (DC + peak AC) or 500Vp-p AC at 1Khz or less.
Delay Line
Permits viewing leading edge of displayed waveform.
3rd Channel Display (A Trigger View)
Display simultaneously channel 1, channel 2, and external trigger signal. The deflection factor is approx. 200mV/div.

TRIGGERING A AND B
A Trigger Modes
Automatic, Normal, Single sweep, TV-V, TV-H.
A Trigger Hold-off
Adjustable control permits a stable presentation of repetitive complex waveform.
A Trigger Source
Internal (Ch1, Ch2), Line, External.
A Trigger Slope
+ or −
A Trigger Sensitivity

	DC to 10MHz	DC to 50MHz
Internal	0.5div	1.5div
External	150mV	500mV

A Trigger Coupling
AC: 30Hz to full bandwidth
HF Rej: 30Hz to 4kHz
LF Rej: 4kHz to 50MHz
DC; 0 to full bandwidth
A External Trigger Input Impedance
1M ohm ±20% in parallel with 30pF approx.
Maximum Input Voltage
250V (DC + peak AC)
500Vp-p AC at 1kHz or less
B Trigger Modes and Source
Automatic, Normal (Internal, External)
B Trigger Slope
+ or −
Trigger Coupling
AC only; 30Hz to full bandwidth
HORIZONTAL DEFLECTION
Time Base A
50ns/div to 0.5s/div in 22 calibrated steps, 1-2-5 sequence. Uncalibrated continuous control between steps 1:<2.5
10x mag extends fastest sweep rate to 5ns/div.
Time Base B
50ns/div to 50ms/div in 19 calibrated steps 1-2-5 sequence. 10x mag extends fastest sweep rate to 5ns/div.

Accuracy
±3% (+10 to +35°C)
±5% (0 to +50°C)
Additional error for magnifier ±2%.
Horizontal Display Modes
A only, A intensified, B delayed.
Calibrated Sweep Delay
Continuous calibrated control between 0.5 and 10x time base A setting.
X-Y OPERATION (CH1: Horiz, Ch2: Vert)
Deflection Factor
Same as vertical deflection
Accuracy
Y: ±3% (+10 to +35°C), ±5% (0 to +50°C)
X: ±5% (+10 to +35°C), ±7% (0 to +50°C)
Additional error for CH1 and CH2 magnifier ±2%
Bandwidth
DC to at least 500kHz
Phase Error
3° or less from DC to 50kHz
CALIBRATOR
0.5v ±1% Frequency 1kHz ±5% square wave
POWER
Line Voltage and Frequencies
108V AC to 132V AC, 49Hz to 61Hz,
114V AC to 120V AC, 49Hz to 400Hz
POWER CONSUMPTION
45W or less at normal line voltage
DIMENSION AND WEIGHT
310(W) x 180(H) x 410(D) mm
(12.2 x 7.1 x 16.1 In.)
9.3kg (20.5 Lb.)
AMBIENT TEMPERATURES
Rated range of use: +10 to +35°C
Limits of operation: 0 to +50°C
Storage and transport: −20 to +70°C
M.T.B.F.
20,000 hours for target value
ACCESSORIES SUPPLIED
Two AT-10AD1.5 probes, 2-A fuse, protective cover, operation manual.

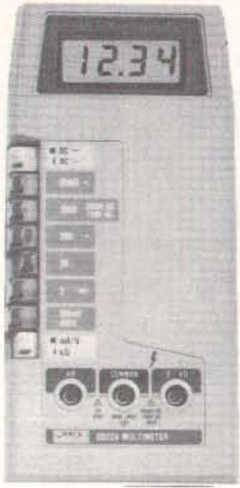
ALSO AVAILABLE: HIT-V100 100 MHz DUAL TRACE OSCILLOSCOPE.
CALL FOR PRICING

ORDER TOLL FREE (800) 423-5633

80 — 43

Three good reasons to buy a handheld DMM from Fluke.

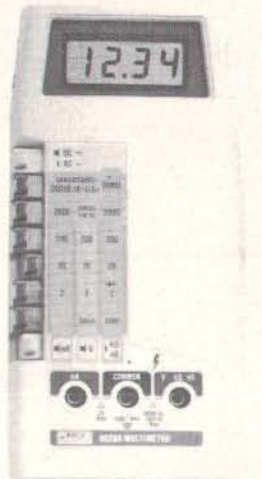
MODEL D800A: THE TROUBLESHOOTER



- Six functions
  - dc voltage
  - ac voltage
  - dc current
  - ac current
  - resistance
  - diode test
- 3½-digit resolution
- 0.25% basic dc accuracy
- LCD display
- Overload protection
- Safety-designed test leads
- Full year parts & labor warranty

FLU-D800A **$115.00**

MODEL 802A: THE ANALYST

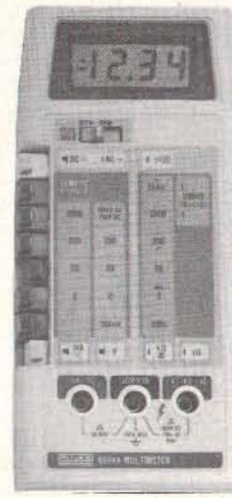


- Seven functions
  - dc voltage
  - ac voltage
  - dc current
  - ac current
  - resistance
  - diode test
  - conductance (1/R)
- 3½-digit resolution
- 0.1% basic dc accuracy
- Overload protection
- Safety-designed test leads
- Two year parts & labor warranty

FLU-D802A **$179.00**

MODEL D804A: THE INVESTIGATOR

- Nine functions
  - dc voltage
  - ac voltage
  - dc current
  - ac current
  - resistance
  - diode test
  - conductance (1/R)
  - logic level and continuity detect
  - temperature (K-type thermocouple)
- Peak hold on voltage and current functions
- Selectable audible indicator for continuity or level detection
- 3½-digit resolution
- 0.1% basic accuracy
- LCD display
- Overload protection
- Safety designed test leads
- Full year parts & labor warranty



FLU-D804A **$219.00**

More hard facts on Fluke handheld DMM's: One-year specifications.

Accuracies are ± (% of reading + no. of digits) 1 year, 18°C to 28°C except as noted.

AC Current	D804A	D802A	D800A
Ranges	2 mA, 20 mA, 200 mA, 2000 mA		
Resolution	0.05% of range (1 μA on 2 mA range)		
Accuracy 2 mA range (45Hz-450Hz)	3% of rdg + 2 digits	2% of rdg + 2 digits	2% of rdg + 3 digits
20-2000 mA ranges (45Hz-450Hz)	1.5% of rdg + 2 digits	1.5% of rdg + 2 digits	2% of rdg + 3 digits
(450Hz-1kHz)	1.5% of rdg + 2 digits	1.5% of rdg + 2 digits	Not Specified

Conductance*	D804A	D802A
Ranges	200 nS	2 mS, 200 nS
Equivalent Resistance Range	5 MΩ to 10,000 MΩ	500Ω to 1 MΩ (2 mS), 5 MΩ to 10,000 MΩ (200 ns)
Resolution	0.05% of range (10^{-10}S on 200 nS range)	
Accuracy	200 nS: 2% of rdg + 10 digits	200 nS: 2% of rdg + 10 digits 2 mS: 0.2% of rdg + 1 digit
Overload Protection	500V dc or rms on all ranges	

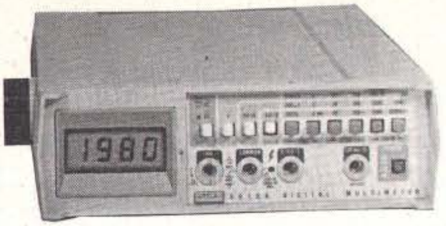
Temperature (D804A Only, requires thermocouple accessory)	
Temperature Sensor	K-type Thermocouple (Chromel-Alumel). See accessories
Range	−20°C to +1265°C
Resolution	1°C
Accuracy	±3°C ±1 digit, −20° to +300°C; 3% of rdg, +300°C to +1265°C (±2° ±1 digit, 0° to +80°C typical) Accuracy includes NBS conformity, calibration stability, zero and reference junction but not thermocouple errors.
Connection	Dual banana isothermal termination provided with Fluke thermocouple accessories. Use Y8104 termination accessory for any K-type thermocouple.
Overload Protection	2A fused up to 250V
Continuity (804A Only) Use for Passive Current Testing	
Ranges	All Resistance and Conductance ranges
Indication	Open Circuit: " ⬥ " Display Continuity: " ⬥ " Display + 2 kHz audio tone (selectable)
Response Time (2 kΩ range)	50 μs (Minimum duration of continuity or open to toggle display or audio tone. Pulse stretcher holds display and tone for approx. 100 ms.)
Overload Protection	500V dc or rms all ranges
Level Detector (804A Only) Use for Active Circuit Testing	
Reference Level	0.8V dc nominal
Display	" ⬥ " for inputs greater than reference " ⬥ " for inputs less than reference " ⬥ " for inputs toggling above and below reference Audio tone coincident with " ⬥ " (switch selectable)
Pulse Response (200 kΩ range)	50 μs (Minimum width of 0 to +3V pulse required to toggle display. Pulse stretcher holds display for approx. 100 ms when short pulses are detected.)
Input Impedance	>100 kΩ, <100 pF
Overload Protection	500V dc or rms

Accuracies are ± (% of reading + no. of digits) 1 year, 18°C to 28°C except as noted

DC Voltage	D804A	D802A	D800A
Ranges	200 mV, 2V, 20V, 200V, 1000V		
Resolution	100 μV on 200 mV range		
Accuracy (all ranges)	0.1% rdg + 1 digit	0.1% rdg + 1 digit	0.25% rdg + 1 digit
Input Impedance	10 MΩ on all ranges		
Normal Mode Rejection	>60 dB at 50 and 60 Hz		
Common Mode Rejection	>100 dB at dc, 50, 60 Hz; 1 kΩ unbalance		
AC Voltage	**D804A**	**D802A**	**D800A**
Ranges	200 mV, 2V, 20V, 200V, 750V		
Resolution	100 μV on 200 mV range		
Accuracy	45 Hz to 1 kHz: 0.75% of rdg +2 digits on all ranges except 750V (1% of rdg + 2 digits) 1 kHz to 2 kHz: 1.5% of rdg + 3 digits on all ranges except 750V (not specified) 2 kHz to 5 kHz: 5% of rdg + 5 digits on all ranges except 200V and 750V (not specified)		45 Hz to 450 Hz: 1% of rdg + 3 digits on all ranges
Input Impedance	10 MΩ, <100 pF on all ranges		
Resistance	**D804A**	**D802A**	**D800A**
Ranges	200Ω, 2 kΩ, 20 kΩ, 2 MΩ, 20MΩ		
Resolution	0.05% of range (0.1Ω on 200Ω range)		
Accuracy 200 Ω range	0.2% of rdg + 3 digits	0.2% of rdg + 3 digits	0.3% of rdg + 3 digits
2 kΩ thru 200 kΩ ranges	0.1% of rdg + 1 digit	0.1% of rdg + 1 digit	0.2% of rdg + 1 digit
2000 kΩ range	0.15% of rdg + 1 digit	0.1% of rdg + 1 digit	0.2% of rdg + 1 digit
20 MΩ range	2% of rdg + 1 digit	2% of rdg + 1 digit	2% of rdg + 1 digit
Open Circuit Voltage	<1.5V on all ranges except 2 kΩ range <3.5V		
Diode Test (Hi-Lo Ohms)	2 kΩ, 200 kΩ, and 20 MΩ ranges supply enough voltage to turn on junctions allowing a "Diode Test" 200Ω, 20 KΩ, and 2000 kΩ ranges can be made in-circuit measurements without turning on silicon junctions		
DC Current	**D804A**	**D802A**	**D800A**
Ranges	2 mA, 20 mA, 200 mA, 2000 mA		
Resolution	0.05% of range (1 μA on 2 mA range)		
Accuracy	0.75% of rdg + 1 digit all ranges		

WE ACCEPT VISA and MASTER-CHARGE

LOW COST DMM'S FOR BENCH OR FIELD



FLUKE®

- 3½ LCD display
- Auto zero, auto polarity
- AC or battery operated models
- One year warranty
- Many other features not found in other DMM'S!

FLU-D810 .... $249.00
FLU-D811 with Ni Cad batteries $289.00

LCD DISPLAY

Large, high contrast, 2000 count, liquid crystal display digits can be seen from a distance and in practically any light. An overrange condition is indicated when only the most significant digit is displayed.

TRUE RMS, 50 kHz AND HIGHER

True RMS assures accurate measurements of nonsinusoidal waveforms. You can notice the difference on even the average power line. Any measurement of ac voltage and current may apply to waveforms having a crest factor of 1:1 (squarewaves) to 1.4:1 (sinewaves) to 3:1 (peaked waveforms). A typical 3dB bandwidth to 200 kHz includes the most significant harmonics of fundamental frequencies to 50 kHz. A Fluke-manufactured hybrid rms converter provides wideband, low noise, accurate measurements at low cost.

CONDUCTANCE MEASUREMENTS

This unique and highly useful function makes possible resistance measurements far beyond the capacity of ordinary multimeters. Conductance is the inverse of ohms (1Ω) and is expressed in Siemens, formerly mhos. Simple conversion of direct-reading conductance to ohms yields resistance values to 10,000 MΩ without special shielding to minimize noise. It is a must for verifying resistance values in high voltage dividers, checking leakage of capacitors, pcb's, cables, and insulators, and for general use above 20 MΩ. You may measure transistor beta and leakage directly using a simple test adapter—more useful and informative than simple junction tests. Ask for application bulletin AB-44 for more details.

ONE YEAR ACCURACY SPECIFICATIONS

A very stable semiconductor Band-Gap reference element lets us guarantee accuracy specifications for one year. That means you can depend on accuracy for long periods of time. But it also means a great savings in calibration expense over the life of the DMM. There are only five calibration adjustments. Some will probably never need to be changed.

SPECIFICATIONS

DC VOLTAGE

Ranges: ±200mV, ±2V, ±20V, ±200V, ±1000V.
Resolution: 100 μV on lowest range, 1V on 1000V range.
Accuracy: +(0.1% of reading + 1 digit) on all ranges.
Overload Protection: To 1000V dc or peak ac on any range, continuous.
Imput Impedance: 10MΩ on all ranges.

AC VOLTAGE (TRUE RMS)

Ranges: 200mV, 2V, 200V, 750V.
Resolution: 100 μV on lowest range, 1V on 750V range.
Accuracy: ±(% of reading + no. of digits).

Range	45 Hz	1 kHz	10 kHz	20 Khz	50kHz	200 kHz
200mv 2V 20V	(0.5%+2)		(1.0%+2)	(5%+3)	*	
200V 750V	(0.5%+2)		Not specified			

*Extended Frequency Response is typically ±3dB at 200 kHz. Above accuracy applies for 5% to 100% of voltage range.

Overload Protection: To 750V rms, 1000V peak, not to exceed 10^7 volt-Hertz product, continuous (except 10 seconds maximum on 200mV and 2V ranges).
Input Impedance: 10MΩ in parallel with < 100 pF.
Resistance:

Range	Resolution	Accuracy	Full Scale Voltage	Max Test Current
200Ω	"+Ω	±(0.2% of rdg+1 digit)	<0.25V	1.30 mA
2kΩ*	1Ω		∞1.00V	1.30 mA
20kΩ	10Ω		<0.25V	10.0 μA
200kΩ*	100Ω		>1.00V	35.0 μA
2000kΩ	1kΩ	±(0.5% of rdg+1 digit)	<0.25V	0.10 μA
20MΩ*	10kΩ		>1.5V	0.35 μA

*Diode test ranges.

LOW POWER OHMS AND DIODE TEST

Three resistance ranges are identified as diode test ranges (→+). They supply enough voltage to turn on a silicon junction so diode and transistor junctions can be tested. The remaining ranges (low power ohms) supply only enough voltage to measure the resistance of in-circuit components other than diodes and transistors.

BATTERY OPTION (—01)

Rechargeable size "C" Ni Cad batteries are installed inside the 8010A or 8012A with this option. They are continually kept charged when the power cord is plugged in and completely recharged in approximately 14 hours. The batteries typically provide up to 40 hours of continuous operation without recharging when measuring dc voltage, up to 15 hours for other measurement functions. The letters "BT" appear in the upper left hand corner of the display about a half hour before the batteries require recharging.

SAFETY AND PROTECTION

Recessed banana jacks reduce shock hazard to an operator. When measuring resistance or conductance, up to 300 volts may be applied with no instrument damage. Transients to 6kV cause no damage when measuring voltage. The standard current input is protected against shorts with a 2A, 250V fuse in series with a 600V fuse. That is double protection—first, for ordinary overloads protected with an ordinary fuse, and, second, for accidental connection to a source of up to 600V ac.

8081A—10 AMPERES AND TRUE RMS

The 8010A will measure 10 amperes of ac or dc current. That is higher than most multimeters, and, because the *true* value of ac current is measured, even complex current waveforms can be measured with accuracy and confidence.

DC Current
Ranges: 200 μA, 2 mA, 20 mA, 200 mA, 2A (and 10A on 8010A).
Resolution: 0.1 μA on lowest range.
Accuracy: ±(0.3% of reading + 1 digit) on all ranges except 10A range on 8010A where accuracy is ±(0.5% of reading + 1 digit).

AC Current (True RMS)
Accuracy: ±(% of reading + no. of digits)

Range	45 Hz	2 kHz	10 kHz	20kHz
200 μA 2 mA 20 mA 200 mA		(1%+2)	(2%+2)	
2000 mA 10A*	(1%+2)		Not specified	

FLUKE DMM ACCESSORIES

FLU-4820S Carrying case (FLU-D810, D811) $35.00
FLU-C90 Carrying case (FLU-D800, D802, D804) . $10.00
FLU-A81 AC adaptor (FLU-D800, D802, D804) ... $15.00
FLU-80J10 10 Amp current shunt $30.00
FLU-Y8133 Deluxe test lead set $15.00
FLU-Y8134 Deluxe test lead set with safety connectors $15.00
FLU-Y8140 Slim test lead set $15.00
FLU-80T150C Temp. probe (°C) $110.00
FLU-80T150F Temp. probe (°F) $110.00
FLU-Y8102 Sheath thermocouple (D804) $50.00
FLU-Y8103 Bead thermocouple (D804) $20.00
FLU-Y8104 Thermocouple termination (D804) $10.00
FLU-Y8008 Touch and hold probe (D810, D811) ... $40.00
FLU-80K40 40KV high voltage probe $75.00
FLU-81RF 100 MHz rf probe $45.00
FLU-82RF 500 MHz rf probe $85.00
FLU-801600 Clamp-on AC current probe 600A $90.00
FLU-Y8100 200A AC/DC current probe $195.00
FLU-Y8101 150A AC current xformer $69.00

HICKOK
the value innovator

LX 304

- 3½ Digits
- ½" LCD Display
- Floating Decimal Point
- Diode Test Function
- Auto Zero, Auto Polarity
- 100 μV Resolution on DCV
- 200 Hr. Battery Life

SPECIFICATIONS

DC VOLTS	200mV, 2V, 20V, 200V, 1000V
Input Impedance	10MΩ, all ranges
Overload Protection	1000V dc/peak ac, except 500V on 200mV range
RESISTANCE	200Ω, 2kΩ, 20kΩ, 200kΩ, 2MΩ, 20MΩ
Resolution	0.1Ω on 200Ω range
Accuracy	±0.9% + 1 digit except ±1.4% + 1 digit on 20MΩ range
Overload Protection	120V dc or rms ac, all ranges, indefinitely 240V dc or rms ac, all ranges, 30 seconds
AC VOLTS	200V, 600V, Avg. sensing—rms calibrated sine wave
Accuracy	±1.0% + 4 digits, 40 Hz to 120 Hz -0.2 dB @ 1 kHz, -2.0 dB @ 5kHz
Input Impedance	4.3MΩ, all ranges
Overload Protection	600V dc or ac rms, all ranges
DC CURRENT	200mA, 1A
Resolution	0.1mA
Accuracy	±1.5% + 1 digit, all ranges except ±2.5% + 1 digit on 200mA range of LX 303
Overload Protection	1.7A all ranges
GENERAL	
Power	Single 9V battery; NEDA 1604 (not incl.) or Hickok AC Adapter
Battery Indicator	"Lo Bat" on display
Dimensions	5⅞" x 3⅜" x 1¾" (14.7 cm x 8.5 cm x 4.3 cm)
Weight	12 oz. (0.33 kg) including battery

HIC-LX304 .... $89.95
ACCESSORIES (See LX 303)

HICKOK

$79.95
LX303

- 3½ Digits
- ½" LCD Display
- 200 Hour Battery Life
- Auto Zero, Polarity and Overage
- 100mv DC F.C. Sensitivity
- 19 Ranges and Functions
- Weighs on 12 ounces

Specifications: DC Volts (5 Ranges): 0.1mV to 1000V; Accuracy +0.5% rdg +0.5% f.s.; Input imped: 10M ohms; Max. input 1kV except 500V on 200mV range. **AC VOLTS (40Hz to 5kHz):** 0.1 to 600 V; Accuracy: +1.0% rdg +0.5% f.s. (−2dB max. at 5kHz); Max input:600V. **RESISTANCE (6 LOW POWER RANGES):** 0.1 ohms to 20M ohms; Accuracy: +0.5% rdg +0.5% f.s. (+1.5% rdg on 20M ohms range); input protected to 120 VAC all ranges. **DC CURRENT (6 RANGES):** .01nA to 100mA; Accuracy: +1.0% rdg +0.5% f.s. **DIMENSIONS AND WEIGHT:** 5-7/8" x 3-3/8 x 1¾", 12 oz.; Power: 9V Batt. (not included) or Hickok AC adapter; READ RATE: 3/sec. OPERATING TEMPERATURE: 0°-50° C.

PART NO.	DESCRIPTION	PRICE
HIC-LX303	DIGITAL MULTIMETER	$79.95
HIC-RC-3	115V AC ADAPTER	$ 8.00
HIC-CC-3	PADDED CARRY CASE	$ 8.00
HIC-VP-10	X10 DC PROBE ADAPTER (Up to 10KV)	$16.50
HIC-VP40	40kV DC PROBE	$38.50
HIC-CS-1	10 Amp DC Current Shunt	$16.50
HIC-TP-20F	Temp Probe (-67 to +302F)	$49.95
HIC-TP-20C	Temp Probe (-55 to +150C)	$49.95

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PRIORITY ONE ELECTRONICS

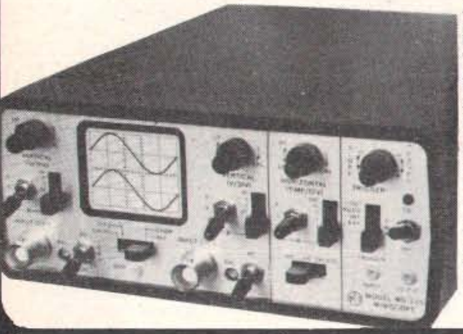
Non-Linear Systems OSCILLOSCOPES

NEW MS-230 Dual Trace Miniscope with 30 MHz Bandwidth!

From the originators of the Digital Voltmeter, the people who have broken sales and performance records for Oscilloscopes, Non-Linear Systems, comes the MS-230 miniscope.

Non-Linear Systems took their engineering and modular construction skills and made a dream a reality, a Dual-Trace 30 MHz miniscope, small enough to fit in most briefcases with room to spare at an affordable price.

FEATURES: • Dual-Trace — 2-channel; separate, chopped or alternate modes. • Warranty — one year parts and labor. • 30-megahertz bandwidth. • External and internal trigger. • Time Base — 0.05 microseconds to 0.2 Sec./div — 21 settings. • Battery or line operation. • Line synchronization mode. • Power consumption less than 50W. • Vertical Gain — 0.01 to 50 volts/div — 12 settings. • Size: 2.9" H x 6.4" W x 8.5" D. • Weighs only 3.5 lbs. with batteries. • TEST MOST DIGITAL LOGIC CIRCUITS INCLUDING MICROPROCESSORS.



VERTICLE: Mode: *CH1, CH2, CH1 & CH2 (Chopped) & CH2 (Alt.) (The following specifications apply to each channel.)* **Bandwidth:** DC to 30 MHz, ±3 db @ 3 division deflection. Typical 4 division deflection is obtainable up to 20 MHz. **Coupling:** AC, DC or ground, switch selectable. Low frequency 3 db point on AC is 3 Hz. **Rise Time:** Approximately 10 nSec @ 3 division deflection. **Vertical Input:** 10 mV/div to 50 V/div in 12 calibrated ranges. Accuracy is 3%. **Input Impedance:** 1 megohm in parallel with 50 pF.
HORIZONTAL: Mode: Internal time base or external, in XY mode, vertical input is thru CH1 and horizontal thru CH2. **Time Base:** 0.1 μsec/div to 0.5 sec/div in 21 calibrated ranges; 3% accuracy. **Bandwidth:** DC to 1 MHz (±3 db). **Deflection factor:** 10 mV/div to 50 V/div in 12 calibrated ranges. **Max. Input Voltage:** 250 V (DC and peak AC). **Trigger Modes:** Automatic, internal, external, and line (line not functional when operating on battery power). **Slope Switch:** + *or* –. **Coupling:** AC. **Sensitivity:** Less than 1 div for interanl trigger and less than 1 volt for external trigger. **CRT Viewing Area (Screen Size):** 1.1" H x 1.35" W. **Graticule:** 0.25 in./div (4 div H x 5 div W). **Power:** 3 rechargable batteries or 115 VAC with transformer. **Battery Life:** Approx. 45 minutes. **Power Consumption:** Less than 50 watts. **Input Connector:** BNC; two shielded cables included. **Size:** 2.9" H x 6.4" W x 8.5" D. **Weight:** 3.5 lbs. with batteries.

NLS MS-230 Miniscope .... **List Price $598.15**

Our Price $525.00

MS-215 MINISCOPE OSCILLOSCOPE

An extremely small dual-trace oscilloscope with big performance at a low price. Portable, battery powered or AC line operation. Rechargeable batteries and charger included.

FEATURES: • Dual trace — 2 channel; separate, chopped, or alternate modes. • 15 MHz bandwidth. • External and internal trigger. • Battery or line operation. • Automatic or line sync. modes. • Power consumption less than 15 watts. • **Time Base:** 0.1 μsec to 0.5 sec/div in 21 settings. • **Vertical Gain:** 0.01 to 50 volts/div in 12 settings. • Weighs only 3 lbs. with batteries.

SPECIFICATIONS: Vertical (Both channels identical). **Mode:** CH1, CH2, CH1 and CH2 (chopped), and CH1 and CH2 (alternate). **Bandwidth:** DC to 15 MHz, ±6 db at 1 div deflection.* **Risetime:** Approx. 23 nsec at 1 div deflection. **Deflection Factor:** 10 mV/div to 50 V/div in 12 calibrated ranges; 3% accuracy. **Max. Input Voltage:** 350 V (DC and peak AC) provided DC component does not exceed 250 V. **Horizontal Mode:** Internal time base or external. In XY mode, vertical input is thru CH1 and horizontal thru CH2. **Time Base:** 0.1 μsec/div to 0.5 sec/div in 21 calibrated ranges; 3% accuracy. **Bandwidth:** DC to 200 kHz (±3 db). **Deflection Factor:** 10 mV/div to 50 V/div in 12 calibrated ranges. **Max. Input Voltage:** 250 V (DC and peak AC). **Trigger: Modes:** Automatic, internal, external, and line (line not functional when operating on battery power). **Slope Switch:** + *or* –. **Coupling:** AC. **Sensitivity:** Less than 1 div for internal trigger and less than 1 volt for external trigger. **CRT Viewing Area (Screen Size):** 1.1" H x 1.35" W. **Graticule:** 0.25 in./div (4 div H x 5 div W). **Power Consumption:** Less than 15 watts. **Power Required:** AC line or rechargeable batteries. **Battery Life:** 3 hours typical. **Input Connector:** BNC; two shielded cables included. **Size:** 2.9" H x 6.4" W x 8.0" D. **Weight:** 3 lbs.

NLS-MS-215 Miniscope .... **List Price $465.45**

Our Price $410.00

* Typical 3 dB point is 8 MHz at 2-division deflection. Typical maximum frequency for full scale (4-div) deflection is 2 MHz.

MS-15 MINISCOPE OSCILLOSCOPE

An extremely small oscilloscope with big performance at a low price. Portable, battery operated, or AC line operation. Rechargeable batteries and charger unit included. 1.1" x 1.35" viewing area.
FEATURES: • 15 MHz bandwidth. • External or internal triggering. • Time Base—0.1μsec. to 0.5 sec./div. in 21 settings. • Battery or line operation. • Automatic and line sync modes. • Power consumption less than 15 watts. • Vertical Gain—0.01 to 50 volts/div. in 12 settings. • Weight—only 3 lbs.

SPECIFICATIONS: Vertical Bandwidth: 15 MHz ± 6 dB at 1 div. deflection*. **Risetime:** Approx. 23nsec. at 1 div. deflection. **Deflection Factor:** 10 mV/div. to 50 V/div. in 12 calibrated ranges; 3% accuracy. **Max. Input Voltage:** 350 V (DC + peak AC) provided DC component does not exceed 250 V. **Horizontal Mode:** Internal time base or external. **Time Base:** 0.1 μsec to 0.5 sec/div. in 21 calibrated ranges; 3% accuracy. **Horizontal Bandwidth:** DC to 200 kHz (± 3 dB). **Deflection Factor:** Approx. 1V/div. **Max. Input Voltage:** 100 V (DC + peak AC). **Trigger Modes:** Automatic, internal, external and line (line does not function when operating on battery power). **Slope Switch:** + *or* –. **Coupling:** DC. **Sensitivity:** Less than 1 div. for internal trigger; less than 1 volt for external trigger. **CRT Viewing Area (Screen Size):** 1.1" h. x 1.35" w. **Graticule:** 0.25 in./div. (4 div. h. x 5 div. w.). **Power Consumption:** Less than 15 watts. **Power Required:** AC line or rechargeable batteries. **Battery Life:** 3 hours typical. **Input Connector:** BNC; two cables included. **Size:** 2.9" h. x 6.4" w. x 8.0" d. **Weight:** 3 lbs. with batteries.

NLS-MS-15 Miniscope .... **List Price $349.80**

Our Price $310.00

* Typical 3 dB point is 8 MHz at 2-division deflection. Typical maximum frequency for full scale (4-div) deflection is 2 MHz.

ACCESSORIES: PROBES

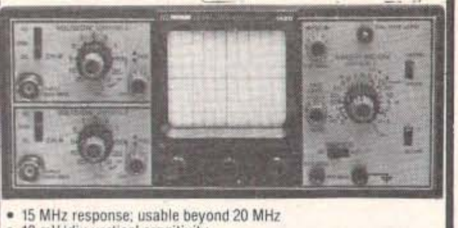
Deluxe 10 to 1 probe with 10 megohm input. 100 HHz probe with 4 interchangeable tips: Spring-loaded retractable cover tip, insulating tip, BNC tip, IC tip, also included cap adjustment tool and zippered vinyl case.
NLS-41-141 .... **$27.00**

DELUXE COMBINATION PROBE
Same as above except the probe has a switch to select; 10 to 1, 1 to 1 or a ground reference position.
BKP-PR37RD Red probe body
BKP-PR37GY Grey probe body .... **$39.50 ea.**

LEATHER CARRYING CASE
The leather case has 2 separate compartments. One to hold the scope, the other to hold the charger, probe, shoulder strap, etc. The case can be worn on the belt, or over the neck. The snaps used on the case are "one way", thus accidental striking of the case against an object will not undo the snaps or let it be pulled off your belt.

NLS-41-180 - Leather Case (MS-230) **$45.00**
NLS-41-140 - Leather Case (MS-15 & MS-215) **$45.00**
NLS-41-134-2 - Charger Unit, 230 VAC
(MS-15 & MS-215) **$7.70**
NLS-41-184-2 - Charger Unit, 230 VAC
(MS-230) **$20.90**

15 MHz dual-trace Model 1420 AC/DC/battery mini-scope



- 15 MHz response; usable beyond 20 MHz
- 10 mV/div vertical sensitivity
- Conveniently fits into ordinary attache case — only 11 x 22 x 30 cm (4.5 x 8.5 x 12") with handle
- Operates on AC, external 10-16 VDC or optional internal battery pack
- 8 x 10 division high-brightness rectangular CRT
- TIME/DIV control selects line/frame rate triggering
- 18 calibrated sweep positions
- Video sync separator standard
- X-Y operation — X axis, through CH.B
- Front-panel probe calibration source
- Weighs only 3.6 kg. (8 lbs.)

B+K PRECISION

SPECIFICATIONS
VERTICAL AMPLIFIER: Deflection Factor: 10 mV/div to 20 V/div, ±5% in 11 ranges, each with vernier adjustment. **Frequency Response: DC:** –15 MHz (–3 dB); **AC:** –10 Hz-15 MHz (–3 dB). **Rise Time:** 24 nsec or less. **Overshoot:** 3% or less. **Input Impedance:** 1 MΩ shunted by 22 pF. **Maximum Input:** 300 V (DC + AC) peak or 600 V p-p. **Operating Modes:** Channel A only; Channel B only; A & B (dual). **Dual-Trace:** Trace automatically chopped at all sweep rates less than or equal to 1 mSEC/div and alternate mode for all sweep rates greater than 1 mSEC/div. **Chop Frequency:** 100 kHz ±20%. **Channel Separation:** Better than 60 dB at 1 kHz.

SWEEP SYSTEM: (Common to Channel A and Channel B). **Type:** Automatic and triggered (NORM); auto provides sweep with no input signal. **Sweep Time:** 1.0 μS/div to 0.5 S/div ±5% in 18 ranges in a 1-2-5 sequence. **Linearity:** 3%; 10X ±5%. **Sweep Magnification:** x10, ±10%, variable between ranges. Extends maximum sweep rate to 100 nS/div.

TRIGGERING: Source: CH A, CH B, (CH A in dual) external. **Automatic:** Sweep obtained without input signal. **Normal:** Sweep is obtained with a displayed signal of one division or more. **Slope:** Sweep can be set to trigger on the positive- or negative-going slope of the trigger waveform. **Coupling:** AC, 20 Hz-20 MHz. **Level:** Continuously variable. **Trigger Sensitivity:** INT: 20 Hz to 15 MHz — 1 Div deflection. EXT: 20 Hz to 15 MHz — 1.0 V p-p. **External Trigger Input:** Max. Input Voltage: 50 V p-p or 24 V. DC + AC peak. **Input Impedance:** 100 KΩ (nominal). Input Capacitance: 35 pF (nominal).

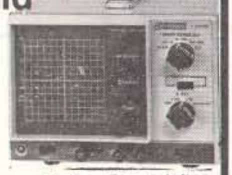
HORIZONTAL AMPLIFIER: Input through Channel B vertical input. **Deflection Factor:** 10 mV/div, to 20 V/div, ±5% in 11 ranges, each with fine adjustment. **Response: DC:** DC to 1 MHz (–3 dB); **AC:** 10 Hz to 1 MHz. **Input Impedance:** 1 MΩ (±5%), 22 pF (±3pF). **Input Protection:** 300 V (DC + AC peak) or 600 V p-p. **X-Y Operation:** With SWEEP TIME/DIV switch in X-Y position, the CH B input becomes the X input (horizontal) and the CH B position control becomes horizontal position control.

OTHER SPECIFICATIONS:
Operating Environment — Temp.: 0 to +45°C. **Calibration:** Internal 1 V p-p ±3% (square wave at 1 kHz ±5%). **CRT:** Rectangular, 8 x 10 div (0.5 cm/div) with P31 phosphor; mumetal shield. **Power Requirements:** 120 VAC-60 Hz, 240 VAC-50 Hz, 10-16 VDC or internal battery. Power consumption: 16 watts (AC). Supplied with 120 VAC adapter. 240 VAC adapter available on special order. **Size:** (HWD) 11 x 22 x 30 cm (4.5 x 8.5 x 12") with handle. 8 x 20 x 25 cm (3.25 x 7.75 x 10") without handle. **Weight:** 3.6 kg (8 lbs.) with optional battery. **Probes:** Two 10:1/direct probes and accessory tips provided. **Options:** BP-14 battery pack, LC-20 deluxe carrying case with shoulder strap and pocket, LC-21 pouch for probes and operating manual.

		LIST PRICE	OUR PRICE
BKP-1420	Miniscope	$825.00	$750.00
BKP-BP14	Battery Pack		$ 55.00
BKP-LC20	Leathercase		CALL
BKP-LC21	Pouch for Probes		CALL

3" 5 MHz Solid State Oscilloscope

$289.00
B&K-PRECISION Model BKP-1405



FEATURES: 5MHz with high sensitivity • Direct deflection input for waveforms to 450MHz • Sharp bright trace • DC amplifiers on both axes • 10mV/div vertical sensitivity • Weighs only 8.5 lbs.
The 1403A is an outstanding value. Bandwidth extends to 5MHz with a sensitivity of 10mV/div or better. With high brightness CRT and smoked-glass filter, waveforms are clear and easy to observe. The graticule features db and division indexing.
SPECIFICATIONS: Vertical Amplifier—Sensitivity: 10mV/div or better. Response: DC, DC-5MHz (–3dB); AC, 2Hz-5MHz (–3dB). Max Input: 600V peak to peak. **Input Impedance:** 1 meg shunted by 35pF. Attenuattor: 1, 1/10, 1/100 multiplier, ±5%. Gain Control Range: greater than 22dB. **Horizontal Amplifier**—Sensitivity: 300mV/division or better. Response: DC-250kHz. Max Input: 100 Vp-p. **Sweep System**—Type: Recurrent. Time Base Ranges: 10-100Hz, 100-1000Hz, 1-10kHz, 10-100kHz; continuously variable between ranges. Sweep Linearity: ±5%. Sync: Internal, negative; external. **Direct Deflection Terminals:** 10V/division sensitivity or better. **General**—Intensity Modulation: 25Vp-p. **Power:** 117/234VAC, 50-60Hz, 10W; three-wire grounded line cord. **Acc.incl.:** Leads, spare fuse, instructions. **Size:** (HWD) 13.1 x 18 x 29 cm (5.25 x 7.25 x 11.5"). **Weight:** 3.8 kg. (8.5 lbs.). Optional Accessories: PR-21 probe. LC-14 case.

WE ACCEPT VISA and MASTER-CHARGE

Non-Linear Systems DIGITAL PANEL METERS DIGITAL MULTIMETERS Counters

MODEL PC-4 PANEL METER EVENT COUNTER

PC-4


The PC-4 is an all-solid-state event counter or totalizer designed for panel mounting and high reliability. It will totalize electronic pulses up to 200,000 per second or mechanical switch closures up to 3000 per second. The .3" high LED display is easily readable up to eight feet. Has multiplexed BCD output and a displayhold feature. **Input Signal Amplitude:** + 3 to + 15 volts or switch open to ground for count. **Decimal Location:** external jumper locates decimal to any of four positions. **Outputs:** 1-2-4-8 BCD parallel. **Power Source:** +5 VDC, ±5%, external. **Size:** 0.9375" h. x 2.5" w. x 3.25 d. **Weight:** 4 oz.

NLS PC-4 Event Counter—Price Each **$64.20**

MODEL RC-5TB UNIVERSAL COUNTER

A DIN size panel-mounted universal counter which can be internally programmed to function as a unit counter, frequency counter. The RC-5TB also permits internal selection of four time bases of 0.01 sec., 0.1 sec., 1.0 sec., or 10 sec. for the frequency counter mode and 1 cycle, 10 cycles, or 1000 cycles for the period frequency ratio and time interval modes. The RC-5TB utilizes terminal block connections and has hold and reset capabilities. **Power Requirements:** + 5 VDC, ±5%. **Operating Temperature Range:** 0° C to +50° C. **Accuracy:** Unit counter frequency counter, ±1 count; frequency ratio/period/time interval, ±1 count + time base accuracy. **Number of Digits:** 5. **Display:** 0.5" high LED. **Count Rate:** Greater than 1 MHz. **Input signal Amplitude:** +3 to +15 volts. **Time Base Crystal Frequency:** 10 MHz. **Frequency:** DC to 1 MHz. **Size:** Behind bezel, 3.58" w. x 1.645" h. x 3.80" d.

NLS RC-5TB Universal Counter · Net each **$144.00**

MODELS PR-5 AND PR-5B DIGITAL PRESET COUNTERS

PR-5

Low-cost electronic digital preset counters to replace mechanical counters. Solid-state LSI design. Up to 500 kHz pulse count rate or 300 contact closures per second. Accepts subtractive pulses for rejection subtraction or for counting downward. Multiplexed BCD output. Guarded preset; can not be accidently changed. Unlimited number of preset count. Applicable to totalizing batch counting, digitizing, timing rate/frequency measurement, press control, coil winding and many other applications. 5-digit, 0.3" high LED display indicates total number of counts less any subtractive counts. Compatible with wide range of sensors. **Count Speed:** Electronic, up to 500 kHz (+3 V to +15 V); contact closure, up to 300/sec. **Number of Resets:** One, built-in; unlimited number with external switches; preset number set by front-panel screwdriver adjustments. **Input Impedance:** 40K ohms. **Reset Capability:** Front-panel pushbutton switch or external contact closure. **Power Requirements:** +12 VDC (±5%), 270 mA max.; Model PR-5B contains internal rechargeable battery pack which provides +12 V stand-by power (without loss of count) for approximately 1 hr. in case of primary power failure. An optional 115 VAC line power supply for operation of Model PR-5 or recharging batteries of PR-5B is available as an accessory. **Operating Temperature:** 0° C to 50° C. **Coincidence Output:** Output signal is provided when displayed count is equal to preset number (TTL logic level) or when zero is reached when counting downward. **Size:** 2.25" h. x 3.25" w. x 4" d. **Weight:** 4.75 oz.

NLS PR-5 Digital Preset Counter—Net Each **$168.55**
NLS PR-5B Counter w/Battery Pfck—
Net Each **$198.55**

RANGE INFORMATION

WHEN ORDERING PANEL METERS FROM THIS PAGE, A VOLTAGE RANGE MAY BE REQUIRED IN THE PART NUMBER. PLEASE USE THE CODES GIVEN.	RANGE	CODE
	0-200V	A
	0-200V	B
	0-20V	C
	0-2V	D
	0-.2V	E

3½ DIGIT PM SERIES THRIFTMETER DIGITAL PANEL METERS

Great performance at a thrifty price. 3½ digits; 3 digits plus 100% overrange. 0.05% accuracy.

FEATURES: • MOS/LSI construction. • Less than 1" high. • Programmable decimal. • No zero adjustment necessary. • Overload indication. • Large, bright, 0.3" LED or LCD display. • Low Power requirement. • Display blanking. • Automatic polarity. • Input voltage protection.

SPECIFICATIONS: Voltage Ranges: 0 – ±0.1999, 0 – ±1,999, 0 – +19.99, 0 – ±199.9, and 0 – 1000 VDC. **Accuracy (at +23° C ±2° C):** ±0.05% of reading +0.05% of full scale. **Input Impedance:** 0.1999 volt range, 100 megohms; 1.999 volt range, 1000 megohms; 19.99 volt range, 1 megohm; 199.9 and 1000 volt ranges, 10 megohms. **Display Height:** 0.3" **Display Type:** PM-349 and PM-350, LED; PM-351, LCD. **Update Rate:** 3 rdg./sec. nominal **Power Requirements;** 5 VDC (±5%), 200 mA, nominal for PM-349 and PM-350; 6mA for PM-351. **Common Mode Rejection:** 80 dB, minimum. **Common Mode Compliance:** +100 mV. **Operating Temperature:** 10° C to 50° C. **Zero Adjustment:** None required. **Decimal Positioning Capability:** Standard. **Control Signals: Polarity Inhibit:** Standard. **Display Inhibit:** Standard. **Scale Factor Adjustment:** ±5%. **Input Voltage Protection:** Standard. **Overload Indication:** Standard. **Bipolar Operation:** Standard. **Automatic Polarity:** Standard. **Special Scaling:** Optional. **Offset Capability:** Optional **Current Meter Operation:** Optional. **Overall Size:** 2-7/8" w. x 15/16" h. x 3¼" d.

with Soldered in components
NLS-PM349-RANGE **$55.65**
Range change resistors, Display and IC's are plug in
NLS-PM350-RANGE **$69.55**
NLS-PM351-RANGE **$78.10**

RANGE: .2V (PM350, PM351 Only), 2V, 20V, 200V, 1000V.
Specify when ordering.

4½ DIGIT PM SERIES DIGITAL PANEL METER

SPECIFICATIONS: Voltage Ranges: 0 – ±1.999, 0 – 19.999, 0 – ±199.99, 0 – ±1000VDC. **Accuracy (at**

NLS-PM450-RANGE **$122.00**

4½ DIGIT RM SERIES DIGITAL PANEL METERS

RM-450

SPECIFICATIONS: Voltage Ranges: 0 – ± 1.9999, 0 – 19.999, 0 – ±199.99, 0 – ± 1000 volts. **Accuracy (at 23° C ± 2°):** 0.02% of full scale. **Input Impedance:** 1.9999 volt range, 1000 megohms; 19.999 volt range, 1 megohm; 199.99 and 1000 volt ranges, 10 megohms. **Display Height:** RM-450, 0.5"; RM-451, 0.4". **Display Type:** RM-450, LED; RM-451, LCD.

NLS-RMS450-RANGE .... **(LED DISPLAY) $133.00**
NLS-RM450TB-RANGE .... **(LED DISPLAY) $133.00**
NLS-RM451-RANGE .... **(LCD DISPLAY) $146.60**
NLS-RM450TB-RANGE .... **(LCD DISPLAY) $146.60**

RANGE: 2V, 20V, 200V, 1000V.
Specify when ordering.

3½ DIGIT RM SERIES DIGITAL METERS

SPECIFICATIONS: Voltage Ranges: 0 – ±. 1999, 0 – ± 1.999, 0 – ± 19.99, 0 – ± 199.9, and 0 – ± 1000 VDC/VAC. **Accuracy** (at 23° C ±2° C): ± (0.05% of reading +0.05% of full scale). **Input Impedance:** 0.199 volt range, 100 megohms; 1.999 volt range, 1000 meghohms; 19.99 volt range, 1 megohm; 199.9 and 1000 volt ranges, 10 megohms. **Display Height:** 0.5" LED: 0.6" LCD. **Display Type:** RM-350 or RM-350TB, LED: RM-351 or RM-351TB, LCD. **Update Rate:** 3 readings/second. **Common Mode Rejection:** 80 db minimum. **Common Mode Compliance:** + 100 mV. **Operating Temperature:** 10° C to 50° C. **Zero Adjustment:** None required. **Size:** 1.89" h. x 4.0" w. x 3.8" d.

NLS-RM350-RANGE .... **(LED DISPLAY) $69.55**
NLS-RM350TB-RANGE .... **(LED DISPLAY) $69.55**
NLS-RM351-RANGE .... **(LCD DISPLAY) $81.30**
NLS-RM350TB-RANGE .... **(LCD DISPLAY) $81.30**

RANGE: .2V, 2V, 20V, 200V, 1000V.
Specify when ordering.

THE NEW TECHNOLOGY TOUCH/TEST 20 MULTIMETER

Voltage Measurements. AC volts & AC millivolts — each function has three ranges; measurements from 10 microvolts to 750 VRMS. DC volts & DC millivolts — each function has three ranges; measurements from 10 microvolts to 1000 VDC.

Temperature Measurements. Degrees Celsius & Fahrenheit — two ranges; measurements from -40° to +150°C or from -40° to +302°F.

Conductance Measurements. Measurements from 0.01 nanosiemens to 1.999 nanosiemens which is equivalent to: from 5 megohms to 100,000 megohms.

Capacitance Measurements. Microfarads, nanofarads & picofarads — six ranges combined; measurements from 1.0 picofarad to 200 microfarads.

Current Measurements. AC amperes & AC milliamperes — four ranges; measurements from 10 microamperes to 10 amperes. DC amperes, DC milliamperes & DC microamperes — seven ranges combined; measurements from 0.01 microamperes to 10 amperes.

Resistance Measurements. Megohms, kilohms & ohms — seven ranges combined; measurements from 10 milliohms to 20 megohms.

Other Tests. Diode Test — A single quantitative means of checking diode and transistor junctions in both conducting and non-conducting directions. Continuity Test — Provides capability for audibly checking conductors and solder joints for shorts and open circuits, plus a go-no-go measurement of the amount of resistance from 10 milliohms to 2000 ohms.

ACCURACY SPECIFICATIONS

DC Voltage — DC volts & DC Millivolts
±(0.2% of Reading + 1 digit)

AC Voltage — AC volts & AC millivolts
±(0.5% of Reading + 2 digits) — 50 Hz to 10 kHz

DC Current — DC amps, DC milliamps & DC microamps
±(1% of Reading + 1 digit)

AC Current — AC amps & AC milliamps
±(1.5% of Reading + 2 digits) — 50 Hz to 10 kHz

Temperature — °C & °F
±(3°C from -40°C to +150°C)

Resistance — ohms, kilohms & megohms
±(0.25% of Reading + 1 digit)
Test Current — 100 nA, 10 μA & 1 mA
Test Voltage — 0.2V max. @ F.S. up to 2 megohms

Conductance — nanosiemens
±(0.2% of Reading + 2 digits)

Capacitance — microfarad, nanofarad & picofarad
±(1% of full scale)

Diode Test
±(0.2% of Reading + 1 digit)
Test Current — 1 mA ±2%

TOUCH/TEST 20 comes complete with Test Leads, Temperature Probe, and Component Test Adaptor.

CAT. PART #	LIST PRICE	OUR PRICE
NLS-TT20	**$399.50**	**$360.00**
NLS-TT20B	**$425.00**	**$385.00**
(With Batteries and Charger Unit)		
NLS-41-140	Leather Carrying Case	**$45.00**

FM-300TB/115 and FM-300TB/230
LCD Display — 45.0 to 99.9 Hz.

3DIGIT FM SERIES DIGITAL LINE FREQUENCY MONITORS

FEATURES: • Crystal controlled time base. • Fits DIN or NEMA cutout. • Large 0.6" LCD or 0.5" LED. • Calibration not required. • Line voltage protection. • Display updated every two seconds. • Terminal block connections.

SPECIFICATIONS: Frequency Range: FM-3TB and FM-300TB, 45.0 to 99.9 Hz; FM-340TB, 045 to 450 Hz. **Time Base:** 1 second, crystal controlled. **Resolution:** FM-3TB and FM-300TB, 0.1 Hz; FM-340TB, 1 Hz. **Accuracy:** FM-3TB and FM300TB, ±0.1 Hz; FM-340TB, ±1 Hz. **Display:** FM-3TB, 0.5" LED: FM-300TB, 0.6" LCD: FM-340TB, 0.6" LCD. **Update Rate:** Once every two seconds **Operating Temperature:** 0° C to +50° C. **Size:** 1.89" h. x 4.00" x 3.80" d.

NLS-FM3TB-115VAC **$103.55**
NLS-FM3TB-230VAC **$105.55**
NLS-FM300TB-115VAC **$109.15**
NLS-FM300TB-230VAC **$112.35**
NLS-FM340TB-115VAC **$112.00**
NLS-FM340TB-230VAC **$115.00**

WE ACCEPT VISA, and MASTER-CHARGE

B+K PRECISION DYNASCAN CORPORATION

Test Equipment

New Sweep/Function Generator......

BKP-3020 LIST PRICE **$350.00** OUR PRICE **$309.00**

...Four Instruments in One!

decoders...Generate double side-band supressed carrier signals for communications system tests...Evaluate attack-time of audio compressors...Sweeptest any passive or active device up to 2MHz.

FEATURES: Four instruments in one package — sweep generator, function generator, pulse generator, toneburst generator • Covers .02Hz-2MHz • 1000: 1 tuning range • Low-distortion high-accuracy outputs • Three -step attenuator plus vernier control • Internal linear and log sweeps • Tone-burst output is front-panel or externally programmable • Variable symmetry for almost any wave shape • Independent control of modulation and carrier level • Most complete low frequency signal source in its price range.

APPLICATIONS: Frequency response tests ... Amplifier square-wave and sweep evaluation ... Tone-burst speaker response tests ... Bias signal source ... substitute signal source for digital and analog circuits ... Pulse signal source ... Check threshold levels for TTL and CMOS logic ... Receiver alignment ... IF response tests... Observe distortion including Transient Intermodulation (TIM) distortion ... Measure linearity of instruments and transducers ... Check for ringing inductors ... Align subaudible and tone-burst.

SPECIFICATIONS: Frequency—Range: .02Hz-2MHz in 7 ranges. (each range provides 1000: 1 frequency control.) **Ext. Control:** VCG range >1000: 1 (linear) on any range with 0-10V input. **Accuracy:** ±5% of f.s. **Stability:** .05% (after 15 min.) **SQUARE WAVE:** Variable amplitude and fixed TTL output. **Symmetry:** 99% to 100kHz. **Rise/Fall Time:** <100ns. **TTL Square Wave:** <25ns. rise/fall time (logic 0<0.4V; logic 1>2.4V). **SINE WAVE—Distortion:** <1% .02Hz to 100kHz, <0.5% typical. **Amplitude Flatness:** Better than ±0.3dB to 2MHz at max. output. **TRIANGLE WAVE—Linearity:** 99% at 100kHz. **Variable Symmetry:** 40:1 range, .02Hz-2MHz. **AM Modulation:** 0-1.5Vp-p ext. signal required to provide 100% modulation. Capable of suppressed carrier operation. **SWEEP—Internal:** Linear or log. **Sweep Rate:** 0.5Hz to 50Hz. **Sweep Width:** Var. 10:1 to 1000:1. **Sweep V. Output:** Proportional to sweep. **Ext. Sweep:** Rear panel VCG input provided. **TONE-BURST—Burst Width:** Adj. from 5-90% of period of internal gating frequency. Ext. gating, burst width determined by TTL gating pulse. **Rep. Rate:** 0.5Hz to 50Hz, set by SWEEP RATE control. **OUTPUT—Amplitude:** 20Vp-p open circuit; 10Vp-p at 50Ω. **Control:** Cont. variable, >20dB. Fixed attenuation, 0-40dB in 3 steps; total 60dB attenuation. **Output Z:** 50Ω ±5%. **DC Offset:** Cont. variable. 0 to ± 10V or ± 5V into 50Ω.

GENERAL—Rear panel jacks: VCG (sweep) input, GCV voltage out (prop. to freq.), AM input, TTL output, ext. burst gate input. **Operating Temp.:** 0-50°C. **Power:** 105-130VAC, 60Hz, 22W. Three-wire cord. **Size (HWD):** 8.1 x 29 x 20cm. (3.2 x 11.3 x 7.8") incl. handle. **Weight:** 1.35kg (3.1 lbs.) CSA listed.

New Low Distortion Function Generator

BKP-3010 LIST PRICE **$200.00** OUR PRICE **$175.00**

APPLICATIONS: Frequency response tests • Amplifier performance evaluation • Bias signal source • Analog/Digital signal substitution • Receiver alignment • Check linearity of test instruments • Check resonant circuits for ringing inductors.

FEATURES: Generates sine, square and triangle waveforms • Variable amplitude and fixed TTL squarewave outputs • 0.1 Hz to 1 MHz in six ranges • Typical sine wave distortion under 0.5% from 0.1 Hz to 100 kHz • Variable DC offset for engineering applications • VCO external input for sweep-frequency tests.

SPECIFICATIONS: Frequency—Range: 0.1Hz to 1MHz in six ranges. (Each range provides >100:1 freq. control.) **Ext. Control:** >100:1 on any range with 0-5.5V input. **Accuracy:** 5% of f.s. **Stability:** .05% (after 15 min.) **SQUARE WAVE—Symmetry:** 99% to 100kHz. **Rise/Fall Time:** <100ns. **TTL Square Wave:** <25ns rise/fall time. **TRIANGLE WAVE—Linearity:** 99% to 100kHz. **SINE WAVE—Distortion:** <1% 0.1Hz to 100kHz; <0.5% typical. **Amplitude Flatness:** <0.3dB to 1MHz at max. output. **OUTPUT—Amplitude:** 20Vp-p open circuit, 10Vp-p into 600Ω. Continuously variable, >30dB range. **Output Z:** 600Ω ± 5%. **DC Offset:** Var. to max. of ± 10V open circuit or ± 5V at 600Ω. **GENERAL—Operating Temp.:** 0-50°C. **Power:** 105-130VAC, 60Hz, 8W. **Size (HWD):** 8.1 x 29 x 20cm (3.2 x 11.3 x 7.8") incl. handle. **Weight:** 1.3kg (2.9 lbs.). CSA listed.

80 MHz Counter with Period Mode and Timer

BKP-1820 LIST PRICE **$300.00** OUR PRICE **$265.00**

FEATURES: • 5Hz to 80MHz reading guaranteed—100MHz typical • Period measurements from 5Hz to 1MHz. • Period average, auto and manual positions • One PPM resolution • Totalizes to 999999 plus overflow • Elapsed time measurements from .01 to 9999.99 seconds plus overflow • One-megohm input resistance • Bright,. 43" high LED readouts.

SPECIFICATIONS: FREQUENCY—Range: 5Hz to 80MHz Gate Time Auto: 10ms (MHz) and 100ms and 1, sec. (kHz). Gate Time Man.: 1 sec. (1Hz reso.). **Accuracy:** ±t.b. accur. ±1 count. **Resolution:** ±0.0001% (i.e. 1PPM of a 6 digit scale). **PERIOD—Range:** 5Hz to 1MHz; μs (100 period aver.) or AUTO reading. **Period Aver. Auto:** 1 period aver. (ms), 10 and 100 period aver. (μs). **Period Aver. Man.:** 100 and 100 period aver. (μs reading with 1ns reso.). **TOTALIZE CHAR.—Range:** 5Hz to 80MHz; 0 to 999999 plus overflow. **Control:** Man. reset to 0; convertible to remote reset. **ELAPSED TIME—Range:** .01 to 9999.99 sec. plus overrange. **Trigger:** TTL or contact closure. **Reset:** Manual, on front panel. **INPUT—Impedance:** 1 MΩ; 25pF. **Coupling:** AC. **Sensitivity:** 30mV rms, 5Hz to 40MHz; 50mV max. at 80MHz. Derate linearly to 100 V (peak AC + DC) at 1kHz, **Max. Input:** 200V (peak AC + DC) DC to 500 Hz, derate linearly to 30V (peak ac + dc) @ 80MHz. **Attenuator:** X10 switch sel. **INT. TIME BASE**—(25°C;½ hour warm-up) 10MHz Crystal Oscillator. **Setability:** ±0.1 PPM (±1Hz). **Stability:** <±1PPM for ±10° Line Voltage Variation;< ±0.001% (i.e. ±10 PPM) from 0° to 50°C ambient. **Max. Aging:** ±1 PPM/YR. **Ext. Input:** TTL Level, switch sel., **General—Power:** 105V to 130V and 212 to 258V, 50/60 Hz. **Size (HWD):** 8.1 x 29 x 19 cm. (3.25 x 11.6 x 7.50") incl. handle. **Weight:** 1.4 kg (3 lbs.).

Portable Autoranging Digital Capacitance Meter

BKP-830 LIST PRICE **$199.00** OUR PRICE **$174.00**

- **Automatically measures capacitance from 0.1pF to 200 mF**
- **0.1pF resolution**
- **No range switching**
- **10 internal ranges for accuracy and resolution**
- **0.2% basic accuracy**
- **Range hold switch**
- **Zero control for test lead compensation**
- **3½ digit LCD display**
- **Banana jacks and special lead insertion jacks**
- **Battery or AC operation**
- **Fuse-protected**

CAPACITANCE

Range: 10 automatically selected ranges with full scale value from 199.9pF to 199.9mF (reads from 0.1pF to 0.2F, with resolution of 0.1pF). **Accuracy:** *Auto* .2% of reading, ±0.5pF, ±1 digit to 19.99uF, 1% of reading, ±1 digit from 20uF to 199.9mF; *Hold* all specs between 180 & 1999 counts same as Auto; for all readings between 0 and 179 counts add: 0.1% of full scale to 19.99mF full scale, 0.5% of full scale from 19.99uF full scale to 199.9mF full scale. **Resolution:** 0.1pF on lowest range and 0.05% of full scale on all other ranges. **Reading Time:** 0.4-1.0 SEC to 0.20mF; increasing to 6 SEC at 200mF. **Zero Control:** Can compensate up to 25pF of test lead capacitance. Minus sign (-) indicates overcompensation. **Overrange Indicator:** (All Ranges) plus sign (+) with blank display and mF LED on.

GENERAL

Display: 3½ digit LCD display. **Front Panel Controls:** Range HOLD switch, ZERO adjust, ON-OFF switch. **Power Source:** 4 standard "C" size cells operating from 4.2-6 volts, nicad, alkaline, or zinc carbon, with provision for AC adapter/charger. (Note: batteries and charger are not supplied.) **Battery Life:** 20 hours minimum, continuous use. **Operating Temperature:** +15°C to +35°C (59°F to 95°F) at stated accuracy. **Usable range at reduced accuracy:** 0°C to 50°C (32°F to 122°F). **Dimension:** 16 x 11 x 6cm (6.4 x 4.4 x 2.4"). **Weight:** 725g. (1.6 lbs.) with batteries. **Optional Accessories:** BC-28 AC adapter/charger, BP-28 battery pack, LC-28 carrying case.

New Portable Digital Capacitance Meter

BKP-820 LIST PRICE **$155.00** OUR PRICE **$140.00**

FEATURES: Measures capacitance to 1 Farad in 10 ranges • Resolves to 0.1pF on lowest range • 4 digit easy-to-ready LED display • 0.5% accuracy • Special lead insertion jacks and banana jacks • Fuse protected • Uses either rechargeable or disposable batteries • Overrange indication.

APPLICATIONS: Measure: unmarked capacitors • cable capacitance • trimmer capacitors • capacitance in switches and other components • capacitor tolerance for incoming inspection, QA; Design use.

SPECIFICATIONS: Capacitance—Range: 0.1pF to 1000 millifarads (1 Farad) in 10 ranges. **Accuracy:** 0.5% of F.S., ± 1 digit to 100μF; 1% of F.S., ± 1 digit from 1000μF to 1000mF. **Resolution:** 0.1pF on lowest range. **Reading Time:** 0.6 sec to 10mF, increasing to 35 sec max. at 1000mF. **Overrange:** Bottom segments of digits are "ON" for overrange. **GENERAL: Display:** 4 digit LED. **Power:** (4-6 volts) 4 standard "C" size cells, nicad, alkaline or zinc carbon, with provision for charger (batteries and charger not supplied). **Battery Life:** 8 hours min. **Operating Temp.:** 0°C to 50°C (32° to 100°F). **Size (HWD):** 16 x 11 x 6cm (6.4 x 4.4 x 2.375"). **Weight:** 675g. (1.15 lbs.) with batteries. **Optional:** BC-28 charger, BP-28 battery pack, LC-28 carrying case.

WE ACCEPT VISA, and MASTER-CHARGE

PANAVISE

LOW-PROFILE BASE
$13.95 PNV-305
SH.WT. 2 lbs.

VACUUM BASE
$19.95 PNV-380

SH.WT. 2 lbs.

$17.95
SH.WT. 2 lbs.
PNV-304
HORIZONTAL JAW VISE HEAD

STANDARD BASE
PNV-300 $13.95
SH.WT. 2 lbs.

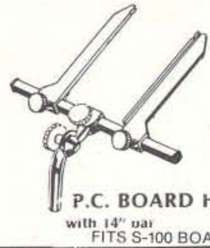
$16.95
PNV-311
SH.WT. 3 lbs.
BENCH CLAMP

WIDE OPENING VISE HEAD

$15.95
PNV-366
SH.WT. 2 lbs.

Not Included

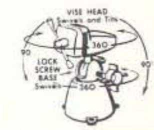
$12.95
PNV-312 TRAY BASEMOUNT
SH.WT. 2 lbs.



PNV-315
$19.95
SH. WT. 1 lb.
P.C. BOARD HOLDER
with 14" bar
FITS S-100 BOARDS

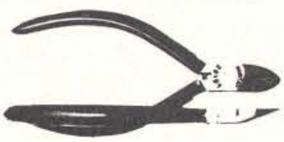
STANDARD VISE HEAD
$16.95 PNV-303 SH.WT. 2 lbs.

PANAVISE TILTS, TURNS, AND ROTATES TO ANY POSITION. IT HOLDS YOUR WORK EXACTLY WHERE YOU WANT IT.



Hunter Tools

FINE CUTTING AND BENDING APPLICATIONS. DELICATE YET DURABLE FLUSH AND SEMI-FLUSH CUTTERS. RECOMMENDED FOR USE ON #18 (.040") AND SMALLER SOFT COPPER WIRE. ALL HUNTER PRECISION ELECTRONIC CUTTERS COME COMPLETE WITH SPRING, COLOR CODED CUSHION GRIP HANDLES, AND FULL POLISH HEAD.



SEMI-FLUSH CUTTING Regular head design for #20 (.032") soft copper or smaller. A popular cutter on the assembly line. Exclusive duradium process (see tech sheet). Makes this cutter tough for electronic production applications.

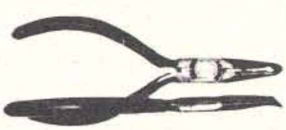
A = 4⅛ HNT-20197(c) $12.25

FULL FLUSH CUTTING Our most popular cutter. Slim taper nose design plus full flush cut at extreme tip combine to make the ideal production cutter. Recommended for #20 (.032") soft copper wire or smaller. Exclusive duradium process (see tech sheet) gives unmatched life to the cutting edge. Perfect for P.C. board and related high density applications where clearance is a factor. A = 4¼"

HNT-20195(c) $12.46

SEMI FLUSH CUTTING Taper nose and tip cutting. This cutter is larger than the #20195 for durability and longer life. Recommended for #18 (.0403") and smaller copper wire. Ideal for P.C. board and related type applications where density allows a larger head profile. A = 4¾"

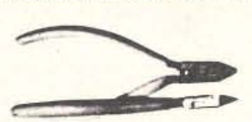
HNT-20186(c) $12.72



TIP DYKE - SEMI-FLUSH CUTTING For getting in those tight spots with maximum visibility. Cuts at the extreme tip and has alignment pin to keep the cutter blades aligned. Recommended for nickel ribbon size .010" x .030" and smaller. Also #22 (.0253") and smaller copper wire. Perfect for work on D.I.P.'s and other I.C. packages where clearance and visibility are factors. A = 4¾"

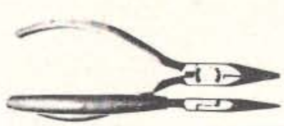
HNT-20190 $20.09

ULTRA FINE "DELICATE" CUTTING AND BENDING APPLICATIONS. DELICATE YET DURABLE



HNT-20177 $10.04

FULL FLUSH CUTTING Tiny delicate cutter. Thin taper jaw plus overall small size. Gets into those tight spots. Ideal for super high density P.C. board work and rework, and other delicate jobs. Recommended for #24 (.020") and smaller copper wire. A = 3 13/16"



A = 4¼"
HNT-20144 $12.46

MINIATURE CHAIN NOSE Miniature for extra fine work. Designed for bending, forming or holding delicate parts and assemblies found in the electronics industry. Also widely used wherever precision pliers are required such as instrumentation work, hobbyists, technicians, etc. Radius edges and smooth gripping surface for protection of delicate assemblies.

HNT-21029 $14.73

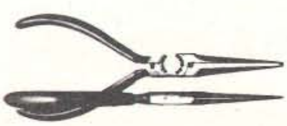
CURVED MINIATURE CHAIN NOSE Similar to #20144 but with curved nose. Gets into those tight spots. Particularly suited to P.C. board rework and instrumentation applications. A = 4½"

NARROW CHAIN NOSE Serrated jaw, radius edged for precision looping, twisting, and bending. Serrated jaw for extra grip with minimum pressure. A delicate yet durable tool for production use.

A = 4¾" HNT-20150(c) $10.04

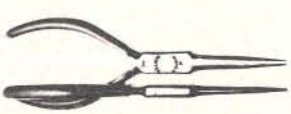
EXTRA LONG NEEDLE NOSE Serrated jaw radius edged. For precision pulling, clamping and holding. Strong taper point insures long working life, serrated jaws for firm grip using less pressure. Reduces fatigue. Perfect for production line use. A = 5⅝"

HNT-20105(c) $11.38



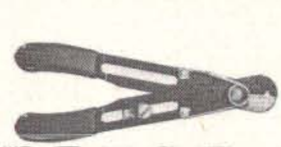
SUPER THIN NEEDLE NOSE Smooth jaws, radius edged for precision bending, reaching, clamping and forming. Delicate needle nose allows access to remote areas. Ideal for guiding leads through high density areas.

A = 4¾" HNT-20114(c) $11.12

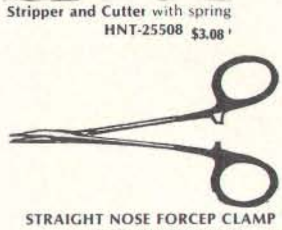


SUPER THIN - EXTRA LONG - NEEDLE NOSE Smooth jaws, radius edged. Similar to #20114 but longer. Extra long "delicate" needle nose allows access to remote areas. A = 6"

HNT-20115(c) $11.79



Stripper and Cutter with spring
HNT-25508 $3.08



STRAIGHT NOSE FORCEP CLAMP
5" Model HNT-54035 $6.43

FORCEP CLAMP (Hemostat, Forcep) Popular in the electronics field with applications throughout industry. Clamps, holds and positions small parts, wires, pins, etc.

KNIFE SETS

HEAVY DUTY KNIFE SET includes three knives, regular #50130, heavy duty #50160 and extra heavy duty #50170, plus 10 assorted blades. Packed in compact clear plastic carrying case.

HNT-50182 $6.43

PRECISION KNIFE SET Popular set both in industry and with the hobbyist. Complete with two most popular handles #50130 and #50160 plus 10 assorted blades. Packed in compact clear plastic carrying case.

HNT-50183 $4.55

Ungar®

MODULAR SOLDERING TOOLS

Designed for Professionals by Professionals

STANDARD LINE SOLDERING TOOLS

The modular "Standard Line" provides the professional with a durable yet economical iron for all types of soldering. Handles are perfectly balanced for comfort and fatigue-free soldering. Heaters provide fast recovery and maximum heat transfer. All irons are 120 VAC.

Note: 1. All electrical Tools & Components are UL Listed and bear the mark. Rated at 120 Volts AC/DC.
2. All Temperature Ranges are Approximate.
3. Tip not included.

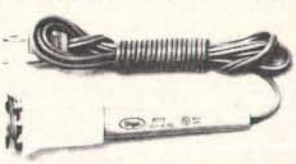
New

#750

MODEL 750 GROUNDED 3-WIRE HANDLE

The handle when assembled with the S or HP Series heaters, is completely grounded from tip to plug and eliminates electrostatic tip potential. A unique, spring-loaded ground plate reflects heat away from the operator's hand. **For use only with S or HP heaters.**

Ungar Model 750 Grounded Handle — UNG-750 .... $9.95



MODEL 777 CLEAN ROOM HANDLE

Convection-cooled, molded grip for "clean room" use. Octagonal handle prevents rolling. Stainless steel heat reflector. Accepts all Standard Line heaters and integral heater/tips, including the S and HP series.

Ungar Model 777 Clean Room Handle — UNG-777 .... $4.95

S SERIES STAINLESS STEEL HEATERS

Constructed of corrosion-resistant stainless steel. Units feature improved 2- or 3-wire versatility, high strength, resistance to abuse and provide positive transfer of heat to the tip. Wedge-shaped base guide permits firm, taper-fit assembly to the handle. The heat reflecting shield also provides a positive ground when used with Model 750 grounded handle. Models 537-S, 1237-S and 4037-S Thread-On Heaters will accept all ¼" Thread-On Tips and, with No. 100 Adapter, all ⅛" Thread-in Tiplets and Nibs; with No. 101 Adapter, all 3/16" Thread-in Tips.

FOR ¼" THREAD-ON TIPS

PART NO.	TEMP.	WATTS	SIZE	PRICE
UNG-537-S	Approx. 700	23	3.67" lg. x .365" diam.	$5.40
UNG-1237-S	Approx. 800	33	3.65" lg. x .375" diam.	$6.47
UNG-4037-S	Approx. 900	45	3.45" lg. x .365" diam.	$8.43

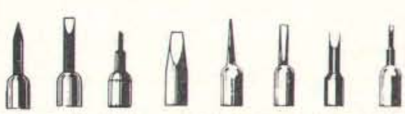
3 WIRE LINE

The ultimate tool...approved grounding ensures maximum safety from static-induced currents to sensitive circuitry. Meets NASA/military standards plus gives you the versatility of more than 40 tips, tiplets, nibs, and three fast-change heater units. Handle up to 30° cooler. New heat resistant cord set. Use ¼" thred-on tips or adapters

COMPLETE 3-WIRE IRON

PART NO.	DESCRIPTION	SUGGESTED USER PRICE
UNG-127	Handle, 650°-750° F., 27W.	$14.88
UNG-135	Handle, 750°-850° F., 35W.	$14.88
UNG-145	Handle, 900°-1000° F., 45W.	$14.88

¼" THREAD-ON TIPS AND ADAPTERS



PL-111 PL-113 PL-114 PL-133 PL-138 PL-151 PL-153 PL-155

TIPS

Ungar No.	Material	Tip Shape	Size, Ins. Ovrl. Lgth.	Size, Ins. Tip Diam.	Net Each
		¼" THREAD-ON TIPS			
UNG-PL111	Plated*	Pencil	1-1/4	1/8	$1.85
UNG-PL113	Plated*	Chisel	1-1/4	1/8	$1.85
UNG-PL114	Plated*	Micro spade	1	.05	$1.85
UNG-PL133	Plated*	Taper chisel	1-1/16	1/8	$1.92
UNG-PL138	Plated*	Needle	1-1/4	3/64	$1.85
UNG-PL151	Plated*	Screwdriver	1-1/4	1/8	$1.85
UNG-PL153	Plated*	Chisel	1-1/16	3/16	$1.85
UNG-PL155	Plated*	Stepped chisel	1-1/4	1/16	$1.85

ADAPTERS

UNG-100	1/4" thread-on heaters to 1/8" thread-in tips	$ .63
UNG-101	1/4" thread-on heaters to 3/16" thread-in tips	$ .63

* Iron clad and silver plated.

Ungar / Princess

SOLDERING STATION

PRINCESS® MICRO LINE

Princess Kit

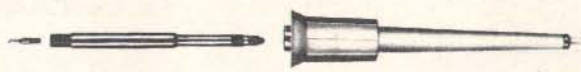
UNG-6975 .... $21.59

Complete Princess iron with 3 interchangeable copper nibs. Includes #6902 2-wire handle, #6910 10W heat capsule and #6950, #6951 and #6952 soldering nibs.

Princess Soldering Station

UNG-6900 .... $28.50

Complete 10W soldering station. Fully assembled iron with 3 copper nibs plus Princess iron holder and cleaning sponge. Includes #6902 2-wire handle, #6910 10W heat capsule and #6950, #6951 and #6952 nibs, #6990 iron holder and sponge.



Designed expressly for the special needs of micro-electronic assembly. This popular precision tool design enables pinpoint hand control, comfort and accuracy assuring hi-yield soldering of delicate circuitry and components in the tightest spaces.

HANDLES

PART NO.	DESCRIPTION	PRICE
UNG-6902	Nylon Pastel Turquoise, Plastic cool-grip, 2-wire plug & cord.	$ 8.72
UNG-6903	Nylon Pastel Turquoise, Plastic cool-grip, 3-wire plug & cord.	$11.78

HEAT CAPSULES (Accepts all ⅛" nibs and tiplets)

UNG-6918	Reach: 2⅝", 775°-850° F., 18W	$10.58

⅛" PRINCESS NIBS

UNG-6960	Plated†	Pencil	3/8	1/16	$1.62
UNG-6961	Plated†	Screwdriver	7/16	1/16	$1.62
UNG-6962	Plated†	Spade	3/8	1/16	$1.62
UNG-6963	Plated†	Precision	3/8	1/16	$2.90
UNG-6953	Copper	Precision	3/8	1/16	$1.58

† Iron clad and gold plated.

DE-SOLDERING EQUIPMENT

PART NO.	DESCRIPTION	PRICE
UNG-50 DK	Ungarmatic® Desoldering Kit. Adapts for fast, easy removal of 14 and 16 pin DIP's. Kit includes 95 Tip Adapter, 6982 Extractor, 6948 Bar Tip, 5013 Super-Wick Desoldering Braid	$12.45
UNG-51 DK	Desoldering Kit. Same as above. With 100 Adapter, fits Ungar Heaters. Standard 1237-S, 4037-S, 37 H.P. 3-wire 135, 145 DI® 361, 362	$11.20
UNG-5002	Super-Wick™ 050 W Desoldering Braid	$ 1.65
UNG-5004	Super-Wick™ 100 W Desoldering Braid	$ 1.89
UNG-6980	DIP Extractor Pliers	$14.95
UNG-6982	Dual In-Line Extractor. Removes up to 16 pin IC's, use 6948 desolder tip	$ 4.22
UNG-6983	TO-5 Extractor, for most TO type cans, use desolder tips: 6943, 6944, 6946	$ 4.87
UNG-6948	Desolder Tip, for DIP's, Iron Clad, pre-tinned	$ 5.00
UNG-7805	Solder-Off Bulb with TEE tip	$ 2.88

PRODUCTION AIDS

PART NO.	DESCRIPTION	PRICE
UNG-8000	Iron Holder. All Irons*	$4.88
UNG-6990	Iron Holder, for Princess Iron only	$7.90
UNG-8800	Iron Holder. All Irons* plus Ungarmatic® and Hot Vac 2000	$9.50
UNG-400	Kleen-Tip Sponge & Tray, fastens to workbench, sponge replaceable	$2.65
UNG-455	Replacement sponge	$1.05

HEAT GUN

Specifically built for electronic assembly, the ⅜" nozzle enables precise, accurate heat flow (750°-800° F.) for a variety of applications including: shrink tubing, curing cements, reflow soldering, cooling/drying, encapsulation, shrink films, and component stressing. Four baffle adapters standard with heat guns for complete versatility. Weighs 13 oz. Convenient 3-way switch. Case of UL recognized glass filled plastic. #6966C complete with 3-wire NEMA plug for approved grounding.

PART NO.	DESCRIPTION	PRICE
UNG-6966C	3-Wire Model, 4 baffle adapters standard	$59.95

UNGARmatic®

MODULAR SOLDERING TOOLS

Designed for Professionals by Professionals

Controlled Soldering Station

THREAD-TOGETHER MODULAR DESIGN FOR QUICK, ON-LINE HEATER OR TIP CHANGE

- Available in 3 preset temperatures; 600°F., 700°F. or 800°F. for any application.
- Closed loop, non-magnetic control.
- Low voltage system; 3 wire grounded.
- Biomechanical designed handle with cool grip for operator comfort. Cord is super flexible 3 wire grounded, heat resistant.
- Large capacity snap-on tray and sponge; removable for optional placement.
- Long life interchangeable tips; iron clad, chrome plated, pre-tinned.
- Designed for use on sensitive components.

Transient Spikes

Transient spikes caused by the switching action of some controlled output soldering stations may be transmitted to the workpiece and may adversely affect a metal oxide semiconductor, particularly if the amplitude of that spike is in excess of the operating voltage of the device. UNGARmatica Temperature Controlled Soldering Stations and Irons suppress transient spikes to less than the 5 volt operating voltage of sensitive MOS devices.

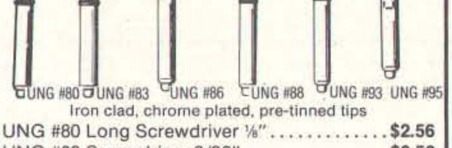
COMPLETE STATION SELECTION GUIDE

FOR THIS TEMPERATURE		ORDER COMPLETE STATION PART #
600°F		UNG-50T6
700°F	(MOST POPULAR)	**UNG-50T7**
800°F		UNG-50T8

LIST PRICE: $69.95

OUR PRICE: $62.96

Each of the above stations include: #70B Power Supply with on/off switch, indicator light, and 3 wire power cord; #71 Handle with 3 wire heat resistant secondary cord; #89 Tray and Sponge; #72 Iron Holder; Controlled Heater with #87 Screwdriver tip 1/16."



Iron clad, chrome plated, pre-tinned tips

UNG #80 Long Screwdriver ⅛" **$2.56**
UNG #83 Screwdriver 3/32" **$2.56**
UNG #86 Needle Tip 3/64" **$2.56**
UNG #88 Long Screwdriver 3/64" **$2.56**
UNG #93 Screwdriver 1/32" **$2.56**

UNG #95 TIP ADAPTER Tip Adapter #95 for special micro applications. This Adapter is designed for the use of ⅛" thread-in Princess Nibs. **$1.59**

EDSYN INC®

PORTABLE DESOLDERING TOOLS

EDS-AS196 $21.95

Special purpose SOLDAPULLT for low static work stations. Deluxe features. Static conductive construction reduces sensitive component damage.

EDS-DS017 DELUXE $17.95

Rugged manual loading tool for volume desoldering. High vacuum. Heavy duty plastic. Fully enclosed shaft for safety. Plunger lock feature for compact storage.

EDS-PT109 $12.50

Sturdy tool for routine desoldering. Slim profile, smooth action, very low recoil. Plunger lock feature for compact storage.

EDS-US340 $11.95

LOW STATIC POTENTIAL

Completely portable. One hand loading, three position stroke adjustment. Swift vacuum action.

EDS-US140 $8.95

Compact manual loading tool. Easy one hand operation. Adjustable three position loading stroke. Low mass plunger, rapid vacuum impulse, negligible recoil.

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MINI VACUUM PUMP

Designed for desoldering microminiature components and fine wiring. Resilient teflon tip, spring arrestor plunger with cleaning shaft.

REPLACEMENT TIP FOR SOLDAPULLTS

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EDS-ST132	MV124 (TIP)	**$1.95**

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DESOLDERING BRAID

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UNG-5004	.100"	**$1.88**

ISO-TIP®

ISO-Tip Cordless Soldering Irons

- Up to 125-150 Joints per charge
- 5-10 Seconds average soldering heat time
- Tip performance up to 50 watts and over 700°F tip temperature
- Built-in work light
- "Lock off" switch
- Rechargeable battery & charger included
- Cannot over charge

ISO-7800 $38.95
Fastest recharging iron on the market. Recharges fully in one hour.

ISO-7740 $28.95
"Quick Charge" Recharges completely in 3 to 4½ hours.

ISO-7540 $25.50
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P.C. Drill attachment with No. 56 Bit.

SOLDER

The best for general wire and printed circuit soldering 60% tin, 40% lead alloy, .032" dia. (21 guage) with triple resincore.

TCH1832	1 lb.	**$13.41**
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COLE-FLEX CORPORATION

IRRADIATED POLYOLEFIN SHRINKABLE TUBING

- Conforms to MIL-I-23053B/5
- Shrinks 50% (2:1 ratio) with only 5% longitudinal shrinkage
- Fewer sizes covering more applications
- Operating temp. range: -55°C to +135°C
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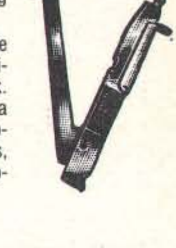
CST 116XX	1/16"	$1.10	$23.83
CST 332XX	3/32"	$1.25	$27.04
CST 18XX	1/8"	$1.30	$28.59
CST 316XX	3/16"	$1.60	$34.73
CST 14XX	1/4"	$2.00	$43.94
CST 38XX	3/8"	$2.25	$48.94

When ordering—Replace "XX" with color code: Color Codes: BK-Black, WH-White, RD-Red, YE-Yellow, BU-Blue, CL-Clear

NIBBLING TOOL

This Nibbling Tool is perfect for cutting, trimming, or notching sheet metal up to 18 guage.

It operates like a punch and die and also works well on aluminum or plastic up to 1/16" thick. We feel that this tool can be a real time-saver for our customers when working on chassis, printed circuit boards and prototype model boards.



TLX-201 NIBBLING TOOL **$8.95**

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Model IBAR4-6 with 6 foot power cord
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- 4 Standard 3-prong plugs
- 4 100 KHz to 200 MHz filters
- Inductively isolated grounds
- Sockets individually filter isolated
- Each socket isolated from power line
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- Circuit breaker protected at 15A.

High Voltage Spike Protection: 1000 A, 8/20 usec; pro-1000 A, 8/20 usec; protection from repeated spikes.
Load Handling: 1875 W MAX. total load; 15A per socket.
Input: 125 VAC, 15 Amps; Standard 3-prong plug.

PROTECT YOUR INVESTMENT PROTECT YOUR DATA WITH

GOF-IBAR46

LIST PRICE **$79.95**

OUR PRICE **$59.95**

POWERMATE

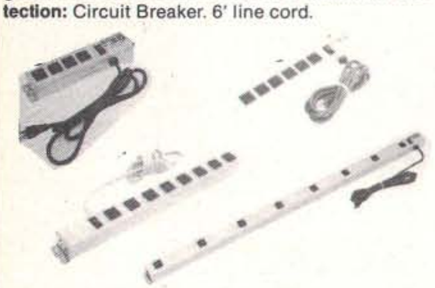
BY GSC™

UL LISTED

MULTIPLE OUTLET STRIPS

Designed with GSC's traditional regard for quality, safety and long life, Powermate Multiple Outlet Strips set a new standard of excellence. The unique two-piece extruded Aluminum Channel body is very strong and rigid for maximum wearability. All Powerstrip models are circuit-breaker protected, CSA and UL listed.

GENERAL SPECIFICATIONS:
Size: 2" W x 1¾" H. (Length according to Model). **Maximum Power Rating:** 15A, 125 VAC, 60 Hz, 1875 Watts continuous duty. **Outlets:** 3-prong "U" ground. **Case:** Anodized, extruded aluminum. **Protection:** Circuit Breaker. 6' line cord.



MODEL	LENGTH	OUTLETS	SWITCH	LIGHT	PRICE
GOF-9P5-6	9"	5	No	No	$15.95
GOF-9PLS4-6	9"	4	Yes	Yes	$16.95
GOF-12P7-6	12"	7	No	No	$17.95
GOF-12PLS6-6	12"	6	Yes	Yes	$18.95
GOF-24PLS8-6	24"	8	Yes	Yes	$24.95
GOF-48PS8-6	48"	8	Yes	No	$32.95
GOF-72PS8-6	72"	8	Yes	No	$36.95

RACK MOUNT OUTLET STRIP

7 OUTLETS WITH SWITCH & PILOT LIGHT

Anodized aluminum. 1 outlet on front, 6 on back. Size: 19" W x 3½ H x 3⅛" D.

GOF-19RRPS7 - with 6 foot power cord. **$34.95**

ANTRONIC Corporation

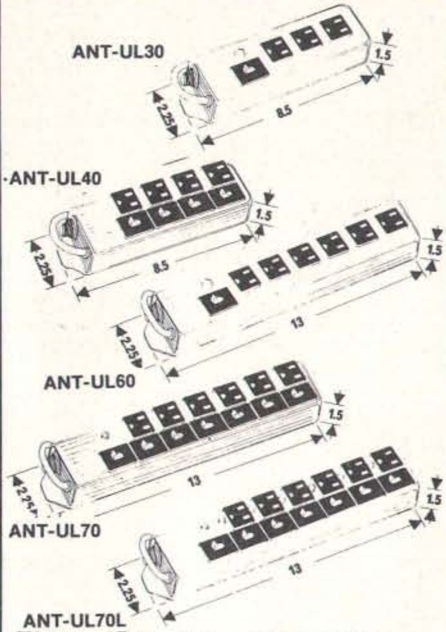
UL LISTED

JUNCTION BOXES

PROTECTION — UL listed and 100% factory tested • Reliable push-to-reset circuit breaker • Tough 14-guage jacketed 3-wire, 6 foot cord with molded plug • Rugged steel housing • Electrical and mechanical grounding throughout unit.

CONSTRUCTION — All wiring mechanically fastened and soldered • Multiple keyhole mounting • Silicone enamel finish • U-ground 3-prong outlets.

UTILITY — Models for every application • End-mounted cords • Full 15 amp, 125 volt A.C. rating.



Model Number	Master Switch & Pilot	Outlets Only 3-Prong	Switched Outlets 3-Prong	Switched w/Pilot 3-Prong	Price
ANT-UL30	1	3	—	—	$16.00
ANT-UL40	—	—	4	—	$19.25
ANT-UL60	1	6	—	—	$18.95
ANT-UL70	1	—	6	—	$29.90
ANT-UL70L	1	—	—	6	$35.05

Magnifier IM SERIES

Perfectly balanced fluorescent lighting with precision magnifier lens. Tough thermoplastic shade. Easy lens removal. New wire clip design permits easy installation and removal of fluorescent tube. Comes with plastic shield to protect tube from soiling and damage.

Colors: Gray, Black. Comes with one 22 watt T-9 Circline fluorescent tube. 3 diopter lens standard. Shipping weight with bracket: 8 lbs.

*Reach: Model IM-10, 42"

		LIST PRICE	OUR PRICE
LDU-IM10A-WH	WHITE	$94.95	$69.95
LDU-IM10A-GY	GRAY	$94.95	$69.95
LDU-IM10A-BK	BLACK	$94.95	$69.95
LDU-IM10A-CB	CHOC BROWN	$94.95	$69.95
LDU-5DLNS	5 DIOPTER LENS		$14.00

SURGEONICS LIMITED

Protection against transient over-voltage

The units consist of a two-gate solid state circuit board protected by a 3 amp fuse.

Upon the occurrence of a transient over-voltage above 6% of the line voltage, the components in the two-gate system react in tandem to allow pico-second response time and energy dissipation.

The transient voltage is thereby suppressed to a safe non-destructive level.

Product Specifications:

- 120 Volts, Single-Phase, 40/70 Hz.
- Steady State Power Dissipation: .85W.
- Transient Energy Dissipation: 20 Joules.
- Max. Peak Current 2000 amps.

INTERCEPTOR

SURGE-LESS SOCKET™

Model ISS 701-R

Patent Pending

SRG-ISS-701-R

1-4	5-9	10-24
$21.80	$20.25	$18.50

Shipping Weight: 8 oz.

CONTROLS . . .
ELECTRICAL SURGES — PROTECTS YOUR VALUABLE ELECTRICAL EQUIPMENT

POWER-SENTRY

Patent Pending

Model PS 701-D

SRG-PS-701-D

1-4	5-9	10-24
$31.50	$29.30	$27.00

Shipping Weight: 8 oz.

COMP-ONICS

ELECTRONIC ATTENUATOR

- **POSITIVE PROTECTION FOR SOPHISTICATED EQUIPMENT**
- **MORE ACCURATE REPORTS**

The Model 708 can be utilized in applications where inductive and switching transients are present and jeopardize the electonic circuitry of data processing equipment, computer power supplies and numerical controlled machinery. When the Componics Model 708 is connected harmonic distortion and transients are instantaneously clamped to a safe efficient operating level.

Patent Pending

SRG-CO 708

1-4	5-9	10-24
$235.00	$218.55	$199.75

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OPEN FRAME POWER SUPPLIES

GSC

FEATURES:
- Designed to comply with all applicable UL & CSA specifications!
- Pluggable IC regulators - user replaceable!
- Remote sensing!
- Fold back currents limiting!
- Unconditionally guaranteed for two years!
- Manufactured and serviced in U.S.A. and Canada!
- Industry standard frame sizes for simplified second sourcing!

Basic features of all GOF Series units include: dual AC primary hookup for 115/230 VAC connection with 50 Hz provision; oversize lo-flux transformers; pluggable IC regulators, 85° computer-grade electrolytics; metal-can power transistors, FR-4 circuit boards with 2 oz. copper track; remote sensing; fold back current limiting; multi-surface mounting with factory-installed captive hardware; industry standard frame size for simplified alternative sourcing.

Premium components are used in all units to enhance reliability. All IC regulators are pre-burned and checked before installation in actual units. IC's are socket mounted and user replaceable. Computer-grade capacitors are rated to 85° operating levels and are built to GSC's own specifications for low ESR to enhance long life. All power transistors are metal-can devices and are graded for important parameters by GSC's own quality assurance group before release to production.

SPECIFICATIONS:

Input:	105-125 VAC / 210-250 VAC, 47-63 Hz.
Line Regulation:	0.05% over entire operating range.
Load Regulation:	0.1% for no load to full load current.
Output Ripple & Noise:	1 mV RMS / 3 mV peak-to-peak typical.
Remote Sensing:	On all models except GOF-M series, FDD100 and GOF 2A-1D.
Transient Response:	No overshoot or undershoot on turn-on or turn-off.
Overload Protection:	Foldback current limited, self restoring.
Ambient Operating Temp.:	Continuous duty from 0°C. to 60°C.
Mounting:	Multi-surface mounting with 8-32 captive hardware.
Cooling:	Convection cooled.

GOF Midget Series: $26.00

GOF M—5	5 V / 1.2 A
GOF M—12	12 V / 0.5 A
GOF M—15	15 V / 0.5 A
GOF M—24	24 V / 0.4 A
GOF M 2—6	2 to 6 V / 1 A
GOF M 10—16	10 to 16 V / 1 A
GOF M 16—24	16 to 24 V / 1 A

Dim.: 1.9" x 4.0" x 4.0"
Weight: 3 lbs.

GOF-3 Series: $74.00

GOF 3—5	5 V / 1.2 A
GOF 3—12	12 V / 8.0 A
GOF 3—15	15 V / 6.6 A
GOF 3—24	24 V / 4 A
GOF 3—28	28 V / 3.5 A

Dim.: 2.75" x 9.0" x 4.87"
Weight: 8 LBS.

GOF-3X Series: $89.00

GOF 3X—5	5 V / 15 A
GOF 3X—12	12 V / 10 A
GOF 3X—15	15 V / 8 A
GOF 3X—24	24 V / 6.5 A
GOF 3X—28	28 V / 4.5 A

Dim.: 2.75" x 9.0" x 4.87"
Weight: 10 LBS.

GOF-FDD-100 Series: $89.00

GOF-FDD100	24 V / 2 A	5 V / 1.5 A	5 V / 5 A
GOF-FDD101	12-15 V / 1 A	5 V / 3 A	12-15 V / 1 A
GOF-FDD102	24 V / 2 A	5 V / 3 A	12-15 V / 1 A

Dim.: 2.80" x 10.25" x 4.00"
Weight: 6 LBS.

Overvoltage Protection Modules

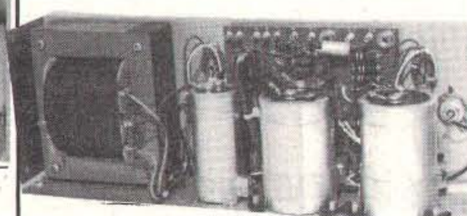
GOF-OVF-1	Below 6 A	$ 8.00
GOF-OVF-2	6 A to 10 A	$12.00
GOF-OVF-3	15 A to 25 A	$24.00

Weight: 1 lb.

GOF-1 Series: $36.00

GOF 1—5	5 V / 3 A
GOF 1—12	12 V / 15 A
GOF 1—15	15 V / 1.2 A
GOF 1—24	24 V / 0.8 A
GOF 1—28	28 V / 0.5 A

Dim.: 1.62" x 4.00" x 4.87"
Weight:



GOF-4 Series: $125.00

GOF 4—5	5 V / 18 A
GOF 4—12	12 V / 12 A
GOF 4—15	15 V / 9.8 A
GOF 4—24	24 V / 6.1 A
GOF 4—28	28 V / 5.2 A

Dim.: 2.78" x 13.00" x 4.87"
Weight: 15 LBS.

GOF 2A-1D Series: $68.00

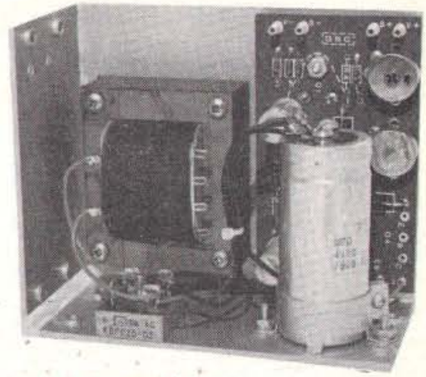
GOF 2A-1D A	12-15 V / 1.5 A	12-15 V / 1.5 A
GOF 2A-1D B	5 V / 1.2 A	5 V / 1.2 A
GOF 2A-1D C	5 V / 1.2 A	15 V / 0.5 A
GOF 2A-1D D	5 V / 1.2 A	24 V / 0.4 A

Dim.: 2.53" x 7.90" x 4.03"
Weight: 5 lbs.

GOF 2A IT Series: $115.00

GOF 2A-1T	5 V / 6 A 12-15 V / 1.5 A	12-15 V / 1.5 A
GOF 2A-1T A	5 V / 6 A 12-15 V / 1.5 A	5 V / 0.8 A
GOF 2A-1T B	5 V / 6 A 18-24 V / 1.0 A	5 V / 3 A
GOF 2A-1T C	5 V / 6 A 12-15 V / 1.5 A	18-24 V / 1.0 A
GOF-FDD 200	5 V / 3 A 24 V / 4.0 A	5 V / 1.0 A

Dim.: 2.78" x 11.00" x 4.87"
Weight: 12 LBS.



GOF-2 Series: $57.00

GOF 2—5	5 V / 6 A
GOF 2—12	12 V / 3 A
GOF 2—15	15 V / 2.8 A
GOF 2—24	24 V / 2.3 A
GOF 2—28	28 V / 2.0 A

Dim.: 2.5" x 4.87" x 5.62"
Weight: 5 lbs.

GOF-5 Series: $140.00

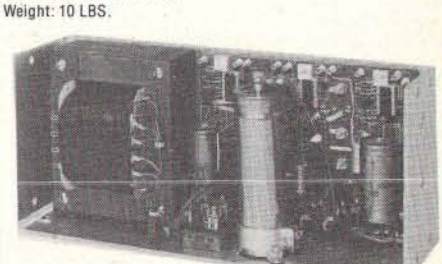
GOF 5—5	5 V / 25 A
GOF 5—12	12 V / 16 A
GOF 5—15	15 V / 13 A
GOF 5—24	24 V / 8.1 A
GOF 5—28	28 V / 6.9 A

Dim.: 6.0" x 13.25" x 4.88"
Weight: 15 LBS.

GOF 2A-2D Series: $90.00

GOF 2A-2D A	12-15 V / 3 A	12-15 V / 3 A
GOF 2A-2D B	5 V / 6 A	5 V / 6 A
GOF 2A-2D C	5 V / 6 A	12-15 V / 3 A
GOF 2A-2D D	5 V / 6 A	24 V / 2.3 A

Dim.: 2.78" x 11.00" x 4.87"
Weight: 10 LBS.



GOF 3A IT Series: $145.00

GOF 3A-1T A	5 V / 12 A	12-15 V / 3 A	12-15 V / 3 A
GOF 3A-1T B	5 V / 12 A	12-15 V / 3 A	18 or 24 V / 2 A
GOF 3A-1T C	5 V / 12 A	5 V / 6 A	12-15 V / 3 A
GOF 3A-1T D	5 V / 12 A	18 or 24 V / 2 A	12-15 V / 3 A

Dim.: 4.30" x 16.75" x 4.88"
Weight: 16 LBS.

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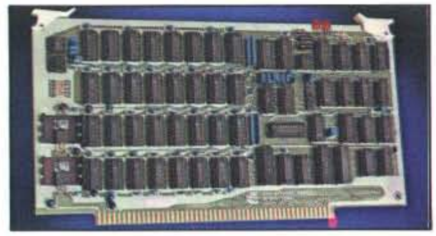

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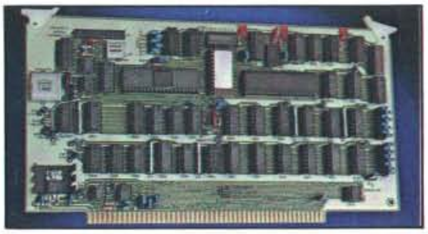

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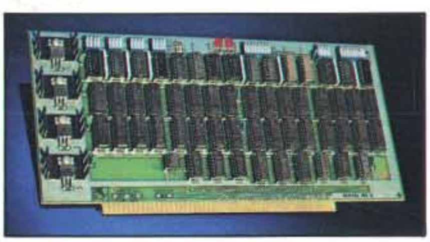

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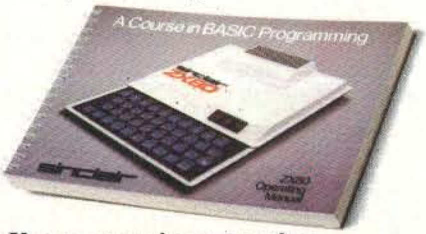

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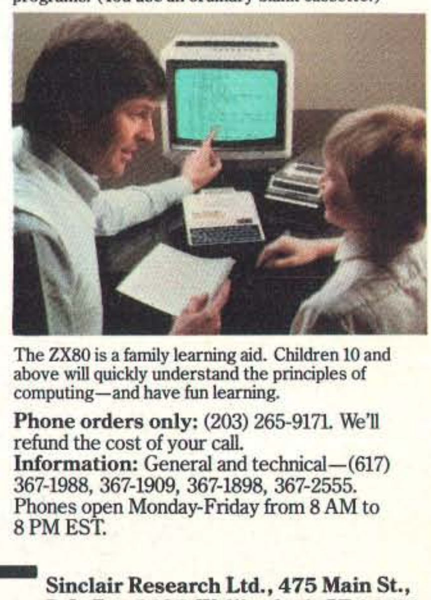

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loads all parameters for the color memories at the indexed color code of 2. The hue, intensity, and saturation are loaded at this address with the data 5, 7, and 2, respectively.

In order to exploit the full capabilities of the frame buffer, we must have some method to access individual elements of the buffer. And we must have the capability of loading all or portions of the frame buffer in order to support selective filling and erasing. If we do not provide this function, it becomes very difficult to produce solid colored or shaded images, which is one of the important advantages of a raster-scan display. Furthermore, if we allow the host to directly load individual elements of the frame buffer, we can produce a full frame that implements algorithms such as *depth queuing* and *shading* that cannot be performed otherwise by the display processor at the pixel level. Thus it is apparent that we do need some sort of load-pixel primitive. In order to increase the utility of this primitive, however, we must introduce the concept of the *viewport*.

Through the graphics-display registers, we can define a rectangular area on the display by a pair of X,Y coordinates (the left and right X boundary and the top and bottom Y boundary). Thus, rather than loading the full screen, we can reference the area bounded by a viewport. This feature permits us to load areas of the display or even to mask portions of the display. To further increase the generality of this primitive, we must also permit loading a single pixel. This feature allows us to change the color of the point we are currently at. We could do the same with the MOV primitive, but this instruction would be shorter. Finally, we can define our load-pixel primitive as:

$$\text{LPIX R,}C_0..C_n$$

where:

R = reference (*F*ull frame, *V*iewport, or *X,Y*)
C_i = color data

Along with this primitive, we must add that a predefined order of filling the pixels must be maintained, such as left to right, bottom to top. For example, the primitive:

LPIX F,0,0,0,0...

loads the entire display with a single color 0.

The next primitives we need do not actually produce an image, but support the previous primitives. First, since we have assumed the existence of graphics-display registers, we must allow the host to load the registers with a value. In this work, we do not specify the types or numbers of graphics-display registers, since they may vary from system to system. However, certain registers will be consistent, such as vector type and current X and Y position. Mnemonically, our load-register primitive can be represented as:

LREG, N,V

where:

N = register name or number
V = value to be loaded

For example, the primitive:

LREG X,4096

loads the X register with the value 4096.

Since some of these registers contain status information, it is important that the host be able to read back the value in the register. For example, if the display processor supports a light pen, it may be necessary for the host to read back the X and Y position coordinates. Mnemonically, our read-register-primitive can be represented as:

RREG N

where:

N = register name or number

For example, the primitive:

RREG Y

reads the contents of the Y register and returns the value to the host.

Since we have assumed the existence of subroutines, there must be some way of loading subroutines in the display-processor memory: thus we need a load-subroutine primitive. We obviously need the parameters of

Text continued on page 276

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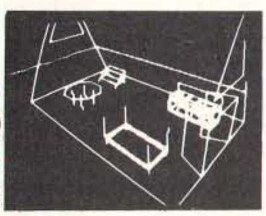

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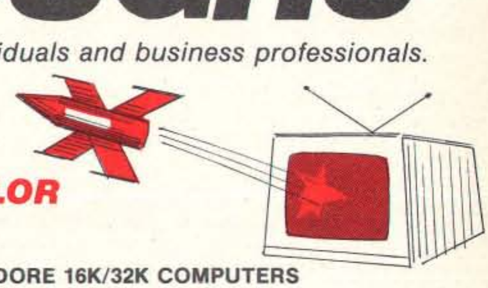

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A Line-Failure Indicator

Hank Olson, POB 339, Menlo Park CA 94025

Have you ever come back from work looking forward to an evening of home computing, only to find that nothing works? The program that was almost debugged during previous evenings is gone?

While nothing short of nonvolatile memory will completely solve this problem, the simple line-failure indicator described here will alert you to problems that occurred while you were away. A simple glance at the three-color display of LEDs (light-emitting diodes) will at least let you know what you are in for. The indicators light as follows:

- green: power is on, no recent failures
- yellow: power has failed and returned
- red: power has been off for a short time
- none: power has been off for a long time

Having different colored LEDs seems best from a human-interface point of view, even though their voltage requirements differ somewhat.

The circuit of the line-status indicator is shown in figure 1. The basic power supply uses a common 6.3 V filament transformer and a bridge rectifier of four 1N4001 diodes. The primary is controlled by SW1, a double-pole switch which prevents the battery from discharging when the unit is off. This supply must provide the current to light one LED plus energize a small relay coil. This represents about 150 ohms, so the RC (resistor/capacitor) time constant of the power-supply filter is about 0.15 seconds. Therefore, if you return to find the yellow indicator on, you will know that there has been a line-voltage dropout of 0.3 seconds or longer.

Looking at figure 1, we see that the green LED is held on by SCR1. The SCR gate can only be triggered into conduction manually by means of SW2. Once this pushbutton switch (SW2) is (momentarily) closed, a pulse of current enters the gate of the SCR from the 0.1 μF capacitor; and the SCR goes into conduction. Since this SCR operates on DC, it will *stay* in conduction until the DC supply fails (meaning that there is an AC line dropout).

When the DC supply fails, the relay K1 is de-energized, closing the "normally closed" contacts and lighting the

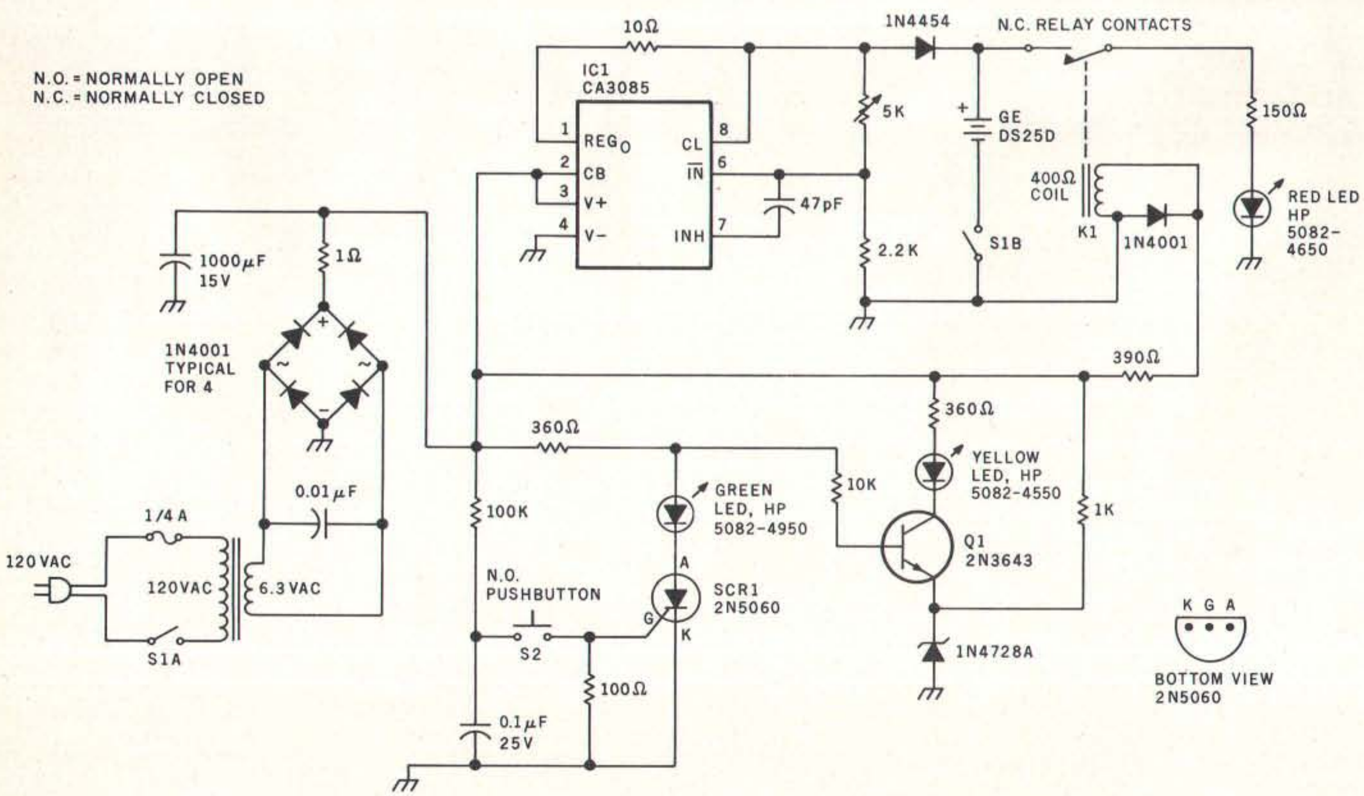

Figure 1: *This power-line-failure indicator uses a silicon-controlled rectifier to detect voltage dropouts. If power should fail for more than 0.3 seconds, the SCR ceases to conduct and the green LED is extinguished, while the red LED lights. The red LED remains on as long as power is out; its power is drawn from a set of rechargeable batteries. Should power return, the red LED goes out and the yellow one is illuminated to indicate this sequence of events.*

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red LED. The 1N4454 serves to disconnect the two-cell nickel-cadmium (nicad) battery from U1 during power outages, so that the *only* load on the battery is the LED. Use of a relay to actuate the battery-to-LED circuit is the best method, because it closes the circuit with nearly zero resistance, while consuming *no* power in the process. The two-cell nicad, a General Electric DS25D, is a rather small unit made for printed-circuit board mounting and thus fits in easily. This tiny battery will light the red LED for several hours when fully charged.

When AC power returns, DC is quickly restored to energize K1 and to charge the battery via IC1, the regulator. IC1 is a voltage regulator, but it also has current-limit capability. The 10-ohm resistor between pins 1 and 8 of the regulator causes charge current to be limited to 20 mA, even if the battery is nearly discharged. As the battery charges and its terminal voltage approaches the regulated voltage output to which IC1 is set, current drops below 20 mA and tapers off in the "constant-voltage" charge mode.

Meanwhile, the SCR remains nonconducting, which allows current to flow via the 360-ohm and 10 k-ohm resistors to the base of Q1, forward-biasing this transistor and lighting the yellow LED. Thus the yellow LED indicates that power has failed and returned. The red LED has, of course, been extinguished with the energizing of K1.

The final step in the sequence is when the person who uses this line-failure detector notices that the yellow LED is lit, and resets SW2. This act causes SCR1 to conduct, diverting current from the base of Q1, extinguishing the yellow LED and lighting the green LED.

Since it takes between 1.5 and 1.8 V to light an LED, I chose a battery consisiting of two nicad cells in series. This gives a battery voltage of 2.4 V, which is adequate to light LEDs of all colors, using series dropping resistors. Since the battery is charged in series with a 1N4454, the voltage-regulator output should be set (by means of the 5 k-ohm variable resistor) to between +2.9 and +3.1 V. This accounts for the series forward-voltage drop in the 1N4454. Note that an RCA-CA3085 is used as a regulator. An LM305H (National Semiconductor) will not substitute for this integrated circuit since it's not made to regulate below +4.5 V. The older National LM300H would work, however.

K1 can be any small relay having a coil voltage from 4 to 8 V DC, with a set of normally closed contacts. The series resistor is adjusted to drop the unregulated +8 V of the DC supply to the desired voltage of the relay coil. In my own case, a small relay (from an old radiosonde transmitter) which had a 400-ohm coil and which closed reliably on +4 V was used. A 390-ohm resistor was then used to drop the +8 V supply to the coil voltage of +4 V.■

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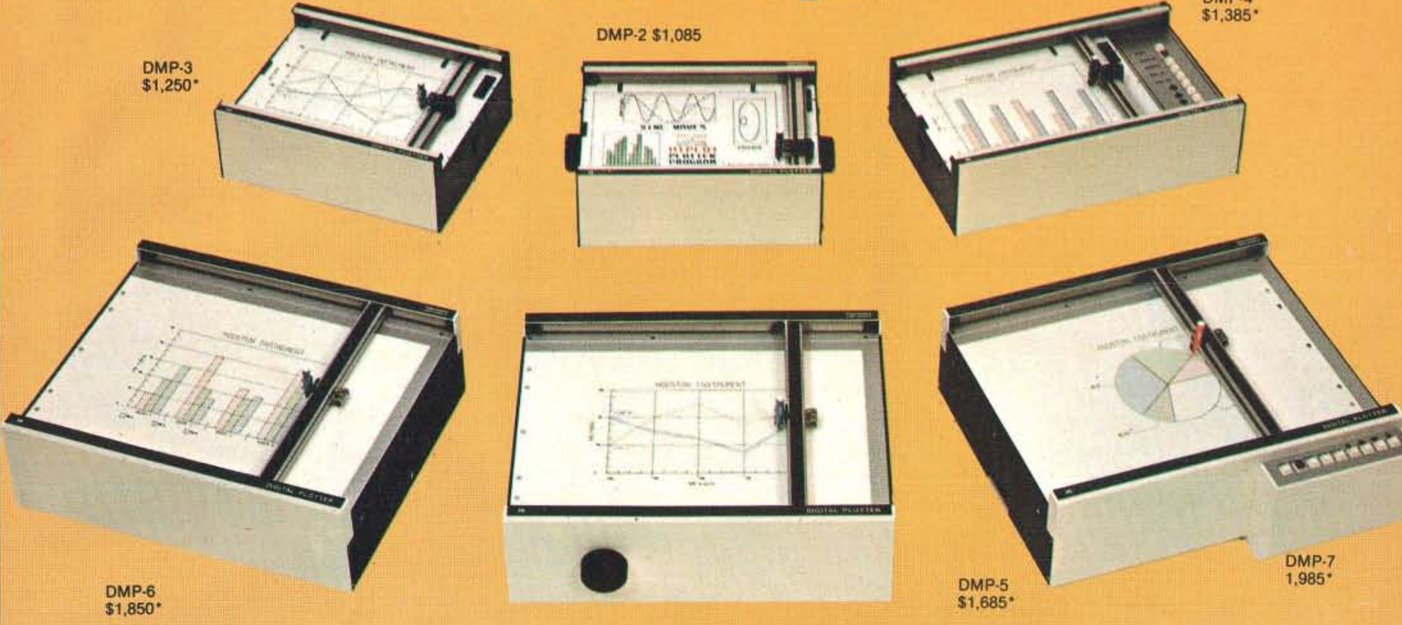

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Language Control Structures for Easy Electronic Visualization

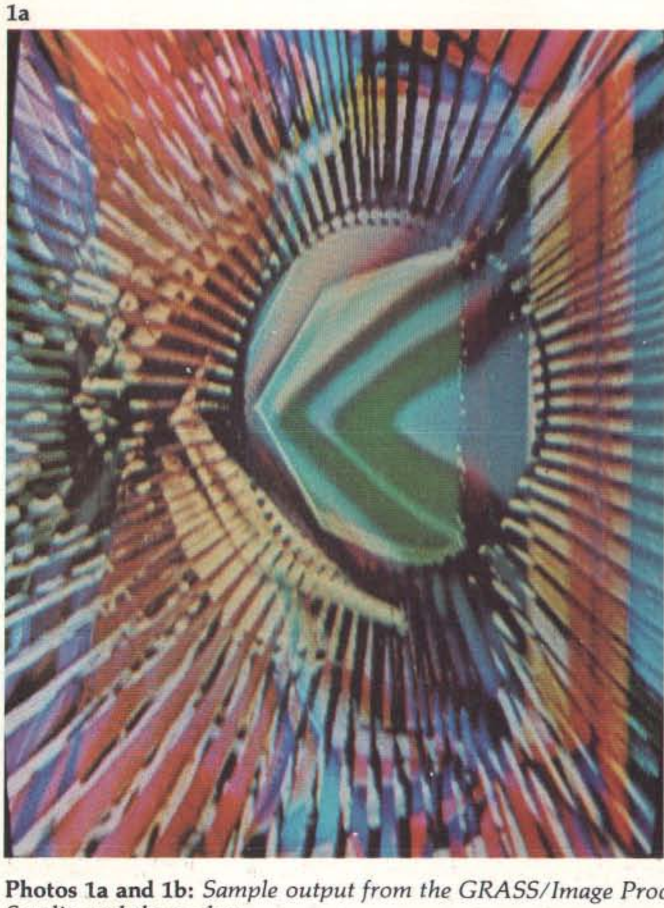
Dr Thomas DeFanti
Electronic Visualization Laboratory
University of Illinois at Chicago Circle
POB 4348
Chicago IL 60680

Control structures are the program- flow manipulation features of the language that you use to beat your computer into submission. BASIC's control structures are embodied in the RUN, GOTO, GOSUB, and RETURN keywords and a few functions, certainly an impoverished set. Highly structured languages like Pascal are rigidly limited to the control structure of subroutines. Lowly structured approaches like assembly language are necessary to implement higher-level languages and real-time systems, because the lack of enforced structure allows an infinite variety of control structures to be used at a cost of great human effort. The execution-speed gain in using assembly language is more due to the efficient building of customized tables and linked lists than to efficiency in adding, subtracting, multiplying, and dividing numbers.

Assembler coding is by no means easy. Note the word "easy": it's important because in one sense it means *"accessible."* In this case, it's your access to complex electronic visualizations.

Electronic visualizations are important because producing and manipulating images, especially animated ones, is a truly multidimensional task which reflects our real-world interactions much more than maintaining an accurate laundry list or printing payroll checks. Producing them demands a lot from software,

1a



1b

Photos 1a and 1b: *Sample output from the GRASS/Image Processor. Photo 1a was made by Guenther Tetz, and photo 1b by Dan Sandin and the author.*

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and making their access easy requires paying attention to the provision of rich control structures in a language.

Electronic Visualization is an intentionally broad term meant to conjure thoughts of computer graphics, animation, image processing, video synthesis, and even advanced word-processing. Anyone successfully producing images for communication is unlikely to reject a technique for reasons of algorithmic purity (as a computer scientist might feel forced to do). Computer hobbyists use the tools at hand, and electronic visualization is the means to the end and the end product of using these tools. Simultaneously, it can be *both* because we are seeing the vast increase of real-time imaging systems, even in microcomputer-based configurations; and controlling these real-time systems can be as feedback-intensive as playing a musical instrument or driving a racing car.

Just to unify the concepts so far, think about this question: what besides the cosmetic packaging governs our choice of a musical instrument or an automobile? It is a combination of capability and user control, of course: having one without the other is useless. So why are the programming languages currently available so impoverished on the control-structure side?

> The most successful approaches to date are basically highly developed, beautifully evolved kluges.

Perhaps it is because computers were invented to process payrolls, not images. Television, on the other hand, is image-oriented and currently uses a host of presently emerging real-time digital techniques and increasingly flexible control structures. As a matter of fact, just about all the television you see these days is digitally processed for purposes of synchronization.

Television is a high-speed medium conducive to parallel and pipeline processing. You are driving television rather than generating it. TV cameras are on all the time and you, as director, are fading, switching, adding titles and constantly throwing away images that you don't want. Control is the name of the game.

The television folk are not about to give up rich, real-time control structures and the computer folk won't give up language. How to get them together is the essence of the task at hand.

Getting Computers and Television Technology Together

Looking at the history of control structures for computer graphics and for television, we see that most computer-graphics usage, with the obvious and exciting exception of video games, is some variety of non-real-time plotting. This is where the money is and where the language development for computer-aided design has been focused. No manufacturer of equipment for computer graphics (excepting the video-game people) now depends on animation for solvency. Plotting is slow and often merely the side output of a large FORTRAN finite-element analysis program. Visual aesthetics are rarely the primary concern, if any concern at all. People who use such systems are highly skilled and highly paid technicians who became that way by having to deal with plotting packages as a condition of employment. If the job were easy, they wouldn't get paid so much.

We are just reaching the point of electronically generating and manipulating images, in real time, under program control. How do we design languages to deal with real time? Or, more important, why do we want such a language, an alphanumeric string-oriented language, at all? Why not use picture-based languages with symbols for motions and timing?

How Can You Control Images Easily?

After about ten years of living with this obvious and nagging question, some conclusions became clear. First, purist approaches to electronic visualization are hopeless. Image control employs a hybrid of languages, several input devices, picture-oriented commands, custom hardware, and a smattering of idiosyncrasies. The most successful approaches to date are basically

highly developed, beautifully evolved kluges. We know what "purism" in coding FORTRAN and BASIC does to image production. Purism in television technique eliminates computer graphics as we know it. So how about using graphic symbols to save the day?

Using symbols in a menu and some sort of manual-selection mechanism is an approach taken by many FORTRAN graphics systems. This limits the number of symbols to those defined in the menu and there is no user-level extensibility in that you cannot create new symbols out of sequences of old symbols, which eliminates the one truly unique feature of computers. To state it bluntly, you can't program with a menu.

What happens, however, if you do find a system that provides for the combination of nonalphanumeric symbols in meaningful ways? In an extremely advanced case, it should look something like Japanese, and you might note that the language used to program computers in Japan is a *phonetic alphanumeric transcription* of their language. They do not program in their extremely beautiful and rich symbol set. Eliminating alphanumeric languages is not such a hot idea, except in turnkey systems.

The second conclusion gestating for the past ten years is that complete parallelism is necessary for controlling images in meaningful ways. You simply must be able to develop sequences independently and merge them in ways that do not necessitate rewriting the programs. Xerox's Smalltalk and certain other languages have this capability, as do television technology and everyday life: making this parallelism easily accessible takes real care.

The third conclusion is that a flexible priority scheme is needed. Some tasks are more important than others, just as in real life and computer operating systems. It is essential to give this capability to the user of an electronic visualization system.

Fourth, providing for user extensibility at several levels is the only way people will easily be able to use a system for applications not envisioned by the designer. I will discuss this later.

Fifth, the system must be software-fault tolerant. Fault-tolerant hardware has been a research area of great importance to real-time control systems, yet language purists still think people should solve problems in structured, orthodox, algorithmic ways. A computer language should provide as many paths to a given communication as possible, as natural languages do, and the kind of error handling that a friend would offer. Allowing nonstructured, nonprocedural, "seat-of-the-pants" programming is often the only salvation when the final goal is aesthetically defined, and is, perhaps, not at all clear. It has been called "fuzzy programming," and it's easy to throw in the recursive, value-returning, clever structured-programming capabilities as well, but limiting yourself to these latter approaches stifles human creativity, problem-solving, and sideways thinking.

Zgrass — A Language for Easy Electronic Visualization

Zgrass is a programming language and operating system written in assembly language for the Z80 microprocessor by Nola Donato, Jay Fenton, and me. Not surprisingly, it embodies all the control structures mentioned so far in this article and

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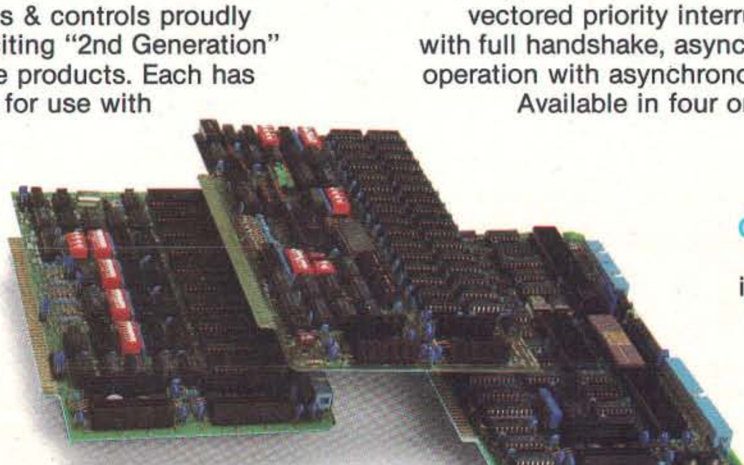

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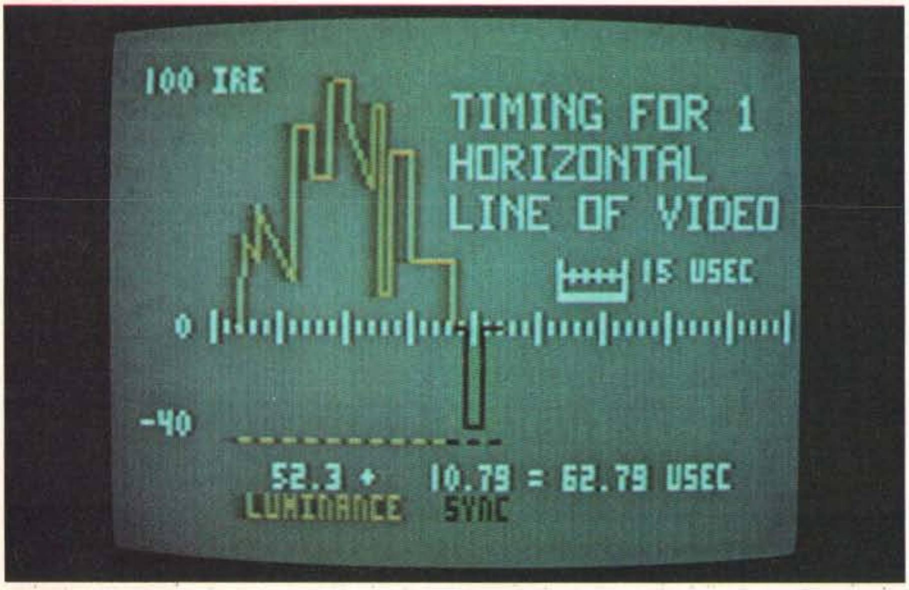

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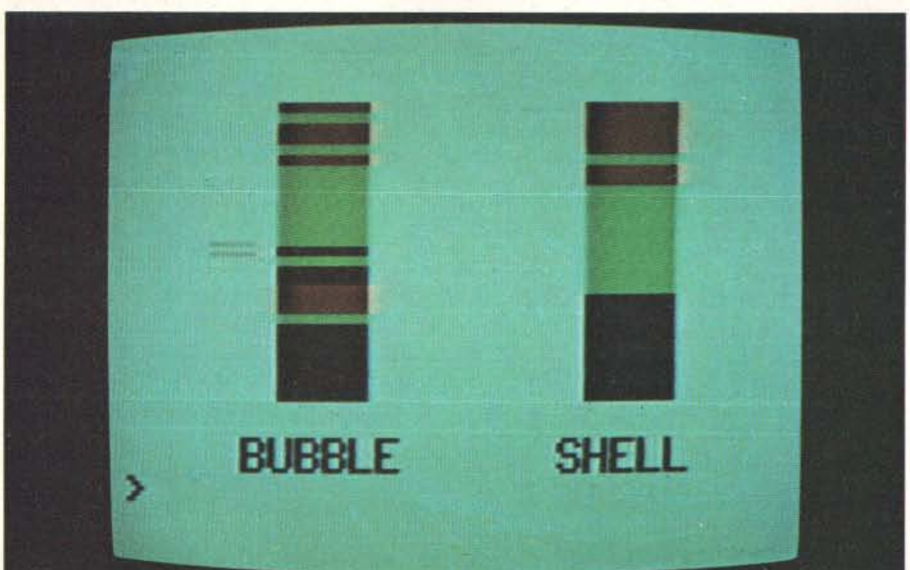
2c



Photos 2a, 2b, and 2c: *Sample output from the first Zgrass system, with a resolution of 160 by 102 pixels, with 2 bits per pixel. Photo 2a was made by Copper Giloth, and photos 2b and 2c by Nola Donato.*

has been in development for ten years.

Zgrass started out as GRASS (Graphics Symbiosis System), a language designed to bring the immense complexity of a Digital Equipment Corporation PDP-11/45 and as Vector General 3DR Display system within the grasp of artists and educators at Ohio State University. It has high levels of interaction, parallelism, priority, and tree-structured manipulations of vector-defined objects. Photos from this system can be seen in "About the Cover... And Some More of the Same," in the October 1977 BYTE, page 22.

GRASS depends on $120,000 of equipment to run — rather expensive for a single-user system — but it is one of the first highly developed non-FORTRAN interactive graphics systems for use by artists.

In 1973, Dan Sandin, inventor of the Image Processor, brought color television usage to our computer graphics work at the University of Illinois at Chicago Circle. Dan and I developed most of the ideas about control structures presented here. Photos 1a and 1b show some output from the GRASS/Image Processor system.

Generating a complete programming language with parsers, compilers, and graphics takes a lot of human effort. More than ten person-years of programming were devoted to GRASS, aided by generous support from the National Science Foundation, National Endowment for the Arts, and others.

GRASS is totally oriented toward real-time generation and control of images for the simple reason that television cannot easily be slowed down for long and/or time-lapse exposures as can be done with film. The control structures for GRASS were developed ad hoc and became increasingly idiosyncratic. Nola Donato, a postgraduate student of mine, decided to teach me how to generalize many of the programming-language concepts. The result was GRASS3, which later became Zgrass.

In 1977, I was led to Jeff Frederiksen at Dave Nutting Associates, who was developing a deluxe home computer for Bally Corporation using the custom integrated circuits they had developed for the Bally Arcade video game. The pros-

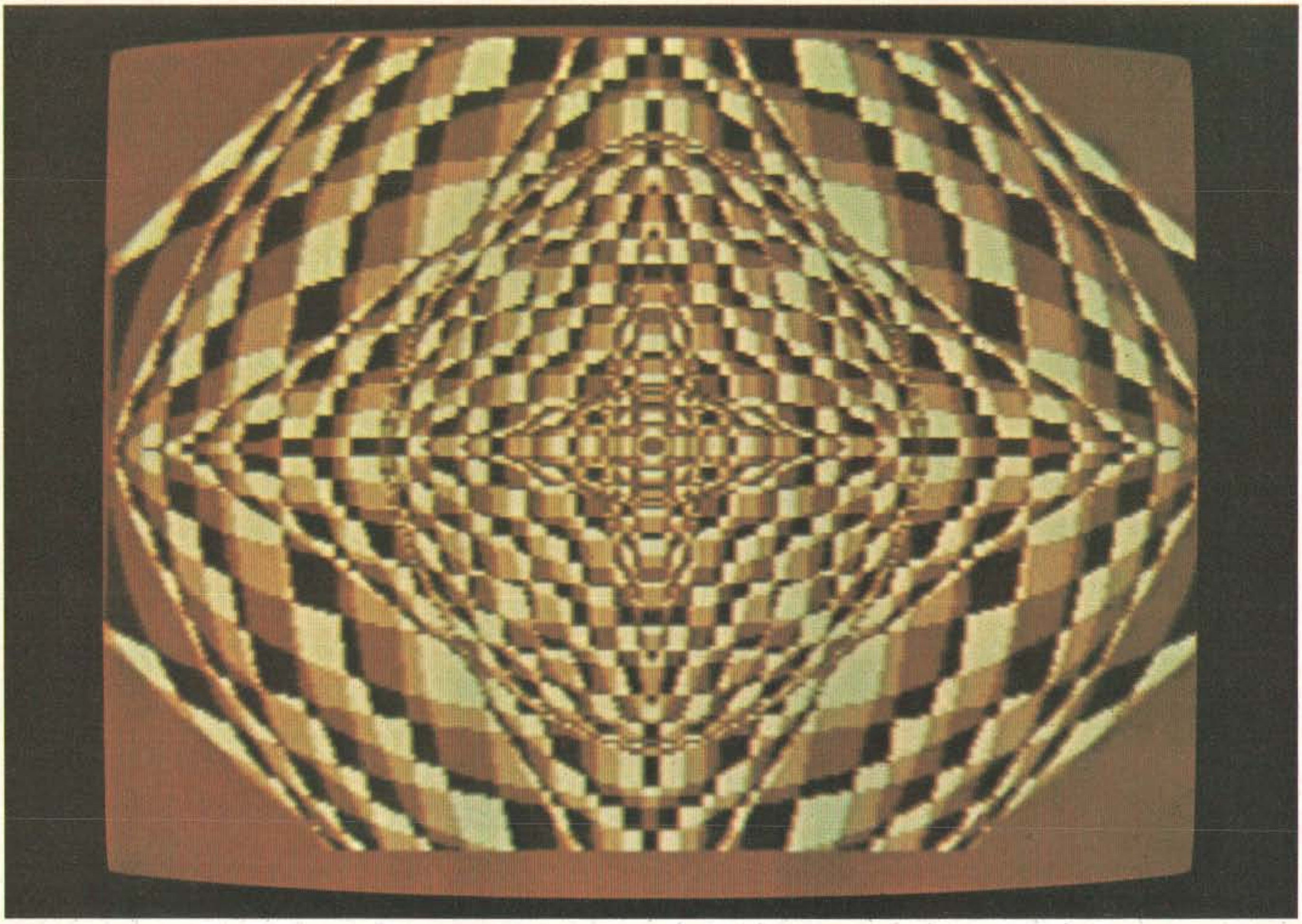


Photo 3: *Sample output from a later version of Zgrass, with a resolution of 320 by 204 pixels with 2 bits per pixel. Photo 3 was made by Frank Dietrich.*

pect of developing a language for fun, one that had user-orientation as the benchmark rather than how many FOR-NEXT loops you could execute per unit time was too good to pass up. I was contracted to produce Zgrass, and in a year, Nola Donato, Jay Fenton (a legendary wizard of video games and pinball-machine operating systems), and I had generated 9000 lines of code. (Much of the work was done not in a lab but in a cabin in the woods of Wisconsin!) Examples of output from this system are seen in photos 2a, 2b, and 2c. Note that the resolution of this first Zgrass machine is 160 by 102 pixels (ie: picture elements), with 2 bits per pixel.

Some confusion arose about whether we were producing a hobbyist machine or a home computer for consumers, so the project was suspended. Even now nobody really knows what a "consumer computer" is supposed to be.

From consulting with less enlightened would-be consumer computer manufacturers, I have perceived that they follow the rather negative view of consumerism. (Few people reading this article would be considered only consumers — I assume that BYTE readers are mostly hobbyists or professionals.) Consumerism is based on great market penetration, and the big question is: "How do you get 90% market penetration like color TV?"

It is also based on consuming, that is, wearing out or getting sick of hardware and software so you go buy more and consume it. The user is expected to supply no creativity, just assume a passive, susceptible-to-entertainment pose — this reminds you of television watching, doesn't it? Well, anything requiring creative energy is akin to hobbyism.

Consumer computers do exist in the form of video games that you can get bored with and buy more — even the advertisements invariably cite the number of new games to be available each month. I don't know how to write a programming language that wears out, though. User-extensibility is planned "nonobsolescence." Zgrass is not a consumer language by current standards.

The project is on active status again, but this time with a hobbyist/professional orientation. We believe there are many people who want a recordable image-producing system for around $3000. The current configuration includes:

- Z80 processor with 16 K bytes of EPROM and 48 K bytes of programmable memory
- custom graphics integrated circuits and floating-point hardware
- dual UARTs (universal asynchronous receiver/transmitters) for connection to larger computers, printers, etc
- RBG (red, blue, green) monitor for best color resolution


C
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- alphanumeric terminal (which the user provides)
- provision for floppy disks, tablet, other I/O (input/output) devices

Eight Zgrass units in this configuration have been alive and well and tied into the Bell-Laboratory-developed UNIX operating system since January 1980. Although I have only discussed software design, I must mention that the hardware to test the concepts really exists! See photo 3 and note that the resolution is now 320 by 204 pixels, with 2 bits used per pixel.

Details of Zgrass Control Structures

Programs in Zgrass are called *macros*. Macros are stored as ASCII (American Standard Code for Information Interchange) character strings and normally contain executable Zgrass commands. The fundamental unit of execution in Zgrass is a command, which is either an assignment statement or a function call.

Zgrass does not require declaration of variable types (with the exception of array dimensioning). The software automatically does all conversions that make sense based on the context. Any argument can be a function call whose returned value is converted to whatever is needed, if at all possible. Literals, indirect references, variables, built-in commands, user-defined commands, and user-defined macros are all handled by the same parser, so the syntax is very predictable. The fact that there are no restrictions on name length helps to produce easily read code.

User-Level Extensibility

Extensibility in Zgrass is achieved in two major ways. First, you can write macros which return values, produce graphics, or ask questions; or, through string-manipulation primitives written by Barb Wilson, you can generate other macros. Macros use arguments in exactly the same way as system commands, and are even named and called like system commands.

To reiterate, macros are simply strings of ASCII characters. When a macro is called, an MIB (Macro Invocation Block) is automatically built. It gives information on the invoking function call, the passed-argument list, and pointers to local variables, and provides room for the returned value. MIBs form a stack which implements the subroutining and block structuring of the language. When the macro returns, the MIB is deleted along with the local variables and unused literal arguments, if any, and control is passed back to the caller.

If arguments are to be passed to a macro, they are read by the normal input command, and print statements are suppressed as long as there are arguments left. If no arguments are present or an insufficient number are passed, the print statements function normally and the macro asks for input from the terminal. This allows macros to be used whether or not you know the arguments wanted, with no extra code by the author of the macro.

Macros can also be executed in parallel as background jobs. When called and suffixed by a ".B", the Macro Invocation Block is added to a background linked list. After that, the macro will run forever (it restarts at the beginning when it tries to return) until Control-C or the stop command selectively kills it. Photo 2c shows two sorting algorithms being compared for execution speed in real time, a tricky task in most languages, easy in Zgrass.

The background parallelism is achieved by interleaving execution of the macro statements. The MIB contains all relevant context for execution, including a pointer to the next command to execute, so switching MIBs after each line has been completed is simple and gives the functional parallelism. If there are five background macros, each one gets a line executed, in turn, round-robin fashion. This construct is simple and straightforward with no bizarre side-effects except that unusually time-consuming commands will make the parallelism temporally step somewhat. Background interleaving is easily understood and used even by the most naive users.

Meanwhile, the keyboard is still active. When the user types a command line, it is executed at a higher priority than the background macros. If the user initiates a macro at keyboard level, it will finish before the background macros continue. In any event, the keyboard overrides the background, again in an obvious, predictable way.

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The user may also specify programs to run as the result of a clock interrupt. When a macro call is suffixed by a ".F", the Macro Invocation Block is chained into a list that is polled every 1/60 second. The user sets the frequency of execution from 1 to 32,768 sixtieths of a second. These foreground macros execute on a higher priority level than the keyboard and background macros so they will start up just about on time (again, delayed only by a time-consuming graphics command). Foreground macros allow a keyboard command to be slipped in during context switching.

Zgrass, then, has three effective levels of priority with parallelism at two of the three levels. Since the Macro Invocation Block maintains all context information, even recursive programming is possible at any level.

One of the severe problems in interpretive, extensible languages like Zgrass is the overhead of parsing and looking up names in name tables. For this reason, Zgrass has a compiler which eliminates the overhead and dramatically increases speed. All the automatic conversions, priority, and parallelism continue to work. Compiling does eliminate some of the interactive debugging features, so you usually debug on the noncompiled version first.

Zgrass System Extensibility

Zgrass also allows extensibility at the system-command level. A system such as this should allow an experienced programmer to write new commands in assembler and interface them to the system easily, certainly without changing the EPROMs (erasable, programmable read-only memories). A transfer vector in low memory and a series of Z80 RST (special restart subroutine-call) instructions allow communication with about one hundred system routines which do parsing, type conversion, graphics primitives, and so on.

Documentation explains what these routines do, and anyone with a cross assembler (or patience for hand assembly) can write new commands of which the system has no prior knowledge. Such extensibility allows virtually infinite variety of specialty graphics commands, device drivers, and so forth to be written and distributed to others on audio tape, disk, or over telephone lines. Terry Disz wrote a debugging program used as a disk-resident command for setting break-points, dumping memory and registers and so on. This capability is not for everyone, but it's there.

The maximum size of one of these user-written nonresident commands is 4 K bytes. Since the typical Zgrass machine has 30 K bytes of programmable memory, the amount of potential custom code is immense. All housekeeping for storage allocation and deletion, maintenance of temporary scratch-pad areas and general cleanup is done by system routines. You only concentrate on the details, obeying a few rules for writing position-independent code.

One further type of extensibility is easy to get. Zgrass has an extra UART which talks to other computers quite nicely. Larger computers can send graphics and character data to your Zgrass machine. Zgrass units can even talk to one another at up to 19.2 k bps!

Error Handling, Debugging and Automated Instruction

Zgrass was designed from the beginning to be a language for writing CAI (computer-aided instruction) programs. In particular, it was designed to be self-teaching to a fairly high degree. When Zgrass is used as a CAI system, the result of providing parallelism, string manipulation, and good error handling is that the student always has the power of the whole language to explore while the author of the CAI programs is also in control.

Since macros are character strings, they can be built and executed. You can take student input, make it into a program (before the student even knows how to edit), let parameters be changed, show the results, and verify certain classes of results both during execution and after. The approaches we have taken to Zgrass CAI are beyond the scope of this article, so I will just mention the system features which make CAI possible.

Error-handling routines normally generate error-message numbers on the terminal. There are about sixty of them and they are quite specific. During regular programming, they are used in conjunction with single stepping, variable printing and other debugging techniques to identify

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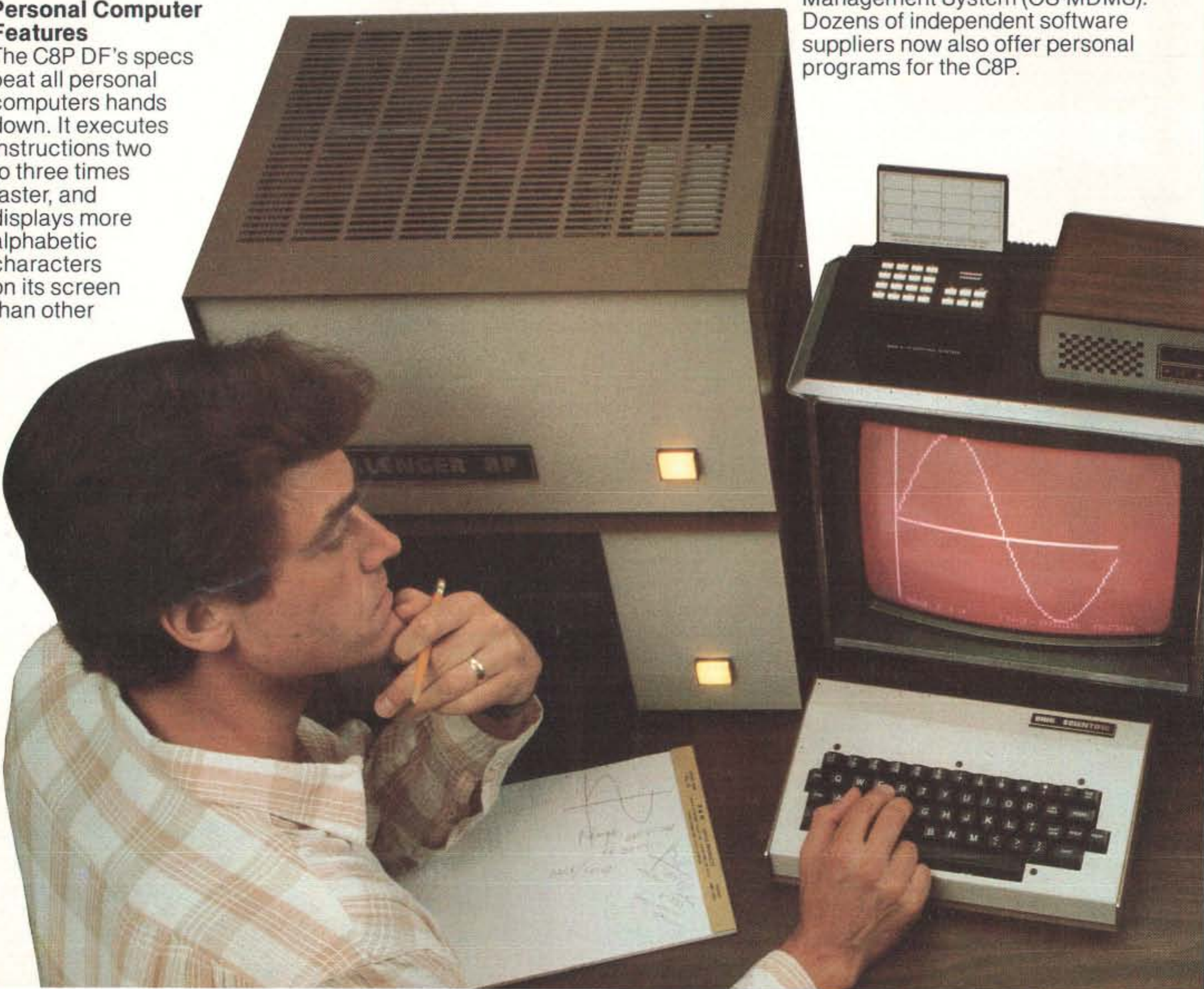
Ohio Scientific offers a large library of personal applications programs, including exciting action games such as Invaders and Star Trek, sports simulations, games of logic and educational games, personal applications such as biorhythms, calorie counter, home programs such as checking and savings account balancers and a home budgeter just to name a few. A new Plot BASIC makes elaborate animations easy, and music composition program allows you to play complex multi-part music through the computers DAC.

At the systems level the machine comes standard with OS-65D, an advanced disk operating system with Microsoft BASIC and an interactive Assembler Editor. Optional software includes UCSD PASCAL and FORTRAN and an Information Management System (OS-MDMS). Dozens of independent software suppliers now also offer personal programs for the C8P.



puter explorations.

Business Computer Features
The C8P DF utilizes dual 8″ floppy disk drives which store up to eight times as much information as personal computer mini-floppies, and an available double-sided option expands capacity to 1.2 megabytes of on-line storage. The C8P DF is compatible with Ohio Scientific's business computer software, including OS-65U an advanced operating system, and an Information Management System (OS-DMS) with supplementary inventory, accounting, A/R-A/P, payroll, purchasing, estimation, educational grading and financial modeling packages. The system also supports word processing (WP-3) and a fully integrated small business accounting system (OS-AMCAP V1.6). The C8P DF's standard modem and printer ports accept high-speed matrix printers and word-processing printers directly.

Home Control and Industrial Control
The C8P DF has the most advanced home monitoring and control capabilities ever offered in a computer system. It incorporates a real time clock and a unique FOREGROUND/BACKGROUND operating system which allows the computer to function with normal BASIC programs, at the same time it is monitoring external devices. The C8P DF comes standard with an AC remote control interface, which allows it to control a wide range of AC appliances and lights remotely, without wiring, and an interface for home security systems which monitors fire, intrusion, car theft, water levels and freezer temperature, all without messy wiring. In addition, the C8P DF can accept Ohio Scientific's Votrax voice I/O board and/or Ohio Scientific's new universal telephone interface (UTI). The telephone interface connects the computer to any telephone line. The computer system is able to answer calls, initiate calls and communicate via touch-tone signals, voice output or 300 baud modem signals. It can accept and decode touch-tone signals, 300 baud modem signals and record incoming voice messages. These features collectively give the C8P DF capabilities to monitor and control home functions with almost human-like capabilities.

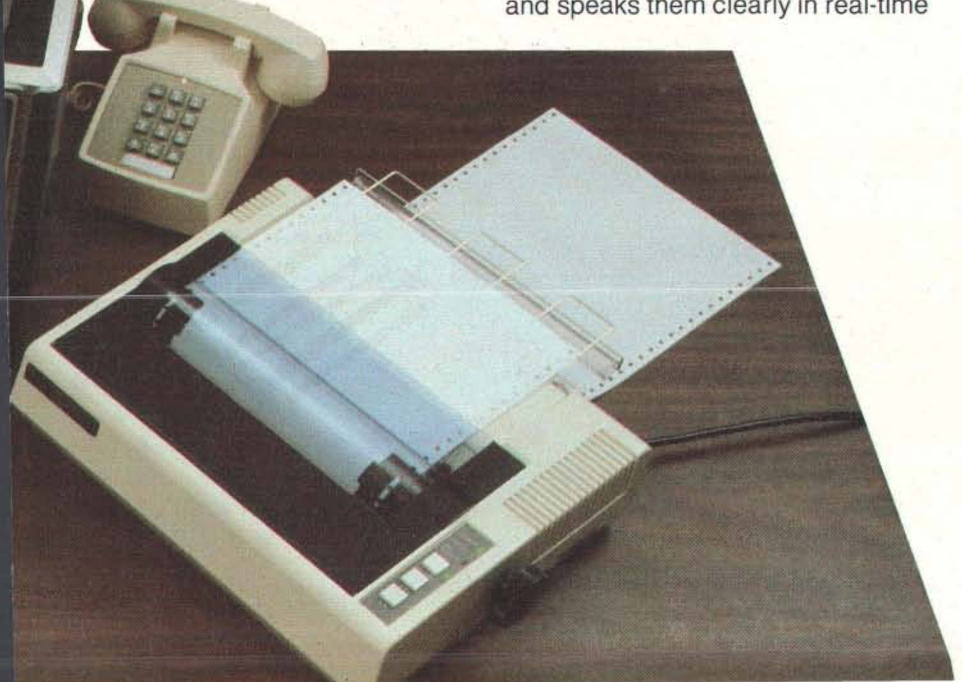
For process control applications, a battery back up calendar clock with automatic computer restart capabilities is available. Ohio Scientific's unique accessory ports allow the connection of a nearly unlimited number of 48 line parallel I/O cards and 12-bit high speed instrumentation quality analog I/O modules to the computer by inexpensive 16-pin ribbon cables.

Exploring New Frontiers
Ohio Scientific's vocalizer software processes normal BASIC print statements with conventional spellings and speaks them clearly in real-time on computers equipped with the UTI (CA-15B or CA-14A). This voice output capability, combined with the C8P's remote control, remote sensing, telephone interface capabilities and reasonable cost open up new frontiers for computer applications.

Documentation
The C8P DF is not a beginner's computer and doesn't come with beginner's documentation. However, Ohio Scientific does offer detailed documentation on the computer which is meaningful for experts, including a Howard Sams produced hardware service manual that includes detailed block diagrams, schematics, parts placement diagrams and parts lists. Ohio Scientific is now also offering fully documented Source Code in machine readable form for OS-65D, the Challenger 8P's operating system allowing experimenters and industrial users to customize the system to their specific applications.

What's Next?
Ohio Scientific is working on a speech recognizer to complement the UTI system, with a several hundred word vocabulary. The company is also developing an 8 megabyte low-cost, add-on hard disk for use in conjunction with natural language parsing to further advance the state-of-the-art in small computers. The modular bus architecture of the C8P assures system owners of being able to make use of these new developments as they become available just as the owner of a 1976 vintage Challenger can directly plug in voice output, the UTI and other current state-of-the-art OSI products.

The C8P DF with dual 8″ floppies, BASIC and two operating systems costs about $3000, only slightly more than you would pay for a dual mini-floppy equipped personal computer with only a fraction of the capabilities of the C8P.



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problems. When teaching, however, the CAI program must trap errors. These fall into three types: syntax, nontermination, and logic.

To trap syntax errors, you should use the ONERROR command which transfers the control to a diagnostic section of the program that you, as a CAI author, will have provided. There you can get the error number, the erroneous argument, and even the entire ASCII text of the line in error with the GETERROR command. You can then explain the problem to the user in whatever level of detail you wish.

Indefinite loops are caught with the LOOPMAX command which sets a limit to the number of control transfers (ie: skips and GOTOs). Once the limit is exceeded, an error is generated and trapped as explained earlier. So, you can catch nonterminating programs or be very meticulous and require efficiency from advanced students by lowering the LOOPMAX appropriately.

Logic errors are trickier and the general case is impossible. However, if you choose suitable problems to solve, you can do some very nice verification. For graphic tasks, the CMPARA command can check a student's building of an image against a prototype. The CAI author can tell if the student's image is a proper subset of the prototype and let it continue. Once a stray pixel is written, CMPARA returns a value of −2 which means the image is "mixed up," and you inform the student immediately. This approach clearly falls short of genuine artificial intelligence, but it is nevertheless quite useful. Several classes at the University of Illinois at Chicago Circle have been taught with great success using a GRASS-coded prototype (called GAIN, by Tom Towle).

Conclusions

Zgrass is a language/system designed to provide easy access to computer graphics and, in general, to computing. It has sophisticated real-time structures and control capability, and it's friendly, extensible, and fun. The language is more efficient than BASIC, more user-oriented than FORTRAN or Pascal, and it has the kind of language-control structures that will help you create your mind's fantastic visualizations on your video screen with more ease than ever before. ■

Glossary

Color: *The 256 colors available in Zgrass form an abbreviated spectrum. You can get four colors on the screen at any one time. The default colors are white, red, green, and blue. They are also known as color 0, color 1, color 2, and color 3. The values are stored in $L0, $L1, $L2, and $L3 unless you modify $HB to use the right-side colors $R0, $R1, $R2, and $R3.*

Color Map: *The color map is the way Zgrass translates color 0 thru color 3 to the 256 available colors. The hardware looks at the values of $L0 thru $L3 before it writes a pixel to the screen. If it is writing a 0, it uses the color stored in $L0; if it is writing a 1, it uses the color stored in $L1, and so on. To change the color map so 1 refers to yellow instead of red, set $L1 to 127. There are actually two color maps, the $Ls and the $Rs. You get to the $Rs by setting $HB.*

Color Option: *The possible values for color option are 0 thru 15. You may need to study your truth tables for inclusive-OR and exclusive-OR (XOR) logical operations to really understand what's going on. The following is functionally true, however:*

Color Option	Meaning
0	*replace with color 0 (white)*
1	*replace with color 1 (red)*
2	*replace with color 2 (green)*
3	*replace with color 3 (blue)*
4	*don't draw (actually XOR with 00)*
5	*XOR screen with color 1 (01 binary)*
6	*XOR screen with color 2 (10 binary)*
7	*XOR screen with color 3 (11 binary)*
8	*change red to white, blue to green (clear bit 0)*
9	*change green to white, blue to red (clear bit 1)*
10	*OR with 01 (if red or white, stay red; if blue or green, stay blue)*
11	*OR with 10 (if green or white, stay green; if red or blue, stay blue)*
12	*replace with red only if white were there*
13	*replace with green only if white or red were there*
14	*increment the color there by 1 (white to red, red to green, green to blue, and blue to white)*
15	*decrement the color there by 1 (white to blue, red to white, green to red, and blue to green)*

Macro: *A string that is supposed to contain legal Zgrass commands. Most programming languages call such things "programs" or "subroutines," but we call them macros. Macros are effectively user-defined commands. Macros can behave just like commands in the sense that you can pass arguments to macros with the INPUT command and return values with the RETURN command. You define a macro just like you define a string, with an assignment to a name or by using EDIT.*

String: *A collection of characters (ie: numbers, letters, punctuation) delimited (ie: enclosed) by single or double quotes or balanced (ie: enclosed) by brackets or braces. If you have to use a string delimiter in a string, make sure that it is delimited by a different string delimiter or things will get very confused. Most likely it will consider the rest of your macro as part of the string. Examples:*

```
"THIS IS A LONGER STRING"
"PRINT A*B*C
SKIP -1 ;.THIS STRING
COULD BE A MACRO TOO"
[THIS IS HOW TO PUT A
QUOTE IN A STRING: " ' "]
[1234]
[ ]
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Book Reviews

Applied Mathematical Physics With Programmable Pocket Calculators

by Robert M Eisberg
McGraw-Hill Book Company, New York NY, 1976
176 pages, softcover
$9.95

This book by Professor Eisberg of the University of California, Santa Barbara is interesting on three counts. First, it introduces the reader to numerical methods for differentiation, integration, and solution of differential equations. Second, these methods are applied to the general problems of mathematical physics, starting with the motion of an oscillator and finishing with Schrödinger's equation. Third, the programs for the solution of the equations in these fields are given for the Hewlett-Packard HP-25 and the Texas Instruments SR-56 calculators.

A reader's first reaction might be that the programs apply only to the solution of the problems of mathematical physics. However, the mathematical procedures that were aimed at these calculators may also be applied to any computer. Furthermore, the problems are in the field of physics, but the methods of solution of these problems should be of interest to the general reader.

This book discusses the derivative and methods of obtaining it, followed by programs and examples. Problems for testing the program are also given. Procedures for integration and summation are introduced with the appropriate programs and examples for solution.

The numerical procedure for the solution of second-order differential equations is developed without the great depth required for mathematical development. These equations are given for both undamped and damped motion, as well as the driven oscillator. The program development and the results obtained are interesting.

The harmonic oscillator section is followed by the coupled oscillator. The examples for the coupled oscillators and their motion are interesting not only for the study of the motion of such systems, but also for the solution of the simultaneous equations involved.

The concept of central force motion is introduced, including orbital path determination. This section concludes with alpha particle scatter due to repulsive forces. A "random" number generator program is introduced and applied to problems of entropy, or run-down evaluation.

Finally, Schrödinger's time-independent equation is introduced and evaluated, and programs are given for the harmonic oscillator and the potential well.

This is an admirable little book on mathematics applied to physics and the programming of such material for the HP-25 and SR-56 programmable calculators. It is also of great interest to the computer programmer because of the procedures discussed, which are adaptable to the computer.

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The Little LISPer

by Daniel P Friedman
Science Research Associates Inc
Palo Alto CA, 1974

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Forms-2

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OEMs

Intel has adopted CIS COBOL and offers it (as iCIS-COBOL) for their Intellec and Intellec II systems. Ideal for OEM's or private label, CIS COBOL was developed entirely by Micro Focus. Send inquiries for CIS COBOL object packs and application vendor terms to MICRO FOCUS or its licensed distributors. Distributor terms also available from MICRO FOCUS.

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$3.95

It might seem a little odd to review a six-year-old book, but there is a good reason for it in this case: LISP has only recently become available for microcomputers. John Allen (guest editor of the August 1979 BYTE special issue on LISP) has promised that his LISP Company will unveil a full line of LISP systems. It will start with a Z80 version and proceed to much more capable LISPs for the new 16-bit microprocessors. Also, LISP interpreters from other sources exist for Z80, 6800, and AM-100 processors.

The next question is how does one learn LISP? Reference manuals give too much detail and not enough feel for the language. Most introductory material gives too little detail and not enough feel for the language, and nearly all books on LISP make the mistake of telling the student what LISP functions are and what they do instead of how to use them. There is an alternative to all this. One can obtain *The Little LISPer*, study it for a short time, and come away with a firm grasp of the essentials of LISP. This grasp is sufficient to make sense out of the rest of the material concerning LISP and LISP-based systems that one might encounter.

The Little LISPer was originally written to provide a two-week course for nonprogrammers.It is one of the best introductions to any language that I have ever read. I went straight through it the day I got it. The sequence of topics (interleaving functions, data structures, programming principles, recursive programming techniques) is laid out with a deft touch that has the student progressing much faster than he realizes. This organization of the material allows the reader to build up a sophisticated sense of the patterns inherent in LISP structures almost without noticing.

Other features that contribute to the relaxed, but speedy, progress of the student are the organization of the entire text into carefully constructed sets of questions and answers and the light humorous touch of the examples.

LISP operates on list structures, and most of the data used in the book are lists of foods. One of the problems for the reader is to determine the list that results from inserting the atom ROAST after the atom CHUCK in a list beginning:

(HOW (MUCH WOOD). . .

Unfortunately the text breaks off too soon, leaving the reader with a clear sense of things he was just about ready to do, but will have to find out about elsewhere. In any case, the author says the reader is "better prepared than he realizes" to learn the details of a full LISP system and many more advanced programming techniques. It is only necessary to become familiar with the full range of features of a complete LISP system before diving into the world of artificial intelligence and numerous other fields.

LISP is a realization and extension (in notation, not computing power) of Church's lambda calculus, one of the most powerful mathematical tools in existence. It is generally considered a remarkable achievement to teach a powerful mathematical technique to nonmathematicians. As far as I am concerned, though, this kind of teaching should be normal, and the usual "math is hard and you're too dumb to learn it" approach should be thrown away. The fact is that most people are not too dumb to learn mathematics of whatever sort, but few people are clever enough to learn improperly presented mathematics. It seems that even fewer are clever enough to present it well. I am delighted to have an opportunity to point out an instance of top-quality textbook writing and to offer my congratulations to Daniel Friedman.

Mokurai Cherlin
APL Business Consultants Inc
POB 1131
Mt Shasta CA 96067

Mathematical Elements for Computer Graphics

by David Rogers and J Alan Adams
McGraw-Hill Book Company, New York NY, 1976
Softcover, 239 pages
$12.95

One of the ironies of computer graphics is that it is the aspect of computer use that most attracts people who do not like mathematics, while it is one of the few fields of computing (contrary to popular belief) that require mathematics. *Mathematical Elements for Computer Grapics* is a good sourcebook of the mathematics, the formulae, and the algorithms required to implement graphics packages and applications on computers of any size. It is especially well suited to personal-computer use, since all of the algorithms are presented in BASIC.

Rogers and Adams assume several things about the reader. First, they assume that the reader is writing, or wants to write, software for a line-drawing display (such as those produced by Tektronix). If you have a television-technology display (like most small-computer users), you will need to devise the software to make it draw lines. They also assume that the reader has a substantial background in mathematics. Unfortunately for this subject, a substantial mathematical background means three terms of college-level calculus plus matrix algebra. Also, the algorithms are presented in Dartmouth BASIC, which requires a fair amount of conversion before it will

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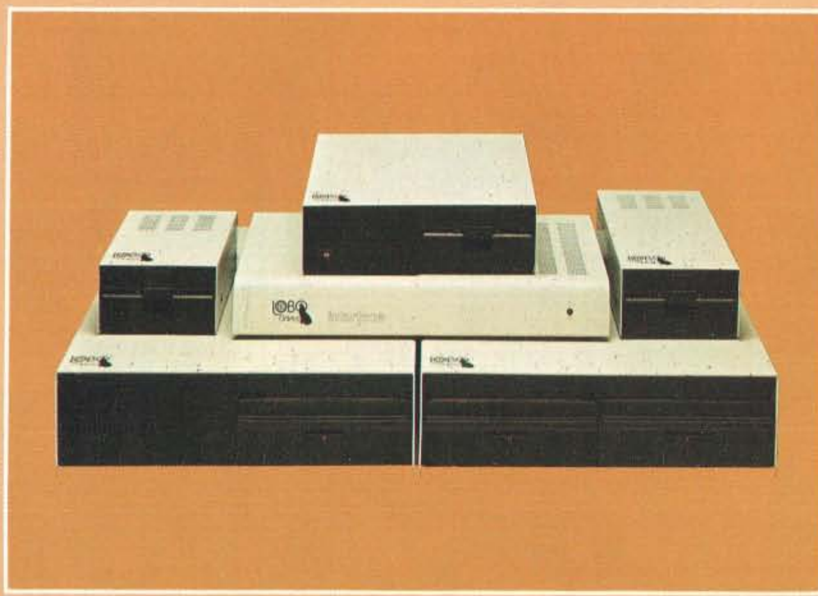

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work in Microsoft BASIC or BASIC-E.

For those of you who have not yet been scared off, you will learn algorithms and techniques for: scaling, rotation, curve representation, three-dimensional displays, three-dimensional transformation, and surface description and display. Of course, I am only summarizing; Rogers and Adams break these topics down into 65 sections, plus algorithms.

So why buy (or borrow) this book ? If you want a text to teach yourself computer graphics, this is the wrong book. It will not really tell you how to put all of the algorithms together into a usable package or application. But, if you already know something about computer graphics and need a reference to give or compare formulae and algorithms, then this is definitely the right book. A caveat is in order: I have not checked any of the algorithms or programs for typographical accuracy. Which is to say, it's a good reference, but not a good text.■

John A Lehman
716 Hutchins #2
Ann Arbor MI 48103

BYTE's Bugs

Duplicated NAND Gate

A drafting error marred Steve Ciarcia's article "A Build-It-Yourself Modem for Under $50" (August 1980 BYTE, page 22). The pin numbers for a section of an integrated circuit were incorrectly marked, duplicating the numbers for a different section.

In figure 1b on page 28, the NAND gate of IC4c should have had its input indicated as being on pins 8 and 9, with output on pin 10. The pin numbers for IC4d are correct as shown.■

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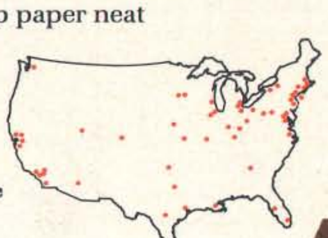

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Books Received

The following is a list of books received at BYTE Publications during this past month. Although the list is not meant to be exhaustive, its purpose is to acquaint BYTE readers with recently published titles in computer science and related fields. We regret that we cannot review or comment on all the books we receive; instead, this list is meant to be a monthly acknowledgement of these books and the publishers who sent them.

Bit-Slice Microprocessor Design, Jim Brick and John Mick; McGraw-Hill Book Company, New York NY 1980; 7¾ by 9½ inches (20 by 24.5 cm), 398 pages, hardcover, ISBN 0-07-041781-4, $18.50.

Computer Peripherals for Minicomputers, Microprocessors, and Personal Computers, C Louis Hohenstein; McGraw-Hill Book Company, New York NY 1980; 6 by 9 inches (15.5 by 23 cm), 312 pages, hardcover, ISBN 0-07-029451-8, $19.50.

Early British Computers, Simon Lavington; Digital Press, Bedford MA 1980; 5¾ by 8¼ inches (15 by 21 cm), 130 pages, softcover, ISBN 0-932376-08-8, $8.

A Guide to Structured COBOL with Efficiency Techniques and Special Algorithms, Pacifico A Lim; Van Nostrand Reinhold, New York NY 1980; 6 by 9 inches (15.5 by 23 cm); 272 pages, hardcover, ISBN 0-442-24585-8, $18.95.

Master Handbook of Electronic Tables & Formulas, third edition, Martin Clifford; Tab Books, Blue Ridge Summit PA 1980; 6 by 8¼ inches (15.5 by 21 cm), 313 pages, softcover, ISBN 0-8306-1225-4, $8.95.

More Chess and Computers: The Microcomputer Revolution, The Challenging Match, David Levy, Monroe Newborn; Computer Science Press, Potomac MD 1980; 5¼ by 8⅜ inches (13.5 by 20.5 cm), 117 pages; softcover, ISBN 0-914894-07-2, $12.95.

Practical Area Navigation, Paul Garrison; Tab Books, Blue Ridge Summit PA 1980; 6 by 9¼ inches (15.5 by 23 cm), 224 pages; softcover, ISBN 0-8306-2286-1, $5.95.

Practical BASIC Programs, Lon Poole; Osborne/McGraw-Hill, Berkeley CA 1980; 8⅜ by 10⅞ inches (20.5 by 26.6 cm), 171 pages, softcover, ISBN 0-931988-38-1, $15.

Project Whirlwind: The History of a Pioneer Computer, Kent C Redmond and Thomas M Smith; Digital Press, Bedford MA 1980; 7⅝ by 9½ inches (18.6 by 24.5 cm), 280 pages, hardcover, ISBN 0-932376-09-6, $21.

Some Common BASIC Programs, third edition, Mary Borchers and Lon Poole; Osborne/McGraw-Hill, Berkeley CA 1980; 8⅜ by 10¾ inches (20.5 by 27.5 cm), 195 pages; softcover, ISBN 0-931988-06-3.

Structured BASIC and Beyond, Wayne Amsbury; Computer Science Press, Potomac MD 1980; 6 by 9 inches (15.5 by 23 cm), 310 pages, softcover, ISBN 0-914894-16-1, $10.95.■

BYTE's Bugs

The First Shall Be Last

The Washington Area Computer Society (WACS) meets on the *last* Friday of the month (not the first) on the campus of the Catholic University of America in Washington, DC, in the first-floor lecture room in Keane Hall, starting at 7:30 PM. Incorrect information about the meeting time had been published in a past issue of BYTE.■

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Complex Number Subroutines

William R Harlow, Department of Mechanical and Industrial Engineering, 836 Rhodes Hall, University of Cincinnati, Cincinnati OH 45221

I teach numerical methods to engineering students at the University of Cincinnati, where we have an Amdahl computer. Also, various departments have purchased Heath, IMSAI, Radio Shack, and Wang systems. Although the big system has built-in hardware to perform complex operations, the smaller systems must have them implemented as subroutines.

Besides the four fundamental operations of addition, subtraction, multiplication, and division, there are several important functions of a complex variable. These include log(z), e^z, sin(z), cos(z), z^p, and others. Since addition and subtraction are so easy to handle, they are not included in the routines listed here.

Listing 1 gives a set of BASIC routines to do the complex operations listed in table 1. Other functions not

Listing 1: *Subroutines for manipulation of complex numbers. See table 1 for a description of the functions calculated. Note that some of the routines use the constant #PI, which should be set to 3.1415926535.*

```
1000 REM
1010 M1=A1*A2-B1*B2:M2=A1*B2+A2*B1:RETURN
2000 REM
2010 D=A2↑2+B2↑2
2020 Q1=(A1*A2+B1*B2)/D:Q2=(A2*B1-A1*B2)/D:RETURN
3000 REM
3010 R=SQR(A1↑2+B1↑2):I=SGN(A1)+3*SGN(B1)+4
3020 ON I GOTO 3050,3060,3070,3110,3080,3090,3100,3060
3030 B=ARCTAN(B1/A1)-#PI:GOTO 3120
3050 B=(-#PI/2):GOTO 3120
3060 B=ARCTAN(B1/A1):GOTO 3120
3070 B=#PI:GOTO 3120
3080 B=0:GOTO 3120
3090 B=#PI+ARCTAN(B1/A1):GOTO 3120
3100 B=#PI/2:GOTO 3120
3110 P1,P2=0:GOTO 3120
3120 R0=P*LOG(R):R=EXP(R0)
3130 P1=R*COS(P*B):P2=R*SIN(P*B):RETURN
4000 REM
4010 I=SGN(A1)+3*SGN(B1)+4
4020 IF I=4 THEN 4120
4030 L=.5*LOG(A1↑2+B1↑2)
4040 ON I GOTO 4060,4070,4080,4120,4090,4100,4110,4070
4050 L2=ARCTAN(B1/A1)-#PI:GOTO 4130
4060 L2=(-#PI/2):GOTO 4130
4070 L2=ARCTAN(B1/A1):GOTO 4130
4080 L2=(#PI):GOTO 4130
4090 L2=0:GOTO 4130
4100 L2=#PI+ARCTAN(B1/A1):GOTO 4130
4110 L2=#PI/2:GOTO 4130
4120 PRINT "LOG(Z) IS UNDEFINED":STOP :RETURN
4130 L1=L:RETURN
5000 REM
5010 E1=EXP(A1)*COS(B1):E2=EXP(A1)*SIN(B1):RETURN
6000 REM
6010 U1=(EXP(B1)-EXP(-B1))/2:U2=(EXP(B1)+EXP(-B1))/2
6020 S1=SIN(A1)*U2:S2=COS(A1)*U1:RETURN
7000 REM
7010 U1=(EXP(B1)-EXP(-B1))/2:U2=(EXP(B1)+EXP(-B1))/2
7020 C1=COS(A1)*U2:C2=SIN(A1)*(-U1):RETURN
8000 REM
8010 IF B1<>0 THEN 8050
8020 IF A1<0 THEN 8040
8030 R1=SQR(A1):R2=0:RETURN
8040 R1=0:R2=SQR(-A1):RETURN
8050 R=SQR(A1↑2+B1↑2)
8060 R1=SQR((R+A1)/2):R2=SGN(B1)*SQR((R-A1)/2):RETURN
```

Line Number	Operation type	Input; Use	Other Variables Used	Output
1000	product $z_1 \times z_2$	A1,B1;A2,B2		M1,M2
2000	quotient z_1 / z_2	A1,B1;A2,B2	D	Q1,Q2
3000	power z^p	A1,B1	P,R,I,B	P1,P2
4000	natural logarithm Ln z	A1,B1	I,L	L1,L2
5000	exponential e^z	A1,B1		E1,E2
6000	sine sin z	A1,B1	U1,U2	S1,S2
7000	cosine cos z	A1,B1	U1,U2	C1,C2
8000	square root $z^{1/2}$	A1,B1	R	R1,R2

Table 1: *Table of complex number operations performed by subroutines in listing 1. In the "Input" column (A1, B1) refers to the complex number A1+B1*i*, where* i *is the square root of −1. In the "Output" column, the two numbers listed are the real and imaginary parts of the answer; eg: the output variables M1 and M2 of the multiplication routine mean that the result of the multiplication is the complex number M1+M2*i.

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included could be the hyperbolic and inverse trigonometric functions. The square root of a complex number was included even though it is a special case of z^p. The only complicated ones are the power and the logarithm. This is due to the angle utilized.

The subroutines have been given large line numbers so that they may be put at the end of a program. Users can certainly renumber these lines or use only those needed for a particular problem.

Two rather simple problems (see listings 2 and 3) are included to demonstrate the use of the functions. Both make use of Newton's method to solve for the roots of a function. This is done using the following iterative formula to obtain a better approximation of z, z_{k+1}, from the current approximation, z_k:

$$z_{k+1} = z_k - f(z_k)/f'(z_k) \text{ where } k=1,2,\ldots$$

An initial or starting value of z is selected ($z=x+iy$). Thus $z_1 = x_1 + iy_1$ is used in $f(z_1)$ and $f'(z_1)$. This will generate a z_2 which is fed back into the right-hand side of the equation to give a z_3, and so on.

The method is rapid in convergence and quite stable. If a certain z_k should make $f'(z_k)$ very small or zero, however, it is best to restart with a new z_1. In the programs shown, a test to stop cycling is made on the $f(z)$:

IF SQR(F1↑2+F2↑2) <1E−6 THEN . . .

This statement stops the iteration when the complex error has a magnitude of less than 10^{-6}.■

Listing 2: *Example program using the subroutines of listing 1. The program given in listing 2a attempts to find a root of the function* $f(z)=e^z - z^2$. *Note that its derivative* $f'(z)=g(z)=e^z - 2z$. *Listing 2b shows two separate runs of the program with starting points of (1,1) and (−1,0); the final results are underlined. Due to the cyclic nature of* e^z, *there are an infinite number of solutions to this problem.*

(2a)

```
10 INPUT "                         KEY IN X,Y ",X,Y
12 PRINT
15 PRINT TAB(14);X,Y
20 A1=X:B1=Y
30 GOSUB 5000
40 P=2
50 GOSUB 3000
60 F1=E1-P1:F2=E2-P2
65 IF SQR(F1↑2+F2↑2)<1E-6 THEN 120
70 G1=E1-2*A1:G2=E2-2*B1
80A1=F1:B1=F2:A2=G1:B2=G2
90 GOSUB 2000
100 X=X-Q1:Y=Y-Q2
110 GOTO 15
120 STOP "              ROOT DETERMINED. KEY RUN FOR A NEW SET"
```

(2b)

```
X1 =  1                   Y1 = 1
      2.912389622375       2.575157181739
      2.187132232955       2.174648753578
      1.760811047732       1.808824533853
      1.603663701734       1.596954184978
      1.58722527008        1.54253028231
      1.588042823737       1.540223443863
      1.588047264669       1.540223501065

X1 = -1                   Y1 =  0
     -.733043605249         0
     -.7038077863239        0
     -.7034674683272        0
```

Listing 3: *Example program using the subroutines of listing 1. The program given in listing 3a attempts to find a root of the function* $f(z)=2z^2 + (-6 - i)z + (20 - i) = (2z + 4 - i)(z - 5)$. *(Its roots are* $(-2 + 0.5i)$ *and 5.) The derivative* $f'(z)=g(z)=4z + (-6 - i)$. *Two runs of the program are shown in listing 3b, with the final results underlined.*

(3a)

```
10 INPUT "                         KEY IN X,Y ",X,Y
12 PRINT
15 PRINT TAB(14);X,Y
20 A1=X:B1=Y
40 P=2
50 GOSUB 3000
60 F1=2*P1:F2=2*P2
70 A2=-6:B2=-1
80 GOSUB 1000
90 F1=F1+M1-20:F2=F2+M2+5
95 IF SQR(F1↑2+F2↑2)<1E-6 THEN 200
100 G1=4*A1-6:G2=4*B1-1
110 A1=F1:B1=F2:A2=G1:B2=G2
120 GOSUB 2000
130 X=X-Q1:Y=Y-Q2
140 GOTO 15
200 STOP          "ROOT DETERMINED. KEY RUN FOR A NEW SET"
```

(3b)

```
X1 =  1                   Y1 = 1
     -3.307692307727      -4.461538461515
     -1.45941644561       -1.379310344755
     -1.434942737807       .532192367931
     -2.053130882705       .4886935917174
     -2.00036624035        .4998063289297
     -2.00000001228        .4999999788526

X1 = 2                    Y1 = 2
      2.207547169882      -2.226415094319
      2.830440251643       1.193459119487
      4.902563504007      -1.877088064073
      4.604564248345      -.193451138577
      5.015324400454       2.68292464E-02
      4.999923902019       1.12126002E-04
      4.999999999177      -2.49665620E-09
```


BYTE's Bits

International Systems and Courseware Exchange

One of the greatest deterrents facing organizations that desire to purchase a microcomputer is the fact that the development of systems applications software is costly and time-consuming. In an attempt to find a solution to this situation, John Earle Associates Inc has met with educators, professionals, and business people to discuss means for alleviating this problem. These discussions culminated in the establishment of the International Systems and Courseware Exchange (ISCE). The purposes of the ISCE are to enable schools, businesses, and professionals to license others to use their proprietary courseware and systems for an annual fee on a lease basis, and to recover the developmental costs of the software through the licensing fee. All schools, governmental agencies, doctors, lawyers, engineers, accountants, businesses, manufacturers, and free-lance developers of systems applications, courseware, or games are welcome to participate, as providers or as users; or as is the case within many businesses and schools, they may be included in both categories.

A free catalog will be provided to each individual or organization with listings in the catalog. Catalogs will be available to others for $10.

The first catalog containing listings of software and all information necessary to order or submit programs will be published in January, 1981. Catalog entries dealing with administrative or business applications should be mailed to Howard R Baldwin, Registrar, University of Akron, 3220 Miles NW, Canton OH 44718. Catalog entries concerning educational or professional applications should be sent to Swen A Larsen, Dean of Science and Technology, World University, Barbosa esq Guayama, Hato Rey, Puerto Rico 00917. For a copy of the catalog or for more information, contact John Earle Associates Inc, POB 12213, Loiza Station, Santurce, Puerto Rico 00914.

Pass the Salt and the Computer, Please

Eleven of the nation's newspapers affiliated with the AP (Associated Press) are experimenting with electronic delivery of news to the home. Through the joint efforts of the newspapers, the AP, and CompuServe Inc, an information networking firm, a daily electronic edition will be published for at least six months. The results of this test will be shared with the 1300 daily newspapers and 3500 radio and television stations that are a part of the AP news cooperative.

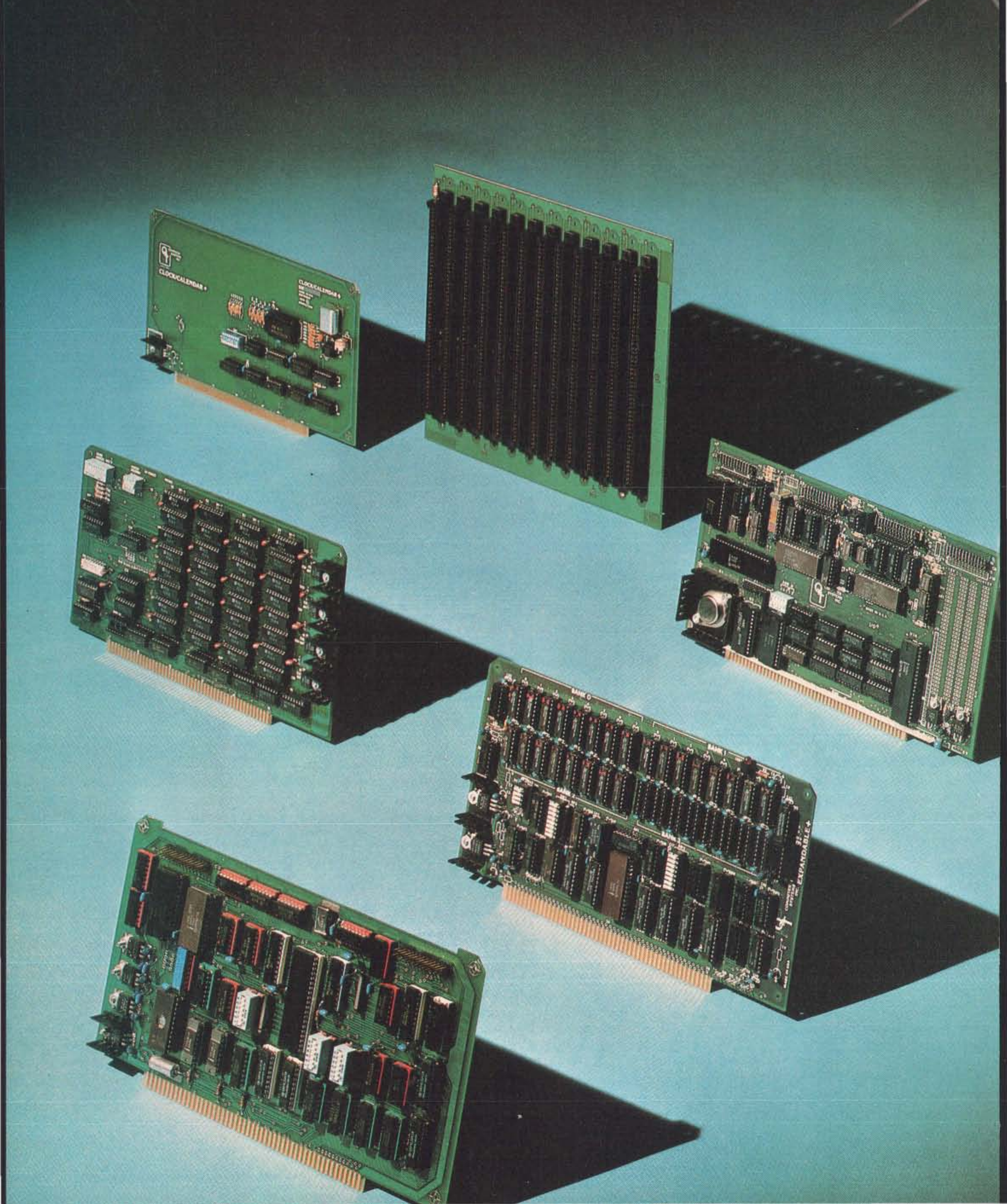
The newspapers participating are *The Columbus Dispatch*; *The Washington Post*; *Los Angeles Times*; *The New York Times*; *Chicago Sun-Times*; *The St Louis Post-Dispatch*; *The Minneapolis Star* and *Tribune*; *The Atlanta Journal* and *Constitution*; *The Norfolk Virginian-Pilot* and *Ledger-Star*; *San Francisco Chronicle*; and *The Middlesex News* (Framingham, Massachusetts). Each newspaper contributes news items and computing expertise to produce the news that is delivered to the CompuServe computers. Customers with a terminal and modem merely have to place a telephone call to link up with the electronic editions. Home users are charged $5 per hour, billed in 1-minute increments. The service operates from 6 PM to 5 AM weekdays and all day on weekends and holidays.

The experimenters hope that the test will provide substantial information on marketing the service, promotion, design of the data base, and new sources of advertising revenue. For more information, contact CompuServe Inc, 5000 Arlington Centre Blvd, Columbus OH 43220, (614) 457-8600.

Tuition-Free Program for Women in Electrical Engineering

A brochure from the University of Dayton outlines a National Science Foundation-sponsored Fast-Track program for women interested in electrical engineering. To qualify, an applicant must hold a bachelor's degree in mathematics, physics, or a related science. Participants earn a certificate that serves to advance them to an academic level equivalent to that of an electrical engineering graduate. Credits earned can be applied toward a bachelor's degree in electrical engineering. A Fast-Track staff at the university offers counseling and guidance, assists in part-time work placement, arranges for partial living expense stipends and placement in engineering jobs at program conclusion. The program commences January 5, 1981, and lasts thru December 19, 1981. Copies of the brochure, entitled *Women Interested in Engineering*, can be obtained by writing or calling Carol M Shaw, Assistant Dean, School of Engineering, University of Dayton, Dayton OH 45469, (513) 229-2736. ■

TOGETHER...A New Beg



...nning! Manufacturing and marketing micro products is our only business.

QT is committed to building uncommon features and quality into each of its micro user products. We're offering you the lowest prices on the QT System+ and other fine products. It's our new beginning.

■ Look At These QT System + Features:

2 Megabyte storage (standard) • Up to 16 Megabytes storage available • Mainframe with power supply and fan • Televideo 920C terminal • CPU-Z80-4MHZ • Two 8" disk drives • Floppy disk controller (double density) • Dynamic Memory (48K—expandable to 64K) • 2K Monitor program and Disk Bios on 2716 EPROM • RAM/ROM/PROM, up to 8K in any combination on CPU • Hard Disk Compatible • 2 Serial/2 Parallel Ports • Real Time Clock • EPROM Programmer • CP/M® 2.2 or 1.4 Operating System • MP/M® Compatible • Full line of business software available.

SYSTEM + SS (1 Megabyte) Sgl Side/Dbl Den $4295.00.
SYSTEM + DS (2 Megabytes) Dbl Side/Dbl Den $4995.00

■ Silence + Mother Boards

No need for termination • Very high crosstalk rejection • 6, 8, 12 and 18 slots available • Has operated to 14 MHZ quietly.

	6-Slot	8-Slot	12-Slot	18-Slot
Bare Bd.	$24.95	$26.95	$29.95	$49.95
Kit	$39.95	$54.95	$69.95	$99.95
A&T	$49.95	$69.95	$89.95	$139.95

■ SBC + 2/4

1K RAM On Board • 2 Programmable Timers • Power On Jump to On-Board 1K or 2K EPROM (2708-2716) • 2 Separate Parallel Ports • Programmable Baud Rate Selection (110 to 9600) • Serial I/O Port (RS-232).

Bare Board	$60.00	Kit	$190.00
1K Mem. Kit	$12.00	A&T	$280.00

■ Expandable + Dynamic Memory (16K to 64K)

Uses 3242 Refresh Chip with delay line • Quiet four layer PC Bd • Supports 16K, 32K, 48K or 64K of memory • 24 Address lines per IEEE specifications • Bank on/off signal selected by I/O port 40 (Hex) per industry standard.

Bare Board	$70.00	48K Kit	$480.00
16K Kit	$280.00	48K A&T	$550.00
16K A&T	$325.00	64K Kit	$525.00
32K Kit	$360.00	64K A&T	$625.00
32K A&T	$425.00		

■ I/O +

Two Independent SYNC/ASYNC Serial Ports • One Strobed 8-Bit Parallel Input Port With Handshaking • Three 8-Bit Parallel Ports (Undedicated, User Configured) • Three Independent 16-Bit Timers • 8 Level Priority Interrupt Controller.

Bare Board	$69.00	A&T	$375.00
Kit	$275.00		

■ RAM + 65

2 or 4MHZ • 16K Static RAM • Uses 2114L static RAMS • Addressable in 4K steps • Memory protection in 1K increments • Features bank selection.

2MHZ		4MHZ	
Bare Board	$20.00	Kit	$225.00
Kit	$200.00	A&T	$250.00
A&T	$240.00		

■ Clock/Calendar +

Day, Week, Month, Year • 24 Hour Time, or 12 Hour AM PM (selectable) • Interval Interrupt Timer: 1024 Hz (approximately 1 millisec), 1 Sec, 1 Min, 1 Hour • On Board Battery Backup • Available for Apple & TRS-80.

Bare Board	$45.00	A&T	$150.00
Kit	$100.00		

■ Mainframe + MF +

• Cabinet • 30A Pwr Supply • 12 or 18 Slot Motherboard • Fan-cooled • AC Line Filter to eliminate EMI • A&T • Power and Reset Switches on front panel.

MF + 12	$450.00
MF + 18	$500.00
MF + W/O Motherbrd	$400.00

■ Mainframe + MF + MD

Accepts 2 Each 5¼" Disk Drives • 18A Pwr Supply • 6-12 Slot Motherboard • Dual-mini-disk provision • Disk Drive Power Supply • AC line filter to eliminate EMI • Power and reset switches on front panel.

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■ DDC-8 +

Available in Brown or TRS 80 Colors • Disk Cabinet for Single 8" Drive • Data Cable • Fan • Accepts Persci, Shugart, Siemens, Remex, QUME.

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Letters continued from page 20:

Impressive Bar-Code Maker

IBM manufactures a type element that could possibly be used to prepare bar-code text that would also be readable by humans. This type element is *not* listed in any of IBM's typeface catalogs. It is called a special-application element, and I guess IBM figures that you know they have it if you want it. The intended application is for the preparation of text for input on a Dataflow Optical Reading System.

This element is currently available only in the standard 88-character format. IBM sales representatives in Michigan could not find out if it was going to be manufactured in the new 96-character format too. This point is not very important, since there are not too many of the new 96-character Selectrics in the computer-users' market. The new Selectric III will use the 96-character element only, so it won't be of much use to anyone in the market to upgrade, since they would lose their investment in the type elements they had.

The element is called *DF-2 OCR* and the part number is 1167659. IBM's current price is $18 for one element, or $16 each for three or more.

IBM recommends that you use a Tech III ribbon (IBM number 1136391) with the DF-2 OCR element; the High-Yield Correctable Film carbon ribbon just doesn't make an adequate impression all the time. The DF-2 OCR is a 10-pitch element, by the way, so don't order it unless you have 10-pitch capacity. I would be interested in hearing from any readers who interface the HEDS-3000 to their computer and use this element to generate the input data.

Michael Essig
POB 828
Jackson MI 49204

Figure 1: *An example of the IBM DF-2 OCR output, using the High-Yield Correctable Film Ribbon.*

The IBM DF-2 Type element is a unique optical character recognition type face combining conventional characters with a bar-code to meet the requirements of the DATAFLOW Optical Reading System. IBM recommends the use of their TECH III ribbon to obtain the highest print quality.

ABCDEFGHIJKLMNOPQRSTUVWXYZ ><#$%Δ&*()•+ :';□ '!?,./

abcdefghijklmnopqrstuvwxyz 1234567890

PROBLEM.

INT(X^N+X*SIN(X^2),X)

SOLUTION.

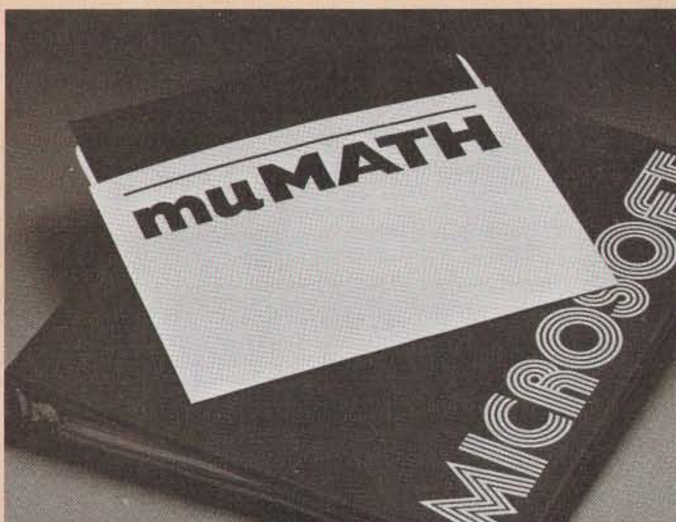
Surprised? You should be. Because until now, no software could solve anything but the most basic arithmetic problems without a series of complicated steps.

A big disadvantage? If you use a microcomputer for scientific, educational or engineering applications, you bet it is. That's why Microsoft has come up with a solution of its own. muMath.

muMath is a symbolic math package you'll recognize immediately as a major advance in microcomputer software.

muMath lets you efficiently and accurately perform the most complex mathematical operations: Exact, infinite precision rational arithmetic. Unbound variables. Complex expressions (even equations may be included). Exact solution of algebraic equations. Plus logarithmic, exponential and trigonometric simplifications and transformations.

That's right. It does in an instant what took you years



to learn at school. Rational arithmetic. Algebra. Trigonometry. Transcendental functions. Symbolic differentiation (including ordinary and partial derivatives.) Symbolic integration of indefinite and definite integrals. Matrix arithmetic and algebra.

Trigonometric simplification? But of course. Just type:

?SIN (2*Y)*(4*COS(X)^3
−COS(3*X)+SIN(Y)*COS
(X+Y+#P1)−COS(X−Y));

Then instantly muMath returns:

@4*SIN(Y)*COS(X)*COS(Y).

Adding fractions? Need you ask?

?1/3+5/6+2/5+3/7;
@419/210.

muMath is written in muSIMP, which is included in the muMath package.

muSIMP is an applicative, recursive language, ideal for describing complex mathematical concepts.

Because of its highly interactive nature and hierarchical structure, muMATH is an excellent math teaching device, from simple arithmetic to calculus.

muMATH is currently available for the CP/M® operating system.

The complete system, including muMATH and muSIMP on disk and documentation is $250. Runs under CP/M.

Just what you need? We thought so. Shoot some questions at us about muMATH. We have all the answers.

Also new from Microsoft: the muLISP interpreter for CP/M. An efficient and reliable LISP system fully capable of supporting serious artificial intelligence efforts. $200.

CP/M is a registered trademark of Digital Research.

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BYTE's BOMB Cards

*From the first year of BYTE to the present we have put great stock in your monthly comments that accompany BOMB (BYTE's Ongoing Monitor Box) cards. We really do read every one of them, and we are often influenced by your comments. What follows is a representative sampling from the cards over the past few issues. By the way, if you'd like to add your votes on this month's articles to our tally, simply fill out the BOMB card at the back of the magazine, using the article table on the second-to-last page as a guide....***CM**

Pournelle:

- *The User's Column* is a very good idea—keep on!
- Pournelle is great!
- More Pournelle please. I'm subscribing.
- Very interesting theme. No more Pournelle, please.
- [Pournelle wrote the] best article on TRS-80 since BYTE began.
- Are Pournelle's articles only to be semiregular? I vote for more.
- Pournelle alone will get me to subscribe.
- Pournelle has no finesse.
- Pournelle helped me decide between Radio Shack, Apple, and Atari... TRS-80 and Omikron here I come.
- Jerry Pournelle's column told me far more about TRS-80 add-ons than I have managed to learn in many weeks of searching.

Ciarcia:

- Mr Ciarcia has done it again.
- Don't lose Steve, he's worth his weight in gold!
- You should put two or three more Steve Ciarcias on the payroll.
- Ciarcia's article was excellent, but only Bo Derek gets a 10.

CAI:

- [I was] glad to have some really good info on CAI!
- There were too many articles on CAI.
- CAI makes as much sense as substituting computer-game playing for physical education. Education is achieved through dint of personal dedication and mental application of effort. Chrome-plated push-button gee-gaws cannot substitute for same.

Others:

- Excellent editorial.
- The editorial by Dr Braun rated a ten.
- Editorials should be rated.
- Your product description of the Apple III was terrific—and they say regular magazines can't get new products published quickly.
- I found the product description of the Apple III outstanding.
- Not being so good at hardware and "systems stuff," I found the July issue more readable than usual.
- Surprisingly, the standard of the July issue was exceptionally low.
- After I finish this BOMB card, I'm going to fill out the subscription form.
- The quality of articles in BYTE is slowly going downhill.
- [July was the] best overall issue of BYTE in a while!
- [July was] a rather dull issue—let's keep it on a professional level.
- Indeed you *are* starting to speak English instead of "highbrow."

How About...

- More hardware!
- More language-oriented articles!
- More homebrew articles!
- More on 16-bit processors!
- Emphasis on personal applications?
- Less educational material—more technical articles?
- Publishing "Favorite Benchmarks" as they come in.
- Publishing information about the Signetics 2650 microprocessor?

Coming up:

- I would like to see articles on homebrew graphics terminals.
- I would appreciate more articles on the new 16- and 32-bit microprocessors.
- I would very much like to see in-depth articles on speech recognition.
- When will you publish more articles on artificial intelligence?
- It would be nice if more articles could appear on fantasy games....

CP/M Vendors?

As the developers of CP/M and MP/M, we at Digital Research are preparing a list of vendors of CP/M-compatible software. We would appreciate the help of BYTE readers in compiling this list for distribution to all interested persons who contact us.

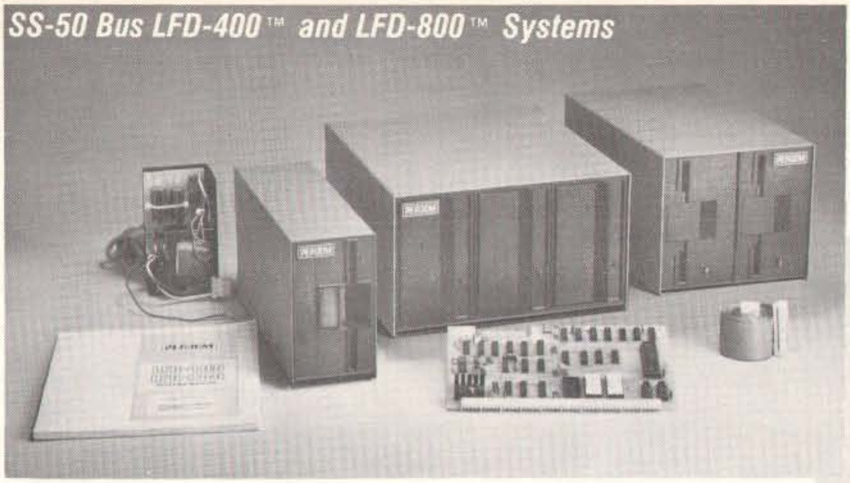
If you are currently marketing CP/M-compatible software, please send us any or all literature pertaining to your software. If you have any questions, please contact Curt Geske, at Digital Research, POB 579, Pacific Grove CA 93950, or (408) 649-3896.

Thank you.

Marilyn Darling
Digital Research

Circle 77 on inquiry card.

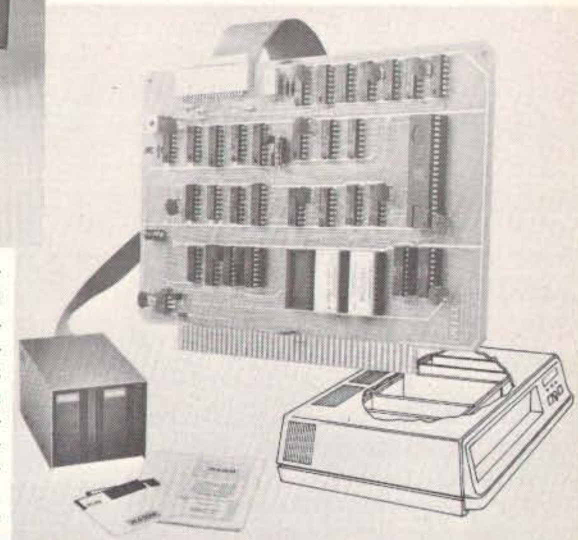
A Few Extraordinary Products for Your 6800/6809 Computer



SS-50 Bus LFD-400™ and LFD-800™ Systems

From Percom . . .

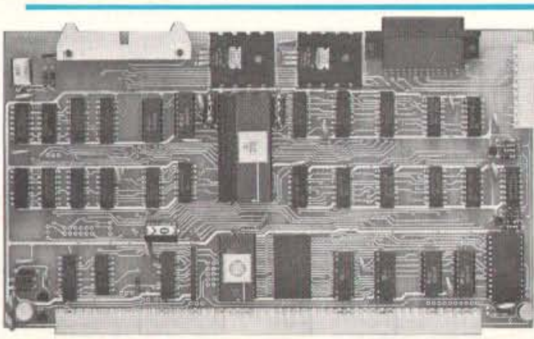
Low Cost Mini-Disk Storage in the Size You Want



EXORciser Bus LFD-400EX™ -800EX™ Systems*

Percom mini-disk systems start as low as $599.95, ready to plug in and run. You can't get better quality or a broader selection of disk software from any other microcomputer disk system manufacturer — at any price!

Features: 1-, 2- and 3-drive systems in 40- and 77-track versions store 102K- to 591K-bytes of random access data on-line • controllers include explicit clock/data separation circuit, motor inactivity time-out circuit, buffered control lines and other mature design concepts • ROM DOS included with SS-50 bus version — optional DOSs for EXORciser* bus • extra PROM sockets on-board • EXORciser* bus version has 1K-byte RAM • supported by extended disk operating systems; assemblers and other program development/debugging aids; BASIC, FORTRAN, Pascal and SPL/M languages; and, business application programs.



The SBC/9™. A "10" By Any Measure.

The Percom SBC/9™ is an SS-50 bus compatible, standalone Single-Board Computer. Configured for the 6809 microprocessor, the SBC/9™ also accommodates a 6802 without any modification. You can have state-of-the-art capability of the '09. Or put to work the enormous selection of 6800-coded programs that run on the '02.

The SBC/9™ includes PSYMON™, an easily extended 1-Kbyte ROM OS. Other features include:

- Total compatibility with the SS-50 bus. Requires no changes to the motherboard, memory or I/O.
- Serial port includes bit-rate generator. RS-232-C compatible with optional subminiature 'D' connector installed. 10-pin Molex connector provided.
- Eight-bit, non-latched, bidirectional parallel port is multi-address extension of system bus. Spans a 30-address field; accommodates an exceptional variety of peripheral devices. Connector is optional.
- Includes 1-Kbyte of static RAM.
- Costs only $199.95 with PSYMON™ and comprehensive users manual that includes source listing of PSYMON™.

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* trademark of the Motorola Corporation.
Prices and specifications subject to change without notice.

Versatile Mother Board, Full-Feature Prototyping Boards

Printed wiring is easily soldered tin-lead plating. Substrates are glass-epoxy. Prototyping cards provide for power regulators and distributed capacitor bypassing, accommodate 14-, 16-, 24- and 40-pin DIP sockets. Prototyping boards include bus connectors, other connectors and sockets are optional.

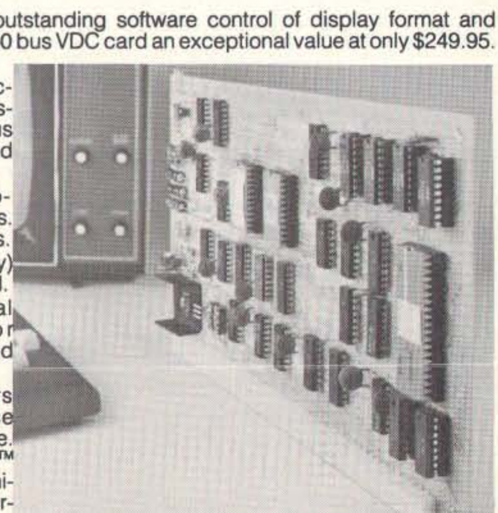
MOTHERBOARD — accommodates five SS-50 bus cards, and may itself be plugged into an SS-50 bus. Features wide-trace conductors. Price: $21.95

SS-50 BUS CARD — accommodates 34- and 50-pin ribbon connectors on top edge, 10-pin Molex connector on side edge. Price: $24.95.

SS-30 BUS CARD — 1¼-inch higher than SWTP I/O card, accommodates 34-pin ribbon connector and 12-pin Molex connector on top edge. Price: $14.95.

The Electric Window™: Instant, Real-Time Video Display Control

Memory residency and outstanding software control of display format and characters make this SS-50 bus VDC card an exceptional value at only $249.95. Other features:



- Generates 128 characters including all ASCII displayable characters plus selected Greek letters and other special symbols.
- Well-formed, easy-to-read 7x12-dot characters. True baseline descenders.
- Character-store (display) memory included on card.
- Provision for optional character generator EPROM for user defined symbols.
- Comprehensive users manual includes source listing of Driver software. Driver — called WINDEX™ — is also available on minidiskette through the Percom Users Group.

PERCOM

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Graphic Color Slides

Part 1

Alan W Grogono
Associate Professor
Department of Anesthesiology
Upstate Medical Center
State University of New York
750 E Adams St
Syracuse NY 13210

Writing the program that creates the screen image can be tedious and time-consuming.

Color slides of graphs, bar charts, and other visual aids are a valuable addition to various public presentations. When made using conventional methods, the slides are expensive to produce and difficult to modify. But when the slide is produced by photographing a computer-generated color image (as described in my article, "Making Color Slides with an Intecolor Microcomputer," January 1980 BYTE, page 20), the slide can be produced inexpensively and the image can be modified easily. Points, lines, bars, and curves can be drawn to represent numeric data.

Unfortunately, writing the program that creates the screen image can be tedious and time-consuming. Many aspects of the program design, such as the selection of suitable scales and the conversion from user-units to screen-units, can be done by the computer. The subroutines given here in listing 1 have been written to provide a common set of routines that can be used to generate different kinds of graphs on a Compucolor II computer with a minimum of effort.

Design Considerations

Ergonomic texts (ie: those that analyze human engineering factors) suggest that scales are most convenient for the user if they are subdivided in steps that are powers of ten—1, 10, 100, 0.1, 0.001, etc. Double- and half-size steps (2 and 0.5) are also acceptable for intermediate ranges, although other scale intervals (such as 0.75, 1.5, 3, 4) should be avoided. Based on this, I have written subroutines to select a suitable step size from the series: 0.1, 0.2, 0.5, 1, 2, 5, 10, 20, 50....

The ideal number of steps depends upon the application. On graph paper, where fine measurements may be made, a large number of smaller steps is useful. On a video monitor or in a color slide, however, a smaller number of large steps is preferable because it is less confusing; around four to eight steps seem to be appropriate. The scale should start and end at a multiple of the step size.

A program that satisfies these criteria should be easy to write; some readers might want to stop at this point and write their own. Unfortunately, there are several pitfalls for the unwary. At several stages of the calculation and graph preparation, it is necessary to avoid calculation errors (for example, producing 2.99999 or 3.00001 instead of 3). Similarly, scale zero might be calculated as 1.000E-06, which looks odd if printed on a graph scale.

The first step of the scaling process is to calculate the range of the data, R, and make an initial guess for the value of the step size, JUMP. This value can be obtained from table 1, or it can be calculated from the following equation:

```
JUMP = 4 * 10↑(INT(0.434295
         *LOG(R/1.21)))
```

(This is essentially line 10315 of the BASIC program in listing 1; the constant 0.434295 is used to obtain the base-10 logarithm from the Compucolor BASIC LOG function, which returns the natural or base-e logarithm.)

Once the initial value of JUMP has been calculated, it is repeatedly divided by 2 until the resulting value for JUMP is less than or equal to one-fourth the value of the range R; this assures that the graph will have at least four steps in the range. The constant 1.21 is chosen to give the relationship between R and JUMP shown in table 1.

Implementation Notes

The program has been written, tested, and employed to illustrate this article on a Compucolor II. The BASIC interpreter recognizes two-letter variable names but tolerates longer names (ie: AXIS, AXES and AX are all equivalent). Names were chosen to avoid BASIC reserved words such as INT, OR, ON, STEP. Thus the variable COL*OR* has been spelled COLOUR, and JUMP has been used in place of STEP. For graphics work this version of the language employs the word PLOT followed by one or more arguments. Table 2 lists the more important plotting codes.

Text continued on page 138

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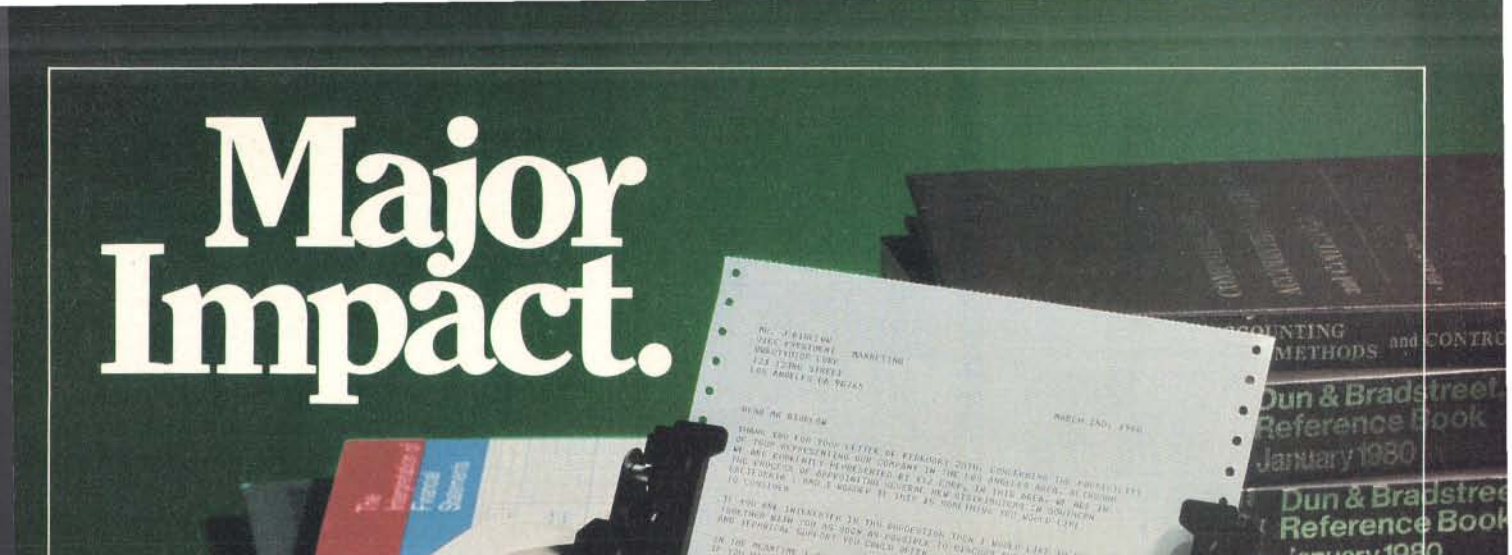
Listing 1: *Collection of plotting subroutines and driver program for the Compucolor II. See text and listing remarks for further description of the subroutines.*

```
5 REM KY 5 REM      GRAPHS. (C)  A. W. GROGONO.  AUG. 1979
6 REM       SUBROUTINES V1
40 RESTORE :CLEAR 200:DIM I$(12)
50 DATA 1, 2, 6, 4:FOR I= 1TO 4:READ COLOUR(I):NEXT I
60 REM WRITE:  60 DIM(ARRAY(25, 1))  TO USE EQUATION SUB
90 PLOT 29, 27, 24, 15, 14, 2, 255, 6, 1, 12, 3, 16, 3:REM CLEAR PAGE
100 REM
101 REM
110 REM  SUBROUTINES  7000 ERASE/REVIEW IMAGES
120 REM                9000 COMPLETE GRAPH OUTLINE
130 REM               10000 DATA ENTRY
140 REM               10100 EQUATION PLOTTING
150 REM               10200 FIND LITTLE AND BIG
160 REM               10300 CALCULATE DATA FOR BORDERS
170 REM               10500 DRAW BORDERS
180 REM               10700 CONVERT USER UNITS TO GRAPH
190 REM               10800 GRAPH UNITS TO TEXT POSITION
200 REM               11000 PLOT POINTS
210 REM               11100 PLOT VECTORS
220 REM               11200 PLOT Y-BARS
230 REM               11300 PLOT X-BARS
235 REM               11500 SAVE ON DISK
240 REM               11800 SELECT COLORS
250 REM               11900 PAUSE
260 REM
270 END
490 REM WRITE EQUATION AT 500, EG: 500 Y= X^2 - 3* X
510 RETURN
6900 REM
6901 REM
6902 REM  ERASE/REVIEW IMAGES
6903 REM
7000 PLOT 2, 255, 27, 24, 6, 11, 14, 12, 3, 11, 7:REM IMAGE ERASE/REVIEW
7005 FOR I= 1TO 12:I$(I)= CHR$ (48+ I- 7* (I> 9)):NEXT I
7010 PRINT "E R A S E / R E V I E W   I M A G E S":PRINT
7020 PRINT ,, "1.  REVIEW IMAGES. ":PRINT
7030 PRINT ,,:INPUT "2.  ERASE IMAGES.  ENTER NUMBER: "; I
7040 IF I= 2THEN 7100
7050 I$= "REVIEWED":GOSUB 7200
7060 FOR I= LOWTO HIGH:PLOT 3, 64, 29, 27, 4:REM LOSE CURSOR
7070 PRINT "LOAD SCREEN. DIS; "+ I$(I):PLOT 27, 27:REM IMAGE
7080 INPUT ""; I$:NEXT I:RETURN
7100 I$= "ERASED":GOSUB 7200
7110 PLOT 27, 4:FOR I= HIGHTO LOWSTEP - 1
7120 PRINT "DEL SCREEN. DIS; "+ I$(I):NEXT I
7130 PLOT 27, 27:PRINT "IGNORE FCS ERROR - EFNF";
7140 PRINT " DURING RENAMING":PLOT 17, 10, 27, 4
7150 J= HIGH- LOW+ 1:FOR I= LOWTO 12- J:REM CLOSE GAP
7160 PRINT "REN SCREEN. DIS; "+ I$(I+ J)+ "TO SCREEN. DIS; "+ I$(I)
7180 NEXT I:PLOT 27, 27:RETURN
7200 PLOT 6, 5* I- 4, 12, 27, 4:PRINT "DIR":REM DIRECTORY
7210 PLOT 27, 27:PRINT , "IMAGES ARE LISTED  SCREEN. DIS; N  ";
7220 PRINT "WHERE N IS THE NUMBER. ":PRINT
7230 PRINT , "ENTER #S OF FIRST AND LAST IMAGES TO BE "; I$; ":"
7235 PRINT :PRINT ,, "FOR A ENTER 10, FOR B ENTER 11 ETC. "
7240 PRINT :PRINT ,,:INPUT "FIRST "; LOW:REM
7250 PRINT :PRINT ,,:INPUT " LAST "; HIGH:REM
7260 PRINT :PRINT ,,:INPUT "PUSH RETURN TO ADVANCE"; I$:RETURN
```

Listing 1 continued on page 130



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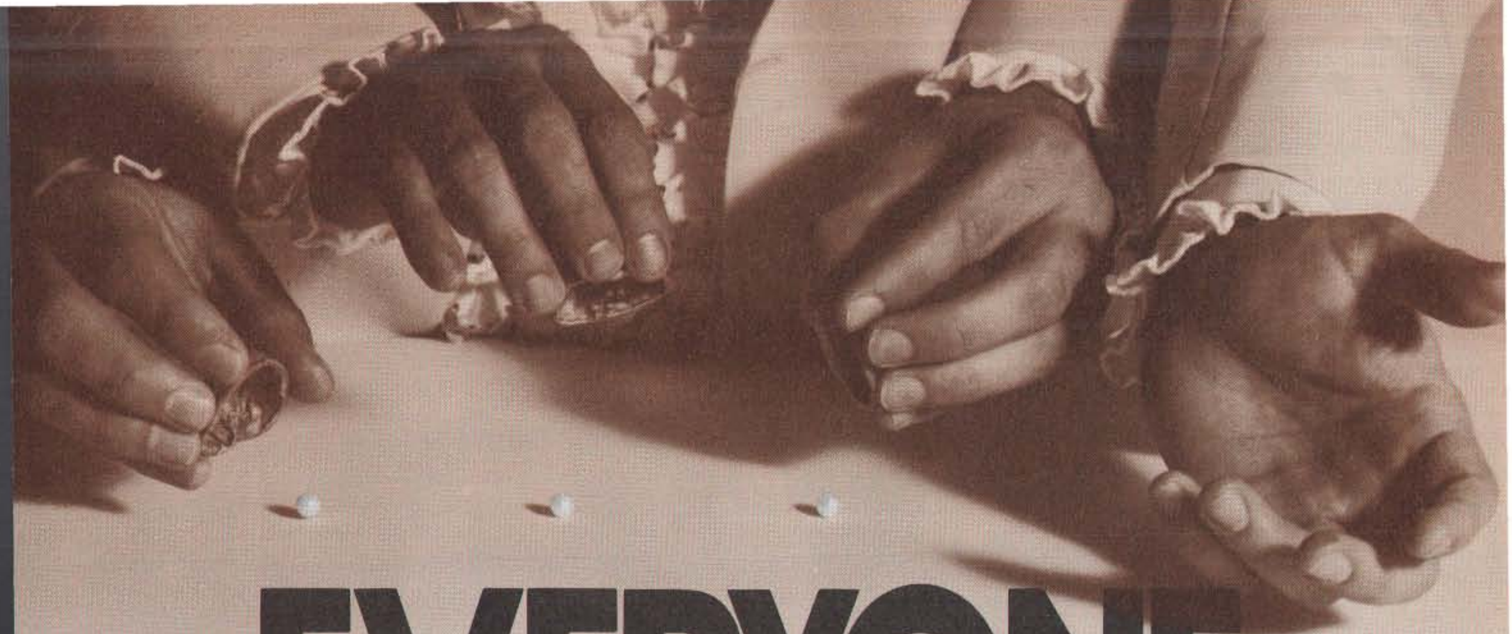
Listing 1 continued:

```
8988 REM
8989 REM
8990 REM   PREPARE COMPLETE GRAPH OUTLINE
8991 REM
8992 REM               CALCULATES LIMITS, SCALE VALUES AND
8993 REM               DRAWS OUTLINE WITH TIC MARKS, SCALES,
8994 REM               TITLE AND AXES LABELS
8995 REM
9000 REM GRAPH OUTLINE
9010 GOSUB 10200:REM DATA RANGE
9020 GOSUB 10300:REM AUTOSCALE
9030 GOSUB 10500:RETURN :REM FRAME
9980 REM
9981 REM
9982 REM   ENTER:
9983 REM
9984 REM                 TITLE$
9985 REM                 NUMBER  OF DATA POINTS
9986 REM                 LABEL$(0)  FOR X-AXIS
9987 REM                 LABEL$(1)  FOR Y-AXIS
9988 REM                 ARRAY(NUMBER, 2)  OF DATA POINTS
9989 REM
9990 REM    NOTE: IF  CHOICE  = 1 THEN ONLY 1 AXIS IS ENTERED
9991 REM
10000 PLOT 6,1,12,14,3,18,13:REM DATA ENTRY
10010 PRINT "D A T A   E N T R Y"
10015 PLOT 10,9,9:INPUT "GRAPH TITLE: ";TITLE$
10020 PLOT 10,9,9:INPUT "NUMBER OF DATA POINTS: ";NUMBER
10021 DIM ARRAY(NUMBER+ 2,2)
10024 PLOT 10,9,9:INPUT "X-AXIS UNITS, INDEPENDANT: ";LABEL$(0)
10025 IF CHOICE= 1THEN LABEL$(1)= "NUMBER":GOTO 10030
10026 PLOT 10,9,9:INPUT "Y-AXIS UNITS,   DEPENDANT: ";LABEL$(1)
10028 LABEL$(2)= LABEL$(1)
10030 FOR ITEM= 1TO NUMBER:REM ENTER POINTS
10040 IF ITEM- 1< > 10* INT ((ITEM- 1)/ 10)THEN 10060:REM PAGE
10050 PLOT 12,10,10:PRINT "POINT",,LABEL$(0):REM
10055 IF CHOICE< > 1THEN PLOT 28:PRINT ,,,,"";LABEL$(1)
10060 IF ITEM- 1= 5* INT ((ITEM- 1)/ 5)THEN PLOT 10:REM SPACE
10070 PRINT :PRINT "";ITEM,,:INPUT "";ARRAY(ITEM,0):REM
10075 IF CHOICE= 1THEN NEXT ITEM:RETURN
10080 PLOT 28,18,9,9,9,9:INPUT "";ARRAY(ITEM,1)
10085 ARRAY(ITEM,2)= ARRAY(ITEM,1):NEXT ITEM:RETURN
10090 REM
10091 REM
10092 REM   WRITE EQUATION
10093 REM
10094 REM              TESTS IS THE EQUATION WRITTEN
10095 REM              INPUT  LITTLE(0)
10096 REM              INPUT  BIG(0)
10097 REM              CALCULATES  ARRAY(25,2)  FROM EQUATION
10098 REM
10100 PLOT 6,5,14,12,3,12,7:REM EQUATION PLOTTING
10110 PRINT "E Q U A T I O N   P L O T T I N G":PRINT :REM
10120 NUMBER= 25:X= 1:Y= .9999:GOSUB 490
10130 IF Y< > .9999THEN 10140:REM JUMP IF EQUATION AT LINE 500
10132 PLOT 3,16,11:PRINT "TYPE EQUATION AT LINE 500":PRINT
10133 PRINT ,,"USING THE RULES OF BASIC. ":PRINT :PRINT
10134 PRINT ,,"EXAMPLE:  500 Y=X^2-3*X":PRINT :REM
10135 PRINT ,,"NOW TYPE  500 ...... ":PRINT
10136 PRINT ,,"THEN TYPE  RUN  AND PRESS  RETURN":END
```

Listing 1 continued on page 132



Listing 1 continued:

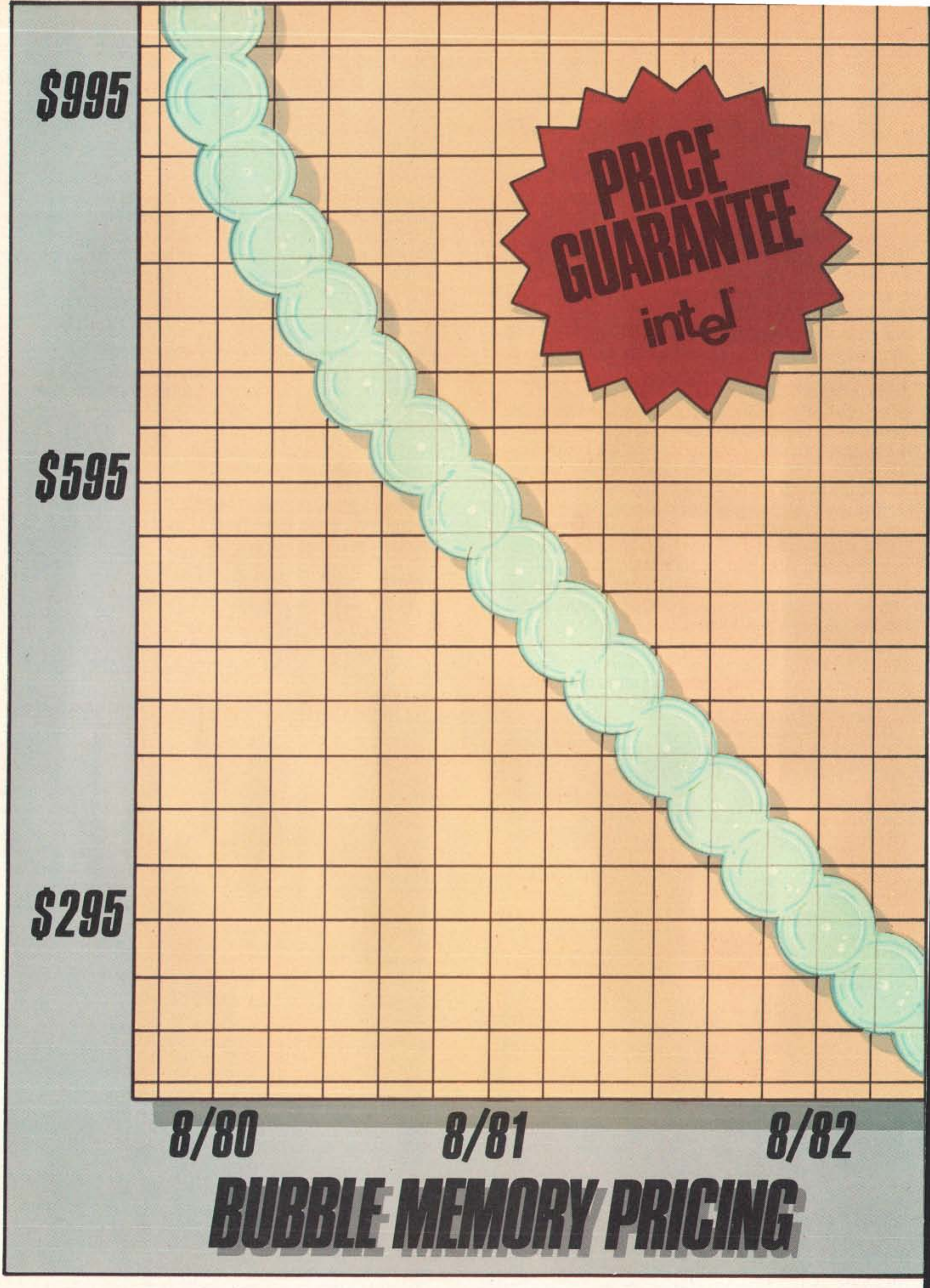
```
10140 IF TITLE$< > ""THEN 10145
10142 PRINT :PRINT ,,"ENTER TITLE (E.G: EQUATION): "
10143 PRINT :PRINT ,,:INPUT "";TITLE$
10145 PRINT :PRINT ,,:INPUT "ENTER LOWEST X VALUE: ";LITTLE(0)
10150 PRINT :PRINT ,,:INPUT "      HIGHEST X VALUE: ";BIG(0)
10160 X= LITTLE(0):FOR ITEM= 1TO 25:GOSUB 490:REM Y FROM EQUAN
10170 ARRAY(ITEM,0)= X:ARRAY(ITEM,1)= Y
10180 X= X+ (BIG(0)- LITTLE(0))/ 24:NEXT ITEM:RETURN :REM INC X
10190 REM
10191 REM
10192 REM   FIND LITTLE(AXIS) AND BIG(AXIS)
10193 REM             FROM  ARRAY(NUMBER,1) IN BOTH AXES
10194 REM
10200 FOR AXIS= 0TO 1:GOSUB 10210:NEXT AXIS:RETURN :REM LO, HI
10210 LITTLE(AXIS)= ARRAY(1,AXIS):BIG(AXIS)= ARRAY(1,AXIS)
10215 FOR ITEM= 1TO NUMBER
10220 IF ARRAY(ITEM,AXIS)> LITTLE(AXIS)THEN 10230
10225 LITTLE(AXIS)= ARRAY(ITEM,AXIS)
10230 IF ARRAY(ITEM,AXIS)< BIG(AXIS)THEN 10240
10235 BIG(AXIS)= ARRAY(ITEM,AXIS)
10240 NEXT ITEM:RETURN
10288 REM
10289 REM
10290 REM   CALCULATE FRAME FROM LITTLE(AXIS) AND BIG(AXIS)
10291 REM
10292 REM             JUMP(AXIS)   IS STEP LENGTH
10293 REM             LOW(AXIS)    IS SCALE LOW
10294 REM             HIGH(AXIS)   IS SCALE HIGH
10295 REM             SCALE(AXIS)  IS SCALE LENGTH
10296 REM             GAPS(AXIS)   IS NUMBER OF STEPS
10297 REM
10300 FOR AXIS= 0TO 1:GOSUB 10310:NEXT AXIS:RETURN :REM SCALE
10310 RANGE= (BIG(AXIS)- LITTLE(AXIS))/ 1.21
10315 JUMP(AXIS)= 4* 10^ (INT (.434295* LOG (RANGE)))
10320 DEF FN I(I)= JUMP(AXIS)* INT (I/ JUMP(AXIS)+ .0001)
10325 FOR I= 1TO 3:JUMP(AXIS)= JUMP(AXIS)/ 2
10330 HIGH(AXIS)= - FN I(- BIG(AXIS))
10340 LOW(AXIS)= FN I(LITTLE(AXIS))
10350 SCALE(AXIS)= HIGH(AXIS)- LOW(AXIS)
10360 GAPS(AXIS)= INT (1.0001* SCALE(AXIS)/ JUMP(AXIS))
10370 IF GAPS(AXIS)< 4THEN NEXT I
10380 EVEN= 2* JUMP(AXIS)* INT (- SCALE(AXIS)/ JUMP(AXIS)/ 2.1)
10390 HIGH(AXIS)= LOW(AXIS)- EVEN
10395 SCALE(AXIS)= HIGH(AXIS)- LOW(AXIS):RETURN
10480 REM
10481 REM
10482 REM   DRAW BORDERS WITH SCALES AND TITLES
10483 REM
10484 REM             USER MAY ALTER
10485 REM             MINSCREEN(AXIS)  AND  MAXSCREEN(AXIS) BUT
10486 REM             SELECT VALUES TO MAKE
10487 REM             RANGE  A MULTIPLE OF 24.  ALSO:
10489 REM
10490 REM             IN 0 AXIS VALUES MUST BE MULTIPLES OF 2
10491 REM             IN 1 AXIS VALUES MUST BE MULTIPLES OF 4
10492 REM
10493 REM             RATIO(AXIS)  IS CALCULATED FROM
10494 REM             RANGE  AND  SCALE(AXIS)
10495 REM
```

Listing 1 continued on page 134

Listing 1 continued:

```
10496 REM              PLACE  IS CALCULATED FOR
10497 REM              TIC MARKS AND SCALE NUMBERS
10498 REM
10500 PLOT 2,255,27,24,29,15,6,COLOUR(1),12:REM DRAW FRAME
10505 MINSCREEN(0)= 18:MAXSCREEN(0)= 114
10510 MINSCREEN(1)= 16:MAXSCREEN(1)= 112
10515 FOR AXIS= 0TO 1:RANGE= MAXSCREEN(AXIS)- MINSCREEN(AXIS)
10520 RATIO(AXIS)= RANGE/ SCALE(AXIS):NEXT AXIS
10522 PLOT 3,(MAXSCREEN(0)+ MINSCREEN(0))/ 4- LEN (TITLE$)/ 2
10523 PLOT 29- MAXSCREEN(1)/ 4:PRINT TITLE$
10525 FOR AXIS= 0TO 1
10530 PLOT 6,COLOUR(1),2,250- 4* AXIS,MINSCREEN(AXIS)- 1
10540 PLOT MINSCREEN(1- AXIS)- 1
10545 PLOT MAXSCREEN(AXIS)+ 2- 2* (AXIS= 1)
10550 PLOT MAXSCREEN(1- AXIS)+ 2- 2* (AXIS= 0)
10555 PLOT MAXSCREEN(AXIS)+ 2- 2* (AXIS= 1),255
10560 J= JUMP(AXIS)/ 2
10565 FOR PLACE= LOW(AXIS)TO HIGH(AXIS)+ JSTEP JUMP(AXIS)
10570 GOSUB 10700:REM TIC MARKS
10580 GRAPH(1- AXIS)= MINSCREEN(1- AXIS)- 2:REM OUTSIDE FRAME
10590 PLOT 6,COLOUR(1):GOSUB 11010
10600 PLOT 6,COLOUR(2):REM NUMBERS
10620 IF ABS (PLACE)< JUMP(AXIS)/ 2THEN PLACE= 0:REM NO EXPON
10630 GRAPH(1- AXIS)= MINSCREEN(1- AXIS)- 8+ 4* AXIS
10640 GOSUB 10800:PLACE$= STR$ (PLACE)
10650 PLOT 3,TEXT(0)- LEN (PLACE$)/ (2- AXIS),TEXT(1)
10660 PRINT PLACE$:NEXT PLACE:NEXT AXIS
10662 PLOT 3,MAXSCREEN(0)/ 2- 4- LEN (LABEL$(0))
10664 PLOT 34- MINSCREEN(1)/ 4:PRINT LABEL$(0)
10666 PLOT 3,MINSCREEN(0)/ 2- 6,29- MAXSCREEN(1)/ 4
10670 PRINT LABEL$(1):RETURN
10688 REM
10689 REM
10690 REM   CALCULATE SCREEN GRAPH POSITION
10691 REM
10692 REM              CONVERTS  PLACE  IN USER UNITS
10693 REM              TO  GRAPH(AXIS)  FROM
10694 REM              RATIO(AXIS), LOW(AXIS), MINSCREEN(AXIS)
10695 REM
10700 J= RATIO(AXIS)* (PLACE- LOW(AXIS)):REM CONVERT USER UNITS
10710 GRAPH(AXIS)= MINSCREEN(AXIS)+ J+ .0001:RETURN
10790 REM
10791 REM
10792 REM   CALCULATE SCREEN TEXT POSITION
10793 REM
10794 REM              CONVERTS  GRAPH(AXIS)  PLOTTING UNITS
10795 REM              TO  TEXT(AXIS)  FOR CURSOR POSITION
10796 REM
10800 TEXT(0)= GRAPH(0)/ 2:REM GRAPH UNITS TO CURSOR POS
10810 TEXT(1)= INT (31.75- GRAPH(1)/ 4):RETURN
10988 REM
10989 REM
10990 REM   PLOT POINTS OR LINES
10991 REM
10992 REM              ARRAY(NUMBER,1)  IS PLOTTED EITHER
10993 REM              AS POINTS OR AS CONTINUOUS LINE
10994 REM
11000 FLAG= 1:GOSUB 11150:RETURN :REM POINTS
11010 PLOT 2,GRAPH(0),GRAPH(1),255:RETURN :REM POINT
```

Listing 1 continued on page 138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With Intel's solid-state bubble memory, all that moves is the information. That means high reliability and low maintenance for your products, even in harsh or unclean environments—the kind where disks and tapes won't go. And since the memory is completely nonvolatile, your data

remains secure when the power goes off. No battery backup or replacement is necessary.

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Circle 85 on inquiry card.

Listing 1 continued:

```
11020 PLOT 2,242,GRAPH(0),GRAPH(1),255:RETURN :REM VECTOR
11100 FLAG= 0:GOSUB 11150:RETURN :REM VECTORS
11150 PLOT 6,COLOUR(3):FOR ITEM= 1TO NUMBER:FOR AXIS= 0TO 1
11160 PLACE= ARRAY(ITEM,AXIS):GOSUB 10700:NEXT AXIS
11170 ON 2+ (ITEM= 1OR FLAG= 1)GOSUB 11010,11020
11180 NEXT ITEM:RETURN
11188 REM
11189 REM
11190 REM   PLOT BAR GRAPHS
11191 REM
11192 REM              ARRAY(NUMBER,1)  IS PLOTTED EITHER
11193 REM              AS VERTICAL OR AS HORIZONTAL BARS
11194 REM
11200 FLAG= 1:GOSUB 11310:RETURN :REM Y-BAR
11300 FLAG= 0:GOSUB 11310:RETURN :REM X-BAR
11310 COLOUR= 2:FOR ITEM= 1TO NUMBER
11320 COLOUR= COLOUR+ 1+ 2* (COLOUR= 4):PLOT 6,COLOUR(COLOUR)
11330 FOR AXIS= 0TO 1:PLACE= ARRAY(ITEM,AXIS)
11340 GOSUB 10700:NEXT AXIS
11350 PLOT 2,250- FLAG* 4,MINSCREEN(FLAG):REM X OR Y BAR
11360 FOR I= GRAPH(1- FLAG)TO GRAPH(1- FLAG)+ 1
11370 PLOT I,GRAPH(FLAG):NEXT I:PLOT 255:NEXT ITEM:RETURN
11490 REM
11491 REM
11492 REM   SAVE IMAGES ON DISK
11493 REM
11494 REM              IMAGES SAVED AS  SCREEN.DIS
11495 REM
```

Listing 1 continued on page 140

Photo 1: *Variation of text height and color. Both text height and color can be changed under program control.*

Text continued from page 126:

Subscripts for array variables commence at 0. In consequence, if NUMBER = 25 and AXES = 1, then the BASIC statement DIM ARRAY (NUMBER, AXES) will define an array with dimensions 26 and 2.

Values of 0 or −1 are assigned to results of logical operations: 0 for false and −1 for true. This poperty is used in line 11170 of listing 1.

It is also possible to change the height and color of displayed text (as shown in photo 1); this is done occasionally within the body of the program in listing 1.

The Subroutines

Listing 1 contains the subroutines that together can be used to produce a graph on the color video-display screen. Subscripted variables, when used with a subscript of 0, refer to some horizontal component of the graph; a subscript of 1 refers to some vertical component of the graph. Certain calculation subroutines (for example, 10200 and 10300) can be accessed at a line ending in "00" to perform calculations for both the X and Y axes, or they can be accessed at the corresponding line ending in "10" to calculate for only one axis.

Some of the more important subroutines are described briefly in the paragraphs that follow:

- 7000—Review or erase images; this subroutine enables graphs stored on disk to be reviewed (displayed) or erased from the disk.
- 9000—Prepare complete graph outline; this subroutine consists of three subroutines that examine the data and draw the appropriate graph frame (see also subroutines 10200, 10300, and 10500).
- 10000—Data entry; the title of the graph, the axes' labels, and data

Listing 1 continued:

```
11500 PLOT 6,COLOUR(2),3,0,31,11,3,13,31:REM SAVE ON DISK
11510 INPUT "ENTER S TO SAVE, OR PRESS RETURN: ";I$:PLOT 28,11
11520 IF I$< > "S"THEN 11540
11530 PLOT 27,4:PRINT "SAVE SCREEN.DIS 6000-6FFF":PLOT 27,27
11540 RETURN
11780 REM
11781 REM
11782 REM   SELECT COLORS
11783 REM
11784 REM                COLOUR(1)   FRAME
11785 REM                COLOUR(2)   SCALE
11786 REM                COLOUR(3)   GRAPH 1
11787 REM                COLOUR(4)   GRAPH 2
11788 REM
11800 PLOT 6,4,3,0,31,11,3,16,31:REM COLOR SELECTION
11802 INPUT "ENTER C TO CHANGE COLOR: ";K$
11804 PLOT 6,COLOUR(2),3,0,31,11:IF K$< > "C"THEN RETURN
11806 PLOT 6,38,12,3,23,7,14:PRINT "COLOR SELECTION"
11810 PRINT :PRINT ,,:INPUT "TOUCH COLOR FOR BACKGROUND: ";I$
11820 I= (ASC (I$)- 16)* 8:PLOT 6,I,12,3,16,11:REM BKD
11830 PLOT 6,I/ 8* 9+ 2+ 4* (I> 40)
11840 DATA "FRAME","SCALES","GRAPH1","GRAPH2":RESTORE 11840
11850 FOR J= 1TO 4:READ I$:PLOT 3,16,9+ 2* J:PRINT "FOR ";I$;
11860 INPUT "";J$:COLOUR(J)= I+ ASC (J$)- 16
11870 PLOT 6,COLOUR(J),3,32,9+ 2* J:PRINT I$:NEXT J:RETURN
11890 REM
11891 REM
11892 REM   PAUSE
11893 REM
11894 REM                "PRESS RETURN TO CONTINUE"
11895 REM                BLINKS BRIEFLY AT BOTTOM OF GRAPH
11896 REM
11900 PLOT 6,COLOUR(1),31,3,18,31:REM PAUSE
11910 PRINT "PRESS RETURN TO CONTINUE":FOR I= 1TO 1000:NEXT I
11920 PLOT 15,3,0,31,11:INPUT "";I$:RETURN
```

Range of Values, R, to Be Plotted	Initial Value for JUMP
0.121 ≤ R < 1.21	0.4
1.21 ≤ R < 12.1	4.0
12.1 ≤ R < 121	40.0
121 ≤ R < 1210	400.0
1210 ≤ R < 12100	4000.0

Table 1: *Initial value for step size (JUMP) given the range (R) of the variable to be plotted. The table can be continued in both directions by either multiplying or dividing all the numbers in a line by 10. Once the initial value for JUMP is found, it is repeatedly divided by 2 until the step size used subdivides the range into at least four intervals—that is, until JUMP ≤ (R/4).*

PLOT 2	Enter graph-plotting mode
PLOT 2, X, Y	Point at X,Y
PLOT 2, 242, X, Y	Vector to X,Y
PLOT 2, 250, X0, Y, XM	Horizontal bar at Y from X0 to XM
PLOT 2, 246, Y0, X, YM	Vertical bar at X from Y0 to YM
PLOT 3, T, L	Cursor to tab T at line L
PLOT 6, C	Defines the color of both the foreground and background
PLOT 8	Cursor to home
PLOT 9	Tab 8 spaces
PLOT 10	Line feed (move cursor down one line)
PLOT 11	Erase line
PLOT 12	Erase page
PLOT 14	Double-height text
PLOT 15	Normal-height text, with blink mode off
PLOT 16 thru PLOT 23	Changes color of foreground or background (whichever is active)
PLOT 27, 4: PRINT "[disk commands]":	
PLOT 27, 27	Execute floppy-disk command
PLOT 27, 10	Write text vertically
PLOT 27, 24	Write text horizontally
PLOT 28	Cursor up
PLOT 29	Enable background color
PLOT 31	Blink on
PLOT 255	Cancel graph-plotting mode

Table 2: *Table of plot codes in Compucolor BASIC. Many functions associated with the color video-display screen are achieved by the use of the PLOT command. The table of PLOT commands here includes all those used in listings 1 and 2.*

are entered in this subroutine. Certain applications (eg: histograms) require only one set of data to be entered. If CHOICE=1, then the subroutine fills only ARRAY (*n*,1), that is, the data entries are placed in ARRAY (0,0), ARRAY (1,0), ARRAY (2,0), and so on. If CHOICE is not equal to 1, then this subroutine expects two sets of data to be entered, filling both ARRAY (*n*,0) and ARRAY (*n*,1). The Y-axis data is duplicated in a third column, ARRAY (*n*,2), thus allowing this data to be manipulated later without being destroyed.

- 10100—Equation plotting; this subroutine tests to see that no equation exists, then invites the user to write an equation at line 500. The equation takes the form Y= (some arithmetic expression using X). Once the equation exists, the subroutine asks for a title and the X-axis limits. The program then uses the equation to calculate twenty-five equidistant data points to fill ARRAY (*n*,1).
- 10200—Find big and little; this subroutine determines the largest and smallest values for the data and stores them in arrays BIG (*n*) and LITTLE (*n*).
- 10300—Prepare values for frame; the step size (JUMP) is calculated in accordance with the constraints described above. This value is used to determine the HIGH and LOW values for the scale. GAPS is the number of JUMPS in the length of the axis (variable SCALE).
- 10500—Draw borders with scales and titles; this subroutine draws

Listing 2: *Demonstration program for the subroutines of listing 1. This short program, when added to the program in listing 1, allows the user to make a graph of a collection of points, an equation, or a series of vertical bars.*

```
5 REM KY 5 REM      GRAPHS. (C)  A. W. GROGONO.  AUG. 1979
6 REM      DEMONSTATION PROGRAM FOR USE WITH SUBROUTINES
40 RESTORE :CLEAR 200:DIM I$(12)
50 DATA 1,2,6,4:FOR I= 1TO 4:READ COLOUR(I):NEXT I
90 PLOT 29,27,24,15,14,2,255,6,1,12,3,16,3:REM CLEAR PAGE
280 REM
290 REM
300 PRINT "S E L E C T   G R A P H   T Y P E:":PRINT
310 PRINT :PRINT ,,"1.  X/Y SCATTER"
320 PRINT :PRINT ,,"2.  PLOT EQUATION"
330 PRINT :PRINT ,,"3.  Y-BAR GRAPH"
340 PRINT :PRINT ,,,:INPUT "ENTER 1 - 3: ";K:PLOT 28,11
350 IF K< 1OR K> 3THEN 340
360 IF K< > 2THEN 390
370 RESTORE :CLEAR 200:FOR I= 1TO 4:READ COLOUR(I):NEXT I
380 K= 2:DIM ARRAY(25,1):REM DIMENSIONS FOR EQUATION
390 ON KGOSUB 10000,10100,10000:REM PREPARE DATA ARRAY
400 GOSUB 9000:REM FRAME
410 ON KGOSUB 11000,11100,11200:REM SCATTER, LINE, Y-BARS
420 GOSUB 11900:REM PAUSE
430 GOSUB 11500:REM SAVE
440 GOSUB 11800:REM SELECT COLORS
450 IF K$= "C"THEN 400
460 GOTO 5
```

the borders for the graph with its scales, labels, and title. The length of each number or word is employed to ensure appropriate positioning. The value of RATIO, calculated here, is used in the subroutine at line 10700.

- 10700—Convert units to screen; a value on one of the axes (in variable PLACE) is converted to its corresponding screen position (stored in variable GRAPH).
- 10800-Converts units for text position; a screen position variable, GRAPH, is converted to its corresponding cursor position and stored in variable TEXT.
- 11000 and 11100—Plot points or lines; the data points in ARRAY are plotted as separate points (11000) or as points joined by lines (11100).
- 11200 and 11300—Plot Y-bars or X-bars; the quantities in ARRAY are plotted as vertical (11200) or as horizontal bars (11300).
- 11500—Save image on disk; this subroutine transfers the finished graph to disk for recall later.
- 11800—Select colors; the colors for the background, frame, scales, and graphs are selected with this routine.
- 11900—Pause; this subroutine causes the words "PRESS RETURN TO CONTINUE" to flash briefly beneath the graph.

A Demonstration Program

The program in listing 2 was written to demonstrate the color-graphics subroutines. Graph type 1 allows data to be entered and displayed as separate points. The program initially selects the colors shown in photo 2a, but the user can select his own colors, as shown in photo 2b.

Photos 3a and 3b illustrate the use of the equation-plotting subroutine, graph type 2. Photo 3a shows the program colors for the first range selected (−2 to +2); photo 3b shows a different set of colors selected by the user for the longer range (−4 to +4). Photo 4a shows how a variable, such as income, can be displayed as a Y-bar, as an example of graph type 3. Photos 4b and 4c show the same data using different colors selected by the user.

The brevity of listing 2 shows that minimal program writing is required to produce these graphs. In fact, if only one type of graph is required

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(eg: points joined by lines), then the total program would be:

```
300 GOSUB 10000 : REM DATA
      ENTRY
310 GOSUB  9000 : REM FRAME
320 GOSUB 11100 : REM PLOT
      LINES
330 GOSUB 11900 : REM PAUSE
340 END
```

Of course, this assumes the presence of the subroutines given in listing 1.

In such a program and in the demonstration program, the X-axis and Y-axis graph scales are determined automatically by the program except where the user selects the X-axis limits for the equation.

Summary

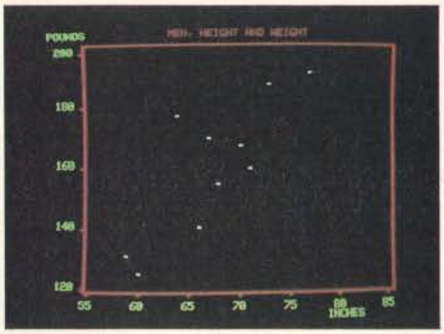
The subroutines in listing 1 were written to illustrate the principles used in determining neat graph scales, and emphasis has been placed on these calculations. The frame is drawn just outside the area in which points will be graphed. This avoids the problem of graphing points that lie directly on the frame; it also avoids the possibility of the color for a nearby graph point spilling onto the frame. The program generates an even number of scale increments for each axis; this ensures uniform spacing of both tick marks and numbers. Colors are critical when the screen is being photographed; light colors on dark backgrounds show up best (this is discussed in detail in my previous article in the January 1980 BYTE).

These subroutines can be used in many graphics applications. As written, they employ two-letter names as well as the variables X, Y, I, J, K, I$, J$, and K$. This allows the user all the remaining single letters. If the user's program defines NUMBER (number of points) and fills ARRAY with the appropriate data, then the subroutines in listing 1 can be used to generate a graph. The graph will be labeled as well if the user defines the variables TITLE$, LABEL$(0), and LABEL$(1).

The photographs used to illustrate this article have been created using a Compucolor II with 16 K bytes of user memory but without the Pincushion Correction Kit. The barrel distortion on the top and bottom can be reduced by using a telephoto lens, but the pincushion effect on each side will then be worse unless the correction kit is installed.

Next month, Part 2 of this article will use the subroutines given here to construct several other kinds of graphs: a different kind of equation-plotting routine, a histogram with the equivalent Gaussian (bell-shaped) curve superimposed, linear and other kinds of regression plotting, and a monthly analysis graph of more than one variable. ■

2a



2b

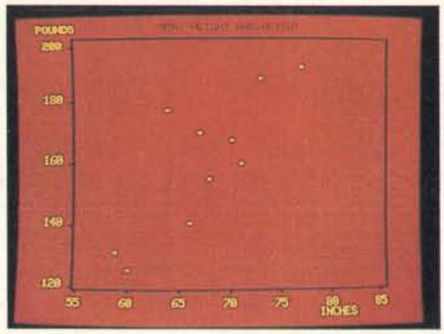
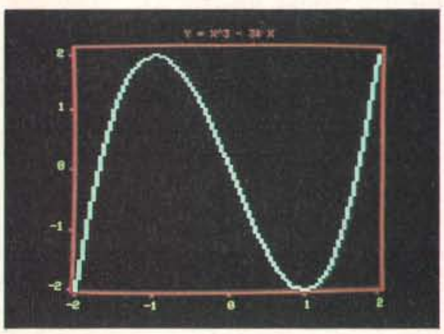


Photo 2: *Examples of point-plotting mode. The computer automatically chooses the colors of photo 2a, but the user can override this to select any other color combination, as in photo 2b. The slight "pincushion" effect can be eliminated by the addition of a corrective kit supplied by Compucolor.*

3a



3b

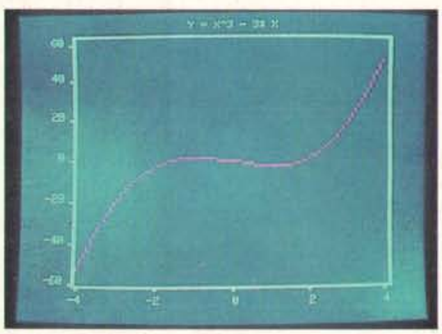
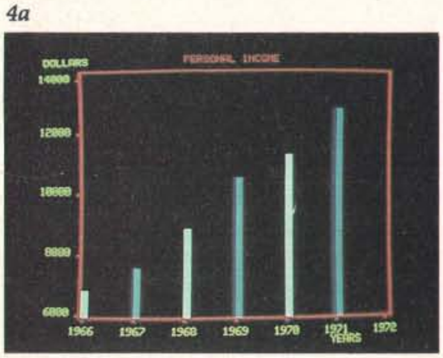
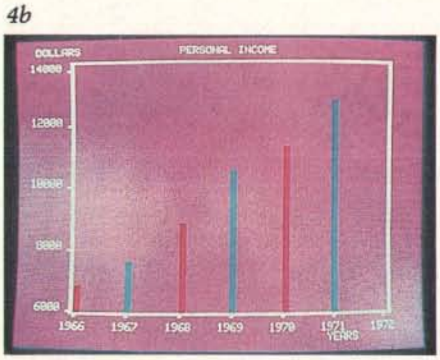


Photo 3: *Examples of equation-plotting mode. The range of both the X and Y axes can be changed, as can the choice of colors. Photo 3a illustrates the standard colors as selected by the computer; photo 3b shows another graph with colors of the user's choice.*

4a



4b



4c

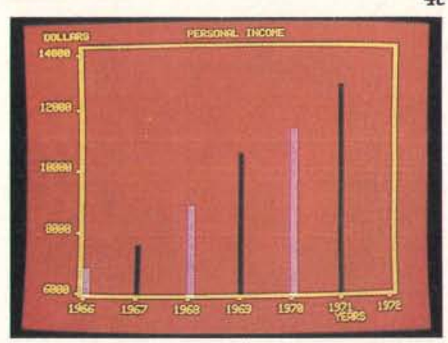


Photo 4: *Examples of bar-graph-plotting mode. Here, the same data is displayed in the standard colors (photo 4a) and two sets of user-selected colors (photos 4b and 4c). Horizontal bar graphs can also be displayed.*

Simple Base Conversions for the TRS-80

James M Curran, 24 Greendale Rd, Cedar Grove NJ 07009

I have noticed that decimal-to-hexadecimal and decimal-to-octal conversions are usually accomplished by means of subroutines, most of which require three to four statements. This is efficient enough for users of a low-level BASIC; however, computer enthusiasts with a BASIC interpreter containing the DEF FN (define function) command long for a simple one-statement conversion. Here are such conversion statements. For those of you who need to convert hexadecimal or octal to decimal, these conversions are also included. I have even thrown in a decimal-to-binary function.

Listing 1: *Definitions for five base-conversion functions. The first statement defines the function for converting decimal to binary numbers. The second and third definitions give the functions for converting from decimal to hexadecimal and from hexadecimal to decimal numbers. Notice that the variable HX$ must be initialized for both of these. The last two statements define the functions for converting from decimal to-octal and from octal to decimal numbers.*

```
1.DEF FN DB#(D)=(D AND 1)+(D AND 2)*5+(D AND 4)*25+
                (D AND 8)*125+(D AND 16)*625+
                (D AND 32)*3125+(D AND 64)*15625+
                (D AND 128)*78125

2.HX$="0123456789ABCDEF"
  DEF FN DH$(D)=MID$(HX$,(D AND -4096)/4096+1-
                (D>32767)*16,1)+
                MID$(HX$,(D AND 3840)/255+1,1)+
                MID$(HX$,(D AND 240)/16+1,1)+
                MID$(HX$,(D AND 15)+1,1)

3.HX$="0123456789ABCDEF"
  DEF FN H$D(H$)=(INSTR(HX$,MID$(H$,1,1))-1)*4096+
                 (INSTR(HX$,MID$(H$,2,1))-1)*256+
                 (INSTR(HX$,MID$(H$,3,1))-1)*16+
                 (INSTR(HX$,MID$(H$,4,1))-1)

4.DEF FN DO#(D)=(D AND 7)+(D AND 56)*1.25+
                (D AND 448)*1.5625+
                (D AND 3584)*1.953125+
                (D AND 28672)*2.44140625

5.DEF FN O$D(O$)=VAL(MID$(O$,1,1))*3276+
                 VAL(MID$(O$,2,1))*4096+
                 VAL(MID$(O$,3,1))*512+
                 VAL(MID$(O$,4,1))*64+
                 VAL(MID$(O$,5,1))*8+
                 VAL(MID$(O$,6,1))
```

These functions can also be used as subroutines by those without the DEF FN command. An AND-statement is necessary, because it performs a logical-AND operation which is used in all three routines to convert decimal to the various other bases.

The first function, which I call FNDB#, returns the binary equivalent of the argument as an eight-digit integer.

The hexadecimal equivalent of the argument is returned by the second function, FNDH$, as a four-character string with leading zeros. Arguments greater than 32767 (7FFF hexadecimal) must be signed; ie: reduced by 65536. For a 1-byte conversion, only the second half of the function is necessary.

My third function, called FNH$D, converts the argument, which must be a four-character string, into its decimal equivalent. In this function, the INSTR command is employed; if your BASIC does not have it, it is easily replaced with a BASIC subroutine. Its function is to return the position in the first string at which the second string begins. FNH$D can also be made into a 1-byte routine by using its second half. Both FNH$D and FNDH$ require HX$ to be initialized.

The final two functions for decimal-to-octal conversions (FNDO# and FNO$D) work similarly to their hexadecimal counterparts. ■

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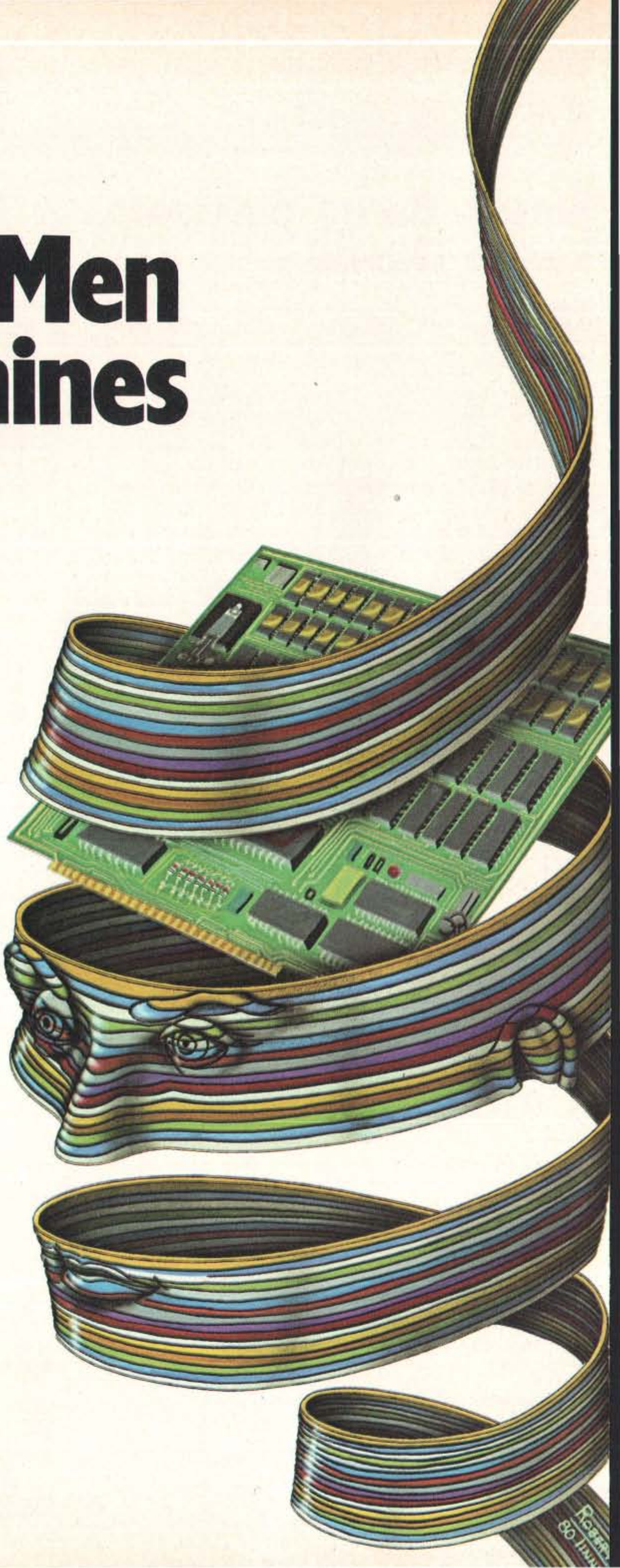

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Circle 92 on inquiry card.

Three-Dimensional Graphics for the Apple II

Dan Sokol
John Shepard
211 Fall Creek Dr
Felton CA 95018

Many articles have been written regarding three-dimensional graphics on home computers. Some involve highly complex hardware such as spinning mirrors, while others rely upon computation-intensive software to project three-dimensional objects on a two-dimensional plane.

Taking an innovative step backwards and rediscovering an old technique, I have been able to create three-dimensional pictures using my Apple II computer. I have generated a number of visually stimulating displays in this manner and would like to share with you the methods used, with the hope that you too will discover new ways to use your computer.

The method is simple. Just take a piece of cardboard, and with a pair of scissors, cut out a pair of eyeglass frames. Next, put a red filter over the left eye opening in the frame and a green filter over the right opening (I did say it was an old idea!). When viewing the screen with the glasses on, anything colored red will not be visible to your right eye, and anything green will not be visible to your left eye (you may have to adjust the tint on your television to optimize this). Anything white will be visible to both eyes.

The image that falls on the retina of your right eye will be the green image on the video monitor, but it will appear to be white! (It's all done in your brain.) The same is true of the red image in relation to your left eye. (We will refer to the red image in our software as violet. This is because the Apple HI-RES graphics cannot generate red.) [*However, see "More Colors for Your Apple," by Allen Watson III, June 1979 BYTE, page 60...***RSS**]

Creating an Image

As you can see by figures 1a and 1b, an image that seems to appear in front of the screen can be made by drawing the green image to the left of the red one. An image that appears behind the screen is simulated by placing the green image to the right of the red one. The apparent depth is determined by the distance between the two colored images.

It should be mentioned that the brain requires a frame of reference to judge distance "properly." An efficient way to provide this reference is to put a white border around the screen. This will define the *neutral* plane. Naturally, any objects on this plane need be drawn only once in white.

The program in listing 1 generates a set of lines which appear to disappear into the distance.

Another simple program is presented in listing 2. This one generates a three-dimensional box.

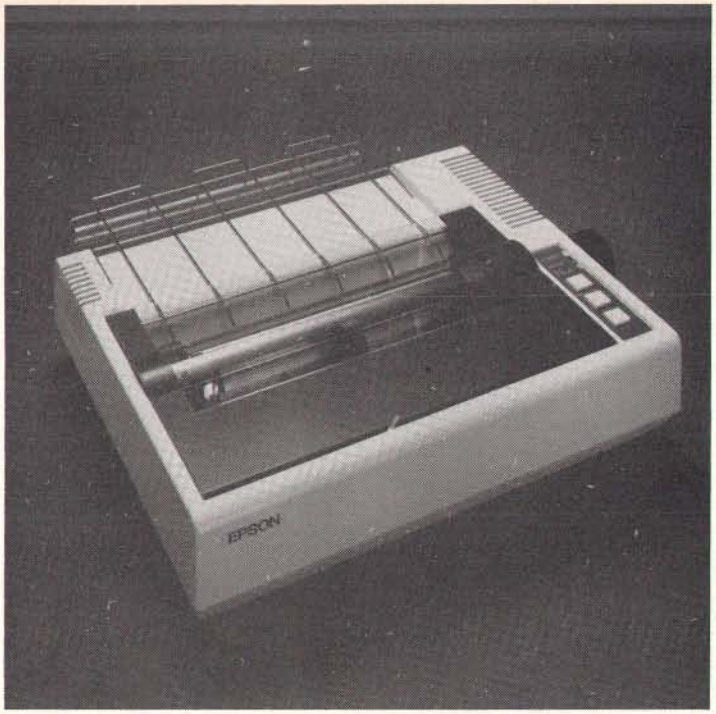
Using the shape-generator programs provided by Apple, the user can make objects appear to be various sizes and depths. This effect can be seen by running the program in listing 3.

You can place as many objects in space as you have room for. There are, however, some guidelines.

- You should draw your images from *back* to *front*. This way any overwriting will look natural.
- As you approach the neutral plane, the two images get closer together. Any place that they are coincident should be white. This can be handled with software. (I didn't say easily.)
- Using other colors generates an unbalanced image in the neutral plane—you experiment.
- You will have to adjust your color television set to match the color of the filters that are being used. The best way to do this is to draw a small green square and a small red square on the screen. Then place a

Text continued on page 154

If you just bought another printer, boy are you gonna be sorry.



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Listing 1: *This Apple integer BASIC program generates three-dimensional lines disappearing into infinity.*

```
  0 XO=YO=COLR=SHAPE=ROT=SCALE
  5 INIT=2048:CLEAR=2062:PLOT=2830:LINE=2836:DRAW=2871:XDRAW=2884
 10 BLACK=0:WHITE=127:VIOLET=85: LET GREEN=42
100 CALL INIT: POKE -16302,0:
150 REM   BUILD THE BORDER
200 COLR=WHITE:XO=0:YO=0: CALL PLOT:XO=279: CALL LINE:YO=191: CALL LINE:XO=0: CALL LINE:YO=0: CALL LINE
205 XO=1:YO=1: CALL PLOT:XO=278: CALL LINE:YO=190: CALL LINE:XO=1: CALL LINE:YO=1: CALL LINE
250 REM
251 REM
252 REM
500 REM  LINES TO INFINITY
510 COLR=VIOLET:XO=25:YO=180: CALL PLOT:XO=260:YO=20: CALL LINE:XO=70:YO=180: CALL LINE
520 COLR=GREEN:XO=60: CALL PLOT:XO=270:YO=20: CALL LINE:XO=10:YO=180: CALL LINE
550 END
```

Listing 2: *An Apple integer BASIC program for generating a three-dimensional box.*

```
  0 XO=YO=COLR=SHAPE=ROT=SCALE
  5 INIT=2048:CLEAR=2062:PLOT=2830:LINE=2836:DRAW=2871:XDRAW=2884
 10 BLACK=0:WHITE=127:VIOLET=85: LET GREEN=42
100 CALL INIT: POKE -16302,0:
150 REM   BUILD THE BORDER
200 COLR=WHITE:XO=0:YO=0: CALL PLOT:XO=279: CALL LINE:YO=191: CALL LINE:XO=0: CALL LINE:YO=0: CALL LINE
205 XO=1:YO=1: CALL PLOT:XO=278: CALL LINE:YO=190: CALL LINE:XO=1: CALL LINE:YO=1: CALL LINE
600 REM
601 REM
602 REM
603 REM  A BOX....
610 COLR=WHITE:XO=150:YO=50: CALL PLOT:XO=250: CALL LINE:YO=150: CALL LINE:XO=150: CALL LINE:YO=50: CALL LINE
615 COLR=GREEN:YO=75:XO=40: CALL LINE
620 XO=140: CALL LINE:XO=250:YO=50: CALL LINE
622 XO=250:YO=150: CALL PLOT
625 XO=140:YO=175: CALL LINE:XO=40: CALL LINE:XO=150:YO=150: CALL LINE:XO=40:YO=175: CALL PLOT
630 YO=75: CALL LINE:XO=140: CALL PLOT:YO=175: CALL LINE
635 XO=41:YO=75: CALL PLOT:YO=175: CALL LINE:XO=141: CALL PLOT:YO=75: CALL LINE
637 COLR=VIOLET
640 XO=30:YO=185: CALL PLOT:YO=85: CALL LINE:XO=130: CALL LINE:YO=185: CALL LINE
642 XO=250:YO=150: CALL LINE
645 XO=130:YO=185: CALL PLOT:XO=30: CALL LINE
650 XO=150:YO=150: CALL LINE:XO=30:YO=85: CALL PLOT:XO=150:YO=50: CALL LINE
660 XO=130:YO=85: CALL PLOT:XO=250:YO=50: CALL LINE
680 END
```

Listing 3: *This program uses the shape stored in the Apple II shape table and transforms it into three-dimensional form.*

```
  0 XO=YO=COLR=SHAPE=ROT=SCALE
  5 INIT=2048:CLEAR=2062:PLOT=2830:LINE=2836:DRAW=2871:XDRAW=2884
 10 BLACK=0:WHITE=127:VIOLET=85: LET GREEN=42
100 CALL INIT: POKE -16302,0:
150 REM   BUILD THE BORDER
200 COLR=WHITE:XO=0:YO=0: CALL PLOT:XO=279: CALL LINE:YO=191: CALL LINE:XO=0: CALL LINE:YO=0: CALL LINE
205 XO=1:YO=1: CALL PLOT:XO=278: CALL LINE:YO=190: CALL LINE:XO=1: CALL LINE:YO=1: CALL LINE
250 REM
700 REM
701 REM
710 REM
800 REM     3-D SQUARES
801 REM    USE SHAPE #1
802 REM   SHAPE #1 = 01 01 24 3F 3F 36 36 2D 2D 24 00
805 ROT=0:SCALE=1:SHAPE=1:XO=5:YO=5
810 FOR I=1 TO 7:SCALE=I:COLR=GREEN:XO=XO+(I*4):YO=YO+(I*4)
820 CALL XDRAW:COLR=VIOLET:XO=XO+I:YO=YO+I: CALL XDRAW: NEXT I
830 XO=XO+32:YO=90:COLR=GREEN:SCALE=SCALE+2: CALL XDRAW:COLR=VIOLET:YO=YO+8:XO=XO+8: CALL XDRAW
840 XO=XO+42:YO=YO-42:COLR=GREEN:SCALE=SCALE+2: CALL XDRAW:COLR=VIOLET:YO=YO+9:XO=XO+9: CALL XDRAW
999 END
```

Editor's Note:

Some Comments on the Programs

The three programs in this article assume that the high-resolution graphics routines have been loaded into the Apple II starting at hexadecimal location C00. The instruction LOMEM:4096 should be executed before loading the programs to protect these routines.

When I was typing these programs into the Apple, I noticed that line 10 of each listing has the statement LET GREEN = 42. At the time I could not understand why the LET keyword was used, so I deleted it. Several syntax errors later I realized the answer.

When "GREEN = 42" is parsed by the BASIC interpreter, the token GR (for graphics mode) is recognized. The rest of the line (EEN = 42) is then unrecognizable to the parser. When "LET GREEN = 42" is analyzed, the keyword LET tells the parser that the next token will be a variable. Therefore, GREEN is not broken into two tokens (GR and EEN).

This little trick could prove very useful when you wish to use a variable name which contains a keyword.

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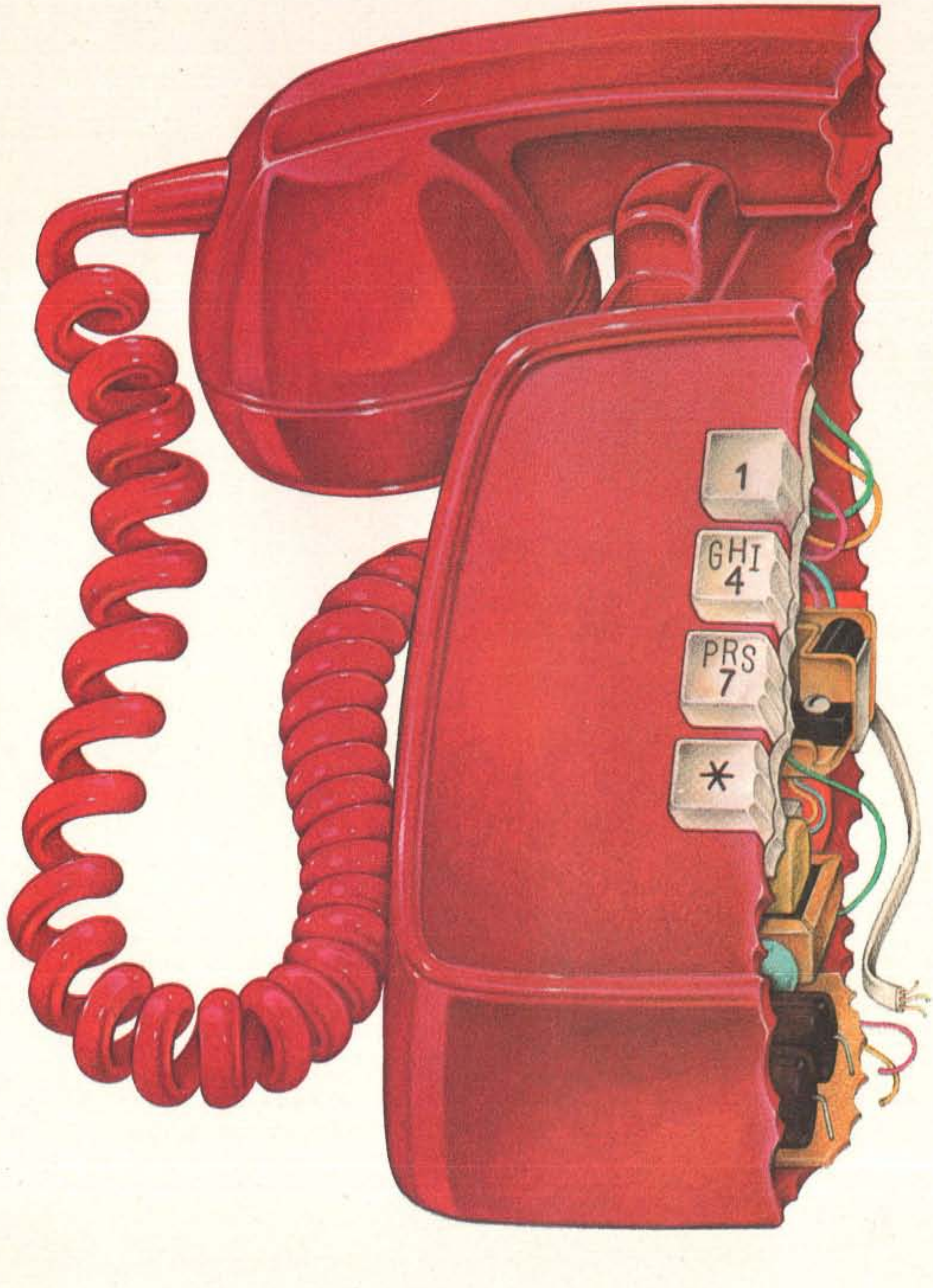


(1a)

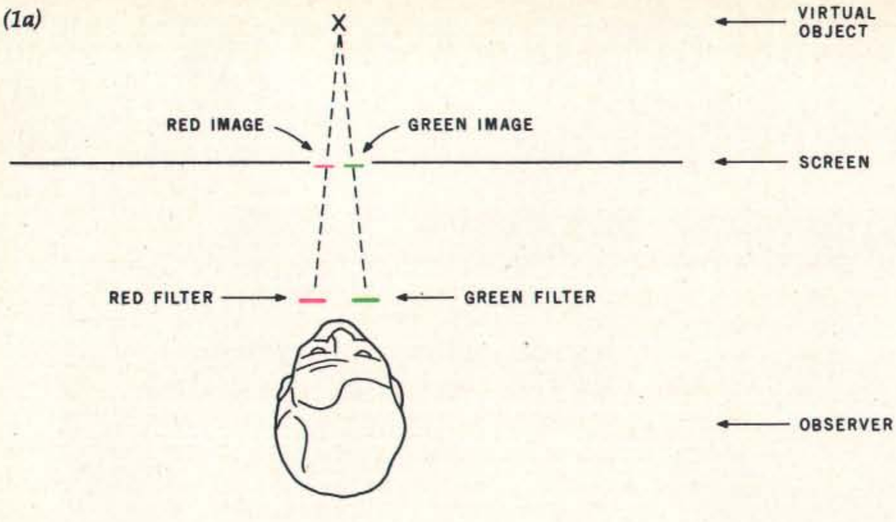
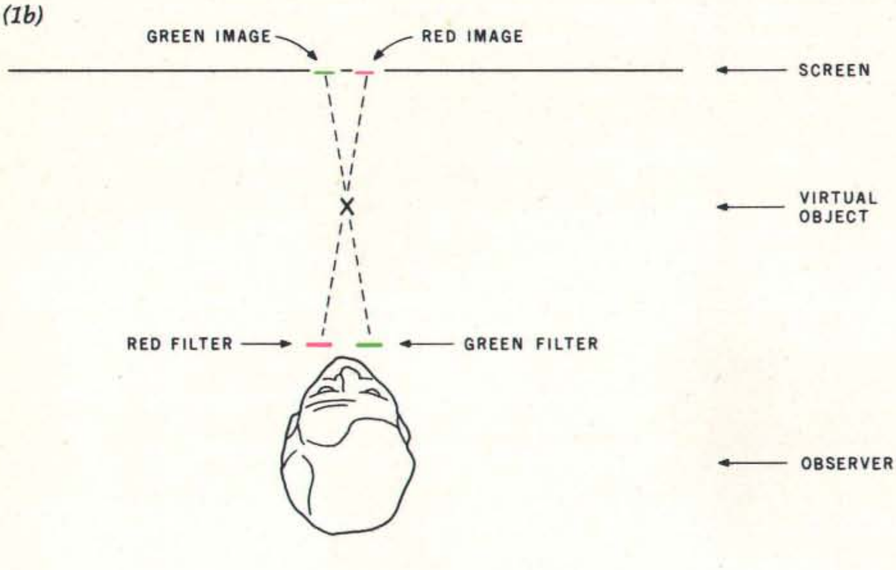


Figure 1: *A figure which appears to be behind the video screen can be produced by drawing the red image on the left side of the screen and the green image on the right side (see figure 1a). By reversing these two images, the image will appear to be in front of the video screen (see figure 1b).*

(1b)



Text continued from page 148:

piece of the green filter over the red square and a piece of the red filter over the green square. Adjust the tint, chrominance (if you have one), and color knobs so that both squares disappear (as much as possible...you may have to double up the filters).

- If you aren't worried about using your color television for other entertainment, you can make the following adjustments to it. On the back of the set are three controls that are (usually) labeled red, green, and blue (or R, G, B; or red screen, blue screen, green screen). These adjust the relative intensity of the three electron guns. If you first mark the initial positions of the three controls with a pencil, you will be able to reset them when you are finished. The adjustment is simple. Turn the blue screen off! This removes all the blue dots from the screen, only red and green remain. After adjusting the television as described in the previous step, reverse the positions of the filters (red over red, green over green) and adjust the red screen so that the intensity of the two squares through the filters appears the same.
- We used colored cellophane, available at most art supply stores, for filters.

There are a number of games that can be adapted to three-dimensional displays with this technique. Have fun!■

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☐ **GLECTOR**—General Ledger option to SELECTOR III-C2. Interactive system provides for customized COA. Unique chart of transaction types insure proper double entry bookkeeping. Generates balance sheets, P&L statements and journals. Two year record allows for statement of changes in financial position report. Supplied in source. Requires SELECTOR III-C2, CBASIC-2 and 56K system .... $350/$25

DMA

☐ **CBS**—Configurable Business System is a † comprehensive set of programs for defining custom data files and application systems without using a programming language such as BASIC, FORTRAN, etc. Multiple key fields for each data file are supported. Set-up program customizes system to user's CRT and printer. Provides fast and easy interactive data entry and retrieval **with transaction processing**. Report generator program does complex calculations with stored and derived data, record selection with multiple criteria, and custom formats. Sample inventory and mailing list systems included. **No support language required** .... $395/$40

MICROPRO

☐ **SUPER-SORT I**—Sort, merge, extract utility as Ⓛ absolute executable program or linkable module in Microsoft format. Sorts fixed or variable records with data in binary, BCD, Packed Decimal, EBCDIC, ASCII, floating & fixed point, exponential, field justified, etc. Even variable number of fields per record! .... $225/$25

☐ **SUPER-SORT II**—Above available as absolute program only .... $175/$25

☐ **SUPER-SORT III**—As II without SELECT/ EXCLUDE .... $125/$25

☐ **DATASTAR**—Professional forms control entry and display system for key-to-disk data capture. Menu driven with built-in learning aids. Input field verification by length, mask, attribute (i.e. upper case, lower case, numeric, auto-dup, etc.). Built-in arithmetic capabilities using keyed data, constant and derived values. Visual feedback for ease of forms design. Files compatible with CP/M-MP/M supported languages. Requires 32K CP/M .... $350/$35

☐ **WORD-STAR**—Menu driven visual word processing system for use with standard terminals. Text formatting performed on screen. Facilities for text paginate, page number, justify, center and underscore. User can print one document while simultaneously editing a second. Edit facilities include global search and replace, Read/Write to other text files, block move, etc. Requires CRT terminal with addressable cursor positioning .... $445/$40

☐ **WORD-STAR-MAIL-MERGE**—As above with option for production mailing of personalized documents with mail lists from DATASTAR or NAD .... $575/$40

☐ **WORD-MASTER** Text Editor—In one mode has superset of CP/M's ED commands including global searching and replacing, forwards and backwards in file in video mode, provides full screen editor for users with serial addressable-cursor terminal .... $145/$25

Software with Manual / Manual Alone

New

☐ **MAGIC WAND**—Word processing system with simple, easy to use full screen text editor and powerful print processor. Editor has all standard editing functions including text insert and delete, global search and replace, block move and library files for boiler plate text. Print processor formatting commands include automatic margins, pagination, headings & footings, centered and justified text. Also prints with true proportional spacing, merges with data files for automatic form letters, and performs run-time conditional testing for varied output. Requires 32K CP/M and CRT terminal with addressable cursor .... $395/$40

☐ **TEXTWRITER III**—Text formatter to justify and Ⓜ paginate letters and other documents. Special features include insertion of text during execution from other disk files or console, permitting recipe documents to be created from linked fragments on other files. Has facilities for sorted index, table of contents and footnote insertions. Ideal for contracts, manuals, etc. Now compatible with Electric Pencil* and Word-Star prepared files .... $125/$20

New

☐ **DATEBOOK**—Program to manage time just like an office appointment book but using the speed and memory of a computer. Keeps track of three appointment schedules (three dental chairs, three attorneys, etc.) at once. Appointments consist of name, reason for the appointment, the date and time, and the length of the appointment. System can be quickly customized for the individual user. Many helpful features for making, changing, finding, and reporting appointments. Requires 48K CP/M and 180K bytes diskette storage. Not available for Apple CP/M .... $295/$25

New lower prices for application Software

PEACHTREE SOFTWARE*

☐ General accounting software for small busi-Ⓛ nesses. Each product can be used alone or with † automatic posting to the general ledger. Supplied in source for Microsoft BASIC 4.51.

GENERAL LEDGER	$530/$40
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☐ Comprehensive accounting software written in Ⓛ CBASIC-2 and supplied in source code. Each Ⓜ software package can be used as a stand-† alone system or integrated with the General Ledger for automatic posting to ledger accounts. Requires CBASIC-2.

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ACCOUNTS RECEIVABLE	$805/$40
INVENTORY SYSTEM	$555/$40
JOB COSTING	$805/$40
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☐ **POSTMASTER**—A comprehensive package Ⓜ for mail list maintenance that is completely menu driven. Features include keyed record extraction and label production. A form letter program is included which provides neat letters on single sheet or continuous forms. Includes NAD file translator. Requires CBASIC-2. .... $150/$20

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☐ Complete interactive accounting software for † business. Each product can be used standalone or with automatic posting to the general ledger. Each product is thoroughly tested and very well documented. Each product requires CBASIC-2.

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ACCOUNTS PAYABLE	$820/$40
PAYROLL	$820/$40
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NEWSLETTER FROM LIFEBOAT

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Feature Articles ! New Software ! Product Comparisons ! Info on CP/M Users Group !

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Software with Manual / Manual Alone

☐ **ANALYST**—Customized data entry and report-† ing system. User specifies up to 75 data items per record. Interactive data entry, retrieval, and update facility makes information management easy. Sophisticated report generator provides customized reports using selected records with multiple level breakpoints for summarization. Requires a disk sort utility such as QSORT, SUPER-SORT or VSORT and CBASIC-2 .... $250/$15

☐ **LETTERIGHT**—Program to create, edit and type letters or other documents. Has facilities to enter, display, delete and move text, with good video screen presentation. Designed to integrate with NAD for form letter mailings. Requires CBASIC-2 .... $200/$25

☐ **NAD** Name and Address selection system—Interactive mail list creation and maintenance program with output as full reports with reference data or restricted information for mail labels. Transfer system for extraction and transfer of selected records to create new files. Requires CBASIC-2 .... $100/$20

☐ **QSORT**—Fast sort/merge program for files with fixed record length, variable field length information. Up to five ascending or descending keys. Full back-up of input files created .... $100/$20

★★★★★★★ CONDIMENTS ★★★★★★★

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☐ **STRUCTURED MICROPROCESSOR PROGRAMMING**—By the authors of SMAL/80. Covers structured programming, the 8080/8085 instruction set and the SMAL/80 language $20

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★★★★★★★ Hearty Appetite. ★★★★★★★

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†Recommended system configuration consists of 48K CP/M, 2 full size disk drives, 24 x 80 CRT and 132 column printer.

Ⓜ Modified version available for use with CP/M as implemented on Heath and TRS-80 Model I computers.

Ⓛ User license agreement for this product must be signed and returned to Lifeboat Associates before shipment may be made.

Ⓘ This product Includes/eXcludes the language Ⓧ manual recommended in Condiments.

Ⓢ Serial number of CP/M system must be supplied with orders.

Ⓩ Requires Z80 CPU.

Ordering Information

MEDIA FORMAT ORDERING CODES
When ordering, please specify format code.

LIFEBOAT ASSOCIATES MEDIA FORMATS LIST. Diskette, cartridge disk and cartridge tape format codes to be specified when ordering software for listed computer or disk systems. All software products have specific requirements in terms of hardware or software support, such as MPU type, memory size, support operating system or language.

Computer system	Format Code
Altair 8800 Disk	See MITS 3200
Altos	A1*
Apple + SoftCard 13 Sector	RG
Apple + SoftCard 16 Sector	RR
AVL Eagle	RB
BASF System 7100	RD
Blackhawk Single Density	Q3
Blackhawk Micropolis Mod II	Q2
CDS Versatile 3B	Q1
CDS Versatile 4	Q2
COMPAL-80	Q2
Cromemco System 3	A1*
Cromemco Z2D	R6
CSSN BACKUP (tape)	T1 #
Delta	A1*
Digi-Log Microterm II	RD
Digital Microsystems	A1*
Discus	See Morrow Discus
Durango F-85	RL
Dynabyte DB8/2	R1
Dynabyte DB8/4	A1*
Exidy Sorcerer + Lifeboat CP/M	Q2
Exidy Sorcerer + Exidy CP/M	Q4
Heath H8 + H17/H27	P4
Heath H89 + Lifeboat CP/M	P4
Heath H89 + Magnolia CP/M	P7
Helios II	See Processor Technology
Horizon	See North Star
iCOM 2411 Micro Floppy	R3
iCOM 3712	A1
iCOM 3812	A1*

Computer system	Format Code
iCOM 4511 5440 Cartridge CP/M 1.4	D1 #
iCOM 4511 5440 Cartridge CP/M 2.2	D2 #
IMS 5000	RA
IMS 8000	A1*
IMSAI VDP-40	R4**
IMSAI VDP-42	R4**
IMSAI VDP-44	R5**
IMSAI VDP-80	A1**
Intecolor	See ISC Intecolor
Intel MDS Single Density	A2
Intel MDS Double Density	A5
Intertec SuperBrain DOS 0.1	R7
Intertec SuperBrain DOS 0.5-2.X	RJ
Intertec SuperBrain DOS 3.X	RK
ISC Intecolor 8063/8360/8963	A1
Kontron PSI-80	RF
Meca 5¼"	P6
Micromation (Except TRS-80 below)	A1*
Micropolis Mod I	Q1
Micropolis Mod II	Q2
MITS 3200/3202	B1
Morrow Discus	A1*
Mostek	A1
MSD 5¼"	RC
North Star Single Density	P1
North Star Double/Quad	P2
Nylac Single Density	Q3
Nylac Micropolis Mod. II	Q2
Ohio Scientific C3	A3
Onyx C8001	T2 #
Pertec PCC 2000	A1*
Processor Technology Helios II	B2
Quay 500	RO
Quay 520	RP
RAIR Single Density	R9

Computer system	Format Code
RAIR Double Density	RE
Research Machines 8"	A1
Research Machines 5¼"	RH
REX	Q3
Sanco 7000 5¼"	RQ
SD Systems 8"	A1*
SD Systems 5¼"	R3
Sorcerer	See Exidy Sorcerer
Spacebyte	A1
SuperBrain	See Intertec
Tarbell	A1*
TEI 5¼"	R3
TEI 8"	A1*
Thinkertoys	See Morrow Discus
TRS-80 Model I 5¼"	R2
TRS-80 Model I + FEC Freedom	RN
TRS-80 Model I + Micromation	A4*
TRS-80 Model I + Omikron 5¼"	RM
TRS-80 Model I + Omikron 8"	A1
TRS-80 Model I + Shuffleboard 8"	A1
TRS-80 Model II	A1*
VDP-40/42/44/80	See IMSAI
Vector Graphic	Q2
Vector MZ	Q2
Versatile	See CDS Versatile
Vista V80 5¼" Single Density	P5
Vista V200 5¼" Double Density	P6
Zenith Z89 + Lifeboat CP/M	P4
Zenith Z89 + Magnolia CP/M	P7

Prices reflect distribution on 8" single density diskettes. If a format is requested which requires additional diskettes, a surcharge of $8. per additional diskette will be added.

Prices F.O.B. New York. Shipping, handling and C.O.D. charges extra.

Manual cost applicable against price of subsequent software purchase.

The sale of each proprietary software package conveys a license for use on one system only.

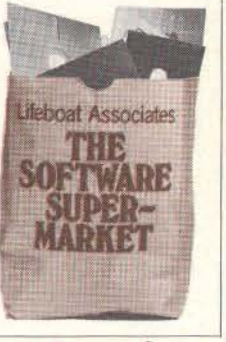
VISA MasterCard

* Single-Side Single-Density disks are supplied for use with Double-Density and Double-Side 8" soft sector format systems.

** IMSAI formats are single density with directory offset of zero.

\# A media surcharge of $25 for orders on tape formats T1 and T2 and of $100 for orders on disk formats D1 and D2 will be added.

The list of available formats is subject to change without notice. In case of uncertainty, call to confirm the format code for any particular equipment.



Lifeboat Associates
1651 Third Avenue, N.Y., N.Y. 10028

The Altos ACS8000 Single-Board Computer

Mark Dahmke
1515 Superior St, Apt 15
Lincoln NE 68521

Altos Computer Systems of San Jose, California, manufactures a series of powerful Z80-based computers aimed mainly at the small-business and scientific-laboratory markets. The company offers a wide variety of models — from one 8-inch, single-density, Shugart floppy-disk drive with 32 K bytes of main memory to four double-density, 8-inch floppy-disk drives, and a hard-disk subsystem with as much as 58 megabytes of on-line storage.

Hardware Design

The ACS8000 series are all single-circuit-card computers based on a Z80A microprocessor running at 4 MHz. All systems come with at least 32 K bytes of 4116 dynamic memory devices. This is expandable to 64 K bytes on two versions of the ACS8000, and to 208 K bytes on the third version.

The system also comes with a 2708 EPROM (erasable programmable read-only memory) that contains the ALTOS-E monitor program. The 2708 is active until CP/M is boot-loaded: it is then disabled and disappears so the entire memory-address space is available as programmable memory. This technique is widely used and is referred to as "phantom read-only memory."

Serial Ports

Even the smallest Altos system comes with a dual-channel, serial I/O (input/output) device. One channel is used for the system console, and the other is set up to drive a printer or another device, such as a modem. The console channel is preset by the ALTOS-E monitor firmware to 9600 bps, with 1 start bit, 1 stop bit, 8 data bits, and no parity. It runs in full-duplex (ie: simultaneous-bidirectional) mode. The 9600 bps data rate of the console is not alterable, but the printer characteristics can be changed after the system is booted up.

Parallel Ports

All Altos computers come with at least two user-defined parallel ports. There are actually two Z80 PIO (parallel input/output) devices, each with two ports, but one is used to control disk operations. The user-definable ports are accessible through an external connector that may be connected to a printer, an EPROM programmer, or a parallel-input keyboard. Both ports are fully programmable.

The Counter-Timer Circuit

The Z80 CTC (counter-timer circuit) is a programmable counter-timer that has four independent channels. Three of the channels (addresses 0 thru 2) are used by the system to set console and printer data rates and disk-head load-delay times. The fourth channel is available to the user and can be programmed as an interval timer or real-time clock.

The Floppy-Disk Controller

The Altos single-density model uses the Western Digital 1771-1

A Visit to Altos

Altos computers have acquired quite a reputation for reliability — it's the sort of thing you hear by word-of-mouth in this industry. To find out more, I paid a visit to Altos recently at the invitation of Dr Roger Vass, the Vice-President of Marketing.

Roger described the extensive quality-control procedures used at Altos, which include several burn-in tests of individual components and complete systems in its testing ovens. Another reason for the low failure rate of the computers (eg: less than 1% are returned to the plant because of defects) is that Altos computers use a single printed-circuit board for the entire computer, thus eliminating many potential interconnection problems.

Interestingly, Altos sells more computers (ie: about 55% at present) overseas than it does domestically, due in part to the company's vigorous marketing activity in Europe. Roger sees the European market as having great potential for American personal-computer companies. Certainly, the growth of the number of publications and public interest at overseas trade shows confirms this. . **.CM**

About the Author

Mark Dahmke is a consulting editor for BYTE Publications and also operates a computer consulting business. He has been involved with computers since 1974 and does a great deal of systems hardware and software design. His interests include writing, photography, voice synthesis, and computer graphics.

Photo 1: *Front view of the Altos ACS 8000-2 computer, which has 64 K bytes of memory and two dual-density, single-sided disk drives.*

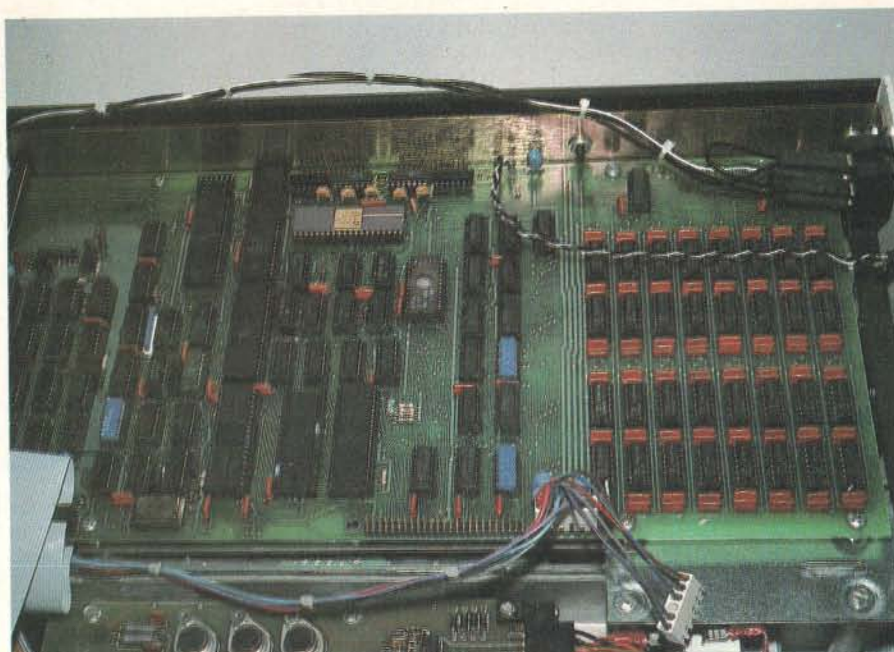


Photo 2: *Interior view of the ACS 8000-2, which is, as are all the Altos models, a single-board, Z80-based computer.*

floppy-disk controller/formatter device to manage up to four 8-inch drives. The 1771-1 is directly integrated into the single-board design of the Altos.

The double-density version requires some additional control circuitry and uses the 1791-1 device; thus the board supporting double-density disks is slightly larger. All versions of the ACS8000 are available with either single-sided or double-sided Shugart drives.

All boards have a fifty-pin expansion connector that allows the user to access all Z80 address, data, and con-

Circle 97 on inquiry card. →

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ple II is a registered trademark f Apple Computers, Inc.
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trol lines. Altos does not use the connector for expansion purposes because of its single-board philosophy, but it is there for the special needs of the users.

Optional Components

The ACS8000 has provisions for some special components that are optional on all of the standard systems. The Z80 DMA (direct memory access) controller is a very sophisticated device that can be programmed to perform block data transfers from memory to memory, from memory to an I/O port, or vice versa. The device can also be programmed to search for a byte within a block, with or without transfer of the block. The device has one DMA channel that can be set up to work in four different modes:

- single-byte mode — in which each memory access operates on a single byte of data
- burst mode — in which the device keeps control of the bus for as long as data is continuously ready
- continuous mode — in which the device retains bus control for the entire operation
- transparent mode — in which the device operates only during memory refresh time so it does not slow down the processor

I was informed by Altos that, although the Z80 DMA device can be plugged into the system, there is no way to use it under CP/M. The OASIS multiuser operating system is set up to use DMA to access a disk, however.

The Advanced Micro Devices Am9511 arithmetic processor is another optional device that provides fixed and floating-point arithmetic and floating-point trigonometric and mathematical operations. It may be used to speed up computational capabilities of the system. All commands and data transfers take place on an 8-bit, bidirectional data bus. Transfers to and from the 9511 may be handled by the Z80 under program control (with IN and OUT instructions) or through the Z80 DMA device. The Am9511 can be programmed to generate interrupts upon completion of arithmetic functions.

Altos also plans to introduce a 2708/2716 EPROM programmer that will plug into the parallel-port con-

Text continued on page 166

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*1 Drive Chassis & Power Supply
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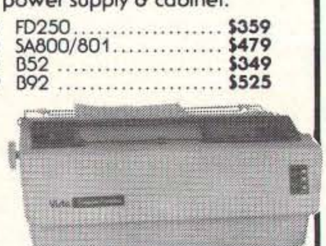
Centronics 702-9	$1,995
Anadex 9501	$1,549
Malibu	$2,395
Spinwriter	$2,549



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- NEWDOS 80
- Cable & Adapter

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CP/M Features With Altos Systems

All the standard CP/M system utilities are available:

- *ED: context (text) editor.*
- *ASM: CP/M standard (no-frills) 8080 assembler.*
- *LOAD: loader, converts hexadecimal-ASCII format files to absolute machine-code files.*
- *DDT: CP/M Dynamic Debugging Tool.*
- *PIP: Peripheral Interchange Program that is used to move and copy disk files from disk to disk and can also be used to copy files from disk to printer or from a reader device to disk.*
- *SYSGEN: CP/M utility that generates new system disks.*
- *DUMP: prints the contents of a file on the display in hexadecimal (base 16) form.*
- *SUBMIT: CP/M batch facility: executes a series of console commands from a disk file.*

Some additional commands and utilities are available:

- *MOVCPM: CP/M utility that is used to relocate the CP/M operating system depending on system memory size.*
- *STAT: displays status of various device assignments and shows the amount of free space left on each on-line.*
- *MTS: memory-test program that performs a destructive memory test on system memory.*
- *SETUP: utility that modifies the boot-load sector of a disk. It also allows a disk to be flagged for single- or double-density operation and sets the printer data rate at boot-load time.*
- *REFORM: disk-formatting utility that allows the user to format a disk for single- or double-density operation. Disks may be formatted to be either IBM 3740- compatible or Intel ISIS-II format. Altos has its own format for double density.*
- *DTEST: disk-test utility that checks out both drives and disks on the system.*
- *SINGLE: followed by the letter designation of a drive (A, B, C, D), will set up the drive for single-density operation.*
- *DOUBLE: works the same as SINGLE but sets the designated drive for double-density operation.*
- *COPY: will copy data track by track from the disk in drive A to drive B.*
- *FILES: will display the file-control-block information in hexadecimal for all files on a disk.*

Other files are included with the system:

- *BOOT.ASM: an assembler source for the boot loader.*
- *ALTOSE.ASM: an assembler source for the ALTOS-E 2708 EPROM.*
- *CBIOS.ASM: an assembler source for the custom Basic Input/Output System (CBIOS) in CP/M. This allows the user to make further operating-system modifications as needed.*

UCSD Pascal Operating System

Initializing the System

In order to make UCSD (University of California, San Diego) Pascal fully operational on the Altos, a user-written procedure that does direct cursor addressing on video terminals must be added to the operating system. Referred to as GOTOXY, the procedure accepts two integer variables as input and positions the cursor on the screen accordingly. Since there are so many different video terminals, it is the responsibility of the user to write the GOTOXY procedure. After compiling it, the user must execute a program called BINDER which links GOTOXY to the SYSTEM.PASCAL file.

The other initialization program is called SETUP. When executed, the user is given a set of options including Help and Teach. SETUP modifies a table of key assignments and terminal commands, allowing the user to customize the operating system to a particular terminal. Most keys may also have a prefix (eg: Escape) to allow for terminals that send escape sequences for certain user-definable keys. For example, many terminals have a separate keypad for cursor control (eg: Up, Down, Home, etc). The escape sequence for "cursor home" on many terminals is Escape-H; or 27,72 in decimal ASCII codes. In SETUP, the cursor-home function could be defined as having a prefix code and the decimal value 72 (or H as the character code).

Other Features

The Pascal Operating System has some other unique features. When compiling a program, Pascal will list error messages and ask if you want to continue or return to the editor. If the latter option is chosen, the operating system loads the editor and places the cursor on the character where the compilation error was detected. This feature saves a great deal of time when correcting syntax and logic errors.

The Filer also has some interesting features. Basically, the Filer is a utility program that lists directories of disks and manipulates files directly in the conventional disk-operating-system mode. On request, the Filer will create a duplicate directory for backup purposes. The Filer also has a routine for locating bad blocks on disk. If a bad sector is found, it will be marked as an immovable file in the directory.

Altos is marketing Pascal/M and a C compiler. The firm is also in the process of providing hard-disk backup on cartridge tape. The company is also introducing an asynchronous communications package for Altos computers (price: $100) and a bisynchronous IBM 3780 protocol package that allows the Altos to go on line in batch mode to an IBM host computer. The price is $1000.

In version II.0 of Pascal, the Debugger package is missing. I was informed by Altos that it was having problems with it and that a new version would be available with the next release. Altos also said that Pascal/M does have a full Debug option and that it will be available shortly.

Text continued from page 162
nector. This project has been delayed because of software development priorities.

Hard-Disk Capability

Altos' third single-board version of the ACS8000 has an on-board hard-disk controller in addition to the floppy-disk controller. Hard-disk storage may start at 14.5 megabytes and can be expanded up to 58 megabytes.

Multiuser Versions

The system that I received was an ACS8000-2 with 64 K bytes of memory and two dual-density, single-sided floppy-disk drives. As described in the literature, the ACS8000-2/MU2 is a two-user system with 112 K bytes of memory and two double-density single-sided drives.

Memory is divided into banks, with a 16 K-byte system area and two or more 48 K-byte user areas. A four-user ACS8000-2/MU4 is the same as an MU2 but with 208 K bytes of memory. The largest non-hard-disk configuration would be an ACS8000-4/MU4 with 208 K bytes of memory for four users and four double-density, double-sided floppy-disk drives.

> All Altos systems run either CP/M or Altos multiuser executive AMEX.

The smallest hard-disk multiuser configuration would be an ACS8000-6/MU2 with 112 K bytes of memory, two double-density, single-sided drives and a one-platter hard disk yielding 14.5 megabytes of space. This system would have four serial I/O ports and two parallel ports.

The largest configuration would be an ACS8000-9/MU4 with 208 K bytes for four users, four double-density, double-sided floppy-disk drives and 58 megabytes of hard-disk space. A total of six serial ports and two parallel ports would be available on the system; these can be used to support four terminals and two other peripherals.

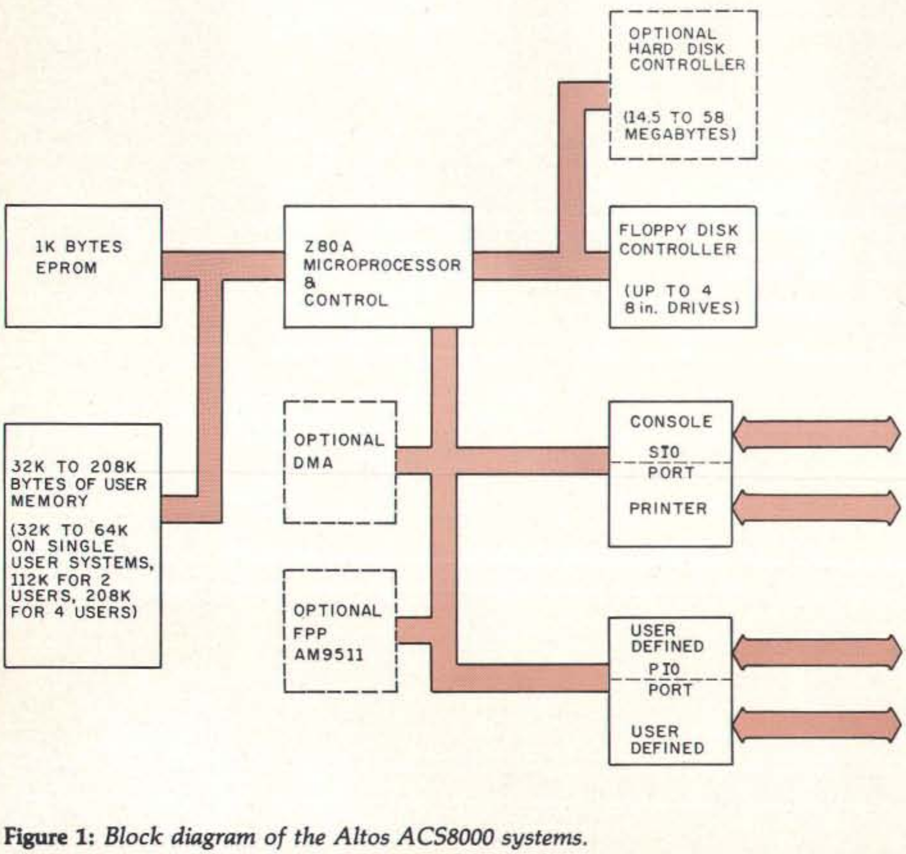


Figure 1: *Block diagram of the Altos ACS8000 systems.*

Software

All Altos systems run either Digital Research's CP/M operating system or Altos multiuser executive AMEX. AMEX is functionally compatible with CP/M, using the same disk formats and operating-system conventions. If you plan to use a hard disk, AMEX is a necessity since straight CP/M supports only floppy disks. CP/M version 2.0, which directly supports hard disks, and MP/M, the multiprogramming version of CP/M, are also available.

Optional Software

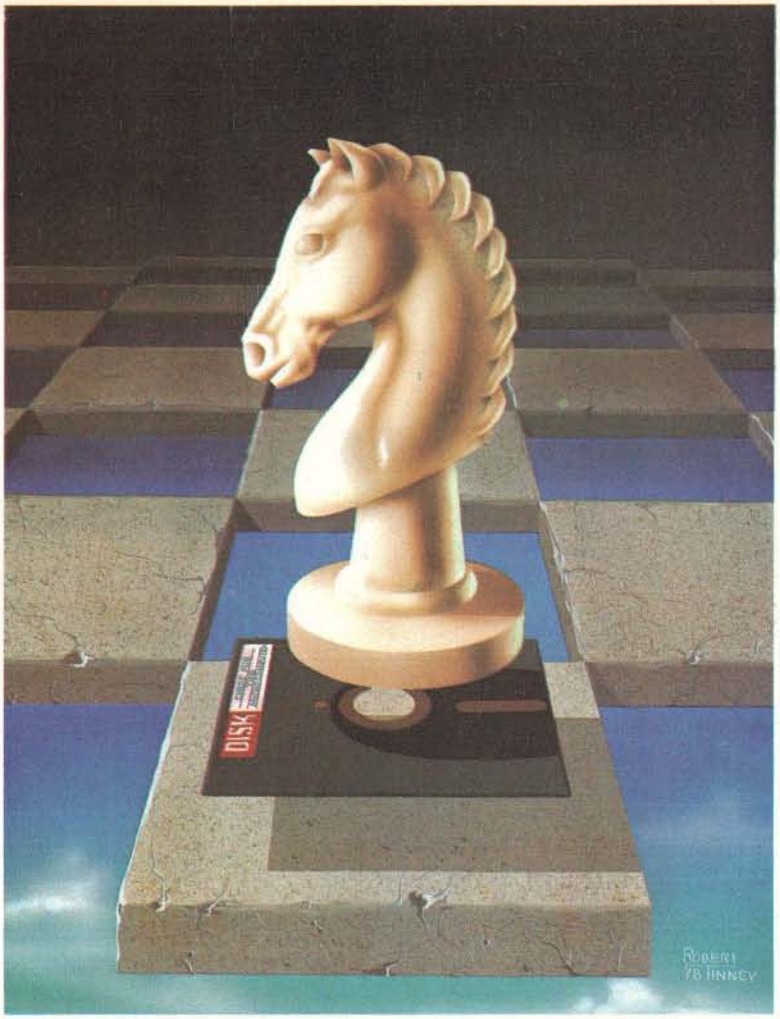
The Altos CP/M has been customized to allow for printout *spooling* and *despooling*. In this process, printed material is stored on disk until the printer is free. This option allows printers to be driven in the background mode so that printing may go on while the computer is doing something else.

Another software option is for use with the Microsoft FORTRAN-80 compiler. A FORTRAN service-subroutine library called APULIB makes use of the Am9511 floating-point processor to speed up arithmetic computations in FORTRAN by a factor of 10 or more. A typical FORTRAN program performing extensive calculations could run about four times faster with APULIB.

The other major software option is the UCSD Pascal operating system. Altos offers it as a separate and distinct operating system for the ACS8000. This operating system consists of a file manager, an editor, a Pascal compiler, a BASIC compiler, a macroassembler for the Z80, an interactive debugger, and a linker/librarian. UCSD (University of California, San Diego) Pascal runs as a P-machine interpreter. All portions of the operating system and some other run-time subroutines are written in Pascal, with the exception of portions of the P-machine interpreter. Pascal is also patched to handle the Am9511 arithmetic processor for greater computational speed. The Z80 CTC is also set up to act like a real-time clock. Unfortunately, the real-time clock is not accessible by the user; it is used internally to improve the performance of the disk interface.

Altos Documentation

The manual shipped with the Altos consists of the following segments:



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- an operating manual which contains a hardware and software overview section
- setup and checkout guides
- a CP/M operating guide
- a troubleshooting section
- all the schematic diagrams

The manual also includes the SA800/801 disk-drive maintenance manual and six publications from Digital Research covering all aspects of CP/M.

Setting Up and Using a New System

My Altos is hooked up to a video terminal set to 9600 bps. When power is applied, the Altos displays the two prompt characters %* on the console, which means that the EPROM monitor is in control. (If reset is depressed, the same response is given.) If a floppy disk is inserted into drive A (the drive on the right-hand side) and reset is depressed, the monitor will automatically begin loading the operating system from the disk. If you are running CP/M, the message "32 K ALTOS DOS VERS 1.47" will be displayed, followed by A> on the next line. The A character means that the disk in drive A is the currently active disk, while the > indicates that CP/M is ready to receive commands.

After the machine displayed the A> prompt, I tried to enter the DIR command to display the directory, with no success. I reset the system and tried again — still nothing. Then I decided to check the RS-232 cable and connectors to see if the transmit and receive lines were hooked up properly. After experimenting with my own 8080-based system to make sure the terminal would talk to it and still finding no problems, I called Altos: the gentleman I spoke with suggested that I make sure that pin 20 (Data Terminal Ready) of the RS-232 cable was hooked up. I took apart my cable and found that pin 20 was not connected. A quick resoldering job solved the problem. (I later discovered that the Altos manual discusses the problem in the section on troubleshooting, but I had apparently not seen it on my first reading of the manual.)

One of my complaints about the Altos is that the console data rate is defined in firmware — in the EPROM. The system can be used only if you have a 9600 bps terminal (at least, to start with). Even after the initial load, there is no way to easily modify the data rate short of creating a new EPROM.

CP/M has a SETUP command that allows the user to change the bootload characteristics of a disk. The printer data rate, the system clock rate (2 MHz or 4 MHz), and the density of the disk may be redefined for each system disk. It would seem reasonable to be able to modify the console data rate also, but this is not currently the case.

Formatting Disks

The next thing I tried to do was to create a backup copy of the master system disk. The documentation for this procedure is fairly accurate, but important instructions are left out.

The first step is to insert a blank disk (with the label side facing down) into drive B, the left-hand drive. The REFORM command will reformat a disk for any of several disk formats. After typing in REFORM, the computer asks you to enter a number corresponding to the type of format that will be used and to indicate whether the blank disk is in drive B (in a two-drive system) or drive D (in a four-drive system).

The first time I tried to format a disk, I got errors on top of errors. The documentation failed to mention that the write protect notch on the disk must be covered to allow read/write operation. Since I usually work with 5 -inch floppy disks, I am used to covering the write protect notch to protect a disk, not to unprotect it. After trying everything I could think of, it finally occurred to me that the notch might need to be covered to work. [*This method of disk protection is standard for 8-inch disks, so neither Altos nor its documentation is in error here. Still, this situation*

At a Glance

Name of computer	*Altos ACS8000 series*
Manufacturer	*Altos Computer Systems 2360 Bering Dr San Jose CA 95131 (408) 946-6700*
Price	*from $2840 (ACS8000-1S)*
Processor	*Z80A (8-bit)*
Memory	*64 K bytes (expandable to 208 K bytes on a multiuser system)*
Mass Storage	*one to four 8-inch, single- or double-density, single- or double-sided, Shugart floppy-disk drives*
Other hardware features	*includes serial printer port, two user-definable parallel ports*
Software included	*ALTOS-E monitor (in read-only memory)*
Hardware options	*an 9511 arithmetic-processor board; Winchester hard disk; multiple users*
Software options	*Operating systems: AMEX, CP/M, MP/M, OASIS, UCSD Pascal.*
Languages	*FORTRAN-80; MBASIC, MBASIC-80, CBASIC II; COBOL-80, CIS COBOL; Vanguard APL, PL/I-80, Z80 Macro Assembler*

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*always causes problems for people who are accustomed to working with 5-inch floppy disks. . . .***GW**]

Altos Demonstration Programs

The CP/M disk that came with the system had a number of demonstration programs, including a biorhythm program in BASIC, a rather poor implementation of tic-tac-toe, a number-guessing game, and a program that did nothing but compute and print square roots. The business package demonstration programs included a payroll generator and an automobile parts-list/inventory program.

The only documentation provided with any of these business demo programs was a single typed page giving hopelessly inadequate operating instructions. I never succeeded in making any of the nongame programs work.

Final Remarks

● The hardware of the Altos ACS8000 is well designed, although the documentation of some of its components is absent. The computer uses several sophisticated, optional support chips such as the counter-timer, the serial and parallel ports, and the Am9511 arithmetic processor. However I had to look over the manufacturers' specification sheets and application notes to find out anything about them.

● The software of the Altos ACS8000 is not as well supported, but the CP/M, AMEX, UCSD Pascal, and OASIS operating systems are available. Altos has provided no software support for the specialized hardware built into the system.

● Languages available from Altos include FORTRAN-80, MBASIC, MBASIC-80, CBASIC II, COBOL-80, CIS COBOL, Vanguard APL, PL/I-80,. and Z80 Macro Assembler. Numerous other languages are available from other sources for use with the CP/M operating system.

● The Altos ACS8000 is strong on hardware and weak on software and documentation. Perhaps someday the Altos people will get around to documenting and supporting the best selling points of their product line. ■

Seventh Annual SIGGRAPH Conference

Kenneth Livingston
225 Nebraska Hall
University of Nebraska
Lincoln NE 68508

Mark Dahmke
1515 Superior, Apt 15
Lincoln NE 68521

The Association for Computing Machinery (ACM) Special Interest Group on Computer Graphics (SIGGRAPH) held its seventh annual conference on July 14 thru 18, at the Seattle (Washington) Center (former site of the Seattle World's Fair). This conference, like all of the recent SIGGRAPH conferences, was extremely well attended. Over 1200 people registered for the two-day preconference tutorials. More than 2300 people registered for the three-day conference itself. Participants came from nearly every state, Canada, several European countries, and Japan.

Preconference Tutorials

Each year, the conference organizers have sought to provide participants with an opportunity to not only attend the conference, but also to acquire additional information and expertise about graphics through a series of tutorial sessions. These are led by well-known computing and graphics professionals from both industry and education. This year's eight tutorial sessions included these topics:

- Introduction to Computer Graphics
- Introduction to Raster Graphics
- Advanced Raster Graphics
- Computer-Aided Design
- Low-Cost Graphics
- Graphic Design and Information Graphics
- Animation Graphics
- User Interfaces to Graphic Systems

These tutorials ranged in level of expertise from novice to expert and provided a means for everyone to advance technically.

The session on low-cost computer graphics addressed issues relating to the use of graphics capabilities of personal-computing hardware. Many of these systems can be configured at costs of about $2000. Given today's economy, systems in this price range can be very appealing to small businesses, public-school systems, and small colleges and universities. At the other end of the scale are large CAD/CAM (Computer-Aided Design/Computer-Aided Manufacturing) systems. Typically, these systems are quite expensive, ranging from $40,000 to $300,000 for top-of-the-line systems. Obviously, smaller and less expensive (and, therefore, less comprehensive and versatile) systems exist. The computer-aided design tutorial addressed the needs of medium- and large-scale industry users of CAD/CAM systems.

Included in this session were discussions of CAD/CAM standards for data bases and techniques used for geometric modeling. Geometric modeling is a term used to describe the process of representing a three-dimensional object by a series of Cartesian, polar, or homogeneous coordinates with (or without) a series of equations. The object may or may not exist prior to the construction of the numerical or geometric model.

Three other tutorials on raster graphics and animation were oriented toward the use of raster-scan devices. Because raster-scan devices essentially use standard television technology, there is a significant price and performance advantage in their use. Personal-computer owners should be aware of this advantage, as many microcomputer systems have utilized raster-scan (television) technology from the beginning. Discussions of algorithms for modeling three-dimensional objects, simulation of light sources (shading and shadows), surface textures, and display optimization dominated these sessions. An emphasis was placed on the creation of realistic-looking images.

Another group of tutorials centered on what might be termed *human factors* in computer graphics. Human factors means the interface between human beings and machines. It is an area of computing in general that, while not being totally overlooked, has certainly been slighted. Those of us involved in interactive computing (including graphics) realized long ago, by necessity, how important a friendly, forgiving, and possibly even *natural* interface is for successful communication between people and machines. The frustration of having an interactive program bomb or hang before completing its task can be overwhelming.

Our batch-oriented colleagues have discovered this recently, primarily because on-line data bases are becoming more popular, and more batch-oriented computing professionals are finding their way into interactive projects. Recently, we have begun to discover the importance of aesthetically pleasing and more understandable graphic output. Many computer-graphics specialists have come into this area from the technical side, rather than from the artistic side. It should come as no surprise, then, that graphic designers can offer much sound advice about graphics layout and design. This information can be very valuable in businesses where executives are accustomed to expecting and demanding professional quality for graphics presented at board meetings and in annual reports. Two tutorials concentrated on psychological aspects, design methodologies, subjective evaluation, and design concepts as they relate to computer-graphics systems.

All of the tutorials were well attended. Although we were unable to attend all of them (they ran concurrently), those sessions we attended were well thought out and carefully presented.

Photos 1 thru 6 by Kenneth Livingston.

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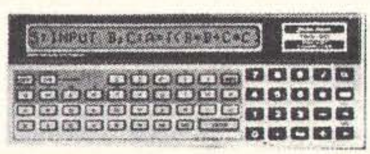

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The Conference

In an attempt to emphasize the importance of graphic-design concepts and the human-factors side of computer graphics, the first session was a special panel presentation chaired by Aaron Marcus, research consultant at Lawrence Livermore Laboratories. This panel featured graphic designers from the United States and Europe. They agreed that we have seen far too many examples of poorly designed graphics—especially computer-generated graphics. Anyone engaging in computer graphics would do well to obtain and read some good textbooks on graphic design, in addition to their computer-graphics texts. While a chart or graph is more understandable than a table of numbers, a well-designed chart or graph is more readable than one which has had no design principles applied to its creation.

The remainder of Wednesday's sessions were split into two concurrent sessions. Papers presented in one group of sessions were quite technical in nature: "The Theory, Design, Implementation and Evaluation of a Three-Dimensional Surface Detection Algorithm" and "Simulation and Expected Performance Analysis of Multiple Processor Z-Buffer Systems." Papers presented in the other group of sessions were more applications-oriented: "Geographic and Data Base Systems" and "Computer Graphics Moves into the Business World."

The latter area is of specific interest to one of us (Livingston), who is currently involved in the integration of computer graphics and market research. According to Carl Machover of Machover Associates, who chaired the business-graphics panel discussion, there are four computers used in business applications for every computer used in CAD/CAM types of applications. Assuming that these figures are accurate, the business-computer graphics potential is enormous. This position is supported by IBM's recent entry into the low-cost, color, business-graphics marketplace with its Model 3279 display terminal. Recent articles in *Harvard Business Review* (January 1980) and the *Wall Street Journal* also seem to reinforce this position.

Thursday's sessions embraced a wide variety of topics. Sessions dedicated to graphics software and languages, surfaces, and applications filled the morning. Papers were presented at these sessions ranging from the design of a LISP-based graphics language, to three-dimensional representation and rendering algorithms, and to stereographic displays of atmospheric data. (This latter session proved to be very interesting to us for reasons having little to do with computer graphics. The materials chosen for displays represented conditions existing in the Omaha, Nebraska, area—sixty miles away from our homes—when the 1975 tornado struck that area.)

Thursday-afternoon sessions were oriented toward rather specialized areas of computer graphics:

- Computer Graphics and Television
- Animation
- CAD/CAM
- User Views of CAD/CAM

Recent uses of computer graphics in television were discussed, including a presentation by ABC Sports on their use during the Winter Olympics. The CAD/CAM sessions included reports on graphics used in planning electrical-distribution systems, ship-hull design, and graphics at the Ford Motor Company. There was also a panel discussion addressing productivity gains and expectations achieved through the use of CAD/CAM systems.

Friday's sessions included discussions of graphics standards, human factors (more), and raster techniques. The question of graphics standards is of particular importance to those who regularly attempt to transport graphics programs or systems from one computing environment to another. While other areas of computing developed standards long ago (eg: COBOL, FORTRAN, Pascal, etc), the graphics area had not attempted such a feat until quite recently. This has all begun to change, thanks to the work of the SIGGRAPH CORE standards committee.

The human-factors presentations included discussions on color and how it is perceived by the human eye, and on a prototype voice- and gesture-input interface being developed at MIT. An afternoon session on raster-graphics techniques completed the conference program.

Perhaps the only negative criticism we offer concerns the famous SIGGRAPH film festival. This has become an annual event since its informal inception, at the first SIGGRAPH conference, on the balcony of one participant's dormitory room at the University of Colorado in Boulder. This year's film festival was held in a hotel ballroom designed to hold no more than 1500 people. With 1900 people packed into the crowded space, and lines waiting to get in, the hotel's management restricted access to the ballroom for safety reasons. A greatly abbreviated second showing left many participants frustrated. The film festival is a forum for some of the best computer graphics and animation produced during the preceding year and is always enlightening and well attended. We sincerely hope next year's conference committee takes the film festival's popularity into consideration during planning.

The Exhibition

Although this was the seventh annual SIGGRAPH conference, it was only the fifth annual SIGGRAPH exhibition. There were ninety-nine vendors listed in the exhibition guide for SIGGRAPH '80. At SIGGRAPH '76 (the first exhibition), there were only ten. This says much about the growth of this part of the industry. Another indicator of growth, according to Ken Anderson of the *Anderson Report* (a newsletter devoted to computer graphics), is the fact that last year the computer-graphics industry reached $1 billion in delivered products. The computing industry as a whole does approximately $40 billion in delivered products per year.

Several vendors at the exhibition were of special interest to personal-computer users. ABW Corporation demonstrated its TEKSIM package. TEKSIM allows the Apple II user to access the Tektronix Plot-10 software. Although the Apple/TEKSIM combination offers only about one-fourth the resolution of a Tektronix terminal, advantages such as lower cost, color displays, selective erase, and standard video output are claimed by the vendor. Apple Computer Inc displayed both the Apple II and III computers. Calcomp, which most of us think of as a vendor for the large-host user, demonstrated its 1051 drum plotter (among other products). The Model 1051 is an RS-232C-compatible, relatively low-cost product, which, considering Calcomp's quality reputation and service organization, makes it a viable product for passive-graphics production on small systems.

Cromemco, with which most personal-computer users are familiar, brought its line of high- and medium-resolution graphics hardware to the exhibition. Recent emphasis on efficient software designed to increase the productivity of the programmer and end user is evident in Cromemco's recently announced high-resolution graphics-software package. Digital Engineering, Inc, was present with its Retro-Graphics printed-circuit board. This transforms the Lear-Siegler ADM-3A terminal into a graphics terminal compatible with the Tektronix Plot-10 software package. This company also makes a cross-hair graphic-input cursor and a printer for the modified terminal. Houston Instruments, a division of Bausch & Lomb Corporation, displayed much of its pen-plotter line and its more recently developed electro-static plotter line.

An eight-color, eight-pen digital plotter was displayed by Soltec Corporation. This is an interesting approach to low-cost, multipen, passive graphics. The plotter is basically a single-pen plotter with "parking stalls" for additional pens and enough native intelligence to relocate each pen for changes in color and line weight, or for an optional cross-hair cursor for digitizing. Summagraphics exhibited its popular Bit-Pad One, a low-cost approach to graphic-data-entry problems.

Tektronix was present with nearly everything in its line of graphics terminals and its stand-alone 4050 series of desk-top graphics computers. Hewlett-Packard also displayed its line of desk-top graphics computers including the Model 9845C color machine. The space-shuttle image on this machine was very impressive.

Also present were vendors oriented toward heavy

Photo 1: *Megatek's new Wizzard color terminal. It also heralds the development of Megatek's device-independent software.*

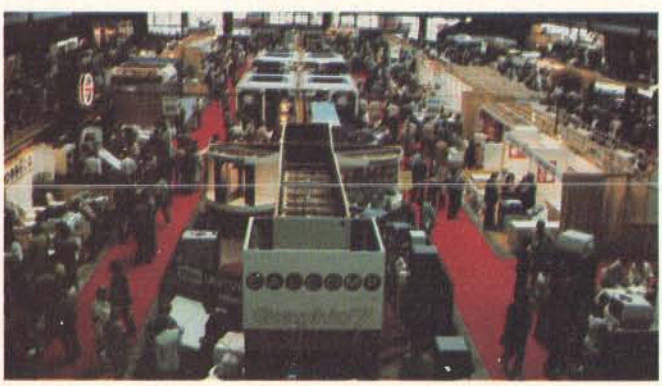

Photo 2: *Overview of exhibition area. The Calcomp booth is in the center foreground. Tektronix is in the center mid-way back. IBM and Hewlett-Packard are in the center rear and Megatek is to the right in the foreground.*

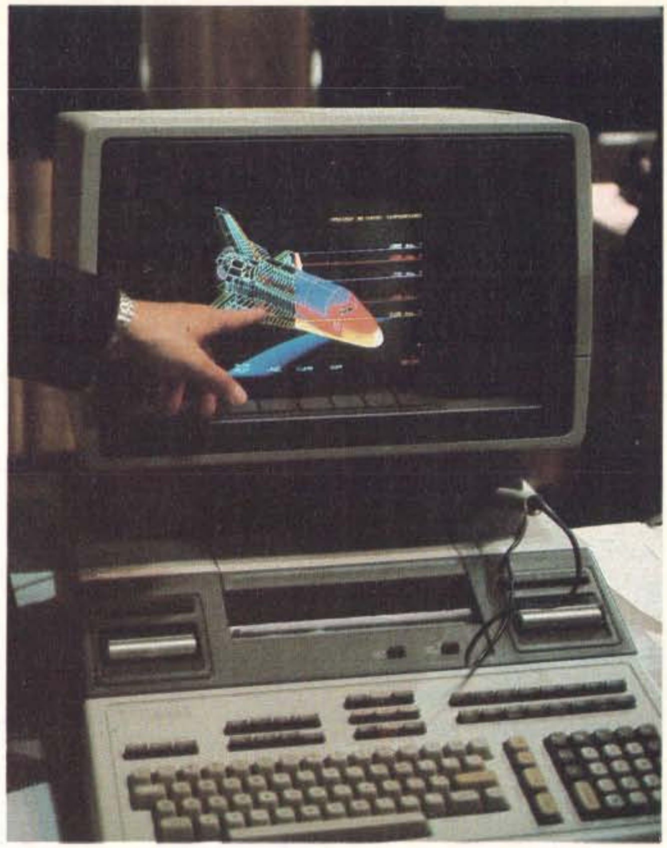

Photo 3: *The Hewlett-Packard 9845C color desk-top computer is being demonstrated by using an image of the space shuttle.*

graphics users. CAD/CAM applications by Computervision, Inc, were shown. IBM showed entries for all levels: the 3279 color terminal for low- to mid-level business-graphics users, the 3277 graphics-attachment feature for the mid-level engineering users, and the 3250 for CAD/CAM applications. Vector General and Adage featured their high-performance vector-display devices. Megatek, with a popular display booth, exhibited its new line of Wizzard graphics terminals.

With nearly 100 vendors displaying recent developments, it is not possible to describe all the new products. Suffice it to say that there was something for everyone at the exhibition. If too little information could be gleaned from vendor representatives at their display booths, many vendors also conducted forum sessions from morning until evening. Technical and management people were there to answer more detailed questions about their products.

There are three things we want to reemphasize as being significant in the computer-graphics industry:

- First, the continued development of lower-cost color graphics terminals—the user's capital expenditures are critical in justifying new approaches in problem solving.
- Second, an increased emphasis on graphics-software standards yielding greater productivity for software developers and end users.
- Finally, the beginning use of computer graphics by and for management, as opposed to its historically limited use as an engineering tool.

These items are very important to the growth of the computer-graphics industry. This exhibition, the conference, and the tutorials were dedicated to enhancing these three areas.

Harvey Kriloff and Robert Ellis, cochairmen of the SIGGRAPH '80 conference, and the SIGGRAPH '80 committee are to be commended for the quality of this year's conference. Next year's conference will be held in Dallas, Texas, and is scheduled for August 3 thru 7. Somehow we expect it to be hotter than the 75 degrees of Seattle. If present trends hold up, however, it will also be a fine and interesting conference. ■

Photo 4: *A Calcomp representative demonstrates the Model 1051 digital plotter.*

Photo 5: *IBM's Model 3279 color-graphics terminal. This terminal is oriented toward business and management graphics rather than toward engineering applications.*

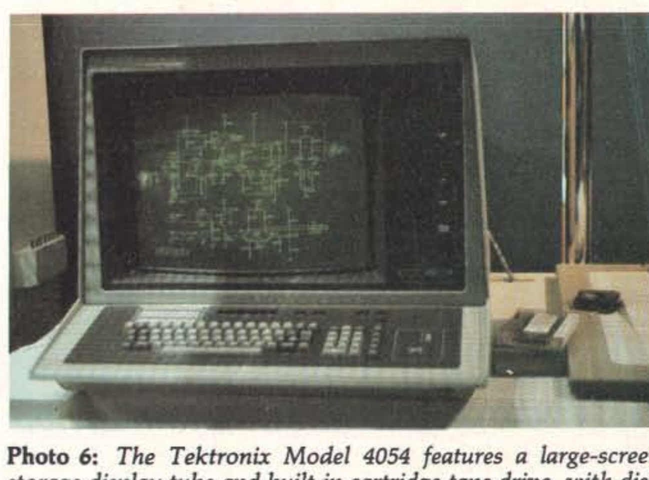

Photo 6: *The Tektronix Model 4054 features a large-screen storage display tube and built-in cartridge-tape drive, with disk drives optional.*

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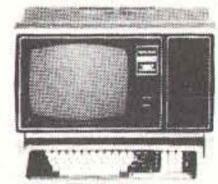



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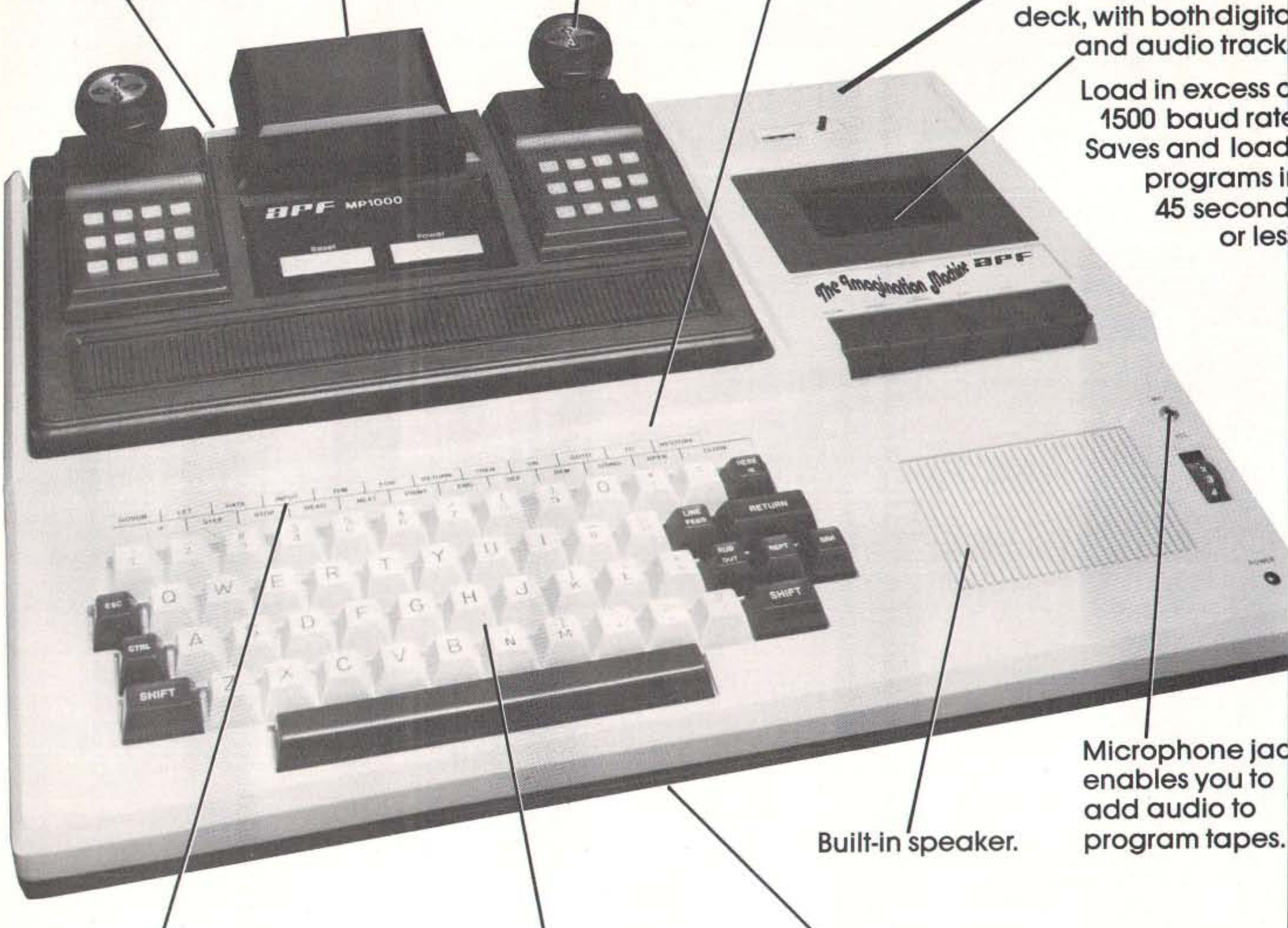

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We know sophisticated users aren't going to be satisfied forever using preprogrammed software. (Even though we offer a large library of educational, entertainment, home and business management programs.) So, we made The Imagination Machine user programmable, in both BASIC and MC6800 machine language. To simplify matters, we've just developed the first and only BASIC TUTOR course on cassette. With it, you can learn to program The Imagination Machine in BASIC, with hands-on training, right at the computer.

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The Imagination Machine has several unique features that can help you use your time at the computer more effectively.

For example, it stores programs **and** data on the same cassette tape. (With other computers, you have to read programs from one tape into the computer, remove the tape, put in another tape and store your data on the new tape.)

Another special feature is The Imagination Machine's unique keyword system, which simplifies BASIC programming. The machine has 24 different programs statements and commands printed at the top of the keyboard. You can enter these 24 into your program without retyping them every time you use them. Instead of typing out "PRINT," for example, you just press two keys and the word appears on the screen. The system helps prevent typing errors and can speed up entering programs.

A third feature is Timed Response Monitoring, which automatically adjusts the computer's pace and level to your own. It makes "tutoring programs," for instance, easier and more interesting to follow.

And then there are The Imagination Machine's three graphic display modes: 1. Alpha numerics, mixed with low-resolution graphics in as many as eight colors. 2. High resolution — up to eight colors — 128 x 192 display. 3. High resolution graphics — up to four colors — with 256 x 192 display.

And expandability.

A personal computer that can't grow along with your growing requirements soon becomes obsolete. So, we designed The Imagination Machine to be expandable. By adding APF's optional "Expansion Box" and interface cartridges, you can hook up any compatible floppy disk or printer, or an additional 8K RAM memory cartridge.

Full mini-floppy system $995.

For small business and professional use, you may require a full mini-floppy system. In that case, order APF's System II. It includes The Imagination Machine, the "Expansion Box," floppy disk interface and 72K-byte, mini-floppy disk drive. All for just $995! No one can come close to that price.

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A Simplified Theory of Video Graphics

Part 1

Allen Watson III
1261 Robbia Ct
Sunnyvale CA 94087

This is an interesting time for choosing a personal computer, especially if you are looking for one with a graphics display. As you can see from the summary of specifications in table 1, the available graphics capabilities of the personal computers are all different, and no one model has a clear advantage over all the others. To make your choice even more difficult, some models exhibit undocumented quirks that are not apparent from the specifications.

Your choice of a video-graphics system will depend on what you want to do with graphics and on the performance of the different computers. While I can't help with the first aspect of your decision, I may be able to help you understand system performance by explaining the operating principles of video displays and describing the various combinations of features available on popular personal computers.

The Importance of Video Graphics

Many applications of personal computers are modeled on conventional practices that have been developed over a period of several years, while graphics displays have been too expensive for general use until quite recently. Many existing computer programs do not use even the simplest graphics, although there are several notable exceptions, such as chess games that use high-resolution graphics to display the board and pieces, and music editors that display standard musical notation.

Here's the important point: computer-graphics displays can produce schematic diagrams, music scores, flowcharts, architectural drawings, and the like that are much easier for the person using the computer to understand than the unadorned columns of numbers that are usually associated with computers. Of course, you still might not be able to afford video-graphics displays as powerful as the one used by NASA to simulate the view seen by the pilot of the space shuttle during its return from orbit. Even though they have their limitations, the current small-computer displays will enable you to do a lot of interesting things.

Raster-Scan Video

While there are several different ways of displaying information on a video screen, all of the personal computers presently available use the same kind of *raster-scan* technique that ordinary television does. We'll take a look at the basic features of this technique, since they are shared by all inexpensive video displays.

Television is an imperfect compromise among several factors:

- resolution, which determines how much detail we can display
- frame rate (to be discussed later), which is the number of complete pictures transmitted in 1 second
- bandwidth, a measure of the frequency response, of the equipment involved

An increase either in resolution or in frame rate requires an increase in bandwidth, which adds to the cost of the equipment. If we must keep within a limited bandwidth, we can obtain better resolution only at the expense of jerkier motion and vice versa. There is a type of television called *slow-scan*, for example, that manages to transmit reasonably detailed images over the narrow-bandwidth channels used by amateur radio operators, but the resulting frame rate is so low that the illusion of motion is lost. We will see how much bandwidth is necessary for ordinary television after we look at the raster-scan process itself.

If we display a sequence of images that change only slightly from one to the next, and do it fast enough, the eye will not be able to separate them: *persistence of vision* will cause the separate images to fuse into a "moving" picture. In order to transmit such a sequence of images electronically, each image must be dissected into a series of dots that may be transmitted one at a time. The television camera does this by rapidly scanning the image in a series of horizontal lines which form a *raster*. The lines are scanned one after another in the same way that a person scans the lines of letters on a printed page. Reading is a process of converting information,

About the Author
Allen Watson III began writing FORTRAN programs for scientific analysis soon after receiving his bachelor's degree in mathematics. Later, as a full-time programmer, he wrote IBM System/360 assembly-language programs for the computer-aided design of calculators and has prepared and presented training courses about the Fairchild F-8 and Motorola 6800. Allen is currently writing and editing user manuals for Apple computers.

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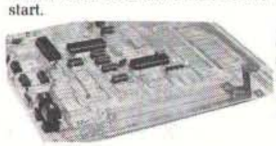

Level "A" is a complete operating system, perfect for beginners, hobbyists, industrial controller use. $129.95

Full 8" disk system for less than the price of a mini (shown with Netronics Explorer/85 computer and new terminal). System features floppy drive from Control Data Corp., world's largest maker of memory storage systems (not a hobby brand!)

Level "A" With Hex Keypad/Display.

LEVEL "A" SPECIFICATIONS

Explorer/85's Level "A" system features the advanced Intel 8085 cpu, an 8355 ROM with 2k deluxe monitor/operating system, and an advanced 8155 RAM I/O . . . all on a single motherboard with room for RAM/ROM/PROM/EPROM and S-100 expansion, plus generous prototyping space.

PC Board: Glass epoxy, plated through holes with solder mask. • **I/O:** Provisions for 25-pin (DB25) connector for terminal serial I/O, which can also support a paper tape reader . . . cassette tape recorder input and output . . . cassette tape control output . . . LED output indicator on SOD (serial output) line . . . printer interface (less drivers) . . . total of four 8-bit plus one 6-bit I/O ports. • **Crystal Frequency:** 6.144 MHz. • **Control Switches:** Reset and user (RST 7.5) interrupt . . . additional provisions for RST 5.5, 6.5 and TRAP interrupts onboard. • **Counter/Timer:** Programmable, 14-bit binary. • **System RAM:** 256 bytes located at F800, ideal for smaller systems and for use as an isolated stack area in expanded systems . . . RAM expandable to 64K via S-100 bus or 4k on motherboard.

System Monitor (Terminal Version): 2k bytes of deluxe system monitor ROM located at F000, leaving 0000 free for user RAM/ROM. Features include tape load with labeling . . . examine/change contents of memory . . . insert data . . . warm start . . . examine and change all registers . . . single step with register display at each break point, a debugging/training feature . . . go to execution address . . . move blocks of memory from one location to another . . . fill blocks of memory with a constant . . . display blocks of memory . . . automatic baud rate selection to 9600 baud . . . variable display line length control (1-255 characters/line) . . . channelized I/O monitor routine with 8-bit parallel output for high-speed printer . . . serial console in and console out channel so that monitor can communicate with I/O ports.

System Monitor (Hex Keypad/Display Version): Tape load with labeling . . . tape dump with labeling . . . examine/change contents of memory . . . insert data . . . warm start . . . examine and change all registers . . . single step with register display at each break point . . . go to execution address. Level "A" in this version makes a perfect controller for industrial applications, and is programmed using the Netronics Hex Keypad/Display. It is low cost, perfect for beginners.

HEX KEYPAD/DISPLAY SPECIFICATIONS

Calculator type keypad with 24 system-defined and 16 user-defined keys. Six digit calculator-type display, that displays full address plus data as well as register and status information.

LEVEL "B" SPECIFICATIONS

Level "B" provides the S-100 signals plus buffers/drivers to support up to six S-100 bus boards, and includes: address decoding for onboard 4k RAM expansion selectable in 4k blocks . . . address decoding for onboard 8k EPROM expansion selectable in 8k blocks . . . address and data bus drivers for onboard expansion . . . wait state generator (jumper selectable), to allow the use of slower memories . . . two separate 5 volt regulators.

LEVEL "C" SPECIFICATIONS

Level "C" expands Explorer/85's motherboard with a card cage, allowing you to plug up to six S-100 cards directly into the motherboard. Both cage and card are neatly contained inside Explorer's deluxe steel cabinet. Level "C" includes a sheet metal superstructure, a 5-card, gold plated S-100 extension PC board that plugs into the motherboard. Just add required number of S-100 connectors.

Explorer/85 With Level "C" Card Cage.

LEVEL "D" SPECIFICATIONS

Level "D" provides 4k of RAM, power supply regulation, filtering decoupling components and sockets to expand your Explorer/85 memory to 4k (plus the original 256 bytes located in the 8155A). The static RAM can be located anywhere from 0000 to EFFF in 4k blocks.

LEVEL "E" SPECIFICATIONS

Level "E" adds sockets for 8k of EPROM to use the popular Intel 2716 or the TI 2516. It includes all sockets, power supply regulator, heat sink, filtering and decoupling components. Sockets may also be used for 2k x 8 RAM IC's (allowing for up to 12k of onboard RAM).

DISK DRIVE SPECIFICATIONS

- 8" CONTROL DATA CORP. professional drive.
- LSI controller.
- Write protect.
- Single or double density.
- Data capacity: 401,016 bytes (SD), 802,032 bytes (DD), unformatted.
- Access time: 25ms (one track).

DISK CONTROLLER/ I/O BOARD SPECIFICATIONS

- Controls up to four 8" drives.
- 1771A LSI (SD) floppy disk controller.
- Onboard data separator (IBM compatible).
- 2 Serial I/O ports
- Autoboot to disk system when system reset.
- 2716 PROM socket included for use in custom applications.
- Onboard crystal controlled.
- Onboard I/O baud rate generators to 9600 baud.
- Double-sided PC board (glass epoxy.)

DISK DRIVE CABINET/POWER SUPPLY

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Beginner's Pak (Save $26.00!) — Buy Level "A" (Terminal Version) with Monitor Source Listing and AP-1 5-amp Power Supply: (regular price $199.95), now at SPECIAL PRICE: **$169.95** plus post. & insur.

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- ☐ **8k Microsoft BASIC in ROM kit** (requires Levels "B", "D" and "E") . . . **$99.95** plus $2 post. & insur.
- ☐ **Level "B" (S-100) kit** . . . **$49.95** plus $2 post. & insur.
- ☐ **Level "C" (S-100 6-card expander) kit** . . . **$39.95** plus $2 post. & insur.
- ☐ **Level "D" (4k RAM) kit** . . . **$69.95** plus $2 post. & insur.
- ☐ **Level "E" (EPROM/ROM) kit** . . . **$5.95** plus 50¢ p&h.
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- ☐ **48k RAM kit** . . . **$399.95** plus $2 post. & insur.
- ☐ **64k RAM kit** . . . **$499.95** plus $2 post. & insur.
- ☐ **16k RAM Expansion kit** (to expand any of the above in 16k blocks up to 64k) . . . **$99.95** plus $2 post. & insur. each.
- ☐ **Intel 8085 cpu Users' Manual** . . . **$7.50** postpaid.
- ☐ **12" Video Monitor** (10MHz bandwidth) . . . **$139.95** plus $5 post. & insur.
- ☐ **Beginner's Pak** (see above) **$169.95** plus $4 post. & insur.
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Computer Model	Text: Lines by Characters	Graphics: Method	Resolution	Aspect Ratio	Color: No. of	Method
Apple II	24 by 40	Subcell Mapping	40 by 48 280 by 192	4:3 4:3	16 6	NTSC NTSC
Atari 400 and 800	24 by 40	Subcell Mapping	160 by 80 280 by 192	8:5 4:3	16 4	NTSC NTSC
Commodore PET	25 by 40	Special	320 by 200	4:3	--	----
Compucolor II	32 by 64	Subcell	128 by 128	4:3	8	R-G-B
Exidy Sorcerer	30 by 64	Special	512 by 240	4:3	--	----
Radio Shack TRS-80	16 by 64	Subcell	128 by 48	4:3	--	----
Texas Instruments TI-99/4	24 by 32	Special	256 by 192	4:3	16	NTSC

Table 1: *A summary of some of the features available in personal computer displays. The graphics capabilities of available personal computers differ, and no one model seems to have a clear advantage. NTSC (National Television System Committee) indicates that American-standard color-video conventions are used. R-G-B indicates that separate red, green, and blue video signals are sent to the monitor.*

which is actually all present on the page simultaneously, into a sequence of words that follow one another in time. In a similar fashion, the raster-scan process converts a picture into a sequence of rapidly changing signal levels which represent the brightness of successive points on each scanning line.

When this rapidly changing signal is picked up by a television-receiving set, it is converted back into a visible raster on the screen of the picture tube. The neck of the picture tube contains an *electron gun* that projects a beam of electrons onto a thin layer of phosphor on the inside of the screen. Wherever the electron beam strikes the phosphor it produces a spot of light whose brightness depends on the intensity of the signal being received.

If the electron beam is swept across the screen so that the spot of light is always in the same relative position as the scanning dot in the camera, the picture will be recreated on the screen. The circuits in the television set controlling the position of the beam must be able to keep in step with the camera, so the picture information is interrupted for a short time at the end of each line (and for a longer time at the end of each frame). During these intervals the signal is changed to an intensity level that is never used for picture information, thus creating *synchronization pulses* that the television circuits can distinguish from the picture signal.

In this country, the repetition rate for the picture-scanning process was set at 60 scans per second so that interference from the 60 Hz AC power line will be synchronized; that is, any visible interference effect will stand still on the screen and be less noticeable than it would be if it were moving. Scanning the entire picture 60 times per second amounts to a lot of information per unit of time, and thus requires a very wide bandwidth. The television designers discovered that they could cut the bandwidth requirement in half by making the camera scan every other line during alternate scanning cycles called *fields*. Two successive fields cover all the lines in the raster 30 times each second, to make a *frame*. (See figure 1.) Since the lines of the two alternate fields mesh between each other, this technique is called *interlaced scanning*.

This seems like a rather complicated way of getting 30 frames per second, and you may be wondering whether television wouldn't work just as well with a straightforward scan of the entire raster, 30 times per second. This concept is fine as far as the 60 Hz power-line interference is concerned, but 30 frames per second is too slow for the human eye to merge the image into a continuous picture without noticeable flicker. If you are familiar with filmed motion pictures, you know that they are projected at only 24 frames per second, but a shutter interrupts each frame so that the effective flicker rate is actually 48 frames per second, fast enough for motion to appear continuous.

There are other factors which also

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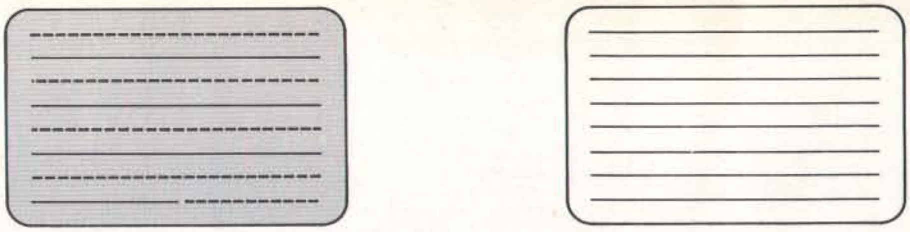

Figure 1: *A comparison of the interlaced (1a) and noninterlaced (1b) raster-scanning schemes. The standard home television receiver displays a picture made up of two alternating* fields, *each composed of 262½ lines. The lines are* interlaced *to produce a high-resolution picture that can be transmitted on a narrow bandwidth signal.*

complicate video-display timing. The *vertical-retrace interval* provides time for the television circuits to return the scanning dot to the top of the screen after each field has been completed. Since no picture information should be viewed during this time, the electron beam must be turned off or *blanked*: so, this time is also called the *vertical-blanking interval*.

A complete frame consists of two field scans and two vertical-retrace intervals. Television in the United States uses a total of 525 lines per frame or 262.5 lines per field. Each vertical retrace uses 21 lines, leaving 241.5 lines per field for the transmission of picture information. The odd half-line per field is necessary in order to make the lines of alternate fields interlace properly.

At 30 frames per second, 525 lines per frame is equivalent to 15,750 lines per second or 63.5 μs per line. Since all the lines are scanned in the same direction, the scanning dot must be returned across the screen between the end of one line and the start of the next. This is called *horizontal retrace* and takes about 15 μs.

Video Monitor Versus the Standard Receiver

So that the engineers at the television station can monitor the quality of the signal that is being transmitted, the picture is displayed on a *video monitor* (something like a television set without the antenna and tuner). It does not pick up other television broadcasts but is connected directly to the station equipment generating video signals. If the outgoing video signal already has the horizontal and vertical synchronizing pulses, it is called *composite video*. Most video monitors are also capable of accepting the video signals and synchronizing signals separately.

Because the monitor gets the signal before it has been through the various distortions imposed on it by the transmission and reception equipment, the picture displayed on a monitor is much sharper than the one on a home television set. The bandwidth of the video signal displayed by a home set is limited to less than 4.5 MHz, while most video monitors can handle 12 MHz or more.

Home television receivers display less of the picture in another respect: they crop off the edges by generating a raster which is too large for the screen. This deliberate *overscanning* is done so that the unavoidable errors in the positioning of the raster (caused by manufacturing tolerances and changes in the power-line voltage) will not leave unsightly gaps at the edges of the picture. In television broadcasting, no important activity is allowed to occur near the edges of the picture where it might be lost. Personal computers that use standard television receivers for their displays must have similar precautions: data is never displayed on the parts of lines near the sides of the screen, or anywhere on the top or bottom lines.

The television signal is transmitted over the air after it is impressed onto a VHF (very-high-frequency) or UHF (ultra-high-frequency) radio signal by *modulation*. Modulation is the modification of some characteristic of the VHF or UHF signal, or *carrier*, in step with the changes in the information that is being transmitted. The particular frequency used for the carrier determines which channel you tune your TV set to in order to pick it up. Circuits in the television can detect the changes in the carrier and extract the information they contain: specifically, the composite-video signal.

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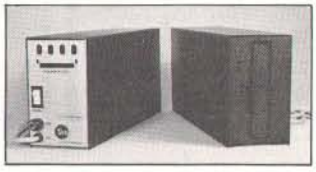

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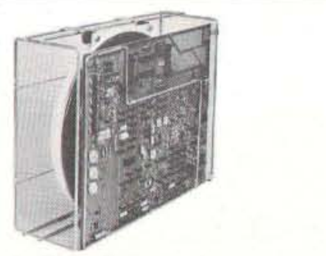

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dinary television set, we must either modify the set internally to give it a direct composite-video input, like that of a video monitor, or else we must add a *modulator* to our computer. The modulator acts like a tiny broadcasting station; it generates a VHF or UHF carrier that corresponds to a standard television channel (which is not being used by a local transmitting station) and modulates it with the computer video signal. The modulated signal can then be connected to the receiver's antenna terminals.

Displaying Computer Data

For our computer to produce a display on a television set or a video monitor, it must generate a composite-video signal. Generating the horizontal and vertical synchronizing pulses is relatively easy, since they just repeat over and over in a fixed numerical relationship. Our computer's internal clock can serve as a stable high-frequency source for a few additional circuits to use in producing the horizontal and vertical synchronizing signals.

> Combining functions helps to keep the cost of personal computing down.

To make the display circuits in personal computers simpler and less expensive, the whole complicated business of interlaced scanning lines and alternating fields has been eliminated in most cases. Instead, the odd half-line per field, which would have been needed to make the field lines interlace, is omitted; this leaves 262 lines per field. Without the interlace, the lines of any two successive fields appear in exactly the same places, so we can just as well think of a computer display as having 60 frames per second, with 262 lines per frame. In fact, a different number of lines per frame may be used if the designer finds it convenient, but the number must be within a few percent of 262 for the display to work with a standard television set.

Video Refresh

While synchronization is easy, generating a video signal with our computer is a little more difficult. First of all, a television picture must be continually regenerated by repeating the entire scanning process 60 times per second. This continual regeneration of the display is called *video refresh*; it requires a stream of data at a rate much too fast for our computer to keep up with—if the system had to compute the data anew for every scan. Instead, most computer designers set aside enough memory to store all of the data that will appear on the display. This reserved memory is called the *video-refresh memory*. Circuits designed especially for video-displaying read data from the refresh memory, in step with the video-synchronizing pulses, and transform the data into the video signal which is displayed.

Using part of the computer's own memory for video refresh has not been the general rule. Most large computer systems include video terminals that are independent of the main computer and contain their own

refresh memory. In other words, a small personal computer is a hybrid: part computer, part terminal. Combining functions in this way helps to keep the cost of personal computing down. Also, putting the refresh memory into the computer makes changing the display faster and easier.

Bit-Mapped Displays

There are several different methods of transforming the data stored in the refresh memory into an effective video display. The most straightforward method is to take the data just as it is read from the refresh memory and transmit it to the display 1 bit at a time. Each 1 bit in this serial bit stream appears on the screen as a spot of light, and each 0 bit as darkness. The size of the refresh memory is matched to the picture scan so that for each bit in the refresh memory there is one spot on the display screen. A one-to-one correspondence of this kind is called a *map*, and this technique for generating computer video displays is called *bit mapping*. An example of a bit-mapped display is shown in photo 1.

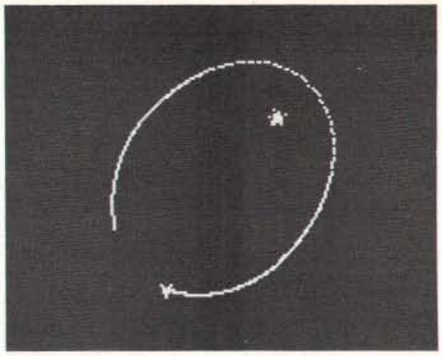

Photo 1: *Example of a bit-mapped display. This simulation of a spaceship in orbit around a star is done on a 180-bit by 150-bit map.*

Since we can program the computer to store data bits into the refresh memory in any pattern we desire, this kind of display can have all the versatility we want, but there are some drawbacks. For one thing, this system requires a large refresh memory. To store a display which is 200 dots high by 300 dots across, for example, takes 60,000 bits or 7500 bytes. Bit-mapped displays are relatively slow, too; just storing 0s into this much memory in order to clear the screen to black takes close to 1 second with the fastest microprocessor.

Displaying only letters and numbers means we can get by with a much smaller refresh memory than is needed for bit mapping. A letter that occupies eight rows of eight dots requires 8 bytes of memory in the bit-mapped display, but we can encode the same letter in ASCII (American Standard Code for Information Interchange) and reduce the size of the refresh memory by a factor of 8. This means that instead of sending the data bits directly to the display, it is necessary to decode each stored character and generate the appropriate video information. To do this, the refresh circuits send the character code (along with signals that indicate which of the eight rows of dots is currently being displayed) to another circuit called a *character generator*. The character generator is little more than a read-only memory that contains the video bit patterns for each of the characters we want to display.

Having a smaller refresh memory more than compensates for the additional cost of the character generator. For example, our 200-dot by 300-dot display has a capacity of 925 characters, in twenty-five rows of thirty-seven characters each. The bit-mapped memory needed for this is 7500 bytes, but we can store 925 characters in only 925 bytes if we use the character generator. It takes only one-eighth as long to update the refresh memory, too. The main drawback is its lack of versatility; we can only display characters of a fixed size and spacing. Obviously, a method of getting many different shapes without increasing the size of the refresh memory would be more flexible.

Using a byte of memory for each character, in all possible combinations of 8 bits, requires a total of 256 different codes. A complete set of uppercase and lowercase letters, numbers, and punctuation takes only ninety-six codes, leaving 160 combinations that we can assign to special shapes useful for graphics. Each special shape must be designed using the same number of dots and rows as the other characters. It may often be necessary to use several of them to make up the image of one object in the display. We can allow for this by setting up special characters such as straight-line segments, corners, intersections, and so on, in various orientations.

Several personal-computer manufacturers have taken this approach. While keeping the speed and small refresh memory of the character-generator-based design, they also have a reasonable graphics capability with good resolution. To compensate for the limited number of special shapes that you can have with this method, the Exidy and Texas Instruments computers have *programmable* character generators so that you can design your own shape characters and change them as needed.

Character Subcells

There is another way to add graphics capability to the character-generator display. Suppose we divide each of the character cells into four subcells, each of which is four dots square. By displaying any combination of these four subcells, with all dots illuminated, there will be sixteen possible shapes which we can display in each character location. By allocating sixteen extra character codes to represent these sixteen combinations, we can have a very versatile graphics system; however, it won't have much resolution. Dividing each character in half horizontally and vertically converts the twenty-five rows of thirty-seven characters in our example to a 50-block by 74-block graphics display.

We could increase the resolution by dividing the character cells into smaller pieces, but the number of combinations of blocks we would have to encode would increase very quickly. If we divide each cell by 4 in each direction, we increase the resolution to 100 by 148; but, there will be sixteen subcells in each character cell so we must store 16 bits of data for each cell. Since there are 65,536 different 16-bit codes, using read-only memory for the character generator becomes impractical. Instead, it is necessary to devise some logical method for generating the subcell patterns by decoding an extra byte of information, using additional circuitry. Also, the refresh memory would have to be twice as big to store these 2-byte codes. This may help to explain why the personal computers that use this approach have relatively low resolution. ■

The Power of VisiCalc

Robert E Ramsdell
POB 59
Rockport MA 01966

At a Glance

Software:	*VisiCalc*
Type:	*Screen-oriented matrix calculator for projections, budgeting, and many other numeric/data manipulations*
Author:	*Software Arts Inc*
Distributor:	*Personal Software Inc, 1330 Bordeaux Dr, Sunnyvale CA 94086, (408) 745-7841*
Price:	*$150.00*
Format:	*5-inch floppy disk*
Language:	*Machine language*
Computers:	*Apple II, Apple II+ or Apple III; Radio Shack TRS-80, Model I or II; Atari 800; Commodore PET and CBM computers, minimum 32 K bytes of programmable memory required, 48 K or more recommended*
Documentation:	*Loose-leaf binder with eighty-page tutorial manual, reference card*
Audience:	*Businessmen, accountants, attorneys, real-estate investors — anyone who needs to use a calculator for determining options available under different scenarios*

Introduction

The most exciting and influential piece of software that has been written for any microcomputer application is VisiCalc. I've been using VisiCalc almost full-time for the past six months and have written over 300 applications (which I refer to as models) for the program. During that time I have learned its strengths and weaknesses and have found that the authors have allowed for a tremendous number of variables and contingencies in its operation. The instant communication between the operator and the screen facilitates and enhances the manageability and interactivity of the program.

Since I am a certified public accountant, the majority of applications I have written are oriented towards accounting, a usage for which VisiCalc is particularly appropriate. In addition, I know of several attorneys who are using the program for estate- and gift-planning, one of whom is maintaining his accounts receivable, as well, on VisiCalc. A number of real-estate agents are using it to perform real-property investment analysis.

About the Program

VisiCalc is an electronic scratch sheet that is sixty-three columns wide (lettered A thru BK) and 254 rows long (numbered 1 thru 254). Any column/row coordinate can be referred to by any other column/row coordinate arithmetically or trigonometrically. Once the relationships between the coordinates have been established in the model, a change in any value which affects other values will be instantly updated. This gives the computer operator the ability to play instant what-if situations with the value in the matrix.

The program has a great deal of flexibility in its formatting, allowing any coordinate to be a label or a value, and allowing columns to be adjusted from three characters to full-screen width. The screen can be split into two windows, either horizontal or vertical, and each can be scrolled independently of the other. This makes the comparison of information extremely easy. Values can be formatted as full-decimal notation (up to eleven significant digits), two-place decimal (for financial usage), and integer.

An annoyance that I have found in the program is its inability to round off integers, which causes columns to add up imperfectly. This often creates the need for a great deal of additional work when attempting to prepare financial information directly from the model.

One of the most powerful features of VisiCalc is its ability to replicate an entire series of coordinate functions with a few keystrokes. When creating models with a series of identical calculations (such as a 10-year business forecast), only the calculations for the first column must be entered. Then the subsequent columns can replicate the same calculations (VisiCalc automatically uses the new coordinates) in a matter of seconds. This is a tremendous time-saving device when elaborate models are being created. The authors of VisiCalc have also provided the ability to insert, delete, and move entire rows and columns. This feature is useful if the model is finished and

About the Author

Robert E Ramsdell, CPA, is a microcomputer consultant who lives and works in Rockport, Massachusetts. His company, Pansophics, Ltd, published federal income tax models for 1979 and 1980 using VisiCalc and markets several other financial modeling packages.

the user discovers that an important calculation was omitted.

VisiCalc can be interfaced through most printers, and various printer configuration routines are set up directly through the program. The program will output to a printer with any number of character widths, so the choice of printer depends on the needs of individual users. Finally, the methods by which the program loads, saves, and deletes models on the disk are very well designed.

Specific Applications

Accounting applications abound for VisiCalc. Financial analysis, business forecasts, and projections which formerly required hours can be completed with VisiCalc in a matter of minutes. The pricing on a bill-of-materials inventory can be updated in a matter of seconds. Productions estimates can be updated instantly. Different scenarios can be examined and variables and constants interchanged until a workable model is achieved. Even with the advent of programmable electronic calculators, the complexity of forecasting (due to the interdependency of the variables) has limited the accountant to either the most rudimentary forecast or the extremely expensive alternative of time-sharing on a large computer.

Sophisticated and statistically valid time-series analysis can be performed on VisiCalc. Lead and lag regression analysis becomes as easy as entering the various formulas. Each of the variables can be changed or updated, and the results of the new analysis will be instantly displayed.

Small businesses will also find uses for VisiCalc. A model can be created which will allow for the printing of a financial statement whenever a trial balance is entered. Financial ratios and analysis are easily performed. The model can even calculate income tax and compare the current results with those of a previous period or a budget. (Some marketed models even print out tax returns.) Also, budgets are relatively easy to prepare (thanks to the replicate command), and changes and updates are easily entered.

More complex models can be designed for areas such as real estate and stock market investment analysis, where many interdependent variables must be given consideration. A change in any of these variables will instantly cause the entire model to be updated, and new comparisons can be made.

Documentation

VisiCalc comes with an eighty-page tutorial manual that's very useful for the beginner and a well-designed reference card. After one reading, however, the manual is not of very much help in running the program. A new manual is being written and may be available soon. In addition, several books are in preparation which will aid the VisiCalc owner in using the program.

Program Constraints

The primary constraint of the VisiCalc program is the programmable memory available to the user. In the Apple II, for example, a 48 K-byte machine will have about 25 K bytes available to the user for modeling. This may sound like a lot, but in fact model files require a lot of room. To compound this problem there is no easy way to move information between models (for example, in a business consolidation), so that using the same basic information in different models can be a big chore.

The only other limiting factor is the fact that the VisiCalc disk cannot be copied or backed up. The obvious reason for this to avoid software piracy, but it could prove to be a problem if someone decided that 5¼ inches was the perfect size for a coaster. There is a dealer program for instant replacement, however.

Data Interchange Format

Software Arts Inc, the creator of VisiCalc, has developed a common language for data (which it uses in VisiCalc) called the DIF (Data Interchange Format). The basic goal of the DIF is to allow the interchange of data between many different kinds of programs (such as data bases, graphing programs, report generators etc). The type of data which is addressed by the DIF is data which is stored in tabular form — columns and rows. By setting up a standard for such data handling it becomes easy to manipulate the data through program control.

Programmers and others who are interested in learning more about the DIF or would like to purchase the *Programmer's Guide to Data Interchange Format* ($1.50) should write to The DIF Clearinghouse, POB 70, MIT Branch, Cambridge MA 02139.

Conclusions

- VisiCalc is an extremely well-designed software package that can be used by anyone with or without a programming background. There is no programming language involved in the use of VisiCalc.
- The instant interaction between the user and the screen facilitates the understanding of the manipulation of the variables in the matrix.
- The ability to interchange data with other programs helps make VisiCalc an integral part of any business systems package.
- VisiCalc is the first program available on a microcomputer that has been responsible for sales of entire systems. ■

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Product Review

The MicroAngelo Video Display

Mark Dahmke
1515 Superior St
Lincoln NE 68521

Introduction

The MicroAngelo high-resolution raster graphics display stands well above other S-100 graphics displays in its price and performance range. Since the MicroAngelo is actually a single-board microcomputer, a great number of functions that previously had to be performed by the host computer are now done in *firmware* on the graphics board. Rather than using the memory-address space of the host as a graphics display buffer (32 K bytes in this case), the host communicates with the MicroAngelo through two parallel ports with simple yet powerful commands. The MicroAngelo decodes these commands and automatically performs the desired functions independently of the host processor. With this parallel-processing capability, system response time is greatly enhanced.

Hardware Overview

The MicroAngelo consists of a Z80A microprocessor with 32 K bytes of on-board programmable memory and 4 K bytes (expandable to 8 K bytes) of PROM (programmable read-only memory) firmware. The board contains all hardware necessary to generate a 512 by 480 dot black-and-white display for a television monitor (10 MHz bandwidth or greater). The board communicates with the host through two parallel ports which may be addressed to any of eight blocks of ports from hexadecimal 00 to F0. The video monitor may be connected via composite video (RS-170 standard) or direct-drive transistor-transistor-logic-level video, horizontal and vertical synchronization.

The MicroAngelo has four possible interrupt sources: data from host, data to host, light pen, and 60 Hz timer. Whenever a data byte is sent by the host or the host reads a data byte sent to it, an interrupt will occur in the MicroAngelo. An interrupt will occur when the light pen is fired and also when the timer produces a pulse. Of these four possible interrupts only the data from host and light pen sources is usually enabled.

At a Glance

Hardware:	*MicroAngelo high-resolution graphics display.*
Use:	*High-resolution raster-scan graphics display which may be used to draw character or graphics images on a standard television monitor.*
Manufacturer:	*Scion Corporation 8455-D Tyco Rd Vienna VA 22180 (703) 827-0888*
Price:	*The MicroAngelo graphics board and firmware (the S-100 board only) is $1095. Also available is the Graphics Subsystem which includes the MicroAngelo S-100 board, a graphics keyboard (IBM Selectric-style keyboard with some special function keys) and a high-resolution 15-inch monitor. Cost: $2495. A light pen is optional.*
Features:	*The MicroAngelo S-100 board generates a 512 by 480 dot black-and-white raster display. Communication between the MicroAngelo and the host computer is facilitated by two parallel ports. The MicroAngelo also has a dumb terminal emulation mode.*
Firmware:	*PROM (programmable read-only memory) firmware is provided onboard the MicroAngelo. High-level commands may be sent via parallel ports. Such functions as "turn on dot" or "draw vector" are implemented by single commands. The on-board Z80 intercepts these commands and performs the desired functions.*
Hardware required:	*Any S-100 mainframe computer or any computer which has an S-100 bus adapter. Although the MicroAngelo uses a Z80 microprocessor, the host processor need not be 8080/Z80 compatible.*
Documentation:	*An eighty-page user's manual is supplied.*
Audience:	*Anyone requiring high-resolution intelligent graphics on a small system.*

A connector is provided for the light pen interface. Several commercially available light pens will work with the MicroAngelo.

Jumper Options

Several on-board jumpers are provided for special applications. For example, it is possible to increase the clock speed of the Z80A microprocessor (and hence the speed of the board) from 4 MHz to 5 MHz, assuming that all the components are capable of operating at that speed. Interrupts (as previously discussed) may be enabled or disabled. The number of visible scan lines may be changed from the default 480 to 448 lines. If this option is chosen, the user is responsible for display management. The PROM sockets may be jumped to either the default 1 K byte per PROM or 2 K bytes per PROM.

Photo 1: *The MicroAngelo Graphics Subsystem. Included in the subsystem are the MicroAngelo S-100 board, the 15-inch high-resolution black-and-white monitor, and a special keyboard that has an IBM Selectric-style layout plus some special function keys on the far left and right. The light pen is optional.*

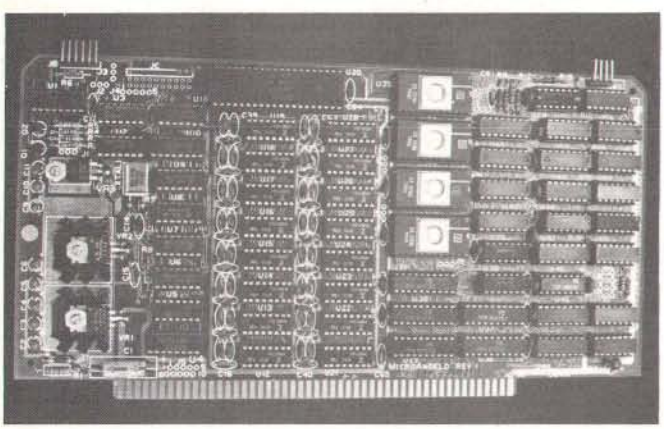

Photo 2: *A close-up of the MicroAngelo S-100 board. The board has a Z80A microprocessor, 32 K bytes of memory, and four 2708 PROMS (expandable to 8 K bytes 2716 PROMs). The board is actually a stand-alone 32 K computer. The video display generates 512 by 480 dots. In the ALPHA mode, up to 85 by 40 characters may be displayed on the screen.*

Adapting-MicroAngelo to Non-S-100 Systems

Since the MicroAngelo uses a simple parallel-port interface to the host system, it may be attached to almost any host system. Data is transferred via the eight parallel input and eight output lines of the S-100 bus connector. Power is supplied through pin 1 (+8 V), pin 2 (+18 V), pin 52 (−18 V), and pin 50 (ground). Address bus lines A7, A6, A5, A4 and pDBIN may be tied permanently high (+5 V); A1 and pWR are tied low (ground). A0 is connected to the host to select whether port 0 or 1 is addressed. (MicroAngelo uses two ports.) sINP and sOUT are connected to the host as input-and-output-control command lines. Using this twelve-line interface, the MicroAngelo becomes a stand-alone graphics display device. If interrupts are required, they may be easily added to the above set of signals.

Firmware

The MicroAngelo firmware is what makes the board so powerful. It takes all the work out of designing software and applications programs for the MicroAngelo. The Screenware Pak I is a well-integrated firmware package that allows the board to be used as a terminal emulator, a graphics display, or both.

If a byte is sent to the MicroAngelo (via the parallel port), it is interpreted by the firmware in one of two ways. If bit 7 (the most significant bit) is turned on, the byte is seen as a command. If it is off, the firmware treats it as an ASCII character and passes it to the terminal or ALPHA mode program.

In the text mode, the board will display forty lines with eighty-five characters per line. Text and graphics may be mixed on the screen. In the dumb terminal mode, the firmware will respond to the following control codes: backspace, horizontal tab, line feed, form feed, carriage return, escape, and delete.

Several features are available in the terminal mode. It is possible to display black-on-white or white-on-black characters, for example. Underlining may be turned on and off, and character overstriking may be allowed or disallowed. Two fonts are available, the standard character set or a user-defined font. The winking cursor may be displayed or inhibited, and the scroll mode may be changed. Scrolling may be done on a line-by-line basis, or, to improve response time, block scrolling may be done. Cursor addressing is available — rows run from 0 to 39, columns from 0 to 84. It is also possible to query the firmware to obtain the current cursor location.

Graphics-Mode Commands

The display may be manipulated in many ways in the graphics mode. First, the graphics cursor may be set to a value, read or queried, or set to the contents of the alpha cursor and vice versa. The format for most graphics-mode commands is:

<Command> <xh> <xl> <yh> <yl>

where *xh* and *xl* are the high and low bytes of the X coordinate and *yh*, *yl* are the high and low bytes of the Y coordinate respectively (in hexadecimal). The coordinates (384,256) would be sent as:

<Command> <01> <80> <01> <00>

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- Basic
- Z-80/8080 Emulator
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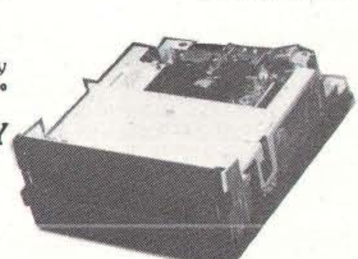

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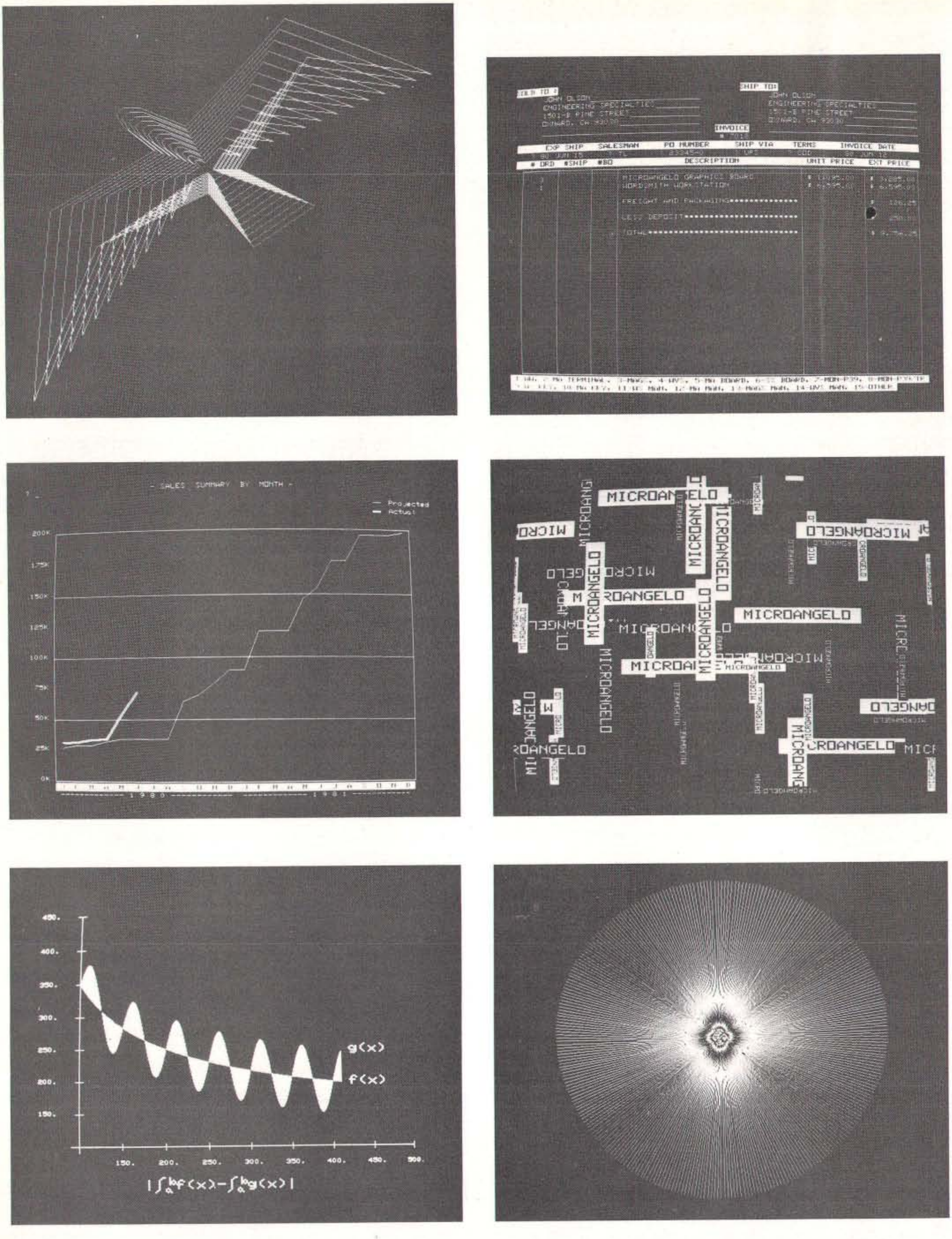

Photo 3a, 3b, 3c, 3d, 3e, 3f: *Sample displays produced with the MicroAngelo graphics board. Vectors may be drawn with single high-level commands.*

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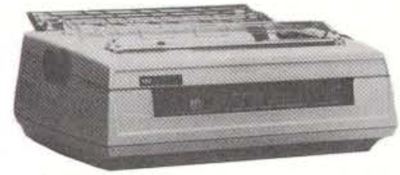

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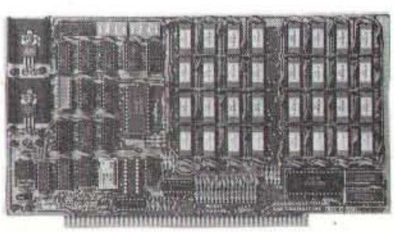

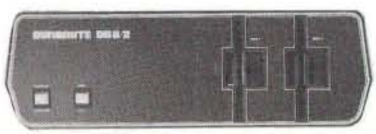

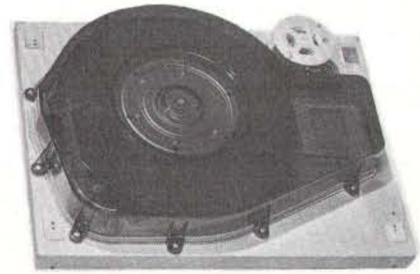

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Circle 123 on inquiry card.

Replacing <Command> with <84> would cause the firmware to set the graphics cursor to (384,256) on the screen. Some commands have no operands such as "clear screen". It is possible, with one command, to toggle the screen figure/ground. This means that every dot on the screen will be complemented (ie: reversed). If a dot is *on* (white), it will be turned *off* (black) and vice versa.

Individual dots may be turned on, off, complemented or queried. The form of this group of commands is also:

<Command> <xh> <xl> <yh> <yl>

In the case of the query command, the response is a single byte from the firmware with a value of 1 or 0.

A vector, the next level of sophistication, may also be turned on, off or complemented. The endpoint of the vector is specified in the command, and the starting point is assumed to be the current value of the graphics cursor.

It is also possible to work with *regions* of the display. If we wish to turn on all dots in a box with corners (X1,Y1), (X2,Y1), (X1,Y2), (X2,Y2) the command:

<95> <x1h> <x1l> <y1h> <y1l>
<x2h> <x2l> <y2h> <y2l>

would be sent. Regions may also be turned off or complemented.

Characters may be *plotted* depending on the graphics cursor and the mode selected for graphics characters. Options available include:

- normal-size or double-size characters
- black-on-white or white-on-black
- direction and orientation

Alternate characters may be defined. When the ALPHA mode alternate-character-set option is employed, sending an ASCII character to the firmware will display the alternate character instead of the standard font character. To define the character, the following sequence of bytes must be sent:

<9A> < asc> <s11> <s10>
<s9> ... <s1> <s0>

where 9A is the command, "asc" is the ASCII character code assigned to the character, and s11, s10, ... s0 are the twelve scan lines (6 bits wide) that make up the character in a 6 by 12 dot array.

Using the Light Pen

The light pen provides a convenient means of entering data or drawing on the screen without having to enter numeric coordinates. The coordinates of the pen may be read directly, along with a flag indicating whether or not the pen has been fired since it was last queried. Cross hairs may be displayed at any point on the screen when using the light pen. Another set of commands allows the cross hairs to be displayed, moved, and queried without regard to the light pen.

Memory Uploading/Downloading

Several commands are provided for dumping and loading the screen, thus allowing the user to save images on disk and restore them for later viewing or editing. Memory blocks may be examined or deposited allowing quick loading of alternate character fonts or user-written code. The firmware allows the user to deposit Z80 instructions in unused blocks of on-board memory. The user code may be defined as an op code and thereafter treated as just another firmware command.

Concerning Gray Levels and Color

The one drawback of the MicroAngelo is that it does not have gray levels — meaning the ability to have levels in between black and white or on and off. However, I was informed by Scion that another product, as yet unnamed, is available. This is another S-100 board which mixes the output of three or more MicroAngelo boards to produce *color, gray levels, or both*; four colors can be obtained with as few as two boards.This scheme does require more than one MicroAngelo board, but compared to other graphics displays with 512 by 480 resolution, this approach is still cost-effective. The board does offer interesting possibilities: 256 gray levels, the 256 possible hues or colors, and the winking of dots on an individual dot basis. Also, it is possible to use the winking effect to alternate between two colors.

Conclusions

The MicroAngelo video display system provides quality high-resolution graphics capabilities to S-100 bus (or similar) microcomputer systems, with an exceptional price-to-performance ratio.

On-board firmware provides a simple but powerful set of commands that makes system integration easy.

Although the board is designed to run on the S-100 bus, it can be easily adapted to almost any other bus or input/ouput port organization and does not require an 8080 or Z80 host computer.■

PMC-80

Level II 16K at $645

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The PMC-80 will operate with any of the many peripherals Radio Shack and other independent vendors have invented to plug into the TRS-80*. Most importantly, the Interface Adapter permits Expansion Interfaces with memory expansion to 48K to be added. An Expansion Interface will also permit the addition of Radio Shack compatible 5¼" disks and disk operating systems, RS 232, printers, etc.

*TRS-80 is a registered trademark of Tandy, Radio Shack.

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Circle 125 on inquiry card.

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This machine is super easy to use because it communicates naturally in BASIC, an English-like programming language. So you can easily instruct it or program it to do whatever you want, but you don't have to. You don't because it comes with a complete software library on cassette including programs for each application stated above. Ohio Scientific also offers you hundreds of inexpensive programs on ready-to-run cassettes. Program it yourself or just enjoy it; the choice is yours.

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French Drill & Tutor	SCE-339	6.50
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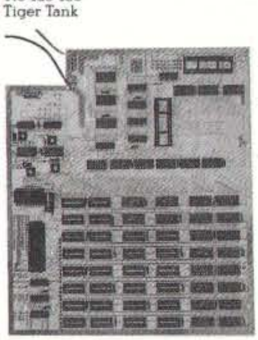
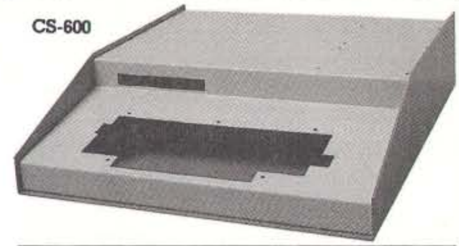


610 Board



Hardware:

Superboard II	as specified in the advertisement.	$279
610 Board	For use with Superboard II and Challenger 1P, 8K static RAM expandable to 24K or 32K system total. Accepts up to two mini-floppy disk drives. Requires +5V@ 4.5 amps.	298
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Getting to Know Your Monitor

Ron Dalpiaz
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There are few more satisfying pursuits than constructing, operating, and maintaining a personal computer system. The brave souls who develop their systems from discrete kits, preferring to assemble the "ideal" combination of peripherals, can feel justifiably proud of their accomplishments.

Inevitably, system failures will occur and can usually be remedied by personal knowledge and help from numerous books and articles on computer-circuit theory. But one frequently neglected area is the operational theory of the most used human-to-computer interface: the monochrome video monitor.

The video monitor is a basic part of most personal computer systems. The theory described here applies to converted television receivers and professional monitors. The two differ mostly in the video amplifier's frequency response and the cathode-ray-tube phosphor color: a professional monitor has a greater frequency response and a green phosphor. Additionally, the professional monitor has no tuner, intermediate frequency amplifier, video detector, sound or AGC (automatic gain control) sections, which are necessary in the broadcast receiver. The latter must have these sections rendered inoperable or selectively switched out when used as a monitor. Our discussion will assume a professional monitor with direct video entry.

The Picture Tube

The fundamental part of the video monitor is the CRT (cathode-ray tube). Various circuits are used to *deflect* and *modulate* the beam.

Figure 1 shows the elements found in the modern picture-tube electron-gun assembly. 6.3 V applied to the heater causes electrons to be emitted or "boiled off" from the cathode surface. The electrons are pulled toward the phosphorus screen by the high positive potential existing at the accelerating anode surrounding the bell of the picture tube. Typically,

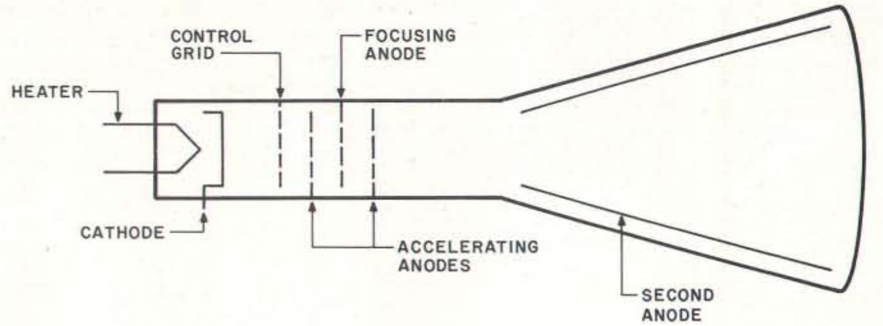

Figure 1: *Internal structure of a cathode-ray tube. The electron beam is emitted by the cathode when it is heated. Electrons are attracted to the screen by a high voltage (12 kV to 20 kV) on the second anode.*

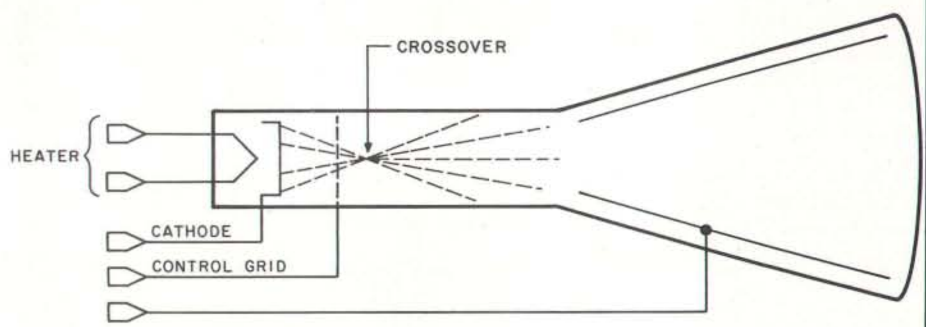

Figure 2: *The crossover effect. Two accelerating anodes, in conjunction with the focusing anode, are used to give a sharp beam and a well-defined screen image. Without the focusing arrangement, the electron beam diverges and splatters.*

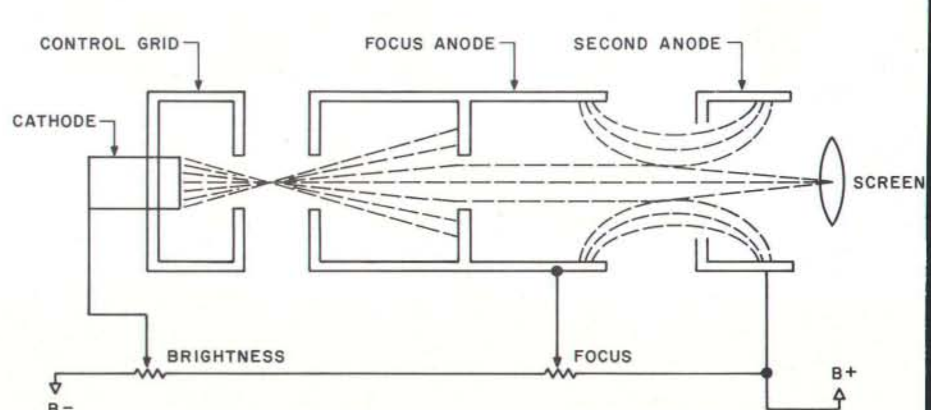

Figure 3: *Focusing the beam. By applying the proper potentials to the anodes and control grids, the electron beam can be "squeezed" to a pinpoint, for displaying the image on the screen.*

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voltages of 12 kV to 20 kV are fed to this anode from the monitor's high-voltage section.

The emitted electrons pass through various control grids and arrive at the screen in the form of a luminescent dot. The brilliance of the dot is controlled by adjusting the potential at the control grid. A voltage more negative than the cathode surface decreases the beam brilliance, while a more positive voltage increases the beam brilliance. Varying the control-grid voltage modulates the beam and produces the shades of black and white that form the picture elements on the monitor screen.

The two accelerating anodes, in conjunction with the focusing anode, are used to give a sharp, well-defined screen image. Without these anodes, the electron beam, after passing through the control grid, would encounter *crossover* and become broad and splattered, as shown in figure 2.

By applying the proper potentials to the accelerating anodes and the focus anode, the beam is squeezed and formed into a well-defined pinpoint suitable for displaying the images on the screen. This result is shown in figure 3.

Deflection Circuits and Rastering

The processes described so far would result in a black screen with a single bright dot in the center of the picture tube. The first step in obtaining a display on the screen is to pull the electron beam from side to side; this illuminates a line on the screen. The beam can be moved from top to bottom, in order to illuminate a whole screen of lines. If this is done rapidly enough, this will produce illumination over the entire area of the picture tube. This process is called *rastering*, and the dimly illuminated screen with no data information present is called the raster.

The *deflection yoke* consists of electromagnetic coils arranged in a vertical and horizontal configuration and is fitted around the picture tube neck; it is the primary device used for deflecting the electron beam. To move the beam from the top to the bottom of the screen (vertically), a rapidly rising (and more rapidly falling) sawtooth-current waveform is passed through the vertical windings of the yoke. Figure 4 shows a sawtooth waveform produced by a typical vertical circuit and the resultant vertical sweep of the beam.

As the current rises (Time A), the buildup of magnetic flux causes the beam to be swept from the top to the bottom of the screen. When the sawtooth reaches maximum value, it rapidly falls to 0 (Time B), causing the beam to be *retraced* from the bottom back to the top of the screen, where the process begins again. During the beam sweep from top to bottom, the trace is visible, but during the retrace the beam is cut off by the *retrace blanking circuitry* to avoid undesirable retrace lines from showing. Vertical sweep of the beam normally occurs 60 times per second.

The sawtooth wave is produced in an oscillator and amplifier section of the television monitor and is fed to the vertical windings of the deflection yoke 60 times per second. Vertical beam deflection, if used alone, would result in a bright vertical line in the center of the darkened screen. To complete the rastering process, the beam must also be deflected from left to right, and this is accomplished by the horizontal circuitry.

The horizontal windings in the deflection yoke are also fed with a sawtooth current originating in the horizontal oscillator and output circuitry. The frequency of this sawtooth is 15,750 Hz. The rising sawtooth current is passed through the horizontal windings in the yoke, causing the beam to be deflected from the left to the right side of the picture. The beam is then cut off by the *horizontal blanking circuitry*, and the rapidly falling sawtooth current sweeps the beam back to the left side of the screen to repeat the process. Figure 5 illustrates a typical horizontal oscillator and deflection circuit and the resultant screen trace.

The horizontal sawtooth voltage is produced by the horizontal oscillator and output section. The sawtooth is coupled into a horizontal output transformer before being fed to the deflection yoke windings. The main purpose of this transformer is to produce the high voltage necessary for the accelerating anode at the picture tube. The rapidly falling sawtooth voltage present during beam retrace is fed to the horizontal output transformer which steps it up to a

Text continued on page 212

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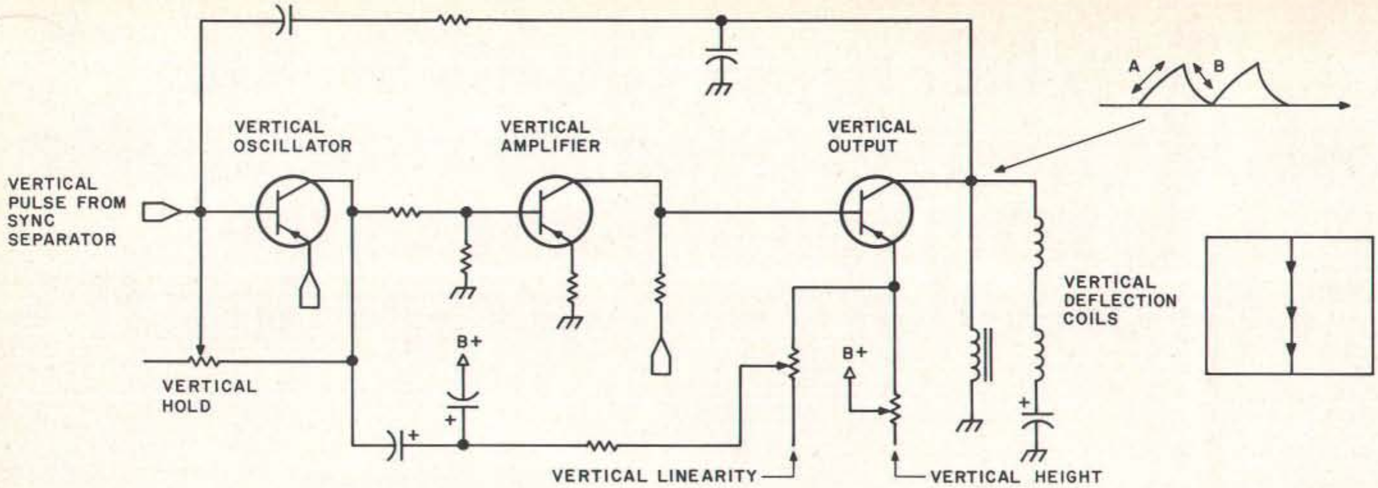

Figure 4: *Typical vertical oscillator/amplifier section. The circuitry shown creates a sawtooth waveform to drive the vertical deflection coils. This enables the electron beam to move from the top of the screen to the bottom 60 times per second.*

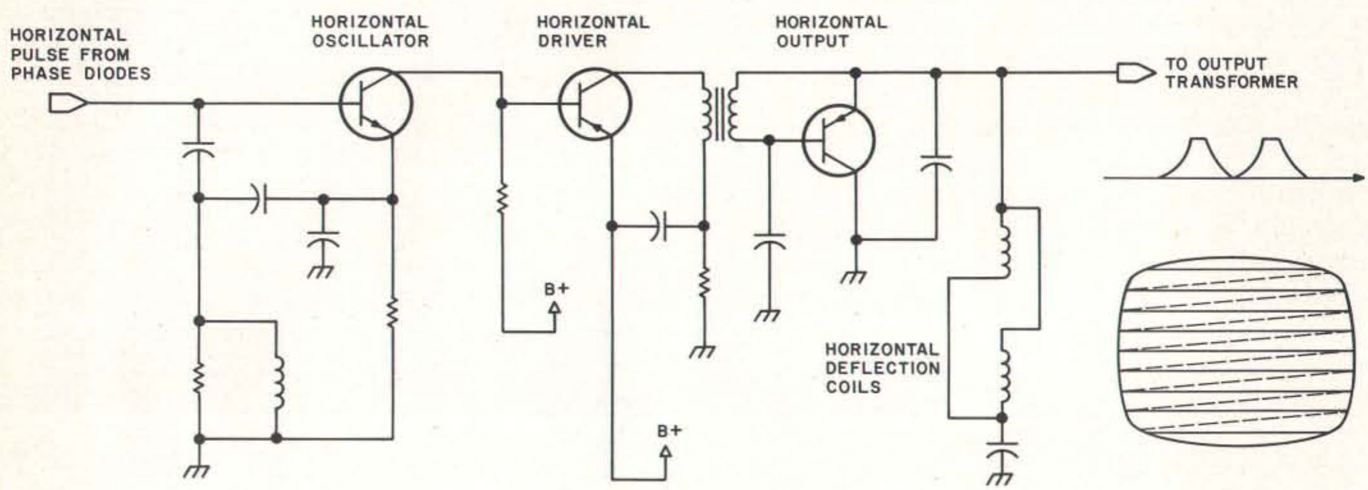

Figure 5: *Typical horizontal oscillator and output yoke. The horizontal deflection coils are driven in a manner similar to the vertical deflection coils, but at a much higher rate of 15,750 Hz.*

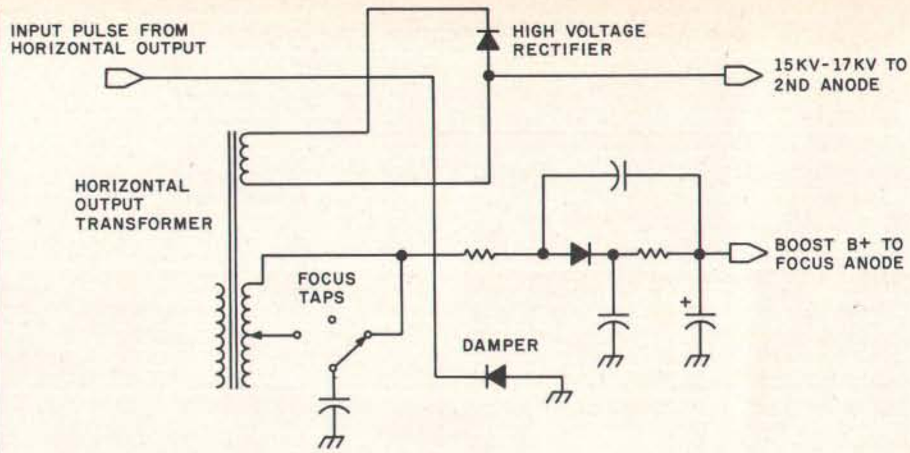

Figure 6: *Typical high-voltage circuit. High-frequency AC from the horizontal-deflection circuitry is also used to produce the high voltage supplied to the focusing and second anodes. After passing through a step-up transformer, the AC is rectified and filtered for use in various other circuits.*

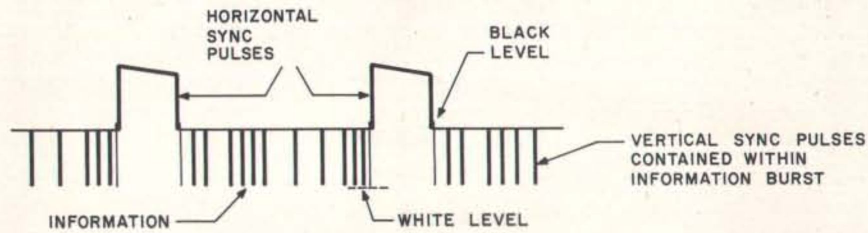

Figure 7: *Composite video signal. The signal sent to most video displays contains large pulses used to keep the horizontal oscillator in time with the picture information. The picture information is essentially an on/off control of the electron beam. In most video monitors, a low pulse turns the beam on, illuminating a dot on the screen; an intermediate voltage turns the beam off.*

Text continued from page 208:
very high potential. This pulsating high voltage is then rectified, filtered, and applied to the picture tube anode. Various taps on the transformer give alternate circuit voltages, including the focus voltage. Figure 6 illustrates a typical high-voltage circuit.

The production of high voltage to accelerate the electron beam combined with the horizontal and vertical deflection of the beam all work together to produce a dimly illuminated raster on the screen.

Interlaced Scanning

A careful study of the raster reveals the precision with which it is produced. The raster is usually composed of 525 finely spaced parallel horizontal lines, approximately 480 of which are visible within the viewing area of the picture tube. The number of lines and the scanning method used depend on the particular video interface used, and I will assume a high-quality monitor used with a video system outputting sixty-four or more characters per line.

The vertical oscillator and output section utilize an interlaced scanning method which traces 262.5 lines across the screen in 1/60 second, then returns to trace a second set of 262.5 lines *between* the previous lines. Each set of lines is called a field, and the two fields combined produce one complete data picture or frame. When the electron beam is modulated to produce a picture, one frame occurs once each 1/30 second, and thirty complete pictures occurring each second are sufficient to give the illusion of a continuous display. Exceptions to this process are video-interface techniques which do not interlace their fields but which trace a complete picture in one field. The 60 Hz scan rate can also vary.

The Composite Video Signal

In order to synchronize the monitor's vertical and horizontal oscillators with the video-interface output, a composite video signal or separate video and synchronization signals are coupled to their respective stages. The purpose of the syn-

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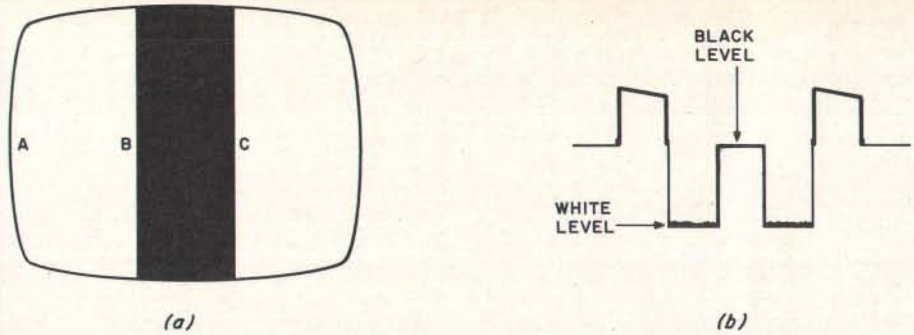

Figure 8: *Sample video display and corresponding composite video signal. The low portion of the composite signal (b) turns on the electron beam to illuminate the screen (a). When the intermediate voltage of the black portion is encountered, the beam is turned off. As the composite signal returns to the low white level, the screen is illuminated again.*

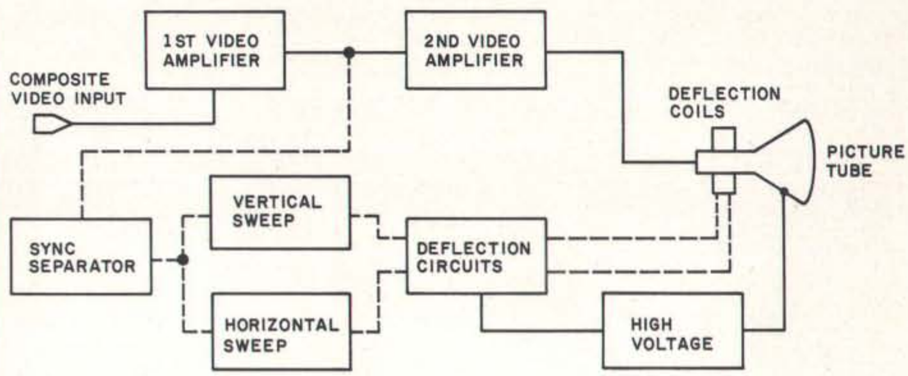

Figure 9: *Block diagram of the signal path in a typical monitor. The solid lines represent actual video information, while the dashed lines indicate the path of synchronization signals.*

chronization signals is to time the vertical and horizontal oscillator stages to the video information fed to the picture tube. Figure 7 is a sketch of the most widely implemented composite video signal.

This signal contains both the horizontal and vertical synchronization pulses (called *sync* pulses) and is applied to the *sync separator* where the horizontal and vertical pulses are separated, amplified, and sent to their respective oscillators to synchronize their respective traces. Included in the vertical sync pulses (assuming interlaced scanning is used) are equalization pulses whose function is to assure that the second field of lines is interlaced with the first.

Electron-Beam Modulation

The last link in the chain to create an image is to modulate the electron beam, turning it on and off to display white dots on the dim raster; this forms the dot matrices arranged as alphanumeric characters. The information contained in the composite video signal is actually a series of voltage reference levels which are amplified in the video amplifier and applied to the control grid or cathode of the picture tube to turn the electron beam on or cut it off. The black field in the display is represented by a voltage near the black level just under the horizontal sync pulse. Figure 7 illustrates this. The white dots in the picture are represented by the white level, or minimum voltage. In scanning the display shown in figure 8a, when the beam begins its trace at point A, the voltage level is minimum, or white as in figure 8b. When point B is reached, the voltage level jumps to the black reference level and cuts off the beam at the picture tube. A black screen is evident. At point C, the beam is on again, and white is presented.

Production of a display on a video terminal is more complex, but the beam is modulated in the same way to produce numerous dots of white

(corresponding to data elements sent from the video interface). Alternate methods employ black data elements on a white field. The frequency response of the video amplifier stages determines how fast the beam can be turned on and off; the faster the response, the more data elements can be displayed on each line with good resolution.

Home Television Receivers

The video amplifier section in a professional monitor differs greatly from that in a television receiver. Television receivers can rarely be modified to produce dots of a rate beyond 5 MHz, while monitors can be purchased with from 12 to 100 MHz response. The converted television receiver must have its tuner, intermediate frequency amplifier and sound section switched out when employing direct video input. The limited frequency response generally allows only up to thirty-two characters per line, but the low cost of such receivers makes them an attractive choice.

After injection and amplification of the composite video signal in a television receiver used for video display, the video is separated from the synchronization pulses, and the latter are sent to the synchronization section. The separated video information is then amplified by the video amplifier, coupled to the picture tube, and used to modulate the electron beam. In systems using separate video and synchronization inputs, the vertical and horizontal pulses are not processed in a synchronization separator, but are fed directly to their respective oscillators. The separate video is directly coupled to the video output stage.

Troubleshooting

When all the circuits described above are working in perfect unison and are synchronized by the composite video signal, a stable display will be produced. A malfunction at any stage in the monitor creates a problem peculiar to that particular section. So, what do you do when the monitor fails?

The first step is to obtain a good, accurate schematic of the circuitry (preferably *before* any problems occur). The manufacturer should supply this. Locating problems can be somewhat simplified by considering a monitor as consisting of the sections shown in the block diagram of figure 9. Using this diagram, we can observe the signal flow lines to generally predict the section where the problem may lie. Some symptoms and their solutions will prove helpful.

- No Video or Raster: Assuming that the power supply is functioning, the absence of raster could mean that the electron beam is not being deflected across the picture tube screen. Perhaps no beam is present, so the logical checkpoint is the high-voltage section to see if the beam accelerating potential is present. Use of a high-voltage probe is necessary here.

  If the high voltage is present at the anode of the picture tube, it is best to measure voltages at the control grid and cathode of the picture tube, assuming that a visual check revealed that the heater was lit. Having cleared the picture tube and proving that a beam can be formed, proceed to check the horizontal-sweep section where voltages originate which directly or indirectly affect both horizontal and vertical deflections of the beam. The final step would be a check of the deflection system itself.

- No Video — Raster Present: A raster always indicates that vertical and horizontal sweep, deflection, high-voltage and low-voltage sections are working. Assuming a video signal is present, we should investigate all portions of the monitor's video amplifier section, also the picture-tube-control-grid and cathode circuits.
- Raster and Video Present — Vertical Rolling: Assuming the vertical hold control does not stop the vertical roll, this indicates that the vertical oscillator is not in step with the video interface signal. The obvious starting point is the vertical sweep section, particularly the vertical oscillator.
- Raster and Video Present — Horizontal Lines: This problem is very similar to the above vertical problem, except that horizontal lines are the problem. Again, this indicates that the horizontal oscillator is out of step with the video interface circuitry. Investigate the horizontal oscillator to correct this problem.
- Raster, Video Present — Display Rolling and Drifting Sideways: This is both a vertical *and* horizontal problem. Obviously the circuit feeding both horizontal and vertical oscillators is at fault, and this would be the synchronization separator or amplifier. When symptoms or tests indicate one section as the probable point of trouble, proceed to check voltages for direct-current biasing and use an oscilloscope to investigate waveforms.

Troubleshooting is a logical, step-by-step procedure. In repairing your monitor, the screen is the best visual aid you have, and should be utilized to the utmost in preliminary generalizations as to the problem circuit. And troubleshooting a video monitor yourself, whether or not it's homebrew, can give you the satisfaction of knowing your hardware a little bit more. ■

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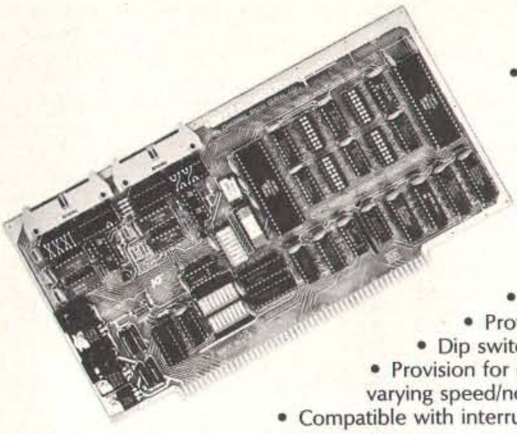

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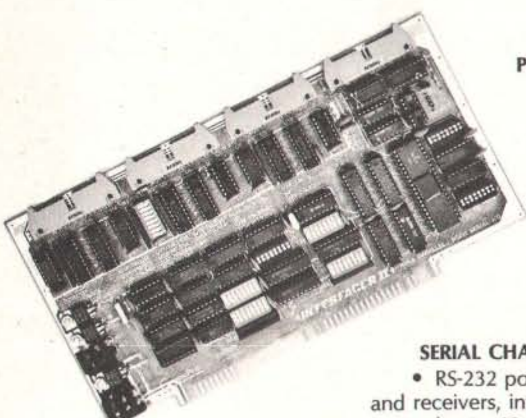

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Digital Storage of Images

Thomas Williams
39A Mill St
Maynard MA 01754

The availability of inexpensive computer memory has brought high-resolution gray-scale and color graphics within the reach of the home computer experimenter. Over the last decade the ability to capture video signals in digital form, manipulate the stored data, and display it has moved from military and research engineers to undergraduates and interested hobbyists.

Quantization

Before examining methods of capturing video signals, let's look at image quantization, which is the process of converting an image into one or more arrays of numbers. The value of each array element represents the measure of light present in the area of a corresponding point in the original image. These array or picture elements are called pixels.

A typical gray-scale image might be quantized into a two-dimensional array of values that range from 0 to 15, representing intensity values from black to white. If the array were 256 by 256 elements or 64 K pixels, each with a 4-bit value, the array would occupy 32 K 8-bit bytes of memory.

Scanning

To perform the quantization, the image is scanned by a transducer capable of converting light into an electronic signal. This signal is sampled periodically, and each sample is converted into a numeric value. Transducer sensitivity, scanning rate, and sampling rate all affect the quality and form of the digital image.

There are basically four methods of scanning images. The first requires the movement of the transducer with respect to the image or scene. This is typically done by drum scanners where an image is spun under a light source and photodiode. (See figure 1.)

No matter how much effort is spent on improving the system, the results are only as good as the input.

The second method deflects either a light beam or sensor optics in two dimensions to scan the image. This method is often used in a device called a flying-spot scanner; such devices were used during the first decades of television for transferring movies to video form for broadcast.

The third method is the use of a television camera. In a television tube (ie: a vidicon) the image is focused on a target that is scanned with an electron beam. (See figure 2.) It can be thought of as a CRT (cathode-ray tube) working in reverse.

The fourth method, which is still rather expensive, is the photodiode-array camera. It uses an integrated circuit which contains an array of photodiodes and circuitry to help scan the array. Advantages of this camera over vidicons are the stability of its geometry (as vidicons require electron-beam deflection which is never completely repeatable and accurate) and the inherent immunity to shock (as vidicons are vacuum tubes and thus sensitive to abuse).

Video Costs

As with anything electronic, there are uncontrollable costs of precious metals and precision parts, and controllable costs of design and assembly. Hardware hobbyists with good supplies of parts can usually find clever ways of cutting costs. Most of us, though, have limited resources and must buy kits or search for bargains on assembled equipment. Video cameras sometimes show up at flea markets in various states of repair and can provide you with a good video signal at very low cost. Home-video enthusiasts and closed-circuit security systems have also provided a marketplace for inexpensive cameras.

Cameras with sufficient quality for use with digital image-capture systems can be quite expensive. The increased costs usually provide more geometric linearity and a more uniform imaging-target surface. Black-and-white cameras range in price from about $200 to $10,000. At the lower end of the price scale you can expect about 5% error in the linearity of the vertical and horizontal scanning. Usually these errors are not noticeable. Geometric linearity is only important when the image-capture system is used for a precise geometric task, such as measurement of object size.

Target nonuniformity *is* a source of concern. Inexpensive cameras may have differences in video level (for uniform illumination) across the im-

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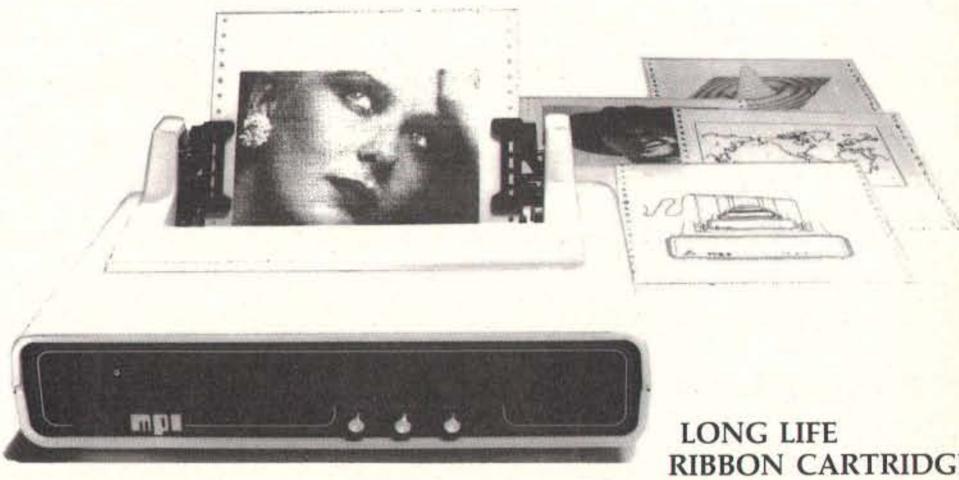
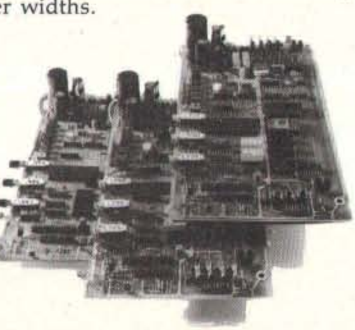


QUALITY

he attractive, durable 88G casework is rmed from impact-resistant, flame-tardant Styron. Microprocessor con-olled stepper motors provide precision ntrol over print head and paper sitioning. Computer quality tractors sition paper for readability and are lly adjustable to accommodate varying per widths.



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he microprocessor array provides the telligence for a dual RS232 serial d a Centronics® type parallel interface. th inputs are fully buffered to allow e 88G to receive data and print multaneously. A 1K character buffer is andard with a 2K buffer available as option.

he short line thruput of the 88G has en increased by incorporation of a *uick Cancel* feature that fully utilizes e bidirectional/unidirectional printing pabilities. Built-in diagnostic and lf-test capabilities allow the user to sily pinpoint system problems and *Power On* confidence st verifies erational status of e printer each ne power is applied.

VERSATILITY

The 88G prints a full upper and lower case 96 character ASCII set with a crisp, clear 7x7 matrix in 80, 96, or 132 column formats. For text processing and correspondence applications, an 11x7, 80 column serif style matrix can be selected by switch or software command. The dual tractor/pressure-feed paper drive system allows the user to choose either pin-feed, roll, or single sheet papers up to 9.5 inches wide.

Complete forms control allows the 88G to be quickly configured for printing single or multiple-ply invoices, purchase orders, checks, or any type of preprinted form. Optional paper roll holders and single sheet feeders can be quickly attached.

The wide use range of the 88G makes it the perfect companion for business systems, data processing, RO teleprinter and terminal printer applications.

GRAPHICS

A high-resolution, dot-addressable graphics option can be added for applications requiring plotting, printing of screen graphics, drawings, illustrations, etc. Single dot print resolution greatly extends the usefulness of the graphics capability. Selection of one of the four horizontal dot densities available customizes the graphic printout, and alphanumerics can easily be included for titling of graphs and illustrations.

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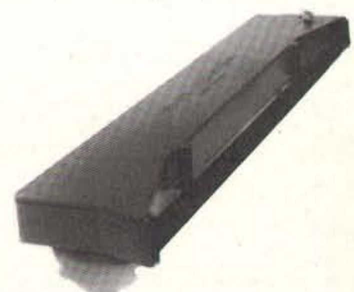

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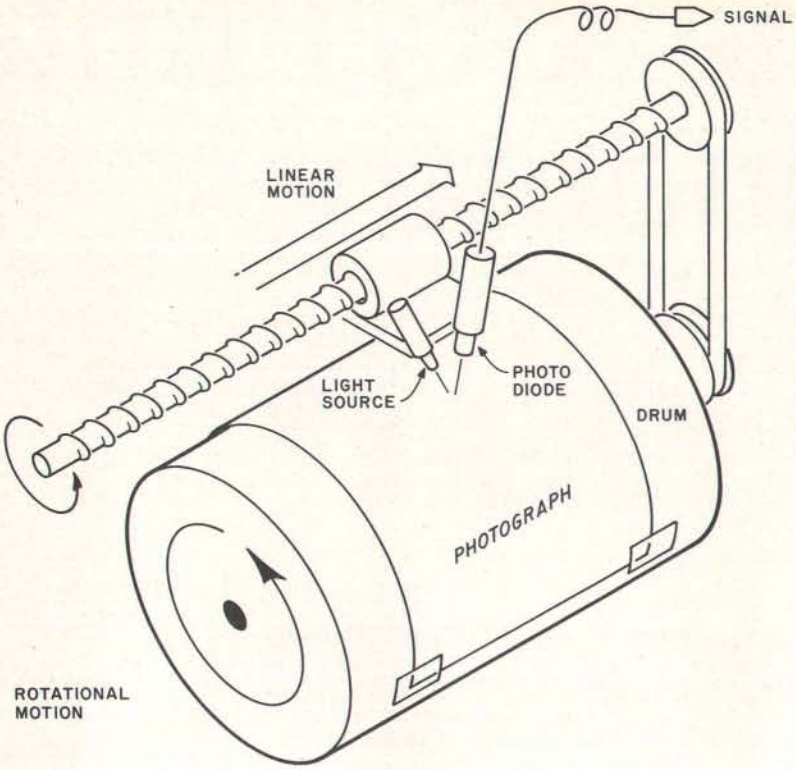

Figure 1: *A drum scanner produces high-quality results by moving the photograph relative to the sensor. Its drawbacks are that it requires precision mechanical construction, works very slowly, and the signal it produces is not video-compatible.*

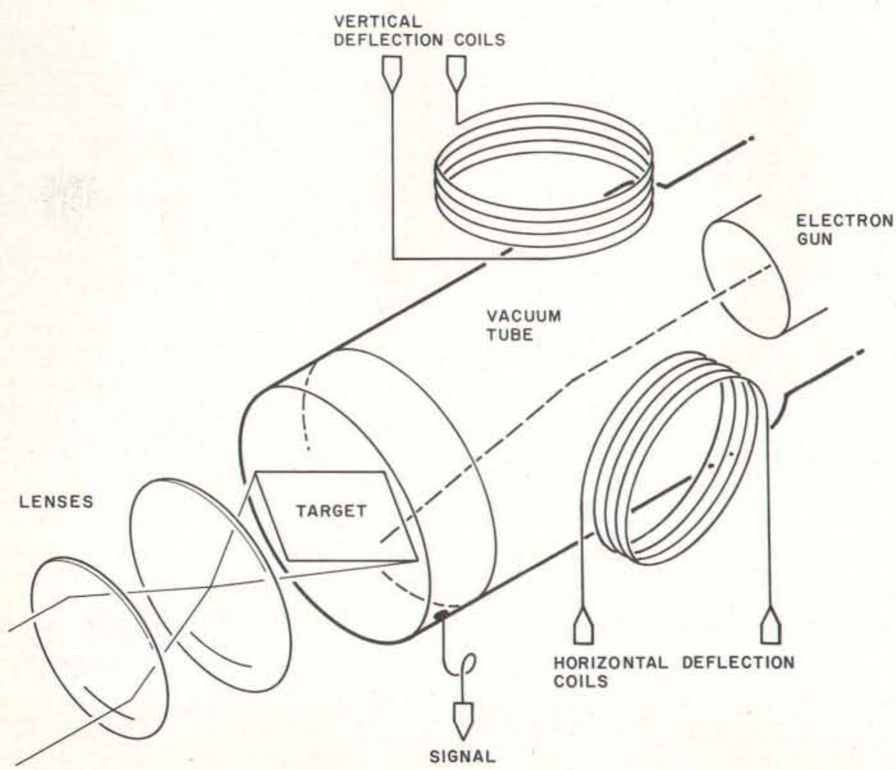

Figure 2: *A vidicon tube. This most popular method of converting an image into an electronic signal uses a photo-sensitive imaging target which is scanned by an electron beam. The resulting signal is the scanned image in the form of a changing voltage. Disadvantages of the vidicon are its unstable geometry (since electron-beam deflection is never completely repeatable and accurate) and its low resistance to shock (since vidicons are vacuum tubes).*

age as much as 20%. This error (also called *shading*) is still present in more expensive cameras where it's typically reduced to 10% or less. Fortunately, the shading effect changes slowly across the image target. Actual defects in the target are often found in inexpensive cameras, leading to black or white spots in the image.

It is possible to make some correction for the effects of shading and defects after the image is quantized. To do so, you first quantize an image of a solid-gray surface. The deviation of each point's value from the average value indicates the amount of correction that is necessary. By storing this image (or an image of corresponding correction values) the recorded target sensitivity can be used to improve the quality of another image quantized from the same television camera.

A television camera is to an image-capture system as an antenna is to a television set. No matter how much effort is spent on improving the system, the results are only as good as the input. Although the system can be made to compensate for some of the deviations in the camera, improvement of the video source is usually the choice for further investment once an image-capture system is in place.

A video image is normally generated in a 4:3 aspect ratio. This means that a properly operating camera produces it in a format that must be presented on a screen with three units of height and four units of width. Typical television sets are adjusted to approximately this ratio. If the video signal is quantized into a square array of square pixels, only a portion of each line should be quantized. (See figure 5.) Because there are approximately 512 lines of useful video image in a frame (approximately 256 lines in a field), it is often convenient to work with 512 or 256 squared resolutions. Some manufacturers of quantizers offer nearly square pixels by quantizing during 3/4 of the horizontal period, while others offer square pixels by digitizing the entire image at 640 by 512, 320 by 256, or other resolutions. Still others offer rectangular pixels. To achieve square pixels, the sampling rates must be increased by a factor of 1.33. If the entire image is to be quantized with square pixels, the memory requirements must also be increased by a factor of 1.33.

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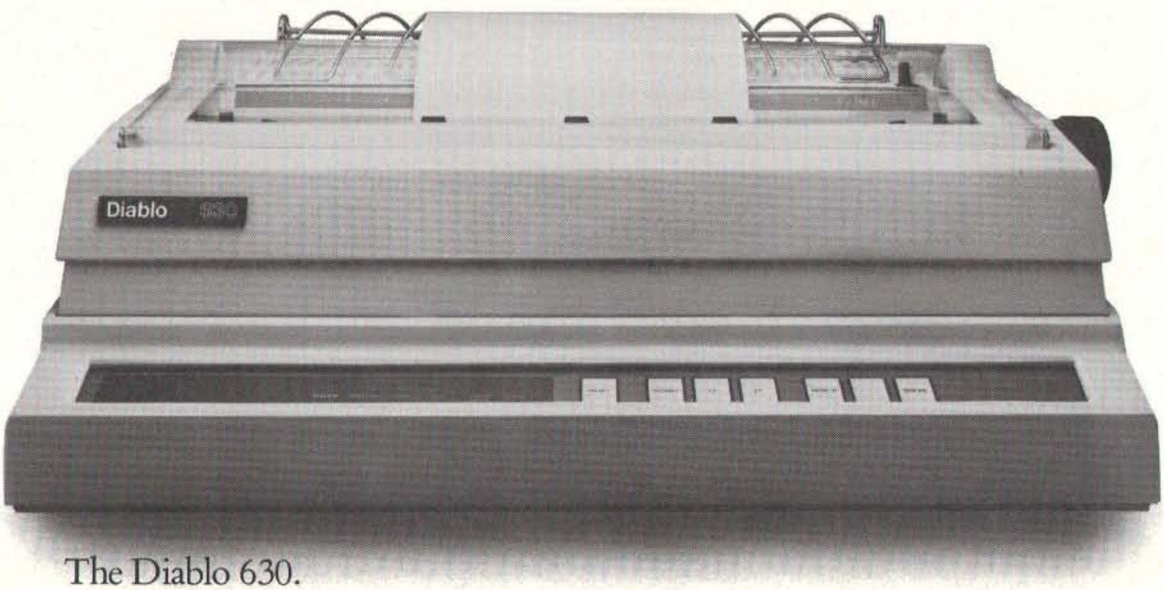

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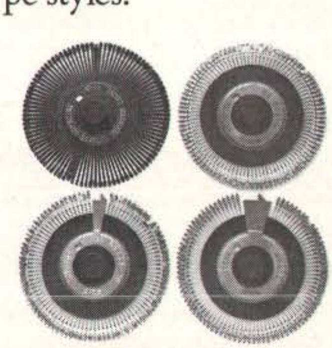

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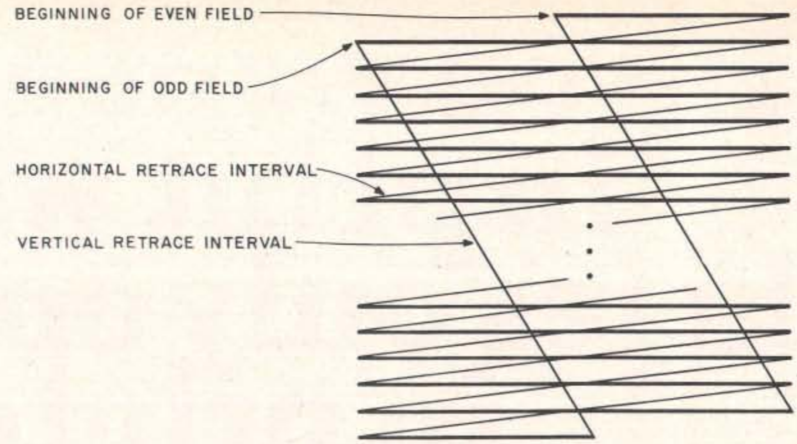

Figure 3: *Video lines are interlaced in a 2:1 ratio to reduce image flicker. Each frame of a video image (1/30 second) is made up of two fields. During the first 1/60 second the even-numbered lines are scanned, followed by the odd-numbered lines during the second 1/60 second. The luminance signal (black-and-white intensity) is indicated by the heavy lines. The narrow lines indicate intervals during which the electron beam is off in order for the deflection circuits to prepare for the next luminance signal.*

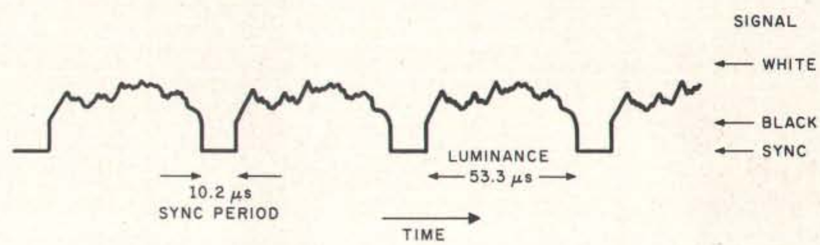

Figure 4: *Each line of a video signal is composed of a horizontal active-line period (53.3 μs), which contains the luminance information, and a sync period (10.2 μs), which contains reference levels and the horizontal sync period.*

Noise and Averaging

Video signals, like all signals, contain noise. It arises from several sources, primarily the circuits which amplify the sensor output. Very high quality video sources can have signal-to-noise ratios exceeding 45 dB. This is approximately equivalent to a noise of $\pm 1/2$ the least-significant bit in a 7-bit quantization. However, many inexpensive home cameras, videotape, and off-the-air sources often exhibit signal-to-noise ratios worse than 25 dB or about $\pm 1/2$ the least-significant bit in a 4-bit quantization. Why is it that such noisy video is still quite acceptable to a viewer? The noise is random; it changes every 1/30 second; and the eye averages out the noise. If you carefully view still video frames, such as on television sports events, the noise becomes apparent.

To improve the noise figure and the quality of the captured image, a number of frames can be pointwise averaged. Several frames are used to accomplish this: the first frame is digitized and stored; the second and successive frames are digitized; and each value is added to the corresponding stored value. The resulting array of numbers is divided by the number of frames used. Thus, the value for each point becomes the average of digitized values for that point across all the frames used, effectively cancelling out random noise. The improvement can be quite dramatic in situations where considerable noise is present. One can expect to achieve about $6.3 \times \log_2 N$ dB improvement for N frames up to a practical limit of about 45 dB. This maximum figure depends on the signal source, and the improvement depends on the randomness of the noise.

Sampling

The process of quantization consists of a sampling and a digitization phase. The sampling phase determines exactly when the signal value is to be frozen in time so the instantaneous value can be converted into a

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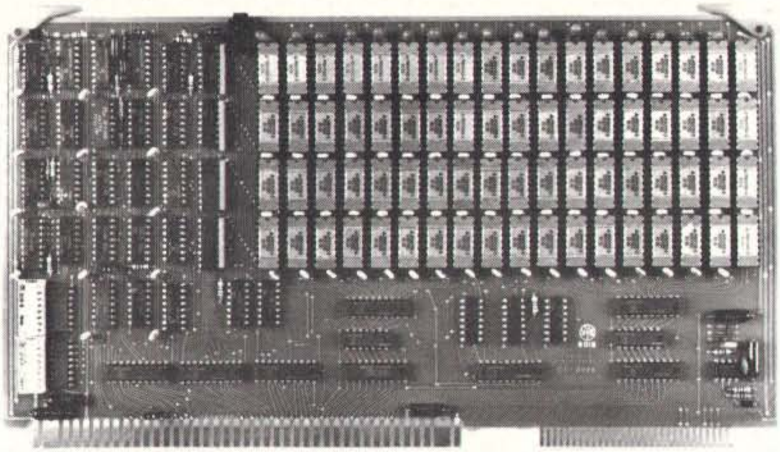

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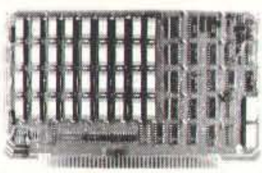

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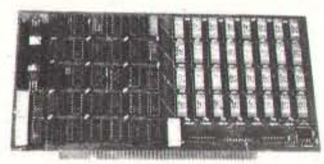

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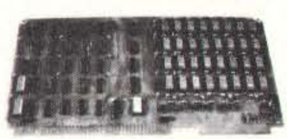

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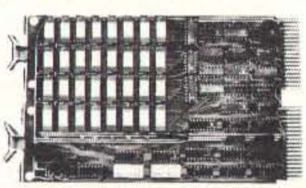

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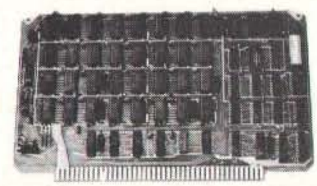

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Multibus is a trademark of the Intel Corp. LSI II is a trademark of Digital Equipment Corp. EXORciser is a trademark of Motorola number (ie: digitized). The sampling function is accomplished by periodically pulsing a sample circuit. The value of the video signal is then used to charge a capacitor that holds that value during the time needed by the digitizer until the next sample pulse. A sample-and-hold circuit provides the necessary components in hybrid or monolithic form. (See figure 6.)

The choice of sampling rate determines the spatial resolution with which the video signal is quantized. The sampling theorem tells us that a sample frequency must be chosen that is at least twice the value of the highest frequency component in the signal that we wish to record. Thus if we choose to sample at 10 MHz, or once every 100 ns, we will be able to record components of the video signal which are changing at rates up to 5 MHz. Sampling at this rate guarantees adequate data for all normal black-and-white video sources, since they contain very little energy beyond 4 MHz.

Examination of the sampling process shows that if there are frequency components in the signal above half of the sampling rate, false information (called *aliasing*) results. (See figure 7.) The aliasing component is effectively a beat frequency between the sampling frequency and the signal components above *half* the sampling frequency. In the case of standard video, the luminance signal is already filtered to roll-off in amplitude above 4 MHz. However, the chrominance signal in color video occupies the range from about 3 MHz to 4.5 MHz.

Therefore, you must either filter the signal to remove frequencies above about 3 MHz, derive a pure luminance signal from a properly designed video demodulator, or use a strictly luminance source, such as a black-and-white television camera. When digitizing at lower resolutions (and sampling at lower rates), the signal must be filtered accordingly.

The quality of a quantized video signal depends on accurate timing. If every element of the digital image is to be precisely aligned with the corresponding element in the video lines above and below it, the digitizer clock must be precisely synchronized with the television horizontal-sync signal. Also, the digitizer clock must not drift during the time between

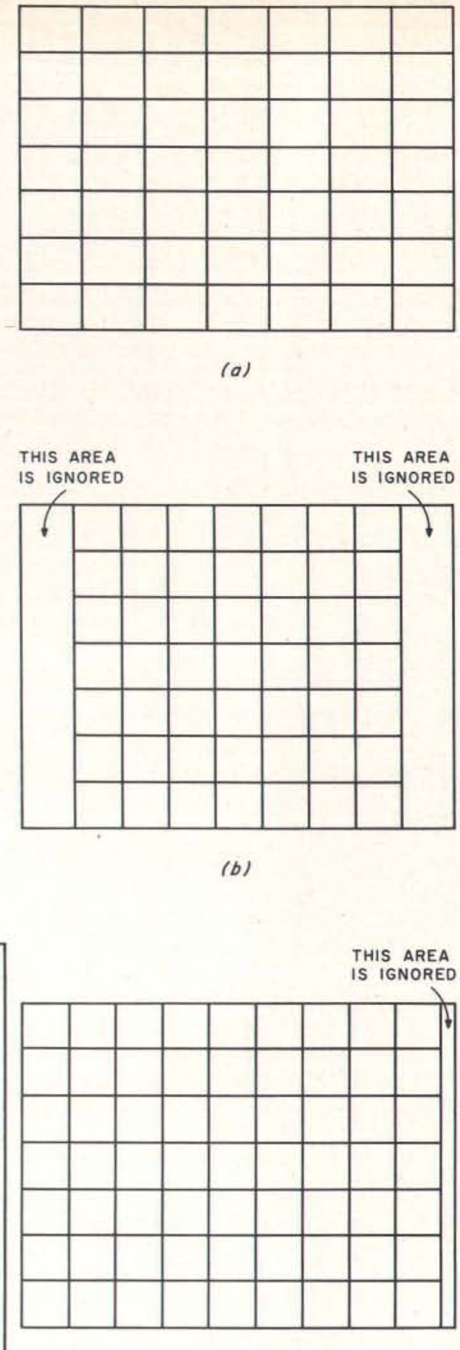

Figure 5: *The aspect ratio (width:height) of normal video is 4:3. The aspect ratio of each individual pixel is determined by the image-sampling rate.*
a: *This 7 by 7 square array of rectangular pixels is produced by sampling the same number of points per line as there are lines in a frame. For example, each line in an American-standard television frame (512 lines) would be scanned as 512 points.*
b: *By increasing the sampling rate by 1.33, square pixels result and a 7 by 7 array results from a square portion of the frame.*
c: *With the same increase in the sampling rate as in* **b**, *nearly the entire frame can be quantized into a 9 by 7 rectangular array of square pixels.*

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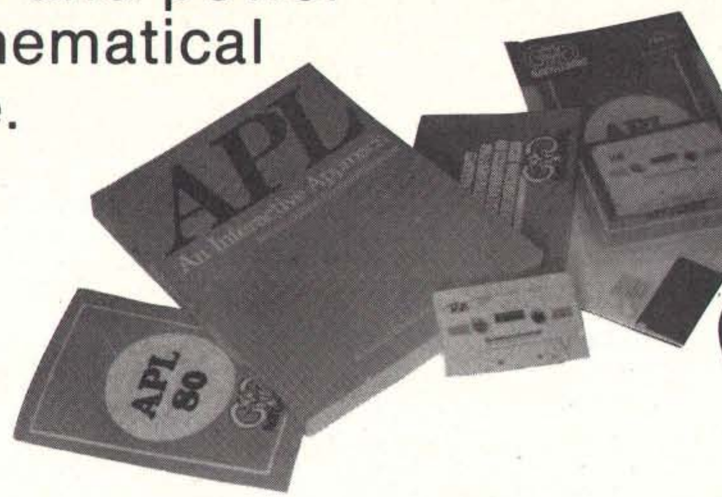

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To aid in learning APL, lessons are included on the disk. Starting from the basics, you are brought step by step through the various programming techniques involved with APL. These lessons act as a tutor in a "learning by doing" atmosphere which will have you "talking APL" in no time. Also available is the book, **APL: An Interactive Approach,** which reinforces many of the examples given in the lessons. The book also provides additional insight into APL programming.

LIMITATIONS

Due to the absence of the special APL character set on the TRS—80, APL-80 uses shifted letters to represent the various APL characters. These shifted letters are identified on the screen by a graphics block before each shifted letter. If you have a modified TRS-80 (Electric Pencil Modification), a lower case driver is included to display the shifted letters on the screen.

In addition to the keyboard limitations, there are several other limitations. Lamination, domino, and matrix inverse are not implemented but can be derived with user-defined functions.

Multiple specifications must be split into two statements unless the left-hand assignment is to a quad. This also applies to implied multiple specifications.

Reduction and reshape (p) are not permitted for empty arguments; the argument of add/drop may not be scalar; empty indices are not permitted.

A quad (q) can't be typed in response to a quad (nor can the name of a function which itself gets input from a quad). Quote-quad (m) is permitted.

No more than 32 user functions can be defined in a single workspace and a function may not contain more than 255 lines.

A comment (c) must occupy a separate line: a comment can't follow a function statement on the same line.

In the tape version, arrays are limited to five (5) dimensions.

FEATURES

APL-80 on disk contains the following features:)SAVE and)LOAD workspace on disk;)COPY other workspaces into current ones; Return to DOS for directory or commands without losing your workspace; Send output to lineprinter; Five workspaces of lessons included; Sequential and random files; 15 digit precision; Monadic and dyadic transposition; Easy editing within FUNCTION lines; Latent expression (FUNCTION can "come up running" when loaded); Tracing of function execution; Real-time clock; User-control of random link; Workspace is 25587 bytes (in 48K machine); Arrays may have up to 63 dimensions.

COMMANDS I APL-80

APL-80 supports the following commands: Absolute value, add, and, assign, branch, catenate, ceiling, chr$/asc, circular, combinatorial, comment, compress, deal, decode, divide, drop, encode, equal, expand, exponential, factorial, floor, format, grade down, grade up, greater, greater/equal, index generator, indexing, index of, inner product, label, less, less/equal, logarithm, maximum, member, minimum, multiply, nand, negate, nor, not, not equal, or, outer product, peek, poke, quad, quote quad, random, ravel, reciprocal, reduction, reshape, residue, reverse, rotate, scan, shape, sign, system, subtract, take, transposition.

SPECIFICATIONS

Minimum system requirements: 32K disk system (48K recommended) Includes APL-80, Five workshapes of lessons, instruction manual.

Price: $39.95 on disk

Reduced feature: 16K Level II tape version, no lessons.

Transpositions, format, and inner product not implemented. Reduced domain for some functions. 6 digit accuracy.

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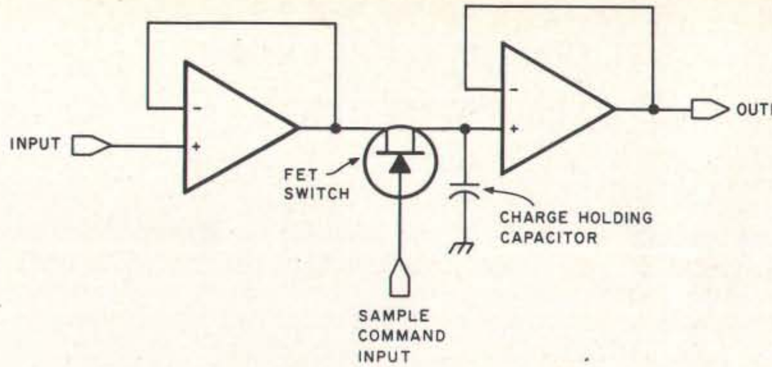
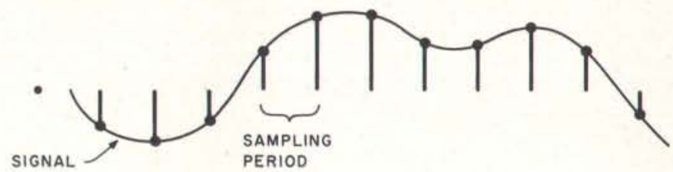
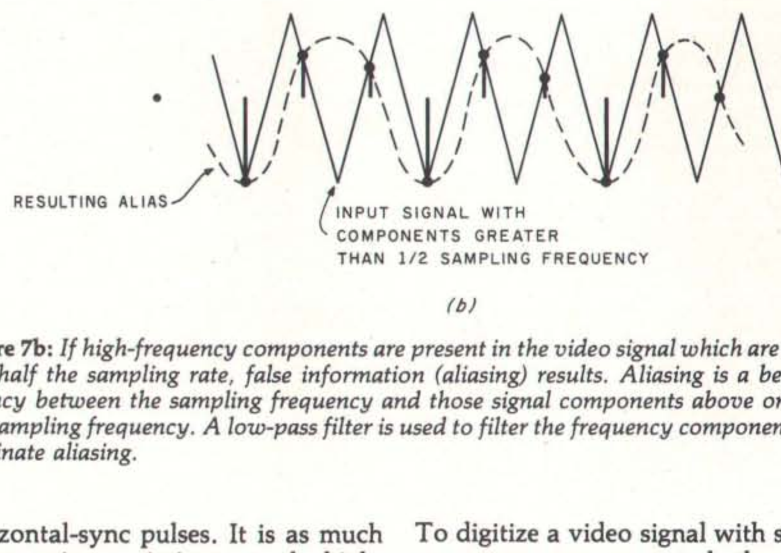


Figure 6: *An image is quantized in two phases: sampling and digitization. Sampling freezes the signal value so that it can be converted into a number (digitized). A sample-and-hold circuit such as shown here performs the sampling phase. Because of the low output impedance of the first operational amplifier, the capacitor is charged nearly instantaneously when the switch is operated by the video signal. The high input impedance of the second operational amplifier holds the capacitor at its full charge during the time the digitizer reads the signal.*



(a)

Figure 7a: *A correctly sampled video signal. Each dot indicates an instantaneous value read by the digitizer.*



(b)

Figure 7b: *If high-frequency components are present in the video signal which are above one-half the sampling rate, false information (aliasing) results. Aliasing is a beat frequency between the sampling frequency and those signal components above one-half the sampling frequency. A low-pass filter is used to filter the frequency components and eliminate aliasing.*

horizontal-sync pulses. It is as much the attention to timing as to the high-speed technology that makes quality digitized video a reality.

Low-Speed Digitization

The digitizer, or A/D (analog-to-digital) converter, is commonly thought of as a device that takes on the order of 20 μs to 50 μs to determine an 8-bit or 12-bit value. Such converters are inexpensive and are adequate for sampling slowly changing signals, such as an audio signal. To digitize a video signal with such a converter, you can sample the signal no more often than about once per scan line. (See figure 8.) During the first frame, the first point of each line is digitized. During the second frame, the second point of each line is digitized, and so forth, until the entire image is digitized. If 512 samples per line are needed, 512 frames of video would be required to digitize every point. Thus, it would take about 17 seconds to complete the digitization of one frame. To do this the camera

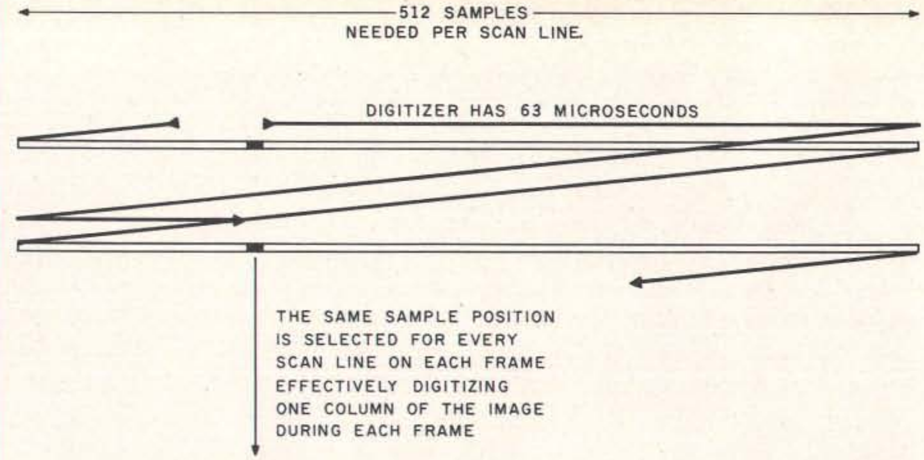


Figure 8: *By sampling a single point per scan line, the digitization of each pixel can be completed within 63 μs, and data is produced at a slow enough rate (15.7 k bytes/second) for transfer to mass storage.*

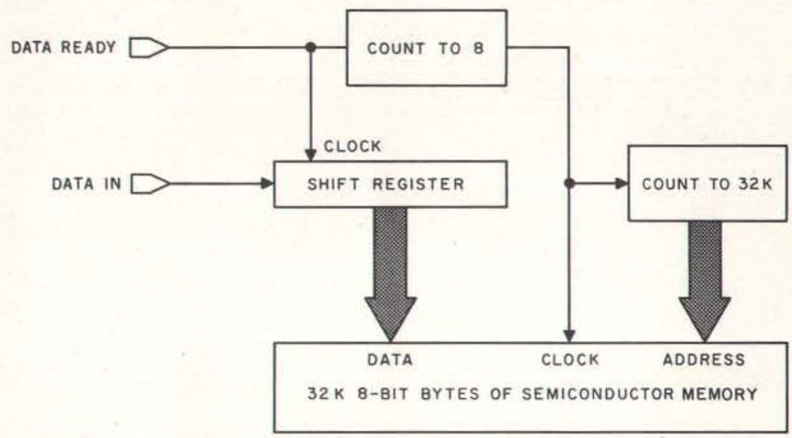


Figure 9: *Through the use of a shift register, standard programmable memory can be used to transfer a single-bit image at video rates. If a single bit is deposited into the shift register every 100 ns, an 8-bit value can be deposited into memory every 800 ns. The same process can be reversed for displaying the image.*

must be stationary on a tripod with respect to the object being viewed to keep the image stable. Tape players with freeze-frame options might seem attractive for this purpose. However, home videotape machines do not produce a truly stable image and are not usually adequate for this purpose.

The digitizer has plenty of time to produce a digital value. Precision is defined by the number of quantization levels, and more can be obtained for a small additional cost. Unfortunately, the sample circuitry must sample a very precise portion of the video signal, and its accuracy becomes more important if greater quantization levels are desired. Additionally, the decay rate of the sample circuitry becomes important because the sample must be held for up to 50 μs versus the 100 ns necessary for the high-speed digitization technique.

The advantages of slow digitization are the use of a relatively inexpensive A/D converter and low data rates, permitting direct storage of the data using floppy disks. The disadvantages are the need to hold the camera and scene stable for a length of time (depending on resolution) and the inability to capture other video sources, such as television programs and videotape. The requirements for the sampling phase are also more substantial than those for the high-speed method.

There is a hidden disadvantage of the low-speed method. The stored image cannot be readily viewed by reversing the process. The only way to reproduce the data in image form is to place a photographic camera in

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muMATH and muSIMP were written by The Soft Warehouse, Honolulu, Hawaii. Priced at $74.95, the package includes muMATH, muSIMP and a complete manual. It requires a Model I TRS-80 with 32K and single disk. muMATH for the Apple II Computer will be available later this year.

You can buy muMATH and BASIC Compiler at computer stores across the country that carry Microsoft products. If your local store doesn't have them, call us. 206-454-1315. Or write Microsoft Consumer Products, 400 108th Ave., NE, Suite 200, Bellevue, WA 98004.

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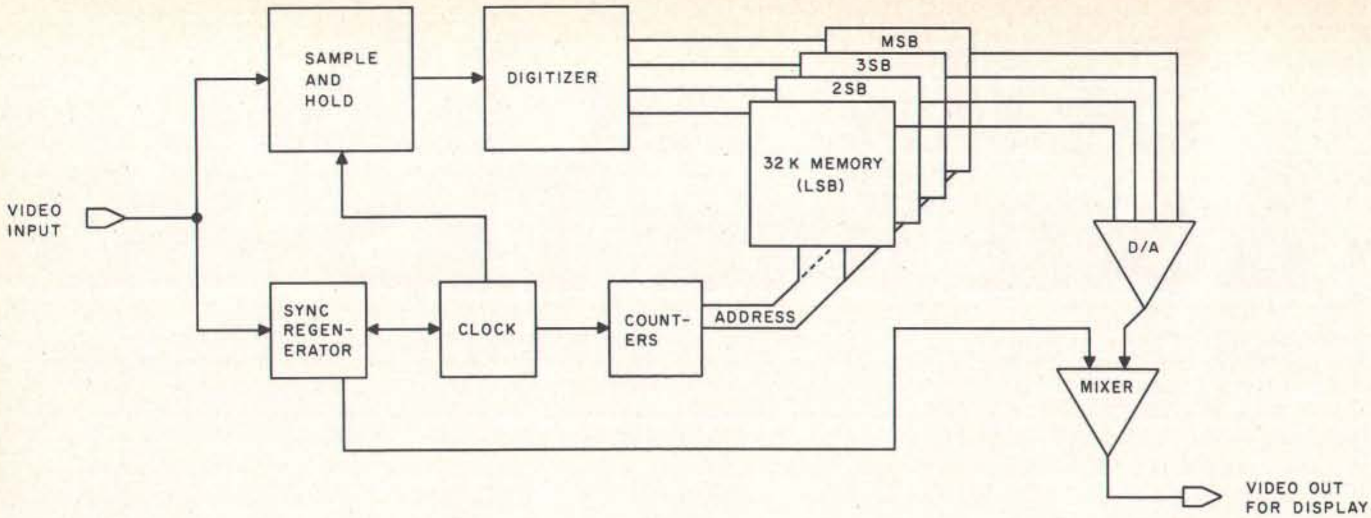

Figure 10: *Block diagram of an image-quantization system. In this example, a single memory board is used for each bit of quantization. Four boards would be needed for a 4-bit quantization.*

front of a television monitor and open the shutter for 17 seconds while the data are converted back into video, one point per line. Then, of course, the film must be processed: this is hardly conducive to interactive use.

High-Speed Digitization

If we want to digitize 512 points during each scan line, the converter must operate at very high speeds. The active portion of a video line is about 53 μs. Roughly, this means it must quantize the signal once every 100 ns. Such converters were available 10 years ago for about \$2000, but today they can be built for less than \$100! Next I'll examine the problems of storing the data produced at this rate.

Most home computers have central memory that can be cycled at about 250 ns to 1000 ns per 8-bit transfer. If the digitizer obtains one 4-bit quantity every 100 ns (at 512 samples per line with rectangular pixels), or 8 bits every 200 ns, standard computer memory cannot cope with the speed requirement. Most experimenters own configurations with 32 K bytes

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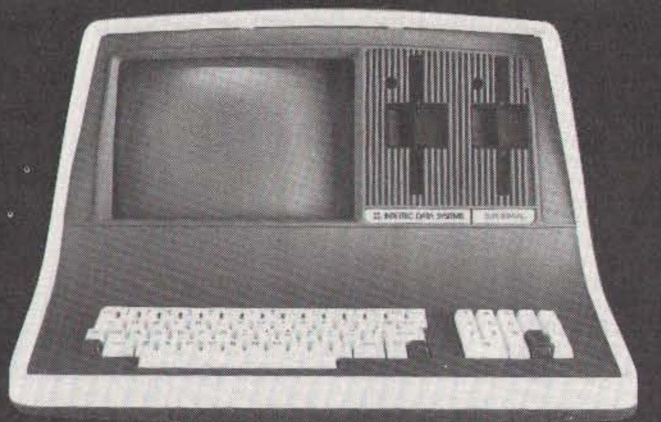

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or less of central memory. Although 32 K bytes would be barely sufficient for a 256 by 256 4-bit image, 128 K bytes are necessary for a 512 by 512 4-bit image. Therefore, memory is usually dedicated to the image-storage function and accessed by the computer through either a processor-controlled or DMA (direct-memory access) port.

The problem of providing large memories capable of 200 ns cycle times can be solved by the sequential nature of data transfers. By dividing memory into a number of parallel segments it's possible to use memories with 800 ns read/write-cycle times to simultaneously digitize, display, and communicate with the computer.

Proper memory organization allows ease of expansion, depending on whether higher spatial resolution or more bits per picture element are anticipated in the future. Also, good designs can be software-reconfigured to trade off spatial resolution for the number of bits per pixel. Methods for reconfiguration are left for the ambitious designers to discover on their own.

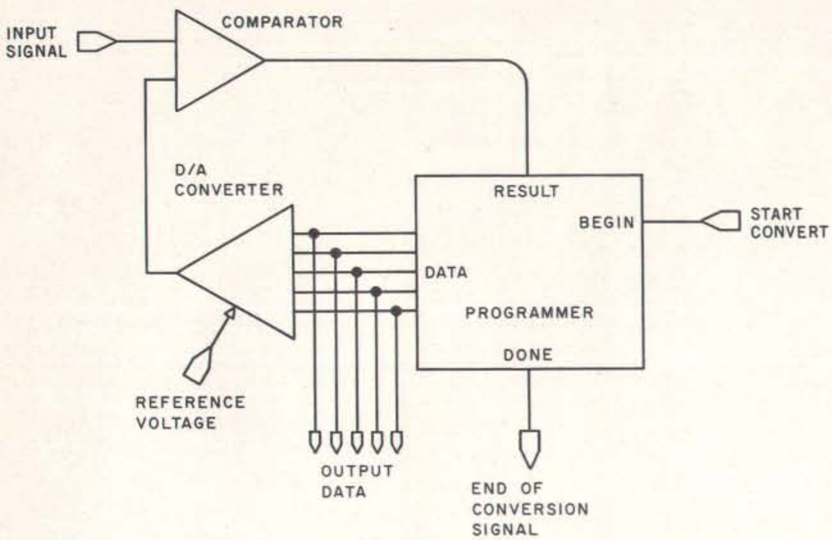

Figure 11: *The configuration of a conventional A/D converter.*

A Hypothetical Design

Assume that we'll require a 512 by 512 image with 4-bit quantization of each pixel. Memory is physically organized as four 32 K-byte memory boards. This is because there are 256 K points in the image, and we wish to have 1 bit of each 4-bit pixel value on each memory board. We will use memory which transfers 8-bit quantities.

If we shift 1 bit every 100 ns into a serial-in, parallel-out shift register, then every 800 ns the resulting 8-bit value can be deposited into memory. (See figure 9.) The same process can be reversed for real-time display. To do so, the memory is read every 800 ns, the 8-bit value is placed into a parallel-in, serial-out shift register, and shifted out at 100 ns per pixel.

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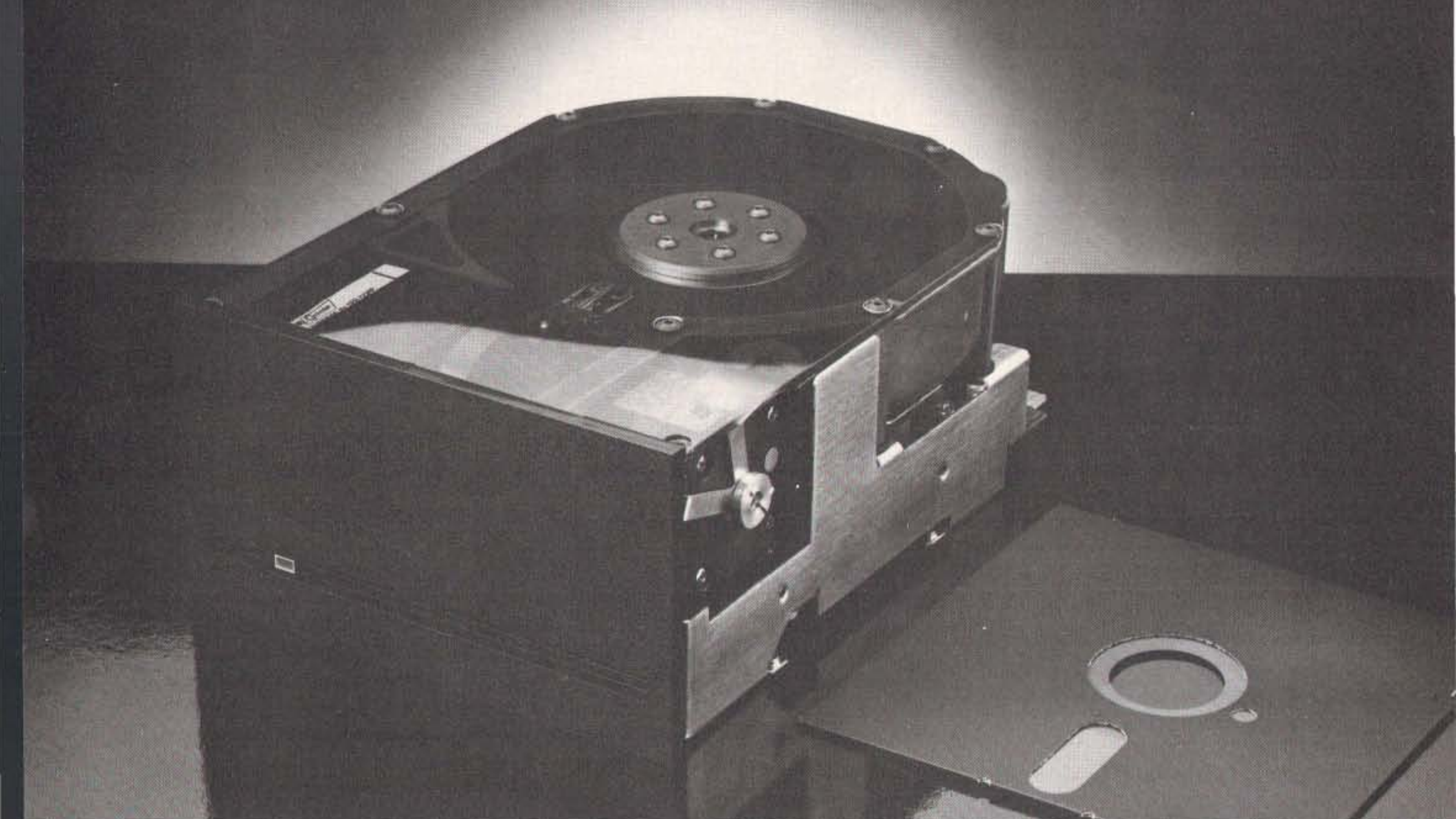

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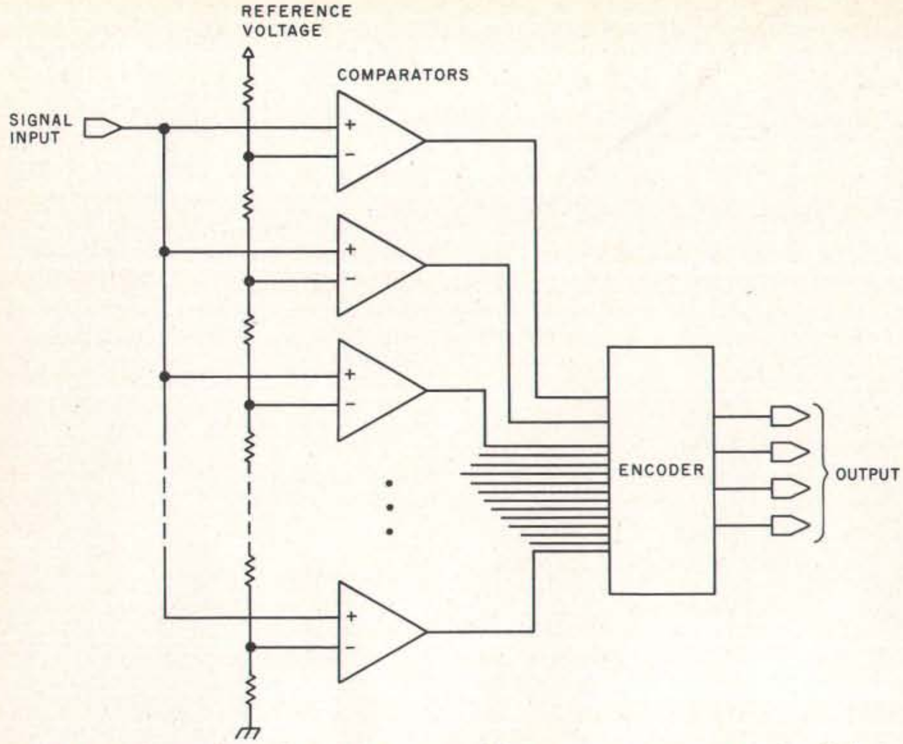

Figure 12: *The small number of bits required for image quantization makes flash (or parallel) A/D conversion practical. One comparator is used for each quantization level. For a 4-bit quantization, sixteen comparators would be needed. A reference voltage equal to full scale is fed to a voltage divider to form a set of comparator thresholds. The output of each comparator is then fed to the encoder, where the number of on comparators is converted into a binary output. Parallel converters are available in DIP form and allow for high data-conversion rates.*

To achieve the desired number of quantization bits per pixel, we stack the appropriate number of memory boards. (See figure 10.) In our case, four boards would be needed for 4-bit pixels. Of course, there would have to be an address bus common to all boards and an extra board to provide control and A/D conversion. The extra board would be needed to decode the video sync signals to keep memory-addressing in step with the video signal. Additionally, D/A (digital-to-analog) conversion and sync generation are necessary to drive a display monitor.

Notice that the memory is running at very slow speeds by modern standards. If we use memory that allows *two* operations per 800 ns, the computer can access or deposit data completely transparent to the digitization or display process.

Now consider high-speed A/D converters. Normal converters use a D/A converter, a programmer, and a comparator to derive a numerical quantity representing the voltage on the input. (See figure 11.) The programmer tries successive numbers, generating successive voltages out of the D/A converter. These voltages are compared with the analog input to determine if they are above or below the input voltage. The comparator output is used by the programmer to decide what number to try next until the process converges on a final value.

The fastest A/D programs take about as many tries as there are bits of quantity. Each try consumes as much time as the total of the programmer gate delay, the D/A-gate delay, the D/A-settling time, and the comparator-settling time. The fastest converters perform conversion on the order of the 100 ns per bit. This is obviously unacceptable for our purposes, since we consider 4 bits to be a minimum quantization and 100 ns to 200 ns to be a maximum conversion time.

The small number of bits that are required does make another conversion technique very practical. It has several names, the most popular being *flash* or *parallel* conversion. It consists of one comparator for each quantization level, or sixteen comparators for 4 bits. (See figure 12.) A reference voltage equal to full scale is fed to a voltage divider (ie: a network of resistors) to form a set of comparator thresholds, and the outputs

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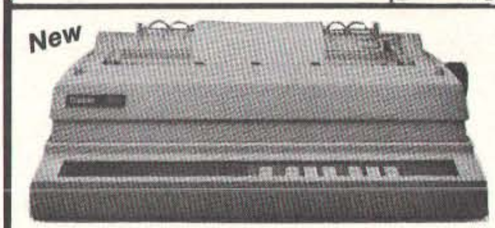

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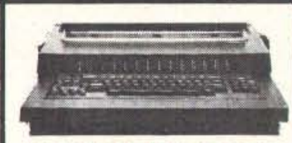

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The Nature of Video Images

The video standard has three primary components, synchronization signals, a luminance (black-and-white) signal, and a chrominance (color) signal. The synchronization (sync) signals tell the receiver when to begin a new frame and a new line. The luminance signal provides intensity values that comprise a picture. The signals are effectively separate, allowing compatibility between color and black-and-white television receivers. Our primary interest here is the luminance signal, but the chrominance signal must still be considered. It must be filtered out of a color video signal before quantization. (The reason for this requirement has been described in the section on sampling.)

Each complete picture, called a frame, takes 1/30 second to complete. To reduce flicker, 2:1 interlacing is used. During the first 1/60 second, the even-numbered lines are displayed; and during the second 1/60 second, the odd-numbered lines are displayed. Each set of lines (half of the frame) is called a field. *(See figure 3.)*

Each field consists of 262.5 lines, each line transmitted in 63.5 μs. Nine of these lines are used for the vertical synchronization pulse, which is actually a series of pulses that are easy for receiver circuits to recognize. Each line is composed of a horizontal active-line period during which luminance information is present, and a sync period when reference levels and the horizontal sync signal are present. The horizontal active period is 53.3 μs, and the sync period is 10.2 μs. (See figure 4.)

of the comparators are fed to an encoder. The analog voltage determines which comparators are on, and the encoder then turns the number of on comparators into the corresponding binary number. The only delays are the settling time of one comparator and the encoder-logic delay. I've built three of these for under $100. They are also commercially available in DIP (dual-in-line package) form in 3-bit or 4-bit designs that allow for cascading to achieve 1 or 2 additional bits.

Summary

Inexpensive semiconductor memory and other technological developments have made digital image storage with real-time video input and output a practical reality for the home computer experimenter. Several complete hardware and software systems are available for the display and digitization of real-time video. At least one company offers an inexpensive, real-time digitizer and display, while several offer very inexpensive digitizers to accomplish low-speed digitization. A high-speed system costs $1500 to $5000 or more, depending on options. The primary price difference is due to the amount of image memory desired. Low-speed systems range from about $350 to $4000.

Flash-conversion products range from $30 to $90 for 3-bit and 4-bit units with about 30 MHz maximum rate. These save you the headaches of finding matched resistor values for homebrew flash converters.

Although there isn't enough information in this brief article to construct an image-capture system, there should be enough to familiarize an ambitious designer with the techniques and problems. You would be well advised to obtain a technical manual from a manufacturer to help assess the potential difficulties. With healthy competition in the growing marketplace for image-capture and display, the power/price ratio of complete systems will continue to increase. ■

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BYTELINES

NEWS AND SPECULATION ABOUT PERSONAL COMPUTING

Conducted by Sol Libes

DEC Opens Computer Museum: Digital Equipment Corporation (DEC), the pioneer in minicomputers, has opened a "computer museum" in the lobby and mezzanine level of its Marlboro, Massachusetts, "Tower Building." It illustrates, through actual equipment, the evolution from calculator to microcomputers. The exhibits include precomputer devices, the four generations of digital logic used in computers, and some early computer systems (eg: PDP-1 with the original *Spacewar* program and others). The museum is open to the public.

Random News Bits: Casio, Inc, the Japanese electronics manufacturer, has introduced a personal computer in the US. The FX-9000P can store programs directly in 4 K-byte CMOS (complementary metal-oxide semiconductor) memory cartridges (with lithium batteries) that can be removed from the unit. The basic unit is priced under $900.... Pascal can now be considered as having "made it." IBM has announced that Pascal will be available for IBM systems using OS/VS and VM/CMS operating systems. IBM will charge $235 a month for it. To think that most microcomputer users pay less than IBM's monthly charge to buy Pascal outright.... A study conducted by the National Institute for Occupational Safety and Health found that videoterminal users suffer problems of eye strain, blurred vision, color perception, numbness, and loss of strength in their arms. These users also experience higher levels of anxiety, depression, confusion, and fatigue....The University of Southern California will offer a graduate degree in voice I/O (input/output). The curriculum includes courses in electrical and biomedical engineering, communications, computer science, linguistics, otolaryngology, and psychology...

Fujitsu Overtakes IBM In Japan: For the past thirty years, IBM has dominated data processing over the entire globe. Now, however, it is reported that in Japan Fujitsu, Ltd, has overtaken IBM in sales. Fujitsu and several other Japanese computer suppliers are now preparing a massive onslaught into the US and European markets.

IEEE Local Network Standard Moves Ahead: The IEEE Local Network Standards Committee expects to have a draft of its standard by year's end. At this time, it appears that the Ethernet system, proposed by Xerox, Digital Equipment Corporation, and Intel, will *not* be adopted as the standard. The reasons for this are that Ethernet is still in a preliminary-definition state with many areas not precisely defined. Further, Ethernet is highly dependent on coaxial cables and a particular modulation technique. Also, Ethernet does not have any provision for acknowledging datagrams, which could lead to possible incompatibilities in error control between different manufacturer's devices.

Super Computer Planned: The Ames Research Center of NASA (National Aeronautics and Space Administration) is planning a special super computer capable of performing a billion floating-point operations per second. The computer will be designed to simulate a wind tunnel. It is expected to have 40 M words of directly addressable memory plus 200 M words of block-addressable memory. NASA wants the system operational in 1986.

US Government Shifting To Smaller Computers: The US government now has a reported 15,000 computers in operation, worth more than $5.4 billion. The trend is shifting from large, costly mainframes to smaller units. In fact, now at least two-thirds of the machines cost less than $50,000.

The GSA (General Services Administration) recently disclosed that at the end of 1979 the three leading computer suppliers were Digital Equipment Corporation (3656 units), Sperry Univac (1778 units), and IBM (1284 units). However, IBM still ranked number one in dollars ($1.45 billion), Control Data was second ($754 million), and Sperry Univac was third ($686 million).

Ribbon Recycling: The word-processing and printer markets have created the new business of recycling printer ribbons. About fifty vendors are offering consumers recycled ribbons at a saving of as much as 60%, along with deliveries in 5 to 10 days.

Several ribbon manufacturers are introducing sealed ribbon cartridges to prevent recycling. They claim that sealing improves ribbon reliability.

Microsoft Signs UNIX Agreement: Microsoft, of Bellevue, Washington, has signed an agreement with Western Electric for the rights to develop and market versions of UNIX, an operating system originated by Bell Laboratories. The Microsoft versions will be specifically designed for 16-bit microprocessors, such as the Intel 8086, Zilog Z8000, and Motorola 68000. The Microsoft version will be called XENIX. UNIX seems to be the most popular minicomputer timesharing operating system in current use. It is very popular in the educational community, probably because Western Electric sold it to educational institutions for a very low fee. However, due to its sophisticated features, UNIX has been gaining in popularity in the profes-

sional and business worlds as well.

Microsoft plans to charge an initial fee for the package ranging from a low of $500 to $3000 for a four-user system. The company also has plans to adapt its BASIC, FORTRAN , and COBOL compilers to run under XENIX. Microsoft has purchased a DEC PDP-11/70 minicomputer specifically for the XENIX development project. The Z8000 version is slated for introduction by year's end, and the 8086 and 68000 versions are to follow sometime in the second quarter of 1981.

Considering that Digital Research plans on developing only an 8086 version of its very popular CP/M operating system, it seems likely that Microsoft's XENIX will become the dominant operating system for 16-bit microcomputer systems.

5-Inch Winchester Disk Drives Coming On Strong: At least a half-dozen companies will have 5-inch hard-disk drives on the market late in the first half of 1981. Latest to jump on the 5-inch disk-drive bandwagon are International Memories Inc (IMI) (the Cupertino, California, firm that marketed the first 8-inch Winchester drive) and Shugart Associates (the largest producer of floppy-disk drives). These drives typically store 5 million to 7 million bytes and sell for less than $1000 in OEM (original equipment manufacturer) quantities.

64 K-Bit Memory Devices Becoming Available: Several integrated-circuit manufacturers are currently supplying samples of the new 64 K-bit programmable memory circuits to OEMs for evaluation and development. Look to see these devices in use starting in early 1981.

The introduction of these components has already caused the price of 16 K-bit devices to drop significantly; just a few months ago, these circuits cost six to eight dollars—now they are four or five dollars. Currently, the 64 K-bit memories are in the forty- to sixty-dollar range, which may drop to thirty or thirty-five dollars in production quantities.

It is expected that Japanese suppliers will dominate the 64 K-bit device marketplace. The 16 K-bit device market has been dominated by American suppliers, although the Japanese currently have 40% of that market. The demand for the 64 K-bit memories does not, as yet, appear to be very strong. However, the price erosion of the 16 K-bit memories and increasing competition from Japanese suppliers should cause the 64 K-bit memory prices to drop quickly.

Protecting The Software Copyright: Software vendors are very concerned about software being pirated by unauthorized copying. The problem is acute simply because it is very easy to duplicate cassette- and disk-based software. Further, it isn't especially difficult to copy software stored in read-only memories.

The personal-computer user does not appear to be the cause of the problem because most of that type of pirating is for personal use, and it occurs only on a small scale without a significant impact on vendor sales. However, several software vendors are complaining that software pirates are making copies of their software packages and selling them. The software pirate frequently changes the name of the software package and may even make some minor changes so that the consumer is unaware that the software is a fraud. The practice appears to be widespread outside the US, where this kind of activity is very difficult to prevent.

As a result, software vendors are seeking ways to prevent pirating. Several are now experimenting with software techniques that cause the copied software to self-destruct if it is run on an unauthorized machine. I suspect that this will prove to be a deterrent for the experimenter and small-time thief, but the professional software pirate should be able to overcome this system.

Tandy, Apple, And Commodore Are Top Personal-Computer Performers: Each year *Datamation* analyzes and rates the top one hundred computer companies. For the second year in a row, Tandy Corporation (parent company of Radio Shack), Apple Computer, and Commodore have made that list. In fact, for this past year Tandy ranked thirty-ninth (up from last year's fifty-eighth), Apple ranked sixty-first (up from one-hundredth last year), and Commodore ranked seventy-fifth (up from ninety-fourth last year). Tandy had gross sales of $150 million, a 131% increase. Apple had $75 million in sales, up from $10 million the previous year, a 650% increase. Commodore had $55 million sales, a 150% increase.

These three personal-computer makers had the highest growth rates of the top one hundred computer-product vendors in the US. IBM, which ranked number one in total sales, had only a 7% increase in sales.

Talking Computers To Be The Rage: 1981 should be the year that consumers first see the widespread use of voice output in products ranging from computers to household appliances. Many manufacturers are currently supplying samples of speech-synthesis integrated circuits to OEM customers. The manufacturers include Texas Instruments, National Semiconductor, General Instrument, Hitachi, and Votrax. The Hitachi HD38880 integrated circuit, for example, can produce up to 200 words or one hundred seconds of speech from data stored in a 128 K-bit ROM (read-only memory). The Texas Instruments TMS5200, essentially the same device used in the Speak & Spell toy, has been given an 8-bit data-bus interface and should operate easily with personal computers.

Random Rumors: It is rumored that Intel, Motorola, and Fujitsu are all working on the development of microprocessors that will implement the IBM System/370 instruction set. Performance is expected to be comparable to an IBM 370/115. IBM is rumored to already have such an integrated-circuit version running.... Xerox is rumored to be attempting to buy Apple Computer.... Digital Equipment Corporation is rumored ready to release a 16-bit microprocessor device that will be compatible with 8080, Z80, and 6800 support circuits. It is expected to have the power of a PDP-11/23. At least one company is rumored to be investigating an S-100 implementation....

MAIL: I receive a large number of letters each month as a result of this column. If you write to me and wish a response, please include a stamped, self-addressed envelope.

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Machine Problem Solving

Part 3: The Alpha-Beta Procedure

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Zero-Sum Games

In many problem-solving situations, the wisdom of a particular decision often depends upon the range of options that someone else may have. Many real-world decision-making environments can be modeled in terms of a two-person game. When each player is aware of his own and his opponent's options at each choice point, the game is described as one of *perfect information*. If the rules of the game require that each player's gain must come at the expense of the other, then the game is strictly competitive, or *zero-sum*. Familiar games that meet these criteria are chess, checkers, three-dimensional tic-tac-toe, go, gomoku, and Othello.

The first two articles in this series considered decision-making situations in which a single individual was responsible for a series of choices. By constructing programs that searched among a large number of choice combinations, we were successful in developing mechanical solutions for these problems. When two people are making choices and each is trying to better his own position at the other's expense, the standard look-ahead search that we described earlier is no longer adequate.

Minimax Strategy

Instead, it is necessary to consider choices in which the two players attempt to satisfy conflicting goals. Most of the important strategic ideas which are used in analyzing these games date back to a very influential book which was written in 1944 by Von Neumann and Morgenstern (see reference 4).

The key idea for our present purposes is the *minimax* strategy. In analyzing any given position in the game, a look-ahead tree is constructed which represents the sequence of options that the two players have (as a hierarchical branching structure which grows exponentially as one proceeds away from the initial position).

The minimax strategy consists of evaluating "final" positions at some arbitrary depth (usually defined by practical constraints of time and space) and then following parent nodes all the way down the tree to the starting position. This path is defined by assuming that each player will decide among the options that are available to him at *his* choice points by selecting the one that guarantees the best possible outcome.

If the terminal evaluations are chosen such that high numbers favor the first player (and low numbers favor the second player), the first player is expected to choose the pathway that guarantees as large a terminal value as possible, and the second player is expected to choose the pathway that guarantees as small a terminal value as possible. In practical terms, the first player always maximizes, the second player always minimizes.

This description would seem to explain the derivation of the name. This is not historically correct, however. The "minimax" name is actually based on the underlying strategic idea that each player attempts to minimize his opponent's maximum potential gain.

History and Practicality

The minimax technique appeared to be of limited practicality when it was first discovered because of the rapid increase in the number of terminal positions as the look-ahead tree grows. The number of terminal positions that need to be analyzed in a minimax search is equal roughly

to the average number of options at each choice raised to a power equal to the depth of the search tree. For example, consider the game of chess, which averages thirty-eight options at each choice point. A minimax search considering a look-ahead of four moves for each player would have 38^8 terminal positions. That is more than 4 trillion (4,000,000,000,000) positions.

You do not have to be a mathematical genius in order to determine that a process that grows exponentially like this one is going to get out of control very quickly. Because of this exponential explosion and because there were no computers in the 1940s, the minimax algorithm initially received little attention.

In practical terms, the first player always maximizes, the second player always minimizes.

The Alpha-Beta Technique

In 1956, at the Dartmouth Summer Research Conference on Artificial Intelligence (see reference 1), John McCarthy pointed out that Bernstein's chess program did not need to analyze all of the terminal positions in order to select the move that was best in terms of the minimax strategy.

Although no formal description of the idea was given at that time, several of the game-playing programs written in the late 1950s appear to have employed an enhanced version of the minimax procedure, which has come to be called the α-β (ie: alpha-beta) pruning algorithm. The name seems to have been coined by McCarthy.

The first clear description of the technique for English-speaking audiences was published in 1969 by Slagle and Dixon (see reference 3). The α-β procedure provides a remarkable increase in the efficiency of the search process; and, with the advent of the high-speed computer in the late 1960s and 1970s, the minimax idea finally came of age.

Although there are many references to the α-β minimax technique in the popular literature, the procedure has not received much detailed analysis in the academic literature. The best expository presentation on this topic is a recent paper by Knuth and Moore (see reference 1). The technical details that enhance the efficiency of the α-β strategy are scattered throughout a number of hard-to-find sources. The purpose of this article is to summarize the main ideas and to present a sample program with the key algorithms.

Treasure Search

To provide an explicit example, I have devised a new game that is easy to play and is easily programmed. One of the difficulties of describing the α-β minimax procedure within the context of a familiar game is that move generation and position evaluation are sufficiently complex that these aspects of the program tend to mask the fine points of the α-β search. The game we will consider involves very straightforward move-generation and position-evaluation routines. For this reason, we will be

able to concentrate on the tree-searching algorithm in the absence of unwanted distractions.

This new game is called Treasure Search and is played on an 8-by-8 grid. A digit between 1 and 9 is randomly assigned to each of the sixty-four squares. Each contestant has a single playing piece which is initially positioned in the central portion of the grid. The players take turns moving their pieces. A piece can be moved only one square at a time in one of four orthogonal directions (ie: north, south, east, or west). The object of the game is to visit squares where a large number has been assigned and to collect as many of these as possible. Once a number has been taken from a square, that location is empty and subsequent visits provide no additional benefits. The first player to accumulate one hundred or more points wins the game.

Table 1 depicts the playing board as it might appear at the start of the game. The human player has the token designated as "X" and always moves first. Move selection is made by pressing one of the four arrow keys (←,→,↑,↓) on the computer keyboard. The program I will present is written for the Radio Shack TRS-80 computer in Level II BASIC.

Treasure Search 4

	8	6	1	7	5	8	9	6
	4	9	5	6	2	6	9	1
George	4	1	4	6	4	7	4	1
0	9	1	4	*	7	5	3	5
	6	2	5	9	X	4	4	4
TRS-80	5	9	9	3	4	8	8	1
0	3	7	6	2	4	5	1	8
	8	8	6	4	6	9	1	3

Which Direction for X?

Table 1: *Starting position for Treasure Search. The human player moves the "X" one square at a time and attempts to collect as many big numbers as possible. The computer moves the "*" on alternate turns with the same objective. The first player to accumulate one hundred points wins.*

The Treasure Search Game

The specific numbers that appear in table 1 are set randomly at the beginning of each game; therefore, a new playing field is present for each and every game. The strategy for each player is to find a pathway in which he can collect large numbers for himself and at the same time deny large numbers to his opponent. The game was originally planned for young children. I have subsequently found that it is fun for children of all ages.

To begin my presentation, I will provide a listing of the computer instructions for creating the playing field and accepting moves from the human player. Subsequently, I will present the algorithm for selecting moves for the machine and then discuss enhancements that substantially increase the efficiency of the search.

The Program

The initial statements in this program are very similar to those at the beginning of its two predecessors. Certain housekeeping functions are required, such as setting aside memory for string storage, clearing the video display, telling the machine to treat all variables as integers, resetting the "seed" for the random-number generator, and initializing important variables:

```
100 CLEAR 100: CLS: DEFINT A-Z: RANDOM:
    SH = 0: ST = 0
```

(Several versions of this program are given in the body of the text and in listings 1 thru 3.) The variables SH and ST represent the cumulative score for the human and the TRS-80, respectively.

Our next objective is to solicit the human player's name so that we can communicate with him in a civilized manner:

```
110 PRINT@463, "PLEASE ENTER YOUR NAME";:
    INPUT N$
```

The next step is to create several arrays that will be needed by the program. Two arrays are needed for remembering move directions (A and D), one is needed to provide an internal representation of the playing field (B), and several more are used by the tree search: M stores the move that is being considered at each level of the look-ahead tree; E stores the evaluation score for each of those moves; Q keeps track of which moves have been considered at each level of the tree; V keeps track of the best pathway value for each level of the tree; Z

remembers a "killer" move for each level of the tree (this is explained later in this article); and PV is used to remember the principal variation that is selected by the tree search. The lines we will need are:

```
120 DIM A(8), B(99), D(4), E(12), M(12)
130 DIM PV(12,12), Q(12), V(12), Z(12)
```

The array representing the playing field, B, is treated as a 10-by-10 grid with the first row having indices of 0 to 9, the second row, 10 to 19, the third row 20 to 29, etc. With this organization, the "squares" adjacent to any position are always separated by a constant value. The square to the right is always the current square plus 1. The square to the left is always the current square minus 1. To go up, add 10; to go down, subtract 10. For move generation, we create an array with the following coefficients:

```
140 D(1) = −10: D(2) = −1: D(3) = 1: D(4) = 10
```

We will use a special feature of the TRS-80's architecture to produce moves for the human player. A special array is needed to take advantage of the fact that the keyboard is memory-mapped.

```
150 A(1) = 10: A(2) = −10: A(4) = −1:
    A(8) = 1: CLS
```

Since our program is designed for children of all ages, we will let the human player adjust the playing strength of the machine. Young children can play against a weak opponent. Older children can select a more competitive opponent.

```
160 PRINT@461, "TRS-80 PLAYING STRENGTH
    (1 TO 5)";:INPUT Y
```

The larger the number, the deeper we will have the machine search.

The variable DM is used to set the maximum depth of the look-ahead search. It is defined as twice the value Y minus 1. This will produce searches of one ply, three plies, five plies, seven plies, and nine plies for playing-strength settings from 1 to 5. A five-ply search involves three moves for the machine and two for the human opponent. *[A ply is a move by either opponent; the combination of one move by both sides is called a play or a turn; thus two plies equal one move. . .* **GW***]* It is also necessary to create the array that provides an internal representation of the playing field. This is done by assigning a digit from 1 to 9 to each of the squares in the playing area:

```
170 DM = 2*Y: FOR I = 11 TO 88:
    B(I) = RND(9): NEXT I
```

The squares that surround the grid are used to designate the edge of the board and are set to a value of 99 for this purpose:

```
180 FOR I = 0 TO 10: B(I) = 99: NEXT I:
    FOR I = 89 TO 99: B(I) = 99: NEXT I
190 FOR I = 19 TO 79 STEP 10: B(I) = 99:
    B(I + 1) = 99: NEXT I
```

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The playing field also needs to be presented on the video display, along with a title for the game:

```
220 CLS: FOR I = 11 TO 88: IF B(I) = 99 THEN 240
230 X$ = RIGHT$ (STR$ (B(I),1): GOSUB 1000
240 NEXT I: PRINT@22, "TREASURE SEARCH";Y;
```

The subroutine starting at line 1000 computes a location on the video screen (R = row; C = column) and prints a character there:

```
1000 R = INT (I/10): C = I-10*R:
     K = 141 + (8-R)*64+C*4
1010 PRINT@K, X$;: RETURN
```

Our next objective is to enhance our video display by printing the names of the contestants on the left-hand side of the screen where the score will be recorded. We also need to put each player's piece on the playing field and to define several useful variables. Y$ is a string variable of twelve blank spaces. Z$ is similar except it represents thirty-two blank spaces. These two variables will be used when we wish to erase part of the video display. The variable T represents the position (row-column) of the computer piece, and H represents the position of the human piece:

```
250 PRINT@256, N$;: PRINT@448, "TRS-80";:
    Y$ = STRING$ (12, " ")
260 T = 54: T$ = "*": H = 45:
    H$ = "X": Z$ = STRING$ (32, " ")
270 I = T: X$ = T$: GOSUB 1000:
    B(T) = 99: B(H) = 99
280 I = H: X$ = H$: GOSUB 1000: GOTO 300
```

The position where each player's piece is located is not available for a move, so those positions in the B array are temporarily set to the value 99.

Now we are ready to create the module that solicits the human's move. First we will start with a message to present when the requested move is not legal. This can occur if the human attempts to move off the playing field or to a position occupied by the machine's playing piece:

```
290 PRINT@788, "ILLEGAL MOVE, TRY AGAIN";:
    FOR I=1 TO 999: NEXT I
```

In most situations, line 290 will not be executed. Instead, the message will usually be a request for the human player's move:

```
300 PRINT@788,Z$;:
    PRINT@788, "WHICH DIRECTION FOR X";
```

The machine waits for the human's response by doing a rapid cycle from the beginning to the middle of line 310. When a keyboard response occurs, the machine checks a special location in memory that keeps track of the arrow keys and determines which bit has been set by the keypress:

```
310 IF INKEY$ = "" THEN 310 ELSE R = PEEK(16444)
```

The player's response is then processed to determine the

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new square (J) for his piece.

A test is also made to make sure that the new square is on the playing field and not currently occupied by the machine's piece:

```
320 R = INT(R/8): J = H+A(R)
330 IF B(J) = 99 THEN 290 ELSE PRINT@788, Z$;
```

If the move is legal, the necessary changes are made to the video display and to the internal representation of the board. In addition, the player's score is modified approximately and a check is made to determine if the game is over:

```
360 I = H: B(I) = 0: X$ = "-":
    GOSUB 1000: SH = SH+B(J)
370 H = J: B(H) = 99: I = H: X$ = H$:
    GOSUB 1000
380 PRINT@321, SH;: IF SH > 99 THEN 930
```

Move-Selection Strategy

This completes the module for soliciting and processing the move selected by the human player. We can see that Treasure Search is much easier to program than more familiar games such as chess or checkers. We are now ready to address the major focus of this article, namely, move selection by the machine. As a first approximation, I will present a relatively simplistic strategy and then subsequently will consider more sophisticated approaches.

The following initial strategy surveys the playing field in each of the four directions from the current position (T) of the machine's playing piece and selects as the best move (BM) the square which has the largest value (BV):

```
530 BV = -1: I = 0
540 I = I + 1: J = T +D(I): IF B(J) = 99 THEN 560
550 IF B(J) > BV THEN BM = J: BV = B(J)
560 IF I < 4 THEN 540
```

This is equivalent to a look-ahead search of one ply. Once a move has been selected, it is then necessary to make that move on the video display and to make the appropriate changes in the internal representation of the playing field. In addition, the score for the machine needs to be modified and a check needs to be made to determine if the game is over:

```
800 I = T: B(I) = 0: X$ = "-":
    GOSUB 1000: PRINT@179, Y$;
810 T = BM: ST = ST+B(T): B(T) = 99:
    I = T: X$ = T$
820 GOSUB 1000: PRINT@513, ST;:
    IF ST < 100 THEN 300
```

To complete the program, we need two messages to signal the end of the game:

```
910 PRINT@915,
    "THANK YOU FOR A PLEASANT GAME";
920 GOTO 920
930 PRINT@917,
    "CONGRATULATIONS, YOU WIN";: GOTO 920
1000 R = INT(I/10): C=I-10*R:
     K = 141+(8 - R)*64+C*4
1010 PRINT @ K, X$;: RETURN
```

[Please note that this simple version of the game is not the version given in listing 1. To acquire this version, type in all the BASIC lines presented so far in the text. . . ***GW****]*

Implementing α-β Techniques

If you run this program on a TRS-80, it will play a legal game, but it will not be particularly challenging. Your children will probably enjoy playing it because they will beat it most of the time. A one-ply look-ahead does not produce brilliant play. To make the machine more intelligent, we need to add the α-β minimax algorithm. To do this, we will substitute the following code for lines 530 to 560:

```
510 DT = DM
520 L = 1: SC = 0: S = -1
530 V(0) = -99: V(1) = -99: M(0) = T:
    M(1) = H
```

The maximum depth of the search, DT, is set to the value DM which was calculated at line 170. Next, we initialize several key variables. The depth of the search (L) starts with a value of 1. The variable that remembers the cumulative difference between the changes in the players' scores (SC) is set to zero. The variable that keeps track of which player has the move (S) is set to a -1.

The array that retains the best values obtained so far at each level of the tree is initialized at a -99 for index values of 0 and 1. The array that keeps track of the move (M) currently being considered at each level of the tree is set to the value T (the location of the machine's piece) for the index value of 0 and to H (the location of the human's piece) for the index value of 1.

The first move considered in the look-ahead process will be for the machine. The value of L at the base of the tree will be 2. You may think this a bit curious, but it is a useful strategy since we will want to refer to V(L − 2) and M(L − 2) at several points in the search process.

To begin the main loop of the tree search, we increase the depth (L) by 1 and then initialize the variable Q (an index for the moves that have already been considered at this level of the tree), the variable S (an index indicating whose turn it is to move), and the variable V (the value for the best move found so far at this level of the tree):

```
540 L = L + 1: Q(L) = 0: S = -S: V(L) = V(L -2)
```

The next step is to increment the Q index so that the machine can consider the next move option at this level of the tree. If we have exhausted all of the move options at this level, it is time to branch to a special section of code that instructs the machine to back up one level in the tree:

```
580 Q(L) = Q(L) + 1: IF Q(L) > 4 THEN 760
```

If the move options at this level have not been exhausted, the machine is instructed to generate the location (J) of a square to which the player can consider moving:

```
590 J = M(L - 2) + D(Q(L))
```

Making Moves

Move generation is quite simple because M(L−2) always represents the current location of the piece of the player whose turn it is to move and D(Q(L)) represents one of the four directions in which a move can potentially be made. I say potentially because the new location could be off the playing field or could already be occupied by the opponent's piece. Our next statement checks for this:

```
600 IF B(J) = 99 THEN 580 ELSE M(L) = J: E(L) = B(J)
```

If the move is legal, the new location is recorded as the current move at this level in array M, and the digit at this location is recorded as the current value at this level in array E. In addition, the internal representation of the playing field, B, is modified to reflect this move, and the variable SC is altered to keep track of the relative points accumulated by each player:

```
610 B(J) = 99: B(M(L − 2)) = 0: SC = SC + S * E(L)
```

In order to provide a visible record of the machine's "thought" process, the machine is instructed to print the move location (J), the cumulative change in the score at this point (SC), and the best value so far at this level, V(L), in the empty area on the right side of the video display. The machine also checks to see if the current depth is the maximum possible depth. If not, it branches to line 540 which starts the main loop again by going one level higher in the tree:

```
620 PRINT@179 + 64*L, J; SC; V(L); " ";:
    IF L < DT THEN 540
```

If the search is at the maximum depth (ie: L = DT), then the machine records the current value of SC as a potential new best value:

```
670 V(L + 1) = −S * SC
```

The next step is to reverse the move we just made. When a new move is made, the board representation is updated at line 610. When the move is taken back at line 680, we refer to the process of "downdating" the board:

```
680 B(M(L)) = E(L): B(M(L − 2)) = 99:
    SC = SC − S * E(L)
```

Negamax

To determine whether the value recorded at line 670 is better than the current value at this level, we employ the *negamax* procedure (see reference 1). This is equivalent to the minimax procedure except that its implementation requires fewer programming steps. Rather than minimizing and maximizing at every other level, the negamax approach always maximizes the results at a given level, but it reverses the arithmetic signs at every other level to produce the identical result as the minimax procedure. (You may recognize the similarity between this approach and the use of the logical NOR operation in circuit design. Two levels of NOR logic are equivalent to a level of ANDs followed by a level of ORs.) The following line implements the negamax calculations:

```
700 IF V(L) < −V(L + 1) THEN
    V(L) = −V(L + 1) ELSE 580
```

If the new value is worse than or equal to the current value, the machine branches to line 580 and considers another move at this level. If the new value is better than the current value, the machine continues to the next statement:

```
740 IF L = 2 THEN BM = M(L):
    PRINT@180, BM; V(2);
```

If the search process is at the base of the tree (L = 2), then the new best move is recorded for later use and an announcement of our new find is printed on the video display. This includes both the new location, BM, and the net difference in the score produced by the anticipated sequence of moves, V(2)

Evaluating for Cutoff

At line 700, the minimax rule was applied to select the best option for the player with the move. The next consideration is whether the current move will produce an α-β cutoff. The logic for this decision is based on the idea that the opponent may already have a move at this level in the tree that guarantees him a value that is at least as

good as the one we are considering in the current branch.

This procedure is easy to implement but not particularly easy to understand. The general idea was explained by an example by W D Maurer in an earlier issue of this magazine (see reference 2), and a detailed exposition is provided by Knuth and Moore (reference 1). For our purposes, the job is accomplished by a single statement:

```
750 IF V(L) < −V(L − 1) THEN 580
```

If the condition specified in line 750 is satisfied, then a cutoff is not called for, and the process branches to line 580, where the next move option is considered at this level. If the condition in line 750 is not satisfied, the process continues to line 760, which instructs the machine to back up one level in the tree:

```
760 L = L − 1: S = −S: PRINT@243 + 64 * L, Y$;:
    IF L > 1 THEN 680
```

The backup procedure includes decreasing the value of L by 1, changing the index that indicates which player has the move, erasing the move information printed on the right side of the video display, and branching to line 680 to execute the downdate instructions for the new value of L. If the value of L decreases to 1, all options at the base of the tree have been examined and the search is completed. In this case, the machine drops to line 800 and makes the move which has been stored by variable BM.

It is important to note that the jump to line 680 for downdating is followed by execution of the minimax test (line 700) for a new best move at the new value of L; sometimes the program proceeds again to line 750, where another cutoff may occur. Note, also, that line 760 can be entered from two different locations. In addition to dropping through from line 750, the machine can be directed to line 760 from line 580 as a result of exhausting all possible move options at a given level. The α-β test at line 750 provides a means for terminating the search at a node before all of the options have been analyzed.

The version of Treasure Search just completed is given in listing 1.

Listing 1: *Listing for the game of Treasure Search, written for the TRS-80 using Level II BASIC. This game, in its various versions, illustrates the usefulness of alpha-beta pruning when searching a tree for the best strategy in a two-player game. The game, as written here, plays an unmodified alpha-beta strategy against a human player. See listings 2 and 3 for additions that cause the computer to play more rapidly.*

```
100 CLEAR 100: CLS: DEFINT A-Z: RANDOM: SH = 0:
    ST = 0
110 PRINT@463, "PLEASE ENTER YOUR NAME";: INPUT N$
120 DIM A(8), B(99), D(4), E(12), M(12)
130 DIM PV(12,12), Q(12), V(12), Z(12)
140 D(1) = -10: D(2) = -1: D(3) = 1: D(4) = 10
150 A(1) = 10: A(2) = -10: A(4) = -1: A(8) = 1: CLS
160 PRINT@461, "TRS-80 PLAYING STRENGTH (1 TO 5)";:
    INPUT Y
170 DM = 2*Y: FOR I = 11 TO 88: B(I) = RND(9): NEXT I
180 FOR I = 0 TO 10: B(I) = 99: NEXT I: FOR I = 89 TO 99:
    B(I) = 99: NEXT I
190 FOR I = 19 TO 79 STEP 10: B(I) = 99: B(I + 1) = 99:
    NEXT I
220 CLS: FOR I = 11 TO 88: IF B(I) = 99 THEN 240
230 X$ = RIGHT$ (STR$ (B(I),1): GOSUB 1000
240 NEXT I: PRINT@22, "TREASURE SEARCH"; Y:
250 PRINT@256, N$;: PRINT@448, "TRS-80";:
    Y$ = STRING$ (12, " ")
260 T = 54: T$ = "*": H = 45: H$ = "X":
    Z$ = STRING$ (32, " ")
270 I = T: X$ = T$: GOSUB 1000: B(T) = 99: B(H) = 99
280 I = H: X$ = H$: GOSUB 1000: GOTO 300
290 PRINT@788, "ILLEGAL MOVE, TRY AGAIN";:
    FOR I = 1 TO 999: NEXT I
300 PRINT@788, Z$;: PRINT@788, "WHICH DIRECTION FOR
    X";
310 IF INKEY$ = "" THEN 310 ELSE R = PEEK(16444)
320 R = INT(R/8): J = H + A(R)
330 IF B(J) = 99 THEN 290 ELSE PRINT@788, Z$;
360 I = H: B(I) = 0: X$ = "-": GOSUB 1000: SH = SH + B(J)
370 H = J: B(H) = 99: I = H: X$ = H$: GOSUB 1000
380 PRINT@321, SH;: IF SH > 99 THEN 930
510 DT = DM
520 L = 1: SC = 0: S = -1
530 V(0) = -99: V(1) = -99: M(0) = T: M(1) = H
540 L = L + 1: Q(L) = 0: S = -S: V(L) = V(L - 2)
580 Q(L) = Q(L) + 1: IF Q(L) > 4 THEN 760
590 J = M(L - 2) + D(Q(L))
600 IF B(J) = 99 THEN 580 ELSE M(L) = J: E(L) = B(J)
610 B(J) = 99: B(M(L - 2)) = 0: SC = SC + S * E(L)
620 PRINT@179 + 64 * L, J; SC; V(L); " ";: IF L < DT THEN
    540
670 V(L + 1) = -S * SC
680 B(M(L)) = E(L): B(M(L - 2)) = 99: SC = SC - S * E(L)
700 IF V(L) < -V(L + 1) THEN V(L) = -V(L + 1) ELSE 580
740 IF L = 2 THEN BM = M(L): PRINT@180, BM; V(2);
750 IF V(L) < -V(L - 1) THEN 580
760 L = L - 1: S = -S: PRINT@243 + 64 * L, Y$;: IF L > 1
    THEN 680
800 I = T: B(I) = 0: X$ = "-": GOSUB 1000: PRINT@179, Y$;
810 T = BM: ST = ST + B(T): B(T) = 99: I = T: X$ = T$
820 GOSUB 1000: PRINT@513, ST;: IF ST < 100 THEN 300
910 PRINT@915, "THANK YOU FOR A PLEASANT GAME";
920 GOTO 920
930 PRINT@917, "CONGRATULATIONS, YOU WIN";:
    GOTO 920
1000 R = INT (I/10): C = I - 10 * R: K = 141 + (8 - R) * 64 +
    C * 4
1010 PRINT@K, X$;: RETURN
```

Traditional Techniques

This completes the α-β minimax module. You may be surprised that this algorithm can be presented in only a few lines of BASIC. The simplicity of the presentation is possible because we used the negamax procedure and because Treasure Search is a simple game. It is very straightforward in terms of move generation (line 590), move evaluation (line 600), and the ease of updating (line 610) and downdating (line 680) the internal representation of the playing field. This simplicity also means that the algorithm will execute fairly rapidly, and thus a search of nontrivial depth can be completed in a reasonable amount of time.

The algorithm that I have presented for the α-β minimax procedure is quite different from the one that appears in most textbooks. Traditionally, the algorithm generates all of the moves at each node and then orders them using a plausibility routine before proceeding to the next deeper level of the tree. This approach is based on

Listing 2: *To implement the killer heuristic, these lines are to be added to listing 1, replacing line 590 of listing 1 and inserting lines 550, 560, and 710.*

```
550 J = Z(L): I = 0
560 I = I + 1: IF J = M(L - 2) + D(I) THEN 600 ELSE IF I < 4
    THEN 560
590 J = M(L - 2) + D(Q(L)): IF J = Z(L) THEN 580
710 IF L > 2 THEN Z(L) = M(L)
```

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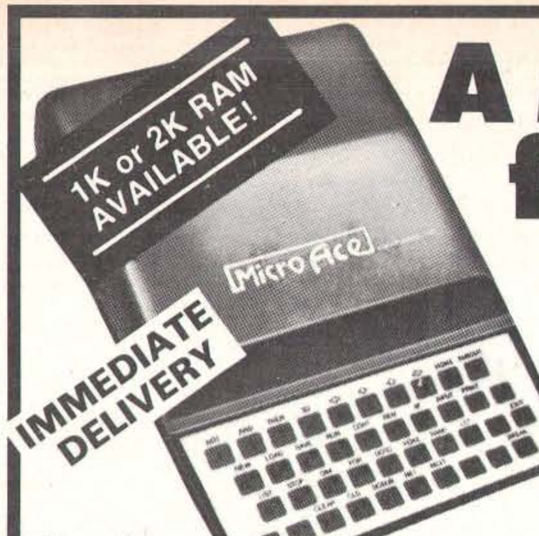

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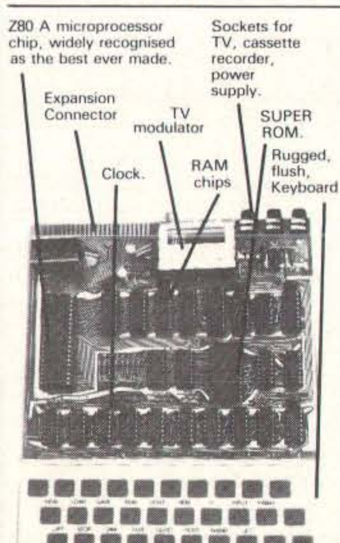

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the well-known finding that the efficiency of the α-β method is increased greatly when the strongest moves for each player are examined first at each level of the tree. The disadvantage of generating, ordering, and storing all of the moves at each level is that most of them will never be examined if an α-β cutoff occurs. If a cutoff can be produced by some other means, a great deal of time and memory can be saved by ignoring most of the moves at each node and omitting the ordering process.

The obvious question is, of course, how can we have our cake and eat it too? The competition among chess programmers over the last decade has led to some useful discoveries that are relevant to this problem. We will consider two of these discoveries that are especially effective in increasing the efficiency of the α-β minimax procedure. The first is the *killer heuristic* and the second is the *iterative search*.

The Killer Heuristic

The killer heuristic is a simple, yet powerful, idea that greatly improves move ordering. Instead of trying to order moves on the basis of a special plausibility analysis, the killer procedure simply remembers moves that were effective in the past. That is, information generated as a byproduct of the regular tree search is remembered; and it is applied later on in the search when a similar situation is encountered. In our implementation, we will remember the move that was judged most recently to be the best by the minimax rule at each level of the tree; each time we visit a new node in the tree, this move will be tried first.

To implement this idea, a few additions and modifications are necessary (see listing 2). When the tree search moves to a higher level, the first move examined should be the killer for that level (lines 550 and 560 of listing 2).

First, the appropriate move is read from the Z array, then a check is made to make sure the move is legal. If the killer does not produce an immediate cutoff, the search process will revert back to the normal procedure of examining each of the possible options. This process is controlled at lines 580 and 590.

We need to modify line 590 of listing 1 to make sure that a move is not examined twice (first as the killer and then as a regular option).

The final step in implementing the killer heuristic is to provide a means for remembering the move which is currently most effective in terms of the minimax strategy at each level of the look-ahead tree. This is accomplished by recording the current move each time the search process finds that it is the best one so far; this is done at line 700 of listing 1.

If the process is at the base of the tree ($L = 2$), then the move need not be recorded since the killer strategy does not apply at this level. It is too late to define a move that should be searched first at the base of the tree. By not altering the killer at $L = 2$, we make sure that the move examined initially will be searched only once even if it turns out not to be the one eventually chosen.

The killer heuristic is a very powerful addition to the α-β minimax algorithm. It requires only a small change in the algorithm, involves a negligible amount of time in terms of code execution, and often results in a decrease of 50% or more in the number of nodes actually visited in the search tree. At the deeper levels of the tree, it accomplishes essentially the same function as plausibility ordering, but does it much more efficiently.

The killer heuristic does not provide a means for ordering the moves when the machine is constructing the initial "limb" of the look-ahead tree. Because the search is a depth-first search, the process begins by selecting a sequence of moves that starts at the base node and goes to the maximum depth. The α-β cutoffs are most effective if this initial limb contains the strongest moves at each node for each player. This first stage of the search can be very time-consuming if the moves that are initially examined are eventually discarded for better ones. Because the killer heuristic employs strong moves only after they have been discovered by the regular search process, it is not helpful in structuring the initial "limb" of the look-ahead process.

The Iterative Technique

A different technique has proven its effectiveness for this purpose. This procedure is the *iterative tree search*. Its effectiveness for increasing the efficiency of the α-β minimax procedure was discovered serendipitously. At Northwestern University, for example, the Slate-Atkin chess-programming team was concerned about time control in move selection. Occasionally, in a complex position, their chess program would conduct its regular look-ahead search and would not complete the task in the amount of time anticipated. In several instances, the search would require four to five times as long as anticipated. This was a serious problem because chess tournaments are conducted under strict time allowances. If a program takes too much time for move selection during the early stages of the game, very little time will be available when it is needed during the latter part of the contest.

To cope with this problem, Slate and Atkin implemented an iterative procedure whereby the search is conducted in stages. At first, a complete two-ply search is conducted, then a three-ply search, then a four-ply search, etc, until a search of the desired depth is reached. The advantage of this procedure for time control is that a search can be aborted at any time and the machine can fall back upon the move selected by the immediately preceding search of one less ply in depth. It is possible to use information gained in the early, shallow searches to help structure (ie: order) the deeper searches.

Interestingly enough, Slate and Atkin discovered that this ordering information caused an increase in the efficiency of the deeper searches which more than made up for the time spent conducting the shallow searches. They also found that the beneficial effect of the iterations increases as the depth of search increases.

The iterative search is much easier to implement than you might think. The key idea for enhancing the efficiency of the α-β search is that the best sequence of moves (as judged by the minimax strategy) from a shallow search can be used to order the initial moves in the deeper search which follows. It is necessary to develop and record the *principal variation* for each of the searches.

This means that, instead of remembering just the best move at the base of the tree, the machine needs to record the best moves for each side at every level of the tree. Thus, it predicts the initial move, the best reply, the best counter-reply, etc. This principal variation is then used for selecting the initial limb for the next deeper search in

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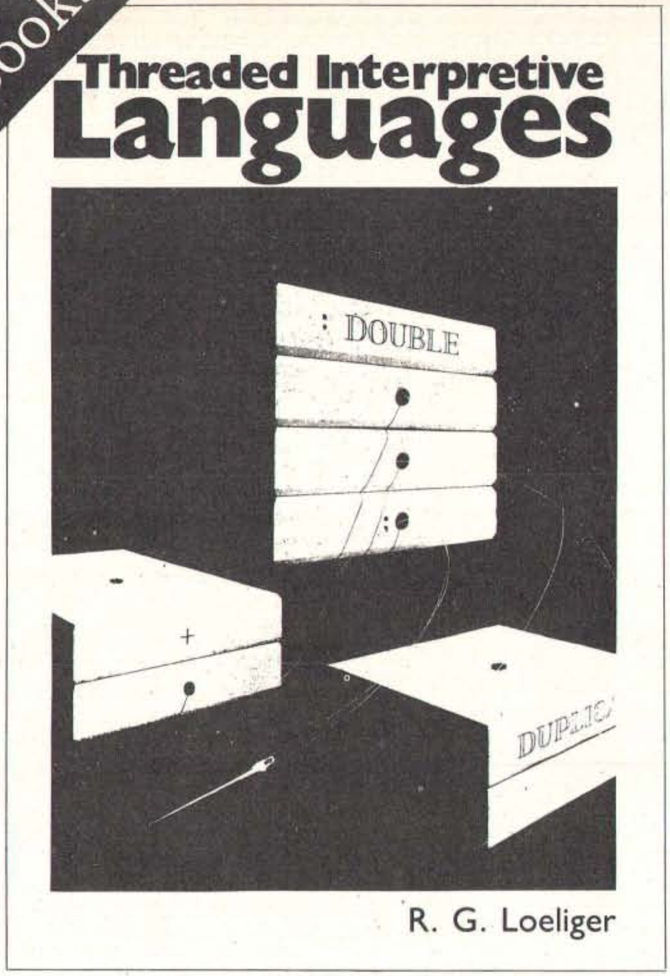

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Listing 3: *Additions to listing 1 to implement an iterative tree search algorithm. These lines are to be added to the combination of listings 1 and 2.*

```
500 FOR I = 4 TO DM: Z(I - 2) = PV(2,I): NEXT I
510 IF PV(2,3) = H THEN DT = DM ELSE DT = 2
720 I = L: PV(L,I) = M(L): IF L = DT THEN 740
730 I = I + 1: PV(L,I) = PV(L + 1,I): IF I < DT THEN 730
780 IF DT = DM THEN 800
790 FOR I = 2 TO DT: Z(I) = PV(2,I): NEXT I: DT = DT + 2:
    GOTO 520
```

the iteration. In our present algorithm, we employ this strategy by placing the principal variation from the previous search in the killer array at the start of each iteration.

The first requirement is the development and storage of the principal variation. This is fairly difficult to explain but not very difficult to implement (see lines 720 and 730 of listing 3). Once we have a principal variation, we then modify the initial preparation for the look-ahead search (see lines 500 and 510 of listing 3).

This accomplishes two important things. At line 500, the killer array receives the moves for each side that were ascertained to be best on the move calculation from the previous turn (not the previous iteration of this turn, but rather the last time the machine made a move). The index I-2 is used because the first two moves anticipated by that variation (one for the machine and one for the opponent) have already been played.

Line 510 checks to see if the opponent actually made the anticipated move. If so, an iterative search is unnecessary since the principal variation from the previous move calculation provides the same ordering information as would be obtained by the iterations. The search depth, DT, is therefore set to the maximum depth, DM. If the opponent does not make the anticipated move, an iterative search is required and therefore the search depth, DT, is set at the minimum value. Note that DT = 2 calls for a one-ply search.

When a search has been completed, it is necessary to determine if the maximum depth has been reached or whether another iteration is required. If the latter case holds true, the principal variation from the most recent iteration is stored in the killer array and the search depth is increased. In our present implementation, each iteration is two plies deeper than its predecessor. Lines 780 and 790 of listing 3 accomplish this task.

Analysis of Modifications

With these additions, the program will select a move in the Treasure Search game by using an iterative α-β minimax procedure enhanced by the killer heuristic. To demonstrate the power of this modified algorithm, I have made some sample runs which count the number of nodes visited in the look-ahead tree in an actual game with and without the various modifications. These results are very informative.

The program was examined in four variations: minimax, α-β minimax, α-β minimax with the killer heuristic, and iterative α-β minimax with the killer heuristic. The version involving the minimax strategy without α-β is produced simply by replacing line 750 with:

```
750 GOTO 580
```

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This eliminates all of the α-β cutoffs.

To insure comparability of our results, an initial game configuration (digit assignment) was constructed and placed in an array such that each game started with the playing field depicted in table 1. In addition, the same series of moves was made by the human opponent in each game. Each version of the program calculated a move for the machine's first four times to play. In each case, the search depth was set for a seven-ply search. The number of nodes in each of the look-ahead trees is presented in table 2. The node count for the iterative search is the sum across all iterations.

An analysis of these results demonstrates the powerful effect of the α-β procedure. By using the IF statement at line 750 in the α-β versions, the search effort is reduced dramatically. In our example with a seven-ply search and with four options at each node, the α-β modification reduces the node count by a factor of about 10. Since there is an approximate linear relationship between the number of nodes in the tree and computation time, the α-β procedure selects a move in one-tenth the time of the full minimax search. Since the two procedures always select the same move, this enhancement in speed comes at essentially no extra cost.

The results in table 2 indicate that the killer heuristic is also a powerful addition to the α-β algorithm. In our example, the node count was reduced by 30% to 50% by simply remembering moves that had proved themselves effective at an earlier stage in the search.

This modification also provides substantial benefits at minimal extra cost in terms of processing time and memory requirement. The empirical analysis presented in table 2 also demonstrates the beneficial effects of the iterative procedure. The number of nodes generated in the calculation for the first move was reduced by almost 25% despite the fact that searches of one ply, three plies, and five plies were conducted prior to the seven-ply search.

In the calculations for moves 2, 3, and 4, the prior principal variation correctly predicted the human's move so that the machine dispensed with the iterations because it already had the ordering information they would have produced. The results presented in table 2 clearly indicate that the iterative procedure enhances the efficiency of the search process.

	Number of Nodes in the Look-ahead Tree			
	First Move	Second Move	Third Move	Fourth Move
Minimax	13157	18456	20029	17609
α-β Minimax	1965	1650	1641	1794
α-β Minimax with Killer Heuristic	969	1023	926	830
Iterative α-β Minimax with Killer Heuristic	753	571	675	363

Table 2: *An empirical analysis of the minimax algorithm and enhancements as applied to the Treasure Search game. Each version of the program conducted a seven-ply look-ahead search.*

Improvements

A comparison of the full minimax procedure as it was employed in the early 1950s with the modern, enhanced α-β procedure indicates a truly dramatic increase in search efficiency. The full minimax procedure averaged approximately 17,000 nodes for the first four move calculations. The modern algorithm as presented in this article averaged approximately 600 nodes for these same four calculations. This difference is large enough to convert an impractical but elegant idea into a powerful programming tool. I should also point out that the effectiveness of these procedures would be even more notable if we had examined a game like chess with more than thirty options at each node instead of a simple game with only four options at each node.

There is an additional way to increase the efficiency of the α-β search. In the present program, the evaluations of the terminal positions are based on a cumulative process in which the treasures collected at each node in the tree are added or subtracted to a running total. As the search process nears the maximum depth of the tree, it is possible to set boundary conditions (ie: a window) that determine whether the final value can influence the selection process.

In many cases, the nonterminal score will be sufficiently deviant that the search can be terminated prematurely wthout any change in the ultimate decision process. This enhancement can significantly reduce the time required to complete the search.

Strategic Weakness

This program for Treasure Search will play a fairly intelligent game. As presented here, however, it has a major weakness. When the game reaches its final stages, the machine continues to search for a pathway which gives it the greatest amount of treasure *in the long run*. This is not an optimal strategy because the game is won or lost at this stage by short-range planning. The first player to reach 100 wins. The machine with its present strategy may pass up a large treasure which would provide an immediate win in favor of a smaller one which ultimately leads to a rich lode. This could throw away an easy win.

Serious players may wish to introduce a special set of instructions for the endgame to correct for this weakness. The machine's game can also be strengthened by converting the program to assembly language. The deeper the look-ahead search, the greater the apparent intelligence of the machine. Conversion to assembly language will permit the program to search six plies deeper without increasing move-selecting time.

This article should provide useful information to anyone who wishes to write a game program which employs the α-β minimax procedure.■

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I have envisioned a system of lighting control that would illuminate any room that I enter, while darkening the one I just left.

In "Ask BYTE," Steve Ciarcia answers questions on any area of microcomputing. The most representative questions received each month will be answered and published. Do you have a nagging problem? Send your inquiry to:

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For this system to work, it must keep track of the number of people in the apartment (if there are more than one), and it must be able to sense their motion from room to room. Thus, if one person is in the living room, and he goes to the kitchen, the kitchen light should come on, while the living room light should go off. If there were more than one person in the living room, the light should remain on until the last person has left. Of course, manual control should be available, and the system should be able to recognize any sensing errors it may make, and reset itself accordingly.

Obviously, I need a doorway sensor that will detect a person passing through, and also detect the direction he is going. Would you suggest ultrasonic sensors, or would infrared optical sensors be more practical? Could you provide some circuit ideas to help me along?

Jim Porter

I am always glad to hear from someone who takes computer control seriously. Having a computer and automating your apartment makes being "gadget happy" sound almost respectable. In any case, I am familiar with your problem, and I'll try to offer a few circuits that might help.

When I first got involved with security systems, I did a lot of investigation on motion detectors, ultrasonics, and infrared systems. Very few companies offer automatic systems that count people and control lights in rooms. This should give you some indication of what you are getting yourself into.

Two possible methods that come to mind are detecting the motion of people within a room or counting them as they enter and exit.

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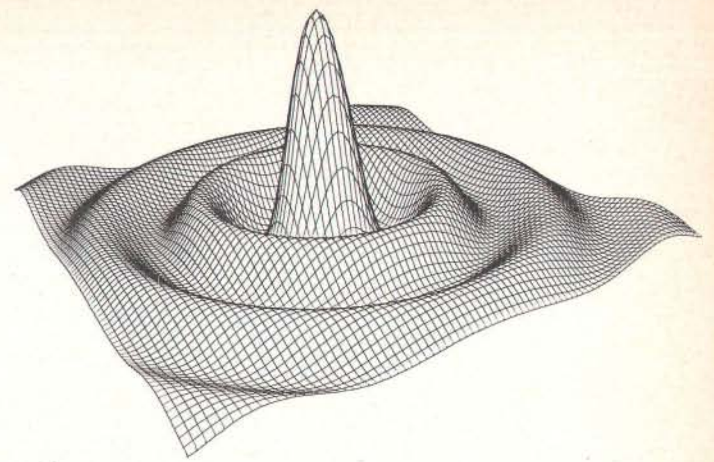

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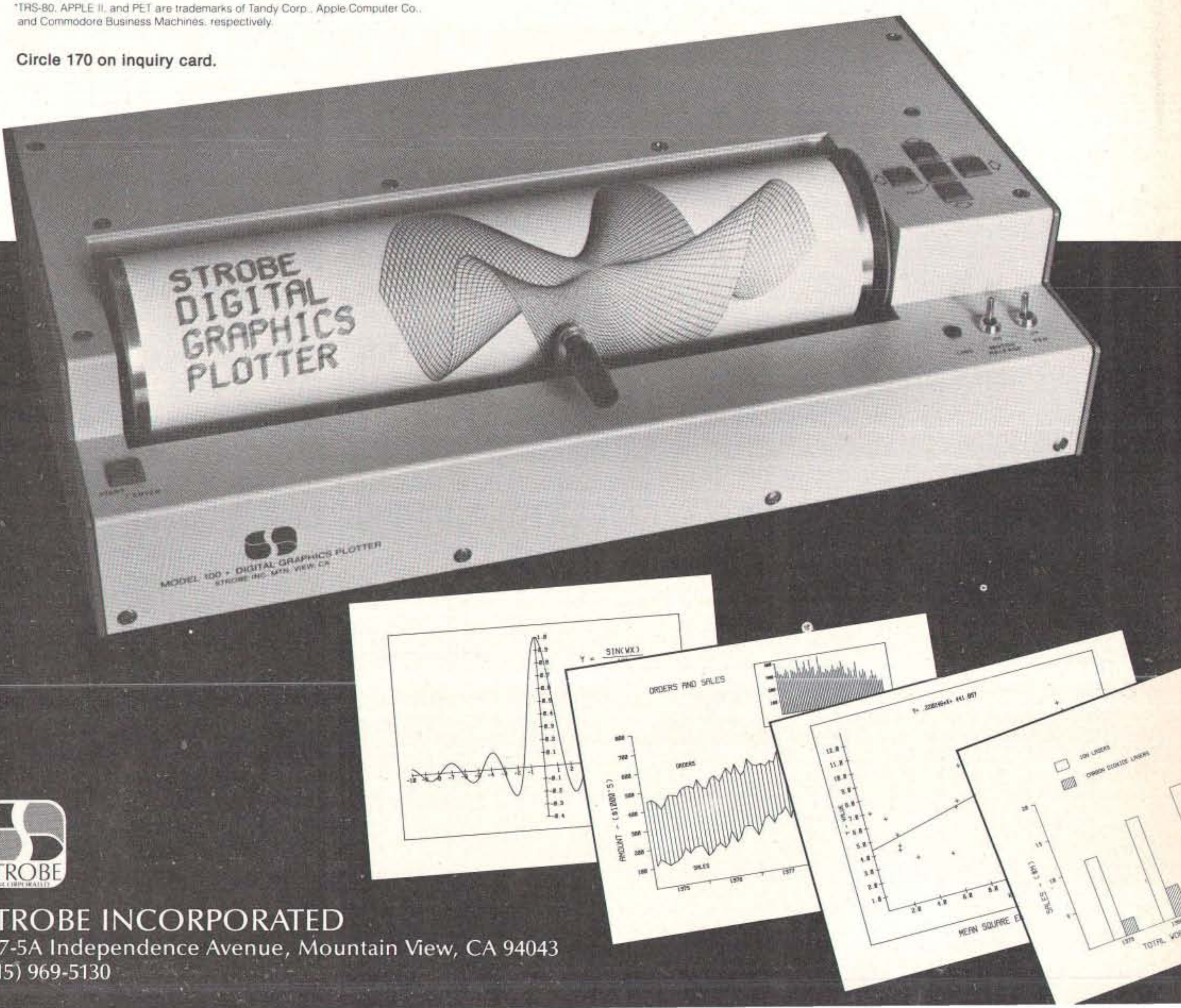

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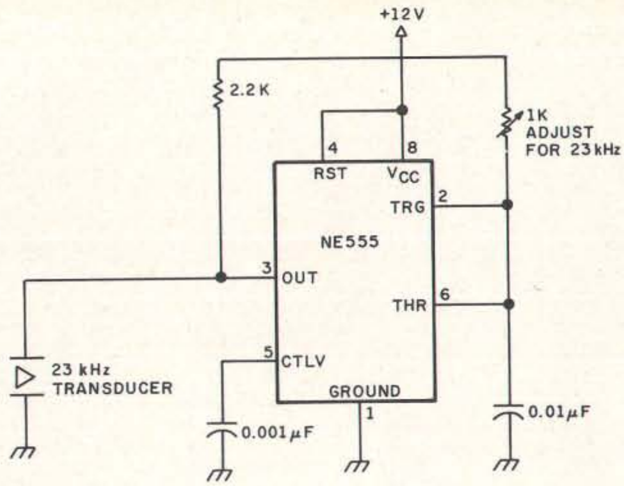

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IC3	7404	14	7	

1b

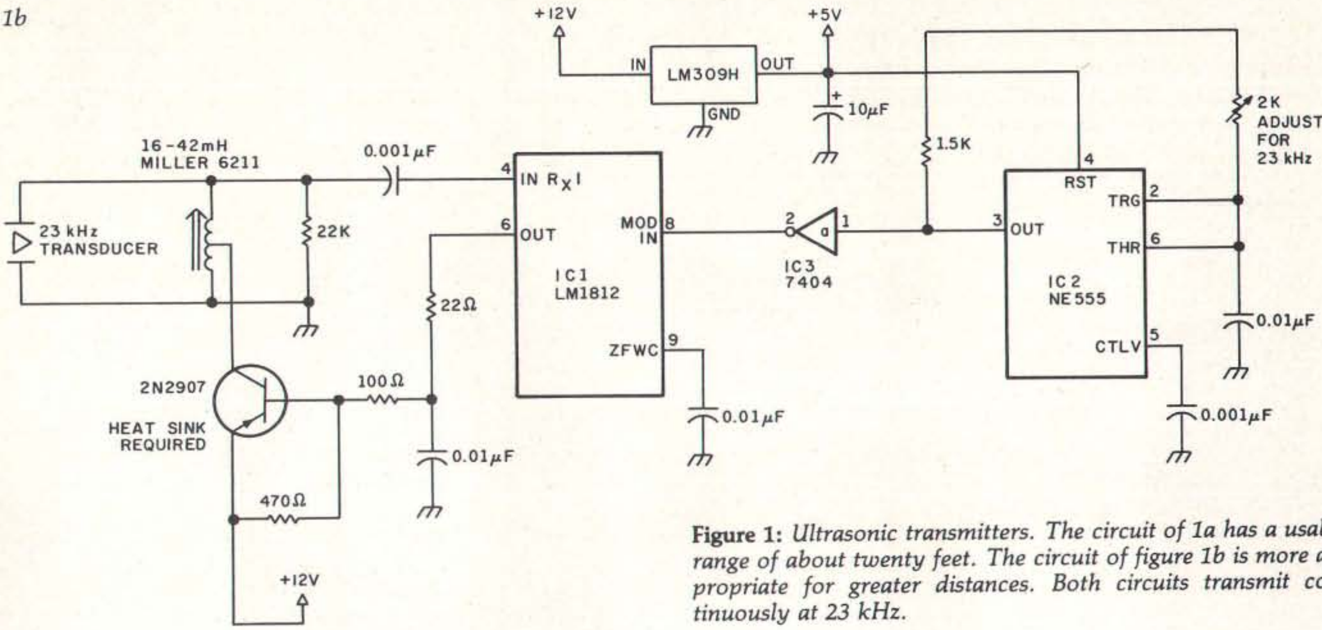

Figure 1: *Ultrasonic transmitters. The circuit of 1a has a usable range of about twenty feet. The circuit of figure 1b is more appropriate for greater distances. Both circuits transmit continuously at 23 kHz.*

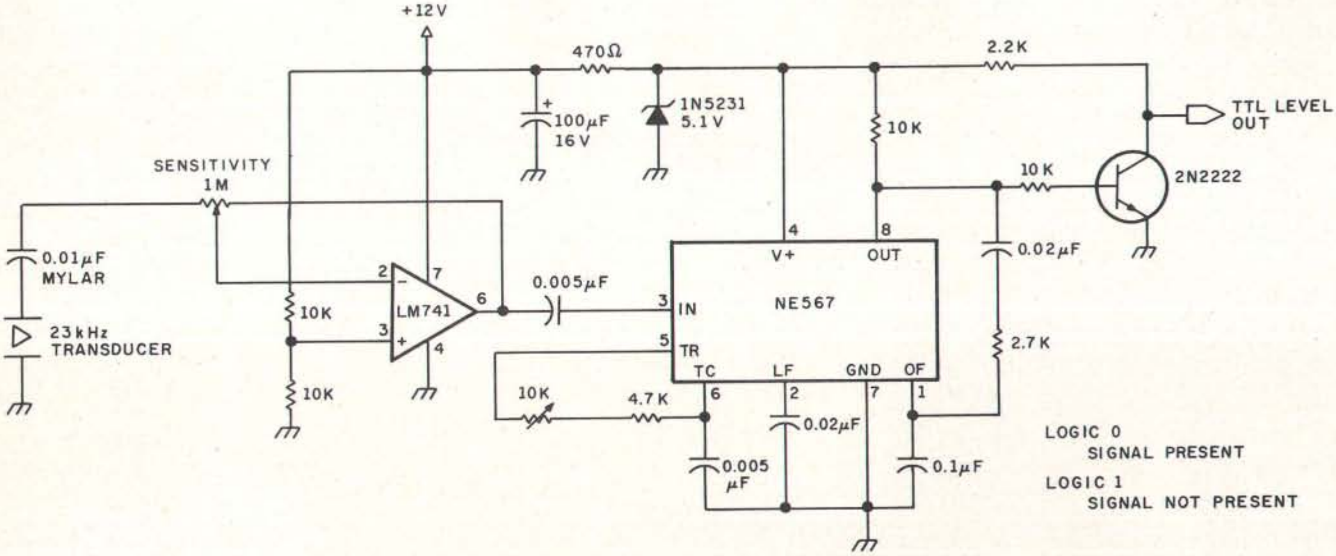

Figure 2: *Ultrasonic receiver. This simple receiver has TTL-compatible outputs, and it will work with either transmitter in figure 1.*

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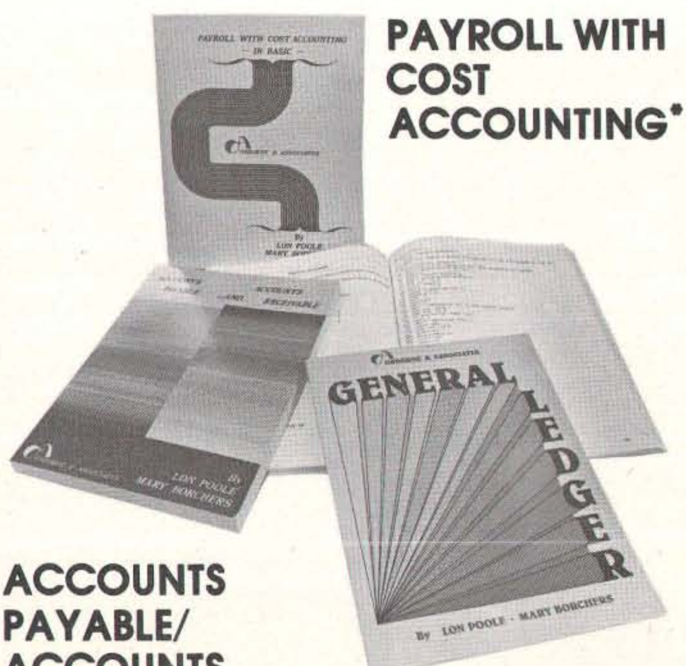

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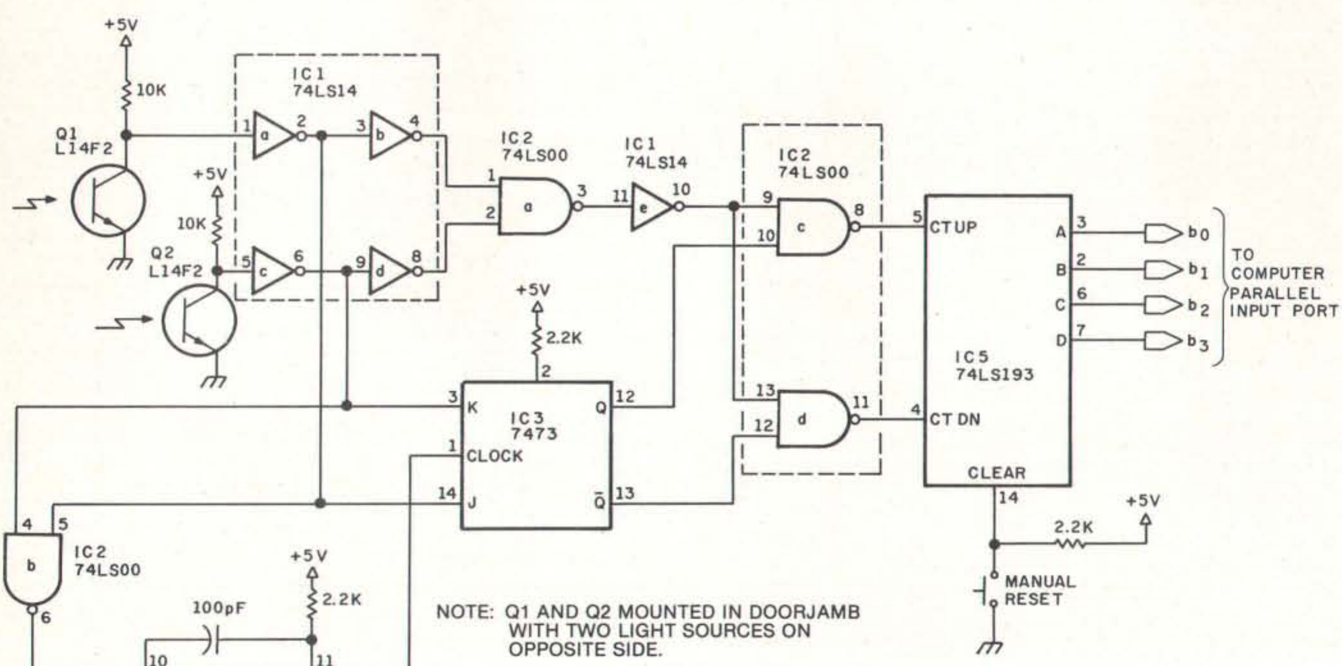
Text continued from page 266:
and the latest designs incorporate an active photosensitive integrated circuit. In fact, Delco Electronics (7 Oakland St, POB 2, Amesbury MA 01913) was offering an under-$30 kit a while back. In your application, with lights flashing on and off this may not be a reliable approach.

There are many ultrasonic systems on the market, and they range in price from $50 to $100. My only criticism of them is that they are prone to false alarms and you may find that the harmonics interfere with the BSR system. If you'd like to try placing one across a doorway or diagonally across a room, you could try the circuits shown in figures 1 and 2. These units operate at 23 kHz. Depending upon the sensitivity setting, they will detect most anything passing through the beam. For small rooms, you won't need much power, so the circuit of figure 1a should suffice. If you need a range of greater than twenty feet, use the higher-power version shown in figure 1b. The receiver for either circuit is shown in figure 2. By the way, the output is TTL (transistor-transistor logic)-compatible. Normally the signal will be a logic 0 (ie: nothing interrupting the beam between the transmitter and receiver); the signal will go to a logic 1 only when someone walks into the room.

The most effective system for detecting motion uses microwave radiation—similar to police radar and operating on the same X-band frequency. In my experience, these are the best by far. They are relatively false-alarm free and very sensitive. I have them installed throughout my home, and I have found their reliability to be exceptional. Unfortunately, they are expensive (in the range of $150 to $400 for domestic installations). A good unit is the Midex 55 made by Solfan (665 Clyde Ave, Mountain View CA 94043). Solfan's more expensive units have contact-closure outputs which would work well in your application.

The final solution to your problem might be to build a people counter. The circuit in figure 3 (sent to me by William Curlew) might be exactly what you need. It consists of two photodetectors (and two separate light sources) mounted in the doorjamb. Normally the light beam is uninterrupted and the output of the photodetectors is low. As long as there is light on both sensors, the output of IC2b is low. As someone starts through the doorway, one of the sensors goes high, clocking the JK flip-flop into one of two direction states. When the person fully enters the doorway, blocking both the sensors, a trigger pulse is generated and sent to gates 2c and 2d. Depending upon the state of the flip-flop, the clock pulse will be directed to either the count-up or count-down line of the 4-bit up/down counter, IC5. The counter will increment as people walk into the room and decrement they walk out. A manual reset is provided to start things out correctly. When the 4 outputs are tied to a parallel input port, your computer can read it as often as necessary to determine how many people are left in the room. Since the counting is done in hardware, timing is not critical. It will accommodate only fifteen people in its present form, so don't have too many guests at your parties. Finally, for absolute certainty, you may want to use it with the ultrasonic circuits previously discussed.

Steve



Number	Type	+5 V	GND
IC1	74LS14	14	7
IC2	74LS00	14	7
IC3	7473	4	11
IC4	74121	14	7
IC5	74LS193	16	8

Figure 3: *This circuit is capable of optically detecting the passage of people through a doorway and maintaining a count of people in a room. The photo-transistors sense motion through the doorway and cause the count stored in IC5 (a 4-bit binary counter) to either be incremented or decremented, depending upon the direction of passage.*

Remote Control in Europe

Dear Steve,

Please tell me if the X-10 remote-control system by BSR could be operated on 220 V 50 Hz in Europe. I see from the schematic diagrams and various pictures it is designed to work on 110 V 60 Hz. Do they have a 220 V system? If not, is there any way I could adapt the system to work on a 220 V system.

Please tell me where I can buy the set (ie: common console, cordless controller, appliance module, lamp module, in-wall switch module) using an American Express card; maybe from Sears as you said in your article. If so, please let me know the address of Sears; for that matter, any reliable dealer who accepts American Express. I'll be grateful for the two answers. Next time you are in Europe drop in and see us. We have a wood stove too, and I hope to connect it to the central heating system.

Rangith Amitirigala
Brugg, Switzerland

Up to this point the X-10 system has been available only in the American version (115 VAC 60 Hz). The custom LSI (large-scale integration) device used in the American units, surprisingly enough, can work on either 50 or 60 Hz. The polarity set on pin 13 of the command-console integrated circuit selects either of the two operating frequencies. These consoles cannot, however, be easily converted from 115 V to 220 V operation without considerable component changes.

A call to BSR (USA) Ltd in New Jersey produced some fruitful answers to your question. Even though BSR is working on a European version of the X-10, another company has just announced availability of a 220 V 50 Hz unit. I suggest that you contact this firm for price and delivery. The source is: Busch-Jaeger Elektro GmbH, 5880 Ludenscheid, Freisenberg, Post Fach 1280, West Germany (BRD).

As for Sears Roebuck and Company, it is my understanding that the firm accepts only its own credit card. Rather than worry about which stores will accept your credit card, you may find it easier to go your local bank (in Switzerland) and arrange for a letter of credit or bank draft when ordering from an American company.

Steve

Operational Amplifiers

I have been using the AD284J isolation operational-amplifier system that you described in "Mind Over Matter" (June 1979 BYTE, page 49) as an EKG (electrocardiogram) monitor, in conjunction with a surplus chart recorder. Can you recommend some books that will help me to learn more about operational amplifiers?

Matsutoshi Uchiyma
Tokyo, Japan

I am glad you are gaining experience with the circuit. As far as expanding your mind a little, I suggest the following books:

- Operational Amplifiers—Design and Applications, *Jerald G Graeme, Gene E Tobey, and Lawrence P Huelsman, McGraw-Hill Book Company, New York NY 1971.*
- Applications of Operational Amplifiers—Third Generation Techniques, *Jerald G Graeme, McGraw-Hill Book Company, New York NY 1973.*
- Handbook of Operational Amplifier Circuit Design, *David F Stout and Milton Kaufman, McGraw-Hill Book Company, New York NY, 1976.*

I hope these help.

Steve

Beyond "Cyclops"

Dear Steve,

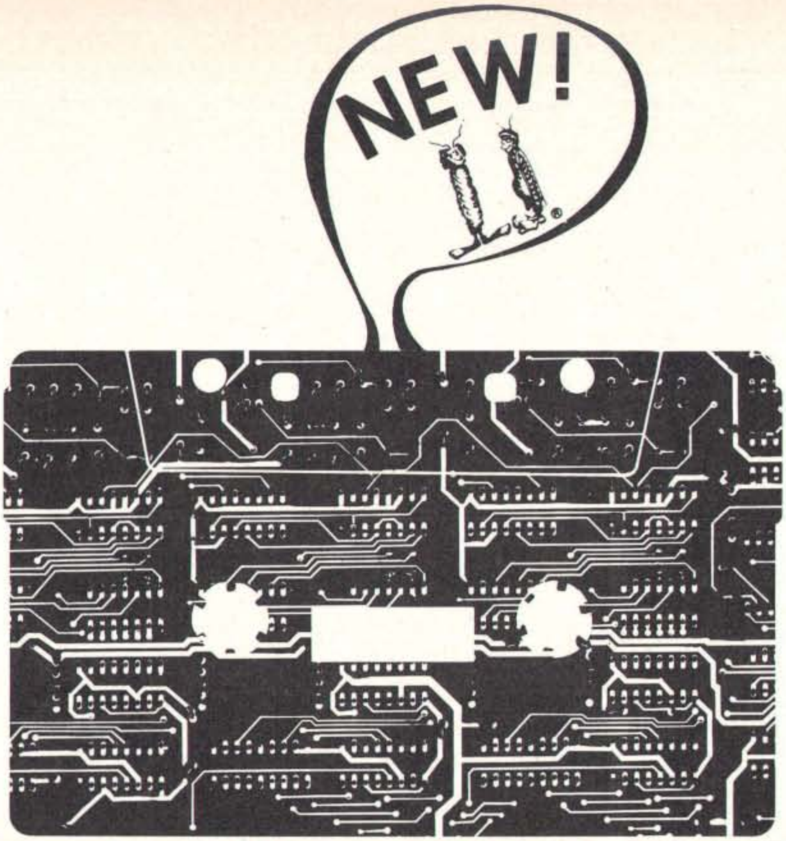
I consider your series of articles the best collection of homebrew-type construction ideas and projects available to the personal-computer experimenter. Your article "Self-Refreshing LED Graphics Display" (October 1979 BYTE, page 58) has prompted me to write you.

I'd like to propose a project to you. I understand that a construction project called "Cyclops" appeared in *Popular Electronics* that actually used a dynamic-memory integrated circuit to act as a "pseudo-image sensor." Can this unique idea be extended to larger-area memory devices? The 4 K-byte circuit would make a nice 64-by-64 element array.

Jesse Newton

Thanks for the pat on the back. Sometimes late at night I need it.

I remember that article



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well, and I have wanted to try exactly what you suggest. I've waited because I want fairly high resolution. Perhaps with the new 32 K and 64 K bit devices I will try it. Give me a little time.

The real problem I have is that there are so many good article ideas. I still want to put a computer in a car, do something with solar heat, remote control, and robotics. As long as you haven't been dissatisfied with everything so far, I trust that I'll find something interesting in the meantime.
Steve

Across-the-Sea File

Dear Steve,

I read with great interest your article "Computerize a Home" (January 1980 BYTE, page 28), and I am interested in the BSR X-10 system.

I contacted the Commercial Section of the US Embassy here and also my employer's purchasing agent in New York, but neither could find me the address of the BSR Company. I would appreciate it if you could tell me the manufacturer's address.

Thank you.
Z Lapidot
Rehovot, Israel

The address for BSR is: BSR (USA) Ltd, Rt 303, Blauvelt NY 10913, telephone: (914) 358-6060. There are many stocking distributors for its products including: The Software Exchange, 6 South St, Milford NH 03055.

BSR is an English company, and there may be outlets closer to you than those listed here.
Steve

Point-to-Point

Dear Steve,

My compliments for a fine set of articles over the years. Only recently have I had the time to try some of the projects you write about. I am planning to build the DVM (digital voltmeter) from your article in the January 1978 BYTE ("Add More Zing to the Cocktail," page 37).

I have contacted the printed-circuit board manufacturer that you mentioned in your article, but it no longer has boards available for that particular project. I do have all the components, and would like to avoid the tedium of hand-wiring the project. Do you have any boards available for a reasonable price?

I plan to use this circuit as part of a solar-energy-collector measurement system (among other things). I'm also trying to work out a method to manage energy consumption around the house.
Frank J Pakulski

A lot of people have built and are using the DVM interface you mention. (Please note a typographical error in table 1 of that article. On IC1 pin 24 is +5 V, pin 13 is ground, and pin 12 is −5 V.) I'm sorry that the company that once sold the components no longer supplies them. I have noticed that companies such as Jameco sell the MC14433 DVM chip, but not the printed-circuit board.

Recently, I have been arranging for boards and kits on some of my articles. This time the sources are more closely regulated and the boards and parts will be available far into the future.
Steve

In-Depth Information Center

Dear Steve,

I would like you to recommend some texts that would introduce me to computer hardware, from basic switching theory through the actual architecture of a computer. I'm tired of superficial prose intended for the general consumer. I need some more in-depth information that is found only in engineering texts. You know, something that presents the computer from the electronics engineer's point of view in a well-structured manner. What do you suggest? As a postscript, I would also like to learn about Pascal.
Daniel R Shook

You ask an extremely difficult question. I have talked to other computer enthusiasts and it seems that (given the wide variety of texts and computer books being published) no two can agree on what is best. I have felt that there is a void in this area, and, as a matter of fact, I have just written a book on building a Z80 computer system from scratch. It's above the introductory level, but not just for engineers—similar to my articles. It should be published in early 1981.

In the meantime, I suggest you join the McGraw-Hill Electronic & Control Engineers book club. Many of its monthly selections are introductory texts written for engineers.

A good book on Pascal is Pascal User Manual and Report—Second Edition, *by Jensen and Wirth from Springer-Verlag.*
Steve

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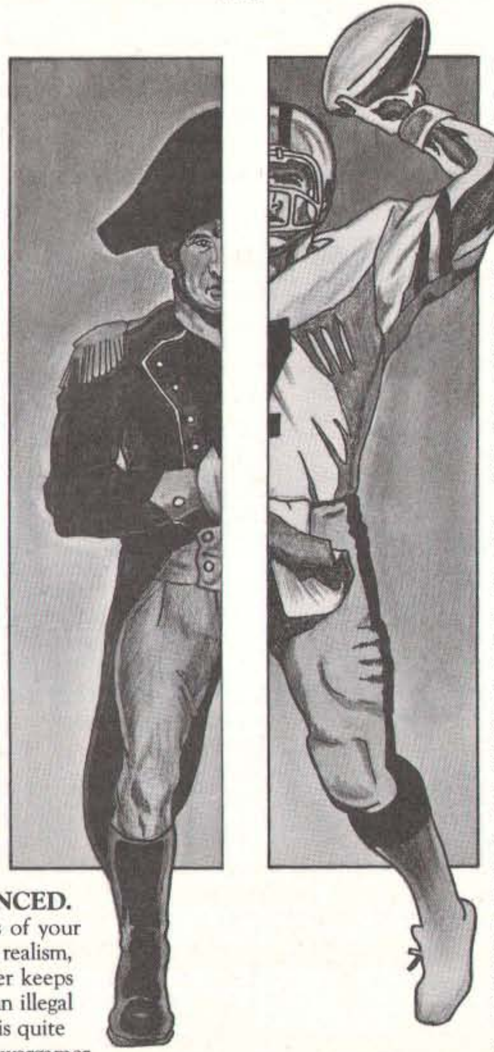

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Text continued from page 82:
subroutine name, length, and the subroutine instructions. Thus, our load-subroutine primitive can be represented as:

$$\text{LSUB N,L,}D_0..D_{n-1}$$

where:

N = subroutine name or number
L = subroutine length
D_i = subroutine instructions

For example, the primitive:

$$\text{LSUB CLIP,5,}D_0..D_4$$

loads a subroutine named CLIP with the given five instructions.

In order to maintain a sense of symmetry with these primitives, we need to include a primitive to read back a given subroutine. Although this feature does not affect the displayed image, it does aid the host in debugging and keeping track of the current status of the display. Thus, we require a read-subroutine primitive, which can be represented as:

RSUB N

where:

N = subroutine name or number

For example, the primitive:

RSUB CLIP

reads the instructions of the subroutine CLIP and presents the data to the host.

We have also assumed the existence of a programmable symbol generator. In order to support this feature, there is the need for some method of loading the generator. We either need to load an entirely new font definition in the symbol generator or alter only certain symbols: thus we must provide the option of loading the entire set or only one element. We can define each symbol by providing data which represents either the vectors that make up the symbol or by defining a bit pattern that forms the image of the symbol. In either case, our load-symbol primitive can be represented as:

$$\text{LSYM M,(A,)}D_0..D_n$$

where:

M = mode (*A*ll symbols or a *S*ingle symbol)
A = symbol code (optional: for single symbol only)
D_i = data mask defining the symbol

For example, the primitive:

$$\text{LSYM S,80,}D_0..$$

loads the symbol numbered 80 with the given data mask.

Symmetrically, we must include a primitive to read back the data describing a single or all symbols. This feature is necessary to be able to produce hard copies of the displayed image. The host must know, if an image is to be plotted, how the current font is defined. We use the same justification as above to support the option of reading all or only selected symbols. Mnemonically, our read-symbol primitive can be represented as:

RSYM M(,A)

where:

M = mode (*A*ll symbols or a *S*ingle symbol)
A = symbol code (optional: for single symbol only)

For example, the primitive:

RSYM A

reads back the entire font definition to the host.

In order to fully support a requirement for hard copy, two final primitives have to be provided. First, since we have assumed the existence of a color-look-up table, we must have some manner of reading back the values of the table to the host. Otherwise, the host would have to keep track of the current color definitions. This primitive thus reduces the host's bookkeeping and allows information on the actual displayed colors to be read back. For the same reasons as we described for the load-color-memory primitive, we must support the same options of reading back either the entire table, one entire parameter, or all parameters for one color code. Mnemonically, we can represent our read-color-memory primitive as:

RCRAM R,M(,A)

where:

R = reference (*I*ntensity, *H*ue, or *S*aturation color memory, or *A*ll)
M = mode (*S*ingle address or *A*ll addresses)
A = address (optional: for single address only)

For example, the primitive:

RCRAM I,A

reads back the contents of the entire intensity color memory.

Finally, we must be able to read back values of the pixel data itself. This feature is necessary not only for the support of hard copy, but allows the host to interrogate the display to read back the values of pixels at specified points in the image. We use the same justification as for the load-pixel primitive to support the various options of reference (full-frame, viewport, or X,Y). Mnemonically, our read-pixel primitive can be represented as:

RPIX R

where:

R = reference (*F*ull-frame, *V*iewport, or *X,Y*)

For example, the primitive:

RPIX F

reads back the contents of the entire display-frame buffer.

This completes our set of graphics

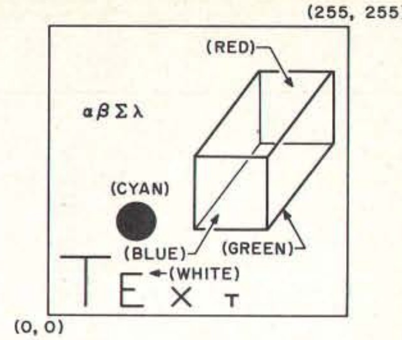

Figure 5: *A sample of the images produced by Micrograph using the primitives of listing 1.*

primitives for a color raster-scan display. The graphics primitives are summarized in table 2. Note that this list does not include primitive instructions for operations such as circle or arc generation. Such features can be generated by existing primitives (using the vector-drawing primitive). Furthermore, circle and arc primitives are difficult to generalize and cannot easily support any more complex curves: their utility is therefore very limited for the cost of their implementation in terms of support hardware and display-processor software. Furthermore, features such as transformations are not included at this level since they presuppose a definite image structure that cannot be known by the display processor. Other

Text continued on page 292

Listing 1: *This arrangement of primitives developed for Micrograph was used to produce the images in figure 5.*

```
MOV   20,10                 (T)
VEC   SHORT,REL,WHITE,20,30
MOV   5,30                  (T)
VEC   SHORT,REL,WHITE,25,30
MOV   30,10                 (E)
VEC   SHORT,REL,WHITE,30,20
MOV   30,10                 (E)
VEC   SHORT,REL,WHITE,40,10
MOV   30,15                 (E)
VEC   SHORT,REL,WHITE,40,15
MOV   30,20                 (E)
VEC   SHORT,REL,WHITE,40,20
MOV   50,10                 (X)
VEC   SHORT,REL,WHITE,60,20
MOV   50,20                 (X)
VEC   SHORT,REL,WHITE,60,10
MOV   70,10                 (T)
VEC   SHORT,REL,WHITE,70,15
MOV   65,15                 (T)
VEC   SHORT,REL,WHITE,75,15
LREG  VPORT,30,45,40,60     (rectangle
      around circle)
LPIX  VPORT,CYAN0..CYAN149
LREG  VPORT,120,60,200,120 (part of
      cube)
LPIX  VPORT,BLUE
LREG  VPORT,170,170,250,230 (part of
      cube)
LPIX  VPORT,RED
MOV   120,60
VEC   SHORT,REL,GREEN,170,170 (part
      of cube)
MOV   200,120
VEC   SHORT,REL,GREEN,250,230 (part
      of cube)
MOV   200,60
VEC   SHORT,REL,GREEN,250,230 (part
      of cube)
MOV   120,120
VEC   SHORT,REL,GREEN,170,250 (part
      of cube)
MOV   20,200
SYM   4,α,β,Σ,λ          (from user-defined
      font)
```

5 types of primitives, 37 instructions, 300 parameters

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The COMPUMAX business applications programs are written with the novice computer user in mind. They are easy to use, yet powerful in their capabilities. Further, COMPUMAX supplies the BASIC source code. Thus the programs are easy to modify.

MICROLEDGER

This General Ledger system performs the essential functions of dual entry bookkeeping and matches revenues and expenses:
MICROLEDGER includes the following programs.:
LEDGER 1 - builds and maintains the CHART OF ACCOUNTS file. This file contains both current and accumulated totals for each account.
LEDGER 2 - builds and updates the JOURNAL TRANSACTION file.
LEDGER 3 - lists both the the JOURNAL file and the CHART OF ACCOUNTS.
LEDGER 4 - computes the TRIAL BALANCE and executes POSTING of journal transactions into the CHART OF ACCOUNTS. An AUDIT TRIAL of all transaction is output.
LEDGER 5 - produces the PROFIT AND LOSS STATEMENT.
LEDGER 6 - produces the BALANCE SHEET. Assets, liabilities and owners' equities are shown by account and by totals. **$140.00**

MICROPAY

An Accounts Payable system, MICROPAY includes the following program & functions:
PAY 1 - initializes both Transaction and Master files, then begins the Accounts Payable process by inputting and adding records in the Transaction file.
PAY 2 - allows for changes and deletions of Transaction and Master records.
PAY 3 - reports outstanding Accounts Payables in four categories; under 30 days, 31-60 days, 61-90 days, and over 90 days.
PAY 4 - reports all outstanding Accounts Payables for a single customer or for all customers, and computes Cash Requirements.
PAY 5 - reports all outstanding Accounts Payables for a single date or for a range of dates and computes the Cash Requirements.
PAY 6 - lists both the Transactions and Master files.
PAY 7 - prints checks and accumulates and journalizes Accounts Payables. This program simultaneously creates entries for the MICROLEDGER file. **$140.00**

MICROREC

An Accounts Receivable system, MICROREC includes the following programs and functions:
REC 1 - initializes Accounts Receivable files, adds A/R record and prints invoices.
REC 2 - accepts receipt of customer payments and changes or deletions of A/R Transaction or Master file records.
REC 3 - reports outstanding Accounts Receivables in four categories; under 30 days, 31-60 days, 61-90days, and over 90 days.
REC 4 - reports all outstanding Accounts Receivables for a single customer, or for all customers and computes Cash Projections.
REC 5 - produces reports for all outstanding Accounts Receivables for a single date or for a range of dates and computes Cash projections.
REC 6 - lists Transaction and Master files and accumulates and journalizes Accounts Receivables, creating JOURNAL entries which communicate with the MICROLEDGER JOURNAL file. **$140.00**

MICROINV

This Inventory Control system presents a general method of Inventory Control and produces several important reports. Its program includes:
INV 1 - initializes Transaction and Master files and adds and updates Transaction and Master records.
INV 2 - handles inventory issued or received, creating inventory records. This program also accumulates and journalizes transactions, producing JOURNAL entries which communicate with the MICROLEDGER file.
INV 3 - lists both Transaction and Master files.
INV 4 - produces the STOCK STATUS REPORT, showing the standard inventory stock data and stock valuation, and the ABC ANALYSIS breaking down the inventory into groups by frequency of usage.
INV 5 - gives a JOB COST REPORT/MATERIALS, showing allocation of materials used year-to-date by each job or work code. (This is complemented by the Job Cost Report/Personnel in the MICROPERS program.)
INV 6 - computes and provides the E.O.Q. (Economic Order Quantities) **$140.00**

MICROPERS

This is a Payroll/Personnel program whose functions include:
PERS 1 - initializes the Master file and allows for entry and updates of Master records.
PERS 2 - initializes the Payroll file and allows for entry and updates of payroll records.
PERS 3 - lists an Employee Master Record or the entire Employee Master file; lists a single Payroll Record or the entire Payroll file.
PERS 4 - computes Payroll and prints the PAYROLL REGISTER. Prints PAYCHECKS and creates JOURNAL entries to be fed into the MICROLEDGER JOURNAL file.
PERS 5 - produces the JOB COST REPORT/PERSONNEL, computes the quarterly 941 bank deposit, and the Annual W-2 run. **$140.00**

All COMPUMAX programs available in machine readable format (diskette form) for the following machines:

TRS-80™ Model I	Micropolis 1053/11
APPLE II	Microsoft under CP/M
PET	CBASIC under CP/M
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FROM ADVENTURE INTERNATIONAL (By Scott Adams)

† 1. **ADVENTURELAND** - You wander through an enchanted world trying to recover the 13 lost treasures. You'll encounter wild animals, magical beings, and many other perils and puzzles. Can you rescue the Blue Ox from the quicksand? Or find your way out of the maze of pits? Happy Adventuring .....

† 2. **PIRATE'S ADVENTURE** - "Yo ho ho and a bottle of rum" You'll meet up with the pirate and his daffy bird along with many strange sights as you attempt to go from your London flat to Treasure Island. Can you recover Long John Silver's lost treasures? Happy Sailing, matey .....

3. **MISSION IMPOSSIBLE ADVENTURE** - Good morning, your mission is to...and so it starts. Will you be able to complete your mission in time? Or is the world's first automated nuclear reactor doomed? This one's well named. It's hard, there is no magic, but plenty of suspense. Good luck .....

4. **VOODOO CASTLE** - Count Cristo has had a fiendish curse put on him by his enemies. There he lies, with you his only hope. Will you be able to rescue him or is he forever doomed? Beware the Voodoo Man .....

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5. **THE COUNT** - You wake up in a large brass bed in a castle somewhere in Transylvani. Who are you, what are you doing here, and WHY did the postman deliver a bottle of blood? You'll love this Adventure, in fact, you might say it's Love at First Byte .....

6. **STRANGE ODYSSEY** - Marooned at the edge of the galaxy, you've stumbled on the ruins of an ancient alien civilization complete with fabulous treasures and unearthly technologies. Can you collect the treasures and return or will you end up marooned forever? .....

7. **MYSTERY FUN HOUSE** - Can you find your way completely through the strangest Fun House in existence, or will you always be kicked out when the park closes? ...

8. **PYRAMID OF DOOM** - An Egyptian Treasure Hunt leads you into the dark recesses of a recently uncovered Pyramid. Will you recover all the treasures or more likely will you join its denizens for that long eternal sleep? .....

9. **GHOST TOWN** - Explore a deserted western mining town in search of 13 treasures From rattlesnakes to runaway horses, this Adventure's got them all! Just remember, Pardner, they don't call them Ghost Towns for nothin'. (Also includes new bonus scoring system!) .... **$14.95 Per Adventure**

* **Note:** Apple requires 24K and has no lower case.
† Recommended for the novice adventurer, with many built-in HELPS!

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VISICALC **$150.00**

Take virtually any problem you would explore using calculator, pen, and paper, working in rows and columns. Apply VisiCalc and you'll see why every reviewer of this product has said the same thing: VisiCalc is the most useful, most important program yet developed for personal computing.

With VisiCalc, you work with an electronic worksheet of up to 63 columns and 254 rows. At the juncture of any column and row you can type in words and numbers. VisiCalc automatically performs all arithmetic functions, net present value, and transcendental functions - instantly!

CCA DATA MANAGEMENT SYSTEM $74.95

DMS Features:
File Creaton and Maintenance:
• Fields may be alphanumeric, numeric, integer, floating point, or fixed decimal with commas.
• Fields may be COMPUTED FIELDS. DMS will compute any field within a record, using constants or other fields in the same record. Functions include add, subtract, multiply, divide, and raise exponential powers.
• Records are easily located, using the SCAN feature. SCAN for records with a field over, below, or between a range of values.
• Records are easily added and updated. DMS "prompts" you with questions.
• Multi-diskette capabilities for larger files - up to 85,000 characters per file!
• Sort the records into almost any order, using up to 10 fields as "keys". So you can sort for customer numbers; within zip code, for instance.
• Delete records, "compact" files, and backup files on data diskettes easily.

Report Features:
• Print reports with records in any order.
• Select fields to be printed.
• Print mailing labels.
• Numeric totals and subtotals can be specified when a value in an unrelated field in the same record changes. For example, sort, subtotal, and print according to department, or month, or customer number, or model number.

GUARANTEED PROFIT **91%** **WINS PLACES SHOWS** **32%** **AVERAGE PROFIT AT ALL TRACKS-1978**

THE HORSE SELECTOR II (FLATS) (By Dr. Hal Davis) **$50.00**

New simplified version of the original Horse Selector. The first Horse Selection System to actually calculate the estimated odds of each horse.
HIGHER PROFITS (OVER 100%) POSSIBLE THROUGH SELECTIVE BETTING ON:
• Rates each horse in 10 seconds.
• Easy to follow rules.
• Can be used with any Apple II Computer.
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• Uses 4 factors (speed rating, track variant, distance of the present race, distance of the last race)
• Using the above factors, the Horse Selector calculates the estimated odds. BET on horses whose actual payoff (from the Tote Board or Morning Lines) is higher than payoff based on estimated odds.
• Using the above factors, the Horse Selector calculates the estimated odds. BET on any selected horse with an estimated payoff (based on Tote Board or Morning Lines) higher than calculated payoff (based on Horse Selector II).
• Source listing for the TRS-80™, TI-59, HP-67, HP-41, Apple and BASIC Computers.
• No computer or calculator necessary (although a calculator would be helpful for the simple division used to calculate estimated odds)

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Listing 2: *The first third of the firmware for Micrograph control, written for the Z80 microprocessor used in the prototype. The remaining portions of the firmware will be included in the next two issues of BYTE, along with hardware construction details (Part 2, December 1980 BYTE), and software (Part 3, January 1981 BYTE).*

```
                *H MICROGRAPH FIRMWARE SYSTEM 1
                *P 62
                ;
                ; MICROGRAPH 1.0 **************************************
                ;
                ; DEVELOPED BY GRADY BOOCH
                ;
                ; MICROGRAPH IS A FIRMWARE CONTROLLED MICROPROCESSOR
                ; BASED COLOR GRAPHICS DISPLAY SYSTEM, BUILT AS A
                ; SINGLE BOARD DISPLAY PROCESSOR, WHICH INTERFACES
                ; TO A STANDARD UNMODIFIED COLOR TELEVISION. THE USER
                ; SEES MICROGRAPH AS A 256 X 256 PIXEL SYSTEM, 8 BITS
                ; PER PIXEL. THIS SYSTEM IS MAPPED TO AN EQUAL OR LOWER
                ; RESOLUTION DISPLAY. GRAPHICS PRIMITIVES ARE AVAILABLE
                ; TO ALLOW THE USER TO CREATE GRAPHICS DISPLAYS.
                ; MICROGRAPH INTERFACES TO A HOST COMPUTER VIA THREE
                ; 8 BIT I/O PORTS. FURTHER INFORMATION IS AVAILABLE
                ; IN THE MICROGRAPH REFERENCE MANUAL.
                ;
0000                    ORG  0                      ;START AT ADDRESS 0
0000  F3        MICROG: DI                          ;DISABLE INTERRUPTS
0001  C38E00            JP   MAIN                   ;JUMP TO MAIN
                ;
                ;*****************************************************
                ;GLOBAL DEFINITIONS, CONSTANTS AND STRUCTURES**********
                ;*****************************************************
                ;
                ; STRUCTURES *****************************************
                ;
                ; STRUCTURES PROVIDES STORAGE FOR ALL FIRMWARE
                ; VARIABLES AND STRUCTURES. DEFAULT VALUES ARE PROVIDED
                ; IN THIS SECTION, WHICH IS STORED IN EPROM. STRUCTURES
                ; ARE MOVED TO RAM DURING INITIALIZATION, AND REFERENCED
                ; VIA THE OFFSETS DESCRIBED IN DEFINITIONS. NOTE THAT
                ; THE FIRST SEVERAL BYTES OF RAM ARE RESERVED FOR THE
                ; GRAPHICS AND MICROPROCESSOR STACKS.
                ;
                ESTRUCT:EQU  $                      ;START OF STRUCTURES
                EGDR:   EQU  $                      ;GRAPHICS DISPLAY REG
0004  00        EGDR0:  DEFB 0                      ;X
0005  00        EGDR1:  DEFB 0                      ;Y
0006  FF        EGDR2:  DEFB 255                    ;PRIMARY COLOR
0007  00        EGDR3:  DEFB 0                      ;SECONDARY COLOR
0008  00        EGDR4:  DEFB 0                      ;FRAME COUNT
0009  02        EGDR5:  DEFB 00000010B              ;VECTOR MODE
000A  00        EGDR6:  DEFB 0                      ;VIEWPORT 0 LEFT X
000B  00        EGDR7:  DEFB 0                      ;VIEWPORT 0 LEFT Y
000C  FF        EGDR8:  DEFB 255                    ;VIEWPORT 0 RIGHT X
000D  FF        EGDR9:  DEFB 255                    ;VIEWPORT 0 RIGHT Y
000E  00        EGDR10: DEFB 0                      ;VIEWPORT 1 LEFT X
000F  00        EGDR11: DEFB 0                      ;VIEWPORT 1 LEFT Y
0010  FF        EGDR12: DEFB 255                    ;VIEWPORT 1 RIGHT X
0011  FF        EGDR13: DEFB 255                    ;VIEWPORT 1 RIGHT Y
0012  08        EGDR14: DEFB 00001000B              ;DISPLAY FORMAT
0013  80        EGDR15: DEFB 10000000B              ;STATUS
                ESLINK: EQU  $                      ;GRAPHICS SUB LINKAGE
0014  A50A      EL00:   DEFW USER                   ;SUBROUTINE 00
0016  A50A      EL01:   DEFW USER                   ;SUBROUTINE 01
0018  A50A      EL02:   DEFW USER                   ;SUBROUTINE 02
001A  A50A      EL03:   DEFW USER                   ;SUBROUTINE 03
001C  A50A      EL04:   DEFW USER                   ;SUBROUTINE 04
001E  A50A      EL05:   DEFW USER                   ;SUBROUTINE 05
0020  A50A      EL06:   DEFW USER                   ;SUBROUTINE 06
0022  A50A      EL07:   DEFW USER                   ;SUBROUTINE 07
0024  A308      EL10:   DEFW GUSER                  ;SUBROUTINE 10
0026  A308      EL11:   DEFW GUSER                  ;SUBROUTINE 11
0028  A308      EL12:   DEFW GUSER                  ;SUBROUTINE 12
002A  A308      EL13:   DEFW GUSER                  ;SUBROUTINE 13
002C  A308      EL14:   DEFW GUSER                  ;SUBROUTINE 14
002E  A308      EL15:   DEFW GUSER                  ;SUBROUTINE 15
0030  A308      EL16:   DEFW GUSER                  ;SUBROUTINE 16
0032  A308      EL17:   DEFW GUSER                  ;SUBROUTINE 17
                ESLONG: EQU  $                      ;GRAPHICS SUB LENGTH
0034  01        ES00:   DEFB 1                      ;SUBROUTINE 00
0035  01        ES01:   DEFB 1                      ;SUBROUTINE 01
0036  01        ES02:   DEFB 1                      ;SUBROUTINE 02
0037  01        ES03:   DEFB 1                      ;SUBROUTINE 03
0038  01        ES04:   DEFB 1                      ;SUBROUTINE 04
0039  01        ES05:   DEFB 1                      ;SUBROUTINE 05
003A  01        ES06:   DEFB 1                      ;SUBROUTINE 06
003B  01        ES07:   DEFB 1                      ;SUBROUTINE 07
003C  01        ES10:   DEFB 1                      ;SUBROUTINE 10
003D  01        ES11:   DEFB 1                      ;SUBROUTINE 11
003E  01        ES12:   DEFB 1                      ;SUBROUTINE 12
003F  01        ES13:   DEFB 1                      ;SUBROUTINE 13
0040  01        ES14:   DEFB 1                      ;SUBROUTINE 14
0041  01        ES15:   DEFB 1                      ;SUBROUTINE 15
0042  01        ES16:   DEFB 1                      ;SUBROUTINE 16
0043  01        ES17:   DEFB 1                      ;SUBROUTINE 17
0044  00        ESPTR:  DEFB 0                      ;GRAPHICS SUB POINTER
0045  00        ESOFF:  DEFB 0                      ;GRAPHICS SUB OFFSET
0046  00        EGPC:   DEFB 0                      ;GRAPHICS STACK POINTER
0047  00        EREF:   DEFB 0                      ;CURRENT VIEWPORT REFERENCE
0048  00        EM:     DEFB 0                      ;VECTOR DRAWING TEMP
0049  00        EMM:    DEFB 0                      ;VECTOR DRAWING TEMP
004A  00        EMN:    DEFB 0                      ;VECTOR DRAWING TEMP
004B  00        ESX:    DEFB 0                      ;VECTOR DRAWING TEMP
004C  00        ESY:    DEFB 0                      ;VECTOR DRAWING TEMP
004D  00        ECOLOR: DEFB 0                      ;VECTOR COLOR STORAGE
004E  0000      EXERRT: DEFW 0                      ;ERROR SERVICE TEMP
0050  C9        ENULL:  DEFB 11001001B              ;FRAME SERVICE
                ;
                ; NON MASKABLE INTERRUPT TRAP ************************
                ;
                ; A TRAP TO THIS LOCATION IN MEMORY OCCURS UPON RECEIPT
                ; OF A NON MASKABLE INTERRUPT. THIS SECTION FORCES A
                ; JUMP TO THE ERROR SERVICE ROUTINE, XERR.
                ;
0066            NMINT:  ORG     102                 ;LOCATION OF INTERRUPT
0066  1834              JR      XERR                ;JUMP TO ERROR SERVICE
                ;
                ; CONSTANTS ******************************************
                ;
                ; CONSTANTS PROVIDLS STORAGE FOR ALL FIRMWAKE CONSTANTS
                ; WHICH ARE STORED IN EPROM.
                ;
                CONST:  EQU  $                      ;START OF CONSTANTS
                IVECT:  EQU  $                      ;START OF INTERRUPT VEC
0068  4601      INT0:   DEFW FRAME                  ;FRAME SERVICE
006A  6801      INT1:   DEFW INPUT                  ;INPUT SERVICE
006C  7601      INT2:   DEFW OUTPUT                 ;OUTPUT SERVICE
                PENTER: EQU  $                      ;PRIMITIVE ENTRY POINTS
006E  6F02              DEFW CALLS                  ;CALLS ENTRY
0070  AB02              DEFW LCRAM                  ;LCRAM ENTRY
0072  1403              DEFW LPIX                   ;LPIX ENTRY
0074  8703              DEFW LREG                   ;LREG ENTRY
0076  9F03              DEFW LSUB                   ;LSUB ENTRY
0078  CB03              DEFW LSYM                   ;LSYM ENTRY
007A  FB03              DEFW MOV                    ;MOV ENTRY
007C  8504              DEFW RCRAM                  ;RCRAM ENTRY
007E  EE04              DEFW RETN                   ;RETN ENTRY
0080  0E05              DEFW RPIX                   ;RPIX ENTRY
0082  5705              DEFW RREG                   ;RREG ENTRY
0084  6505              DEFW RSUB                   ;RSUB ENTRY
0086  8705              DEFW RSYM                   ;RSYM ENTRY
0088  BB05              DEFW SYM                    ;SYM ENTRY
008A  4606              DEFW VEC                    ;VEC ENTRY
008C  8807              DEFW WAIT                   ;WAIT ENTRY
```

Listing 2 continued on page 282

YOUR MODEL II CAN HAVE SNAPP!

SNAPP II EXTENDED BASIC

A family of enhancements to the Model II BASIC interpreter. Part of the package originated with the best of APPARAT, INC.'s thoughts in implementing NEWDOS BASIC. The system is written entirely in machine language for SUPER FAST execution. The extensions are fully integrated into Model II BASIC, and require NO user memory, and NO user disk space. The package is made up of the following five modules, each of which may be purchased separately:

XBASIC - Six single keystroke commands to list the first, last, previous, next, or current program line, or to edit the current line. Ten single character abbreviations for frequently used commands: AUTO, CLS, DELETE, EDIT, KILL, LIST, MERGE, NEW, LLIST, and SYSTEM. $25

XREF - A powerful cross-reference facility with output to display and/or printer. Trace a variable through the code. Determine easily if a variable is in use. $40

XDUMP - Permits the programmer to display and/or print the value of any or all program variables. Identifies the variable type for all variables. Each element of any array is listed separately. $40

XRENUM - An enhanced program line renumbering facility which allows specification of an upper limit of the block of lines to be renumbered, supports relocation of renumbered blocks of code, and supports duplication of blocks of code. $40

XFIND - Permits quick and easy location of specified strings or keywords within the program text. $30

SAVE - on the purchase of the entire package. $140

CONVERT

This remarkable utility converts "V" format files (the sequential format used by the SHACKS, COBAL and BASIC Compilers) to the "F" format files (the sequential file format used by the BASIC interpreter and BASCOM), and vice versa. Without this product, programs written for the interpreter will have to be RE-KEYED to be used by the SHACKS Compiler BASIC. $75.00

SKRUNCH

A SUPER FAST TRSDOS UTILITY. Compresses your BASIC programs to an absolute minimum. Typically saves 30-40% space, even for programs without REM statements! Also results in 7-10% improvement in execution speed. $35

SBASIC — Model I and Model II

Program in a high-level, full structured BASIC! The BEST of the BASIC pre-processors. PERFORM named subroutines. CONDITIONAL case structures. WHILE loops. UNTIL loops. And much more. Forget about line numbers. Model II version is compiled, and SUPER FAST. From Ultimate Computer Systems

Model I $50
Model II $75

DOSFIX

A collection of patches to TRSDOS and BASIC to enhance their usability and function includes our well-known BREAK7E patches to keep the break key from being used accidentally. FREE WITH ANY MODEL II SOFTWARE PACKAGE.

FRIEND

FOUR NEW TRSDOS COMMANDS!

SHOW — A much better multi-disk directory display. Let's you see only those files you want, and includes date of last update.

MOVE - A much better file copying command. Copy/Move whole groups of files, renaming them at the same time, if desired, with just 1 command!

ERASE - Better than KILL, better than PURGE.

PRINT - Print BASIC programs from disk, whether saved in ASCII or compressed.

All 4 DOS commands allow fast processing of one, or complete groups of files, based on generic naming and wild card specifications. Enhanced functions too numerous to fully describe here.

EXAMPLES:

SHOW PAY*/BAS*

Directory display of all '/BAS' files on all diskettes which begin with 'PAY.'

MOVE PAY*/BAS:1 TO =/OLD:3

Save current versions of payroll programs to drive 3, changing extensions to '/OLD.'

MOVE OLD*/* TO NEW =/= :1

Copy all files on drive 0 which begin with 'OLD,' regardless of extension, to drive 1, changing the first 3 letters of the filename to 'NEW,' but retaining the same file extension.

Save time!
Reduce frustration!
Eliminate ERROR 33! $75

HOSTII / TERMII

Allows 'remote control' of a Model II from another Model II, or any ASCII terminal. If terminal is a Model II, accurate screen positioning (PRINT @) is fully supported! Requires NO user memory! This system is designed to provide software support to our customer locations without ever leaving the office. $50

BUGZAP

A powerful utility oriented toward the machine language programmer. Display/Modify/Print/Memory/Disk sectors. Use this to help you learn more about the internals of the Model II. $50

SNAPP, INC.

8160 Corporate Park Dr.
Cincinnati, Ohio 45242
Ohio residents call collect
(513) 891-4496
Call Toll Free
1 - 800 - 543-4628

Most products will soon be available for the Model I. CALL FOR DETAILS!

MASTER / SLAVE

This software package was designed to support the transferring of files from one Model II to another, via direct connection or modem/phone line connection. ALL kinds of files, and baud rates up to 9600 are fully supported. Transfer files in either direction, even with the SLAVE Model II UNATTENDED! $150

SPOOLER — Model I and Model II

Our workhorse! This package, available for Model I, in the TRSDOS/NEWDOS or NEWDOS 80 versions, or for the Model II, greatly enhances system performance when running typical business applications. Many applications have been benchmarked to run nearly TWICE AS FAST with the SPOOLER installed. Installs in minutes, and no changes are required to your programs. Preferred Model II versions require NO user memory. Optional features for the Model II version only: Serial printer support, and DISK SPOOLING support. The DISK SPOOLING support is particularly recommended for word processing applications. $100

SERIAL PRINTER OPTION $50
DISK SPOOLING OPTION $50

ROUTE

Causes LPRINT data to be sent to the video screen! A great help in writing and debugging programs when no printer is available, you have a slow printer, or you are just in a hurry. Can be turned on and off from within your BASIC program. Requires NO user memory. $25

SCREEN

Supports the copying of the full video screen to the printer. Can be invoked by the operator with a keystroke, or from your program with a USR call. Requires NO user memory. $25

SAVE

Retrieve the resident BASIC program following an accidental SYSTEM, or a system crash. DON'T BE WITHOUT THIS ONE. YOU NEVER KNOW WHEN YOU WILL NEED IT! $35

TERMS OF SALE:

Credit card customers, add 3% C.O.D. customers add $3. Ohio residents add 4½% sales tax. Shipments normally made the same day we receive your order.

OUR GUARANTEE:

If your diskette arrives damaged, we will replace it without charge. If you ever accidentally damage it, we will replace it for a $10 handling charge. For a period of one year, we will provide you with any enhancements or updates for a $10 handling charge. For a period of one year, if errors are discovered in the programs, they will be corrected without charge. In the event we cannot correct an error, you may return the program material for a refund.

TRS-80 is a trademark of the Radio Shack division of Tandy Corporation.
NEWDOS and NEWDOS/80 are trademarks of Apparat, Inc.

Listing 2 continued:

```
               ; DEFINITIONS ****************************************
               ;
               ; DEFINITIONS PROVIDES GLOBAL DEFINITIONS OF VARIOUS
               ; STRUCTURES AND VARIABLES.
               ;
               DEFIN:  EQU   $                 ;START OF DEFIN
               RESTART:EQU   0                 ;RESTART ADDRESS
               PBOTTM: EQU   4096              ;BOTTOM OF PRIVATE RAM
               MSTACK: EQU   PBOTTM+63         ;BOTTOM OF MICRO STACK
               GSTACK: EQU   MSTACK+64         ;BOTTOM OF GRAPH STACK
               STRUCT: EQU   GSTACK+1          ;START OF STRUCTURES
               RELOC:  EQU   -ESTRUCT          ;RELOCATION CONSTANT
               GDR:    EQU   RELOC+EGDR        ;GRAPHICS DISPLAY REG
               GDR0:   EQU   RELOC+EGDR0       ;X
               GDR1:   EQU   RELOC+EGDR1       ;Y
               GDR2:   EQU   RELOC+EGDR2       ;PRIMARY COLOR
               GDR3:   EQU   RELOC+EGDR3       ;SECONDARY COLOR
               GDR4:   EQU   RELOC+EGDR4       ;FRAME COUNT
               GDR5:   EQU   RELOC+EGDR5       ;VECTOR MODE
               GDR6:   EQU   RELOC+EGDR6       ;VIEWPORT 0 LEFT X
               GDR7:   EQU   RELOC+EGDR7       ;VIEWPORT 0 LEFT Y
               GRD8:   EQU   RELOC+EGDR8       ;VIEWPORT 0 RIGHT X
               GDR9:   EQU   RELOC+EGDR9       ;VIEWPORT 0 RIGHT Y
               GDR10:  EQU   RELOC+EGDR10      ;VIEWPORT 1 LEFT X
               GDR11:  EQU   RELOC+EGDR11      ;VIEWPORT 1 LEFT Y
               GDR12:  EQU   RELOC+EGDR12      ;VIEWPORT 1 RIGHT X
               GDR13:  EQU   RELOC+EGDR13      ;VIEWPORT 1 RIGHT Y
               GDR14:  EQU   RELOC+EGDR14      ;DISPLAY FORMAT
               GDR15:  EQU   RELOC+EGDR15      ;STATUS
               SLINK:  EQU   RELOC+ESLINK      ;GRAPHICS SUB LINKAGE
               L00:    EQU   RELOC+EL00        ;SUBROUTINE 00
               L01:    EQU   RELOC+EL01        ;SUBROUTINE 01
               L02:    EQU   RELOC+EL02        ;SUBROUTINE 02
               L03:    EQU   RELOC+EL03        ;SUBROUTINE 03
               L04:    EQU   RELOC+EL04        ;SUBROUTINE 04
               L05:    EQU   RELOC+EL05        ;SUBROUTINE 05
               L06:    EQU   RELOC+EL06        ;SUBROUTINE 06
               L07:    EQU   RELOC+EL07        ;SUBROUTINE 07
               L10:    EQU   RELOC+EL10        ;SUBROUTINE 10
               L11:    EQU   RELOC+EL11        ;SUBROUTINE 11
               L12:    EQU   RELOC+EL12        ;SUBROUTINE 12
               L13:    EQU   RELOC+EL13        ;SUBROUTINE 13
               L14:    EQU   RELOC+EL14        ;SUBROUTINE 14
               L15:    EQU   RELOC+EL15        ;SUBROUTINE 15
               L16:    EQU   RELOC+EL16        ;SUBROUTINE 16
               L17:    EQU   RELOC+EL17        ;SUBROUTINE 17
               SLONG:  EQU   RELOC+ESLONG      ;GRAPHICS SUB LENGTH
               S00:    EQU   RELOC+ES00        ;SUBROUTINE 00
               S01:    EQU   RELOC+ES01        ;SUBROUTINE 01
               S02:    EQU   RELOC+ES02        ;SUBROUTINE 02
               S03:    EQU   RELOC+ES03        ;SUBROUTINE 03
               S04:    EQU   RELOC+ES04        ;SUBROUTINE 04
               S05:    EQU   RELOC+ES05        ;SUBROUTINE 05
               S06:    EQU   RELOC+ES06        ;SUBROUTINE 06
               S07:    EQU   RELOC+ES07        ;SUBROUTINE 07
               S10:    EQU   RELOC+ES10        ;SUBROUTINE 10
               S11:    EQU   RELOC+ES11        ;SUBROUTINE 11
               S12:    EQU   RELOC+ES12        ;SUBROUTINE 12
               S13:    EQU   RELOC+ES13        ;SUBROUTINE 13
               S14:    EQU   RELOC+ES14        ;SUBROUTINE 14
               S15:    EQU   RELOC+ES15        ;SUBROUTINE 15
               S16:    EQU   RELOC+ES16        ;SUBROUTINE 16
               S17:    EQU   RELOC+ES17        ;SUBROUTINE 17
               SPTR:   EQU   RELOC+ESPTR       ;GRAPHICS SUB POINTER
               SOFF:   EQU   RELOC+ESOFF       ;GRAPHICS SUB OFFSET
               GPC:    EQU   RELOC+EGPC        ;GRAPHICS STACK POINTER
               REF:    EQU   RELOC+EREF        ;CURRENT VIEWPORT REF
               M:      EQU   RELOC+EM          ;VECTOR DRAWING TEMP
               MM:     EQU   RELOC+EMM         ;VECTOR DRAWING TEMP
               MN:     EQU   RELOC+EMN         ;VECTOR DRAWING TEMP
               SX:     EQU   RELOC+ESX         ;VECTOR DRAWING TEMP
               SY:     EQU   RELOC+ESY         ;VECTOR DRAWING TEMP
               COLOR:  EQU   RELOC+ECOLOR      ;VECTOR COLOR STORAGE
               XERRT:  EQU   RELOC+LXERRT      ;ERROR SERVICE TEMP
               NULL:   EQU   RELOC+ENULL       ;FRAME SERVICE
               FREE:   EQU   STRUCT+NULL+1     ;START OF FREE RAM
               SYMTAB: EQU   5120              ;START OF SYMBOL TABLE
               PTOP:   EQU   6143              ;TOP OF PRIVATE RAM
               CR0:    EQU   7168              ;COLOR RAM 0
               CR1:    EQU   7184              ;COLOR RAM 1
               CR2:    EQU   7200              ;COLOR RAM 2
               RBOTTOM:EQU   8192              ;START OF REFRESH RAM
               RTOP:   EQU   14335             ;REFRESH TOP SYSTEM I
               ;
               ;*****************************************************
               ;MAIN PROGRAM BLOCK***********************************
               ;*****************************************************
               ;
               ; MAIN ***********************************************
               ;
               ; MAIN IS THE DRIVER FOR MICROGRAPH. AFTER INITIALIZING
               ; SP, MAIN CALLS INIT. FETCH AND EXEC ARE CALLED IN
               ; TURN TO PROCESS THE GRAPHICS PRIMITIVES.
               ;
               ; CALLS           INIT
               ;                 FETCH
               ;                 EXEC
               ;
               ; CALLED BY       NOT APPLICABLE
               ;
               ; REGISTERS       SP      (STACK POINTER)
               ;
               ; I/O             NONE
               ;
               ; STRUCTURES      NONE
               ;
008F  313F10   MAIN:   LD    SP,MSTACK         ;INITIALIZE SP
0092  CD8401           CALL  INIT              ;INITIALIZE THE SYSTEM
0094  CDED01   LOOP:   CALL  FETCH             ;FETCH A PRIMITIVE
0097  CD3D02           CALL  EXEC              ;EXECUTE A PRIMITIVE
009A  18F8             JR    LOOP              ;REPEAT INDEFINITELY
               ;
               ;*****************************************************
               ;INTERRUPT SERVICE ROUTINES***************************
               ;*****************************************************
               ;
               ; XERR ***********************************************
               ;
               ; XERR SERVICES AN ERROR CONDITION. XERR IS CALLED UPON
               ; A TRAP TO THE NON MASKABLE INTERRUPT VECTOR.
               ; XERR ALLOWS A SYSTEM RESET, REGISTER DUMP, OR MEMORY
               ; DUMP VIA THE DIAGNOSTIC DIRECTIVES.
               ;
               ; CALLS           SENDBY
               ;
               ; CALLED BY       NON MASKABLE INTERRUPT
               ;
               ; REGISTERS       A       (DUMP)
               ;                 B       (DUMP)
               ;                 C       (DUMP)
               ;                 D       (DUMP)
               ;                 E       (DUMP)
               ;                 H       (DUMP)
               ;                 L       (DUMP)
               ;                 IX      (DUMP)
               ;                 IY      (DUMP)
               ;                 SP      (DUMP)
               ;
               ; I/O             PORT 2  (STATUS)
               ;                 PORT 4  (INPUT)
               ;
               ; STRUCTURES      GDR15   (STATUS)
               ;
009C  FB       XERR:   EI                      ;ENABLE INTERRUPTS
009D  F5               PUSH  AF                ;SAVE A AND F
009E  DDCB0FC6         SET   0,(IX+GDR15)      ;SET ERROR
00A2  DDCB0FCE         SET   1,(IX+GDR15)      ;SET ERROR
00A6  DD7E0F           LD    A,(IX+GDR15)      ;GET THE STATUS
```

Listing 2 continued on page 284

Circle 181 on inquiry card.

Listing 2 continued:

```
00A9  D302              OUT  (2),A           ;DISPLAY THE STATUS
00AB  DDCB0F66  XERR0:  BIT  4,(IX+GDR15)    ;WAIT FOR DIRECTIVE
00AF  28FA              JR   Z,XERR0         ;JUMP UNTIL RECEIVED
00B1  DB04              IN   A,(4)           ;GET THE DATA
00B3  DDCB0FA6          RES  4,(IX+GDR15)    ;RESET THE STATUS
00B7  F5                PUSH AF              ;SAVE A AND F
00B8  DD7E0F            LD   A,(IX+GDR15)    ;GET THE STATUS
00BB  D302              OUT  (2),A           ;OUTPUT THE STATUS
00BD  F1                POP  AF              ;RESTORE A AND F
00BE  CB7F              BIT  7,A             ;TEST FOR RESET
00C0  C20000            JP   NZ,RESTART      ;RESTART IF SET
00C3  CB77              BIT  6,A             ;TEST FOR DIRECTIVE
00C5  204F              JR   NZ,XERR1        ;JUMP IF SET
00C7  F1                POP  AF              ;RESTORE A AND F
00C8  F5                PUSH AF              ;SAVE A COPY OF A AND F
00C9  CD910A            CALL SENDBY          ;SEND A
00CC  78                LD   A,B             ;GET B
00CD  CD910A            CALL SENDBY          ;SEND B
00D0  79                LD   A,C             ;GET C
00D1  CD910A            CALL SENDBY          ;SEND C
00D4  7A                LD   A,D             ;GET D
00D5  CD910A            CALL SENDBY          ;SEND D
00D8  7B                LD   A,E             ;GET E
00D9  CD910A            CALL SENDBY          ;SEND E
00DC  7C                LD   A,H             ;GET H
00DD  CD910A            CALL SENDBY          ;SEND H
00E0  7D                LD   A,L             ;GET L
00E1  CD910A            CALL SENDBY          ;SEND L
00E4  DD22CA10          LD   (STRUCT+XERRT),IX ;GET IX
00E8  DD7E4A            LD   A,(IX+XERRT)    ;GET LOW BYTE
00EB  CD910A            CALL SENDBY          ;SEND IT
00EE  DD7E4B            LD   A,(IX+XERRT+1)  ;GET HIGH BYTE
00F1  CD910A            CALL SENDBY          ;SEND IT
00F4  FD22CA10          LD   (STRUCT+XERRT),IY ;GET IY
00F8  DD7E4A            LD   A,(IX+XERRT)    ;GET LOW BYTE
00FB  CD910A            CALL SENDBY          ;SEND IT
00FE  DD7E4B            LD   A,(IX+XERRT+1)  ;GET HIGH BYTE
0101  CD910A            CALL SENDBY          ;SEND IT
0104  D9                EXX                  ;SAVE PRIMARY REGISTERS
0105  2E00              LD   L,0             ;CLEAR POINTER
0107  2600              LD   H,0             ;CLEAR POINTER
0109  39                ADD  HL,SP           ;GET THE STACK POINTER
010A  7D                LD   A,L             ;GET THE LOW BYTE
010B  CD910A            CALL SENDBY          ;SEND IT
010E  7C                LD   A,H             ;GET THE HIGH BYTE
010F  CD910A            CALL SENDBY          ;SEND IT
0112  D9                EXX                  ;RESTORE PRIMARY REGS
0113  F1                POP  AF              ;RESTORE A AND F
0114  1895              JR   XERR0           ;DO IT AGAIN
0116  F1        XERR1:  POP  AF              ;POP A AND F
0117  08                EX   AF,AF'          ;SAVE A AND F
0118  D9                EXX                  ;SAVE PRIMARY REGISTERS
0119  DDCB0F66  XERR2:  BIT  4,(IX+GDR15)    ;TEST THE INPUT
011D  28FA              JR   Z,XERR2         ;JUMP IF NOT SET
011F  DB04              IN   A,(4)           ;GET THE DATA
0121  6F                LD   L,A             ;SAVE THE LOW ADDRESS
0122  DDCB0FA6          RES  4,(IX+GDR15)    ;RESET THE FLAG
0126  DD7E0F            LD   A,(IX+GDR15)    ;GET THE STATUS
0129  D302              OUT  (2),A           ;SEND THE STATUS
012B  DDCB0F66  XERR3:  BIT  4,(IX+GDR15)    ;TEST THE INPUT
012F  28FA              JR   Z,XERR3         ;JUMP IF NOT SET
0131  DB04              IN   A,(4)           ;GET THE DATA
0133  67                LD   H,A             ;SAVE THE HIGH ADDRESS
0134  DDCB0FA6          RES  4,(IX+GDR15)    ;RESET THE FLAG
0138  DD7E0F            LD   A,(IX+GDR15)    ;GET THE STATUS
013B  D302              OUT  (2),A           ;SEND THE STATUS
013D  7E                LD   A,(HL)          ;GET THE MEMORY DATA
013E  CD910A            CALL SENDBY          ;SEND THE DATA
0141  D9                EXX                  ;RESTORE PRIMARY REGS
0142  08                EX   AF,AF'          ;RESTORE A AND F
0143  C3AB00            JP   XERR0           ;DO IT AGAIN
                ;
                ; FRAME ************************************
                ;
                ; FRAME IS THE INTERRUPT SERVICE ROUTINE FOR A FRAME
                ; COUNT INTERRUPT (FIRST PRIORITY, MASKABLE INTERRUPT).
                ; FRAME FIRST SETS THE FRAME INTERRUPT FLAG, INCREMENTS
                ; THE FRAME COUNT, CALLS NULL, THE FRAME SERVICE
                ; ROUTINE IN RAM, AND THEN RESETS THE FRAME INTERRUPT
                ; FLAG. NOTE THAT THE OUTPUT TO THE DISPLAY PORT FORCES
                ; A RESET OF THE INTERUPT FOR THE FRAME INTERRUPT,
                ; SINCE THE INTERRUPT IS PRODUCED ON THIS PORT.
                ;
                ; CALLS         NULL
                ;
                ; CALLED BY     FIRST PRIORITY MASKABLE INTERRUPT
                ;
                ; REGISTERS     A       (TEMPORARY)
                ;               F       (TEMPORARY)
                ;               IX      (INDEX)
                ;
                ; I/O           PORT 0  (DISPLAY)
                ;               PORT 2  (STATUS)
                ;
                ; STRUCTURES    GDR4    (FRAME COUNT)
                ;               GDR14   (DISPLAY FORMAT)
                ;               GDR15   (STATUS)
                ;
0146  F5        FRAME:  PUSH AF              ;SAVE A AND F
0147  FB                EI                   ;ENABLE INTERRUPTS
0148  DDCB0FD6          SET  2,(IX+GDR15)    ;SET FRAME INTERRUPT
014C  DD7E0F            LD   A,(IX+GDR15)    ;GET GDR15
014F  D302              OUT  (2),A           ;UPDATE THE STATUS
0151  DD3404            INC  (IX+GDR4)       ;INCREMENT FRAME COUNT
0154  CDCC10            CALL STRUCT+NULL     ;CALL NULL IN RAM
0157  DD7E0E            LD   A,(IX+GDR14)    ;GET THE DISPLAY FORMAT
015A  D300              OUT  (0),A           ;SEND IT
015C  DDCB0F96          RES  2,(IX+GDR15)    ;RESET FRAME INTERRUPT
0160  DD7E0F            LD   A,(IX+GDR15)    ;GET THE STATUS
0163  D302              OUT  (2),A           ;UPDATE THE STATUS
0165  F1                POP  AF              ;RESTORE A AND F
0166  ED4D              RETI                 ;RETURN FROM INTERRUPT
                ;
                ; INPUT ************************************
                ;
                ; INPUT IS THE INTERRUPT SERVICE ROUTINE FOR AN INPUT
                ; INTERRUPT (SECOND PRIORITY, MASKABLE INTERRUPT).
                ; INPUT SIMPLY SETS THE INPUT INTERRUPT FLAG IN GDR15.
                ;
                ; CALLS         NONE
                ;
                ; CALLED BY     SECOND PRIORITY MASKABLE INTERRUPT
                ;
                ; REGISTERS     A       (TEMPORARY)
                ;               F       (TEMPORARY)
                ;               IX      (INDEX)
                ;
                ; I/O           PORT 2  (STATUS)
                ;
                ; STRUCTURES    GDR15   (STATUS)
                ;
0168  F5        INPUT:  PUSH AF              ;SAVE A AND F
0169  FB                EI                   ;ENABLE INTERRUPTS
016A  DDCB0FE6          SET  4,(IX+GDR15)    ;SET INPUT INTERRUPT
016E  DD7E0F            LD   A,(IX+GDR15)    ;GET GDR15
0171  D302              OUT  (2),A           ;UPDATE THE STATUS
0173  F1                POP  AF              ;RESTORE A AND F
0174  ED4D              RETI                 ;RETURN FROM INTERRUPT
                ;
                ; OUTPUT ***********************************
                ;
                ; OUTPUT IS THE INTERRUPT SERVICE ROUTINE FOR AN OUTPUT
                ; INTERRUPT (THIRD PRIORITY, MASKABLE INTERRUPT).
                ; OUTPUT SIMPLY RESETS THE OUTPUT INTERRRUPT FLAG IN
                ; GDR15.
                ;
                ; CALLS         NONE
                ;
                ; CALLED BY     THIRD PRIORITY MASKABLE INTERRUPT
                ;
```

Listing 2 continued on page 286

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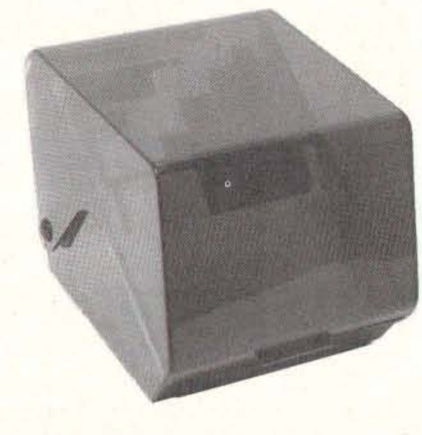

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Listing 2 continued:

```
            ; REGISTERS     A       (TEMPORARY)
            ;               F       (TEMPORARY)
            ;               IX      (INDEX)
            ;
            ; I/O           PORT 2  (STATUS)
            ;
            ; STRUCTURES    GDR15   (STATUS)
            ;
0176 F5       OUTPUT: PUSH AF                  ;SAVE A AND F
0177 FB               EI                       ;ENABLE INTERRUPTS
0178 DDCB0F9E         RES  3,(IX+GDR15)        ;RESET OUTPUT INTERRUPT
017C DD7E0F           LD   A,(IX+GDR15)        ;GET GDR15
017F D302             OUT  (2),A               ;UPDATE THE STATUS
0181 F1               POP  AF                  ;RESTORE A AND F
0182 ED4D             RETI                     ;RETURN FROM INTERRUPT
            ;
            ;*************************************************
            ;PROCESS DRIVERS*********************************
            ;*************************************************
            ;
            ; INIT ********************************************
            ;
            ; INIT INITIALIZES MICROGRAPH. THE INPUT/OUTPUT PORTS
            ; ARE FIRST SET UP, THEN MEMORY IS CLEARED. INIT THEN
            ; LOADS THE RAM WITH THE STRUCTURES, THEN SET THE
            ; INTERRUPTS.
            ;
            ; CALLS         NONE
            ;
            ; CALLED BY     MAIN
            ;
            ; REGISTERS     A       (TEMPORARY)
            ;               B       (TEMPORARY)
            ;               C       (TEMPORARY)
            ;               D       (TEMPORARY)
            ;               E       (TEMPORARY)
            ;               H       (POINTER)
            ;               L       (POINTER)
            ;               IX      (INDEX)
            ;               IY      (INDEX)
            ;               I       (INTERRUPT VECTOR)
            ;
            ; I/O           ALL PORTS
            ;
            ; STRUCTURES    ALL STRUCTURES
            ;
0184 3E0F     INIT:   LD   A,00001111B         ;LOAD MODE 2 MASK
0186 D301             OUT  (1),A               ;SET UP DISPLAY
0188 D303             OUT  (3),A               ;SET UP STATUS
018A D307             OUT  (7),A               ;SET UP OUTPUT
018C 3E4F             LD   A,01001111B         ;LOAD MODE 1 MASK
018E D305             OUT  (5),A               ;SET UP INPUT
0190 3E68             LD   A,INT0              ;GET FIRST VECTOR
0192 D301             OUT  (1),A               ;SET UP FRAME INTERRUPT
0194 3E6A             LD   A,INT1              ;GET SECOND VECTOR
0196 D305             OUT  (5),A               ;SET UP INPUT INTERRUPT
0198 3E6C             LD   A,INT2              ;GET THIRD INTERRUPT
019A D307             OUT  (7),A               ;SET UP OUTPUT INT
019C 3E07             LD   A,00000111B         ;DISABLE INTERRUPTS
019E D303             OUT  (3),A               ;SET STATUS
01A0 3E87             LD   A,10000111B         ;ENABLE INTERRUPTS
01A2 D301             OUT  (1),A               ;SET DISPLAY
01A4 D305             OUT  (5),A               ;SET INPUT
01A6 D307             OUT  (7),A               ;SET OUTPUT
01A8 3E80             LD   A,10000000B         ;SET STATUS
01AA D302             OUT  (2),A               ;SET STATUS
01AC DD218010         LD   IX,STRUCT           ;SET UP IX
01B0 DD360000         LD   (IX+0),0            ;CLEAR FIRST WORD
01B4 218010           LD   HL,STRUCT           ;POINT TO STRUCT
01B7 118110           LD   DE,STRUCT+1         ;POINT TO NEXT STRUCT
01BA 014D00           LD   BC,FREE-STRUCT      ;SET UP COUNT
01BD EDB0             LDIR                     ;CLEAR PRIVATE RAM
01BF 210400           LD   HL,ESTRUCT          ;POINT TO STRUCTURES
01C2 118010           LD   DE,STRUCT           ;POINT TO STRUCT
01C5 014D00           LD   BC,ENULL-ESTRUCT+1  ;GET THE COUNT
01C8 EDB0             LDIR                     ;LOAD STRUCTURES
01CA 210020           LD   HL,RBOTTOM          ;POINT TO RBOTTOM
01CD 3600             LD   (HL),0              ;CLEAR FIRST LOCATION
01CF 110120           LD   DE,RBOTTOM+1        ;POINT TO NEXT LOCATION
01D2 010018           LD   BC,RTOP-RBOTTOM+1   ;GET THE COUNT
01D5 EDB0             LDIR                     ;CLEAR REFRESH RAM
01D7 DDCB0FBE         RES  7,(IX+GDR15)        ;CLEAR INIT STATUS
01DB DD7E0F           LD   A,(IX+GDR15)        ;GET GDR15
01DE D302             OUT  (2),A               ;UPDATE STATUS
01E0 3E00             LD   A,0                 ;CLEAR A
01E2 ED47             LD   I,A                 ;SET UP INTERRUPT VECTOR
01E4 ED5E             IM   2                   ;SET INTERRUPT MODE 2
01E6 FB               EI                       ;ENABLE INTERRUPTS
01E7 DD7E0E           LD   A,(IX+GDR14)        ;SET DISPLAY MASK
01EA D300             OUT  (0),A               ;SET DISPLAY MODE
01EC C9               RET                      ;RETURN
            ;
            ; FETCH *******************************************
            ;
            ; FETCH OBTAINS PRIMITIVES AND DATA FROM THE INPUT
            ; STREAM (FROM THE HOST COMPUTER OR A GRAPHICS
            ; SUBROUTINE IN RAM). FETCH FIRST SETS THE FETCH STATUS.
            ; IF SPTR IS SET, FETCH GETS DATA FROM THE CURRENT
            ; SUBROUTINE POINTER. OTHERWISE, FETCH GETS DATA FROM
            ; PORT 4, THE HOST INPUT. IN ANY CASE, THE PRIMITIVE
            ; OR DATA IS PUT IN REGISTER A. FETCH FINALLY CLEARS
            ; THE FETCH STATUS.
            ;
            ; CALLS         NONE
            ;
            ; CALLED BY     MAIN
            ;               LCRAM
            ;               LPIX
            ;               LREG
            ;               LSUB
            ;               LSYM
            ;               MOV
            ;               RCRAM
            ;               RSYM
            ;               SYM
            ;               VEC
            ;               WAIT
            ;               GETBK
            ;
            ; REGISTERS     A       (RETURNS DATA)
            ;               B       (TEMPORARY)
            ;               D       (POINTER)
            ;               E       (POINTER)
            ;               H       (POINTER)
            ;               L       (POINTER)
            ;               IX      (INDEX)
            ;               IY      (INDEX)
            ;
            ; I/O           PORT 2  (STATUS)
            ;               PORT 4  (INPUT)
            ;
            ; STRUCTURES    GDR15   (STATUS)
            ;               SLINK   (GRAPHICS SUBROUTINE LINKAGE)
            ;.              SPTR    (GRAPHICS SUBROUTINE POINTER)
            ;               SOFF    (GRAPHICS SUBROUTINE OFFSET)
            ;
01ED DDCB0FF6 FETCH:  SET  6,(IX+GDR15)        ;SET FETCH FLAG
01F1 DD7E0F           LD   A,(IX+GDR15)        ;GET GDR15
01F4 D302             OUT  (2),A               ;UPDATE THE STATUS
01F6 DDCB405E         BIT  3,(IX+SPTR)         ;TEST SPTR
01FA 200F             JR   NZ,FETCH1           ;JUMP IF SUBROUTINE
01FC DDCB0F66 FETCH0: BIT  4,(IX+GDR15)        ;TEST INPUT INTERRUPT
0200 28FA             JR   Z,FETCH0            ;JUMP IF NO INTERRUPT
0202 DB04             IN   A,(4)               ;GET DATA FROM HOST
0204 F3               DI                       ;DISABLE THE INTERRUPTS
0205 DDCB0FA6         RES  4,(IX+GDR15)        ;RESET INPUT INTERRUPT
0209 1825             JR   FETCH2              ;JUMP TO THE EXIT
020B D5       FETCH1: PUSH DE                  ;SAVE D AND E
020C E5               PUSH HL                  ;SAVE H AND L
020D FDE5             PUSH IY                  ;SAVE IY
020F DD5E40           LD   E,(IX+SPTR)         ;GET SUB POINTER
```

Listing 2 continued on page 288

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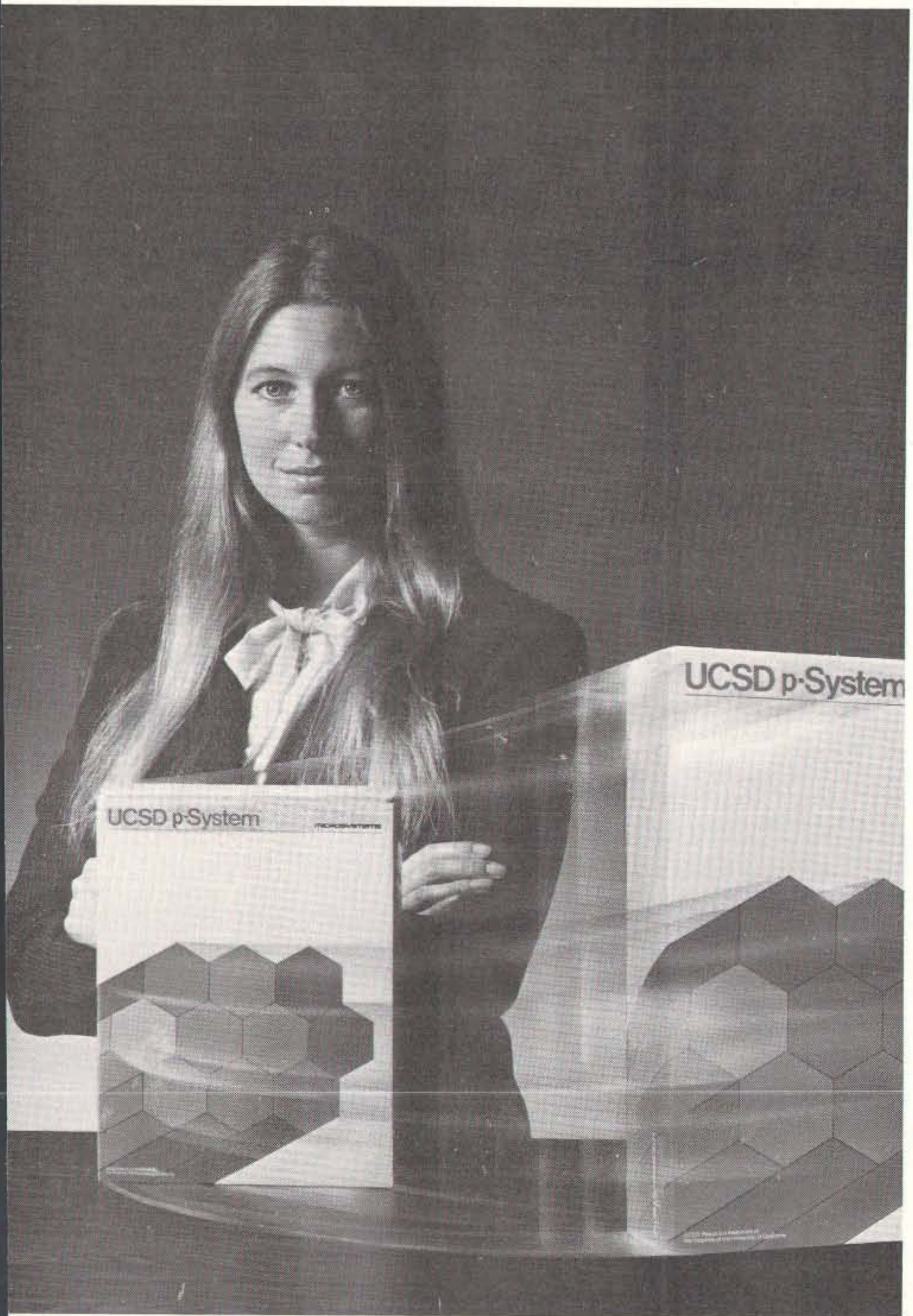

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Circle 183 on inquiry card.

Listing 2 continued:

```
0212   CB23              SLA   E                  ;MULTIPLY BY TWO
0214   1600              LD    D,0                ;CLEAR POINTER
0216   FD219010          LD    IY,STRUCT+SLINK    ;LOAD SLINK ADDRESS
021A   FD19              ADD   IY,DE              ;ADD THE OFFSET
021C   FD6E00            LD    L,(IY+0)           ;GET LOW BYTE OF ENTRY
021F   FD6601            LD    H,(IY+1)           ;GET HIGH BYTE OF ENTRY
0222   DD5E41            LD    E,(IX+SOFF)        ;GET OFFSET
0225   1600              LD    D,0                ;CLEAR POINTER
0227   DD3441            INC   (IX+SOFF)          ;INCREMENT OFFSET
022A   19                ADD   HL,DE              ;ADD OFFSET
022B   7E                LD    A,(HL)             ;GET DATA FROM SUB
022C   FDE1              POP   IY                 ;RESTORE IY
022E   E1                POP   HL                 ;RESTORE HL
022F   D1                POP   DE                 ;RESTORE DE
0230   F5       FETCH2:  PUSH  AF                 ;SAVE A AND F
0231   DDCB0FB6          RES   6,(IX+GDR15)       ;RESET FETCH FLAG
0235   DD7E0F            LD    A,(IX+GDR15)       ;GET GDR15
0238   D302              OUT   (2),A              ;UPDATE THE STATUS
023A   FB                EI                       ;ENABLE THE INTERRRUPTS
023B   F1                POP   AF                 ;RESTORE A AND F
023C   C9                RET                      ;RETURN
                ;
                ; EXEC ***********************************************
                ;
                ; EXEC IS THE FIRST STEP IN EXECUTING A GRAPHICS
                ; PRIMITIVE. EXECUTE FIRST SETS THE EXECUTE FLAG, THEN
                ; CALLS PRIMAT, WHICH JUMPS TO THE PROPER PRIMITIVE.
                ; AFTER A RETURN FROM THE PRIMITIVE ITSELF, EXEC
                ; RESETS THE EXECUTE FLAG AND RETURNS.
                ;
                ; CALLS          PRIMAT
                ;
                ; CALLED BY      MAIN
                ;
                ; REGISTERS      A        (TEMPORARY)
                ;                F        (TEMPORARY)
                ;                IX       (INDEX)
                ;
                ; I/O            PORT 2   (STATUS)
                ;
                ; STRUCTURES     GDR15    (STATUS)
                ;
023D   F5       EXEC:    PUSH  AF                 ;SAVE A AND F
023E   DDCB0FEE          SET   5,(IX+GDR15)       ;SET EXECUTE FLAG
0242   DD7E0F            LD    A,(IX+GDR15)       ;GET GDR15
0245   D302              OUT   (2),A              ;UPDATE THE STATUS
0247   F1                POP   AF                 ;RESTORE A AND F
0248   CD5502            CALL  PRIMAT             ;CALL PRIMAT
024B   DDCB0FAE          RES   5,(IX+GDR15)       ;RESET EXECUTE FLAG
024F   DD7E0F            LD    A,(IX+GDR15)       ;GET THE STATUS
0252   D302              OUT   (2),A              ;UPDATE THE STATUS
0254   C9                RET                      ;RETURN
                ;
                ;****************************************************
                ;GRAPHICS PRIMITIVES*********************************
                ;****************************************************
                ;
                ; PRIMAT *********************************************
                ;
                ; PRIMAT IS THE SECOND STEP IN EXECUTING A PRIMITIVE.
                ; PRIMAT FIRST DETERMINES THE PRIMITIVE NUMBER FROM THE
                ; OP CODE IN REGISTER A, AND PUTS A POINTER TO THIS
                ; ADDRESS IN IY. HL IS THEN LOADED WITH THE SUBROUTINE
                ; ADDRESS ITSELF. THEN PRIMAT JUMPS TO THIS LOCATION.
                ; NOTE THAT A RETURN FROM A PRIMITIVE SUBROUTINE
                ; ACTUALLY RETURNS TO EXEC.
                ;
                ; CALLS          NONE
                ;
                ; CALLED BY      EXEC
                ;
                ; REGISTERS      A        (PRIMITIVE OP CODE)
                ;                D        (POINTER)
                ;                E        (POINTER)
                ;                H        (POINTER)
                ;                L        (POINTER)
                ;                IY       (INDEX)
                ;
                ; I/O            NONE
                ;
                ; STRUCTURES     PENTER   (PRIMITIVE ENTRY POINTS)
                ;
0255   5F       PRIMAT:  LD    E,A                ;SAVE A COPY OF A IN E
0256   CB3B              SRL   E                  ;SHIFT THE OP
0258   CB3B              SRL   E                  ;   CODE RIGHT FOUR
025A   CB3B              SRL   E                  ;   BITS TO MAINTAIN
025C   CB3B              SRL   E                  ;   THE OFFSET
025E   CB23              SLA   E                  ;MULTIPLY BY TWO
0260   1600              LD    D,0                ;CLEAR THE POINTER
0262   FD216E00          LD    IY,PENTER          ;LOAD PENTER ADDRESS
0266   FD19              ADD   IY,DE              ;ADD THE OFFSET IN HL
0268   FD6E00            LD    L,(IY+0)           ;LOAD LOW BYTE OF SUB
026B   FD6601            LD    H,(IY+1)           ;LOAD HIGH BYTE OF SUB
026E   E9                JP    (HL)               ;JUMP TO SUBROUTINE
                ;
                ; CALLS **********************************************
                ;
                ; CALLS CALLS A MICROCOMPUTER OR GRAPHICS SUBROUTINE.
                ; CALLS FIRST DETERMINES WHAT TYPE OF SUBROUTINE IS
                ; BEING CALLED. IF IT IS A MICROCOMPUTER SUBROUTINE,
                ; CALLS CALLS THE PROPER SUBROUTINE AS LINKED BY
                ; SLINK. OTHERWISE, CALLS PUSHES SPTR AND SOFF ON THE
                ; GRAPHICS SUBROUTINE STACK, AND LOADS THE NEW VALUES
                ; FOR SPTR AND SOFF. FUTURE DATA FROM FETCH THEN COMES
                ; FROM THIS GRAPHICS SUBROUTINE UNTIL A RETN IS
                ; EXECUTED.
                ;
                ; CALLS          USER     (INDIRECTLY, BY DEFAULT)
                ;                GUSER    (INDIRECTLY, BY DEFAULT)
                ;                USER SUB(INDIRECTLY)
                ;
                ; CALLED BY      PRIMAT   (INDIRECTLY)
                ;
                ; REGISTERS      A        (PRIMITIVE OP CODE)
                ;                B        (TEMPORARY)
                ;                D        (TEMPORARY)
                ;                E        (TEMPORARY)
                ;                H        (POINTER)
                ;                L        (POINTER)
                ;                IX       (INDEX)
                ;
                ; I/O            NONE
                ;
                ; STRUCTURES     GPC      (GRAPHICS STACK POINTER)
                ;                GSTACK   (GRAPHICS STACK)
                ;                SOFF     (GRAPHICS SUBROUTINE OFFSET)
                ;                SLINK    (GRAPHICS SUBROUTINE LINKAGE)
                ;                SPTR     (GRAPHICS SUBROUTINE POINTER)
                ;
026F   CB5F     CALLS:   BIT   3,A                ;TEST T BIT IN OP CODE
0271   2014              JR    NZ,CALLS1          ;JUMP IF GRAPHICS SUB
0273   FD219010          LD    IY,STRUCT+SLINK    ;GET LINK BASE
0277   E607              AND   00000111B          ;MASK ALL BUT SUB
0279   CB27              SLA   A                  ;MULTIPLY BY TWO
027B   5F                LD    E,A                ;SAVE A IN E
027C   1600              LD    D,0                ;CLEAR POINTER
027E   FD19              ADD   IY,DE              ;ADD OFFSET TO IY
0280   FD6E00            LD    L,(IY+0)           ;GET LOW ADDRESS
0283   FD6601            LD    H,(IY+1)           ;GET HIGH ADDRESS
0286   E9                JP    (HL)               ;JUMP TO SUBROUTINE
0287   217F10   CALLS1:  LD    HL,GSTACK          ;LOAD GSTACK ADDRESS
028A   DD5E42            LD    E,(IX+GPC)         ;GET GPC
028D   1600              LD    D,0                ;CLEAR POINTER
028F   B7                OR    A                  ;RESET THE CARRY
0290   ED52              SBC   HL,DE              ;SUBTRACT POINTER
0292   DD4640            LD    B,(IX+SPTR)        ;LOAD SPTR
0295   70                LD    (HL),B             ;PUT SPTR ON GSTACK
0296   DD4641            LD    B,(IX+SOFF)        ;LOAD SOFF
0299   2B                DEC   HL                 ;UPDATE THE POINTER
029A   70                LD    (HL),B             ;PUT SOFF ON GSTACK
```

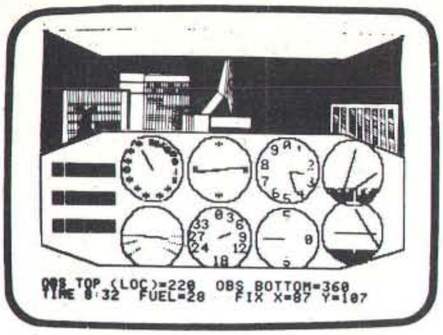
Listing 2 continued on page 290

Listing 2 continued:

```
029B  E60F                AND   00001111B         ;AND ALL BUT SUB
029D  DD7740              LD    (IX+SPTR),A       ;UPDATE SPTR
02A0  DD364100            LD    (IX+SOFF),0       ;UPDATE SOFF
02A4  DD3442              INC   (IX+GPC)          ;INCREMENT GPC
02A7  DD3442              INC   (IX+GPC)          ;INCREMENT GPC
02AA  C9                  RET                     ;RETURN
                    ;
                    ; LCRAM ******************************************
                    ;
                    ; LCRAM LOADS THE COLOR RAMS. LCRAM FIRST DETERMINES IF
                    ; A SINGLE OR ALL COLOR RAMS ARE TO BE LOADED. IF A
                    ; SINGLE COLOR RAM IS TO BE LOADED, THE ENTIRE RAM OR
                    ; A SINGLE ADDRESS OF THAT RAM IS TO BE LOADED.
                    ; OTHERWISE, ALL THREE COLOR RAMS ARE EITHER ENTIRELY
                    ; LOADED, OR A SINGLE ADDRESS IN ALL THREE IS LOADED.
                    ;
                    ; CALLS           FETCH
                    ;                 GETBLK
                    ;
                    ; CALLED BY       PRIMAT   (INDIRECTLY)
                    ;
                    ; REGISTERS       A        (PRIMITIVE OP CODE)
                    ;                 B        (COUNTER)
                    ;                 D        (TEMPORARY)
                    ;                 E        (TEMPORARY)
                    ;                 H        (POINTER)
                    ;                 L        (POINTER)
                    ;                 IY       (INDEX)
                    ;
                    ; I/O             NONE
                    ;
                    ; STRUCTURES      CR0      (COLOR RAM 0)
                    ;                 CR1      (COLOR RAM 1)
                    ;                 CR2      (COLOR RAM 2)
                    ;
02AB  CB47          LCRAM:  BIT   0,A               ;TEST SINGLE BIT
02AD  2820                  JR    Z,LCRAM1          ;JUMP IF SINGLE
02AF  FE1D                  CP    00011101B         ;TEST FOR ALL
02B1  2009                  JR    NZ,LCRAM0         ;JUMP IF NOT ALL
02B3  0630                  LD    B,48              ;SET COUNT OF 48 BYTES
02B5  21001C                LD    HL,CR0            ;SET START ADDRESS
02B8  CD9A08                CALL  GETBLK            ;CALL GETBLK
02BB  C9                    RET                     ;RETURN
02BC  0610          LCRAM0: LD    B,16              ;SET COUNT OF 16 BYTES
02BE  E60C                  AND   00001100B         ;MASK OFF OPCODE
02C0  CB27                  SLA   A                 ;SHIFT OFFSET
02C2  CB27                  SLA   A                 ;SHIFT OFFSET
02C4  6F                    LD    L,A               ;SAVE OFFSET
02C5  2600                  LD    H,0               ;CLEAR POINTER
02C7  11001C                LD    DE,CR0            ;GET START ADDRESS
02CA  19                    ADD   HL,DE             ;ADD TO OFFSET
02CB  CD9A08                CALL  GETBLK            ;CALL GETBLK
02CE  C9                    RET                     ;RETURN
02CF  FE1C          LCRAM1: CP    00011100B         ;TEST FOR ALL
02D1  2027                  JR    NZ,LCRAM2         ;JUMP IF NOT ALL
02D3  CDED01                CALL  FETCH             ;GET OFFSET
02D6  E60F                  AND   00001111B         ;MASK ALL BUT OFFSET
02D8  6F                    LD    L,A               ;SAVE OFFSET
02D9  2600                  LD    H,0               ;CLEAR POINTER
02DB  11001C                LD    DE,CR0            ;GET START ADDRESS
02DE  19                    ADD   HL,DE             ;ADD TO OFFSET
02DF  5D                    LD    E,L               ;RESTORE LOW BYTE
02E0  54                    LD    D,H               ;RESTORE HIGH BYTE
02E1  FD210000              LD    IY,0              ;CLEAR INDEX
02E5  FD19                  ADD   IY,DE             ;MOVE POINTER TO INDEX
02E7  CDED01                CALL  FETCH             ;GET DATA
02EA  FD7700                LD    (IY+0),A          ;LOAD DATA IN CR0
02ED  CDED01                CALL  FETCH             ;GET DATA
02F0  FD7710                LD    (IY+16),A         ;LOAD DATA IN CR1
02F3  CDED01                CALL  FETCH             ;GET DATA
02F6  FD7720                LD    (IY+32),A         ;LOAD DATA IN CR2
02F9  C9                    RET                     ;RETURN
02FA  0601          LCRAM2: LD    B,1               ;SET COUNT OF 1 BYTE
02FC  E60C                  AND   00001100B         ;MASK ALL BUT OFFSET
02FE  CB27                  SLA   A                 ;SHIFT OFFSET
0300  CB27                  SLA   A                 ;SHIFT OFFSET
0302  6F                    LD    L,A               ;SAVE A IN L
0303  2600                  LD    H,0               ;CLEAR POINTER
0305  CDED01                CALL  FETCH             ;GET OFFSET
0308  E60F                  AND   00001111B         ;MASK ALL BUT OFFSET
030A  85                    ADD   A,L               ;ADD OFFSET
030B  6F                    LD    L,A               ;SAVE OFFSET
030C  11001C                LD    DE,CR0            ;GET START ADDRESS
030F  19                    ADD   HL,DE             ;ADD OFFSET
0310  CD9A08                CALL  GETBLK            ;GET DATA
0313  C9                    RET
                    ;
                    ; LPIX ******************************************
                    ;
                    ; LPIX LOADS PIXEL DATA ACCORDING TO THE GIVEN
                    ; REFERENCE. LPIX CAN LOAD EITHER FULL FRAME, ONE
                    ; PIXEL AT XY, OR AN ENTIRE VIEWPORT. THE PIXELS ARE
                    ; LOADED IN EITHER THE PRIMARY COLOR, THE SECONDARY
                    ; COLOR, OR AS SPECIFIED IN THE DATA WHICH FOLLOWS.
                    ; LPIX FIRST CHECKS TO SEE IF ONLY A SINGLE POINT IS
                    ; LOADED. IF SO, LPIX LOADS THE APPROPRIATE COLOR.
                    ; OTHERWISE, LPIX SETS A FLAG IF THERE IS A FULL FRAME
                    ; LOAD REQUESTED. NEXT, LPIX DETERMINES WHAT COLOR IS
                    ; REQUESTED. IF THE COLOR DOES NOT FOLLOW, A FLAG IS
                    ; SET, AND THE APPROPRIATE COLOR IS LOADED FROM THE
                    ; GRAPHICS DISPLAY RESIGTERS. NEXT, LPIX CLEARS BOTH
                    ; X AND Y, AND PROCEDES TO LOAD THE PIXELS FROM
                    ; THE ORIGIN OF THE DISPLAY TO THE TOP, ONE LINE AT
                    ; A TIME. CLIP IS CALLED IF THERE IS A VIEWPORT
                    ; REFERENCE TO SEE IF THAT POINT WILL ACTUALLY BE
                    ; LOADED. LPIX IS COMPLETED WHEN X AND Y RECYCLE TO THE
                    ; ORIGIN.
                    ;
                    ; CALLS           FETCH
                    ;                 CASE
                    ;                 CLIP
                    ;                 POKE
                    ;
                    ; CALLED BY       PRIMAT   (INDIRECTLY)
                    ;
                    ; REGISTERS       A        (PRIMITIVE OP CODE, TEMPORARY)
                    ;                 B        (CASE)
                    ;                 C        (CLIP SUCCESS FLAG)
                    ;                 D        (FULL FRAME FLAG)
                    ;                 E        (COLOR FOLLOWS FLAG)
                    ;                 IX       (INDEX)
                    ;
                    ; I/O             NONE
                    ;
                    ; STRUCTURES      GRD0     (X)
                    ;                 GDR1     (Y)
                    ;                 GDR2     (PRIMARY COLOR)
                    ;                 GDR3     (SECONDARY COLOR)
                    ;                 REF      (REFERENCE)
                    ;
0314  1E00          LPIX:   LD    E,0               ;CLEAR COLOR FOLLOWS
0316  1600                  LD    D,0               ;CLEAR FULL FRAME
0318  E60F                  AND   00001111B         ;CLEAR OP CODE
031A  DD7743                LD    (IX+REF),A        ;SAVE REFERENCE
031D  DDCB433E              SRL   (IX+REF)          ;ADJUST REFERENCE
0321  DDCB433E              SRL   (IX+REF)          ;ADJUST REFERENCE
0325  CB5F                  BIT   3,A               ;TEST REFERENCE
0327  2821                  JR    Z,LPIX4           ;JUMP IF VIEWPORT REF
0329  CB57                  BIT   2,A               ;TEST REFERENCE
032B  2819                  JR    Z,LPIX3           ;JUMP IF FULL FRAME
032D  CB4F                  BIT   1,A               ;TEST COLOR FOLLOWS
032F  200E                  JR    NZ,LPIX1          ;JUMP IF COLOR FOLLOWS
0331  CB47                  BIT   0,A               ;TEST COLOR TYPE
0333  2805                  JR    Z,LPIX0           ;JUMP IF PRIMARY COLOR
0335  DD7E03                LD    A,(IX+GDR3)       ;LOAD SECONDARY COLOR
0338  1808                  JR    LPIX2             ;JUMP AROUND FETCH
033A  DD7E02        LPIX0:  LD    A,(IX+GDR2)       ;LOAD PRIMARY COLOR
033D  1803                  JR    LPIX2             ;JUMP AROUND FETCH
033F  CDED01        LPIX1:  CALL  FETCH             ;GET THE COLOR DATA
0342  CD390A        LPIX2:  CALL  POKE              ;POKE THE COLOR AT XY
```

Listing 2 continued on page 292

Circle 184 on inquiry card.



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1. **SCREEN EDITOR.** Use the editor to insert, replace, delete or rubout characters in your BASIC program. Watch the line change on the screen! Gone are the days of typing in a whole line to change one character!
2. **FUNCTION KEYS.** SYSTEM 2 allows 12 keys to be programmed to represent one or more characters or up to several lines of text each! After a key is programmed, by simply hitting key, all the text is sent to the processor just as if you typed it in on the keyboard! Function keys may be used in all modes of operation, including the editor. This feature is handy for lengthy and/or often used commands and may include multiple statements.
3. **RENUMBERING ROUTINE.** With a single keystroke your program is renumbered. Starting line number and increment may be changed.
4. **BASIC BUFFER PROTECTOR.** SYSTEM 2 sends a (CR) when the BASIC BUFFER is full. This prevents BASIC from crashing.
5. **PRINTER DRIVER.** Simply hit CTRL P to direct output to Centronics printer.
6. **RIVIVAL ROUTINE.** If NEW or CLOAD are typed, or RESET is hit by mistake, your program may be recovered. This is a safety device.

OTHER FEATURES
- RUNSTOP stops execution until any other key is hit.
- CLEAR clears screen then sends a (CR). Hit CLEAR to start on 'new page'.
- CTRL characters such as ESC, LF and CLEAR don't return ?SN ERROR.
- RUB doesn't require the SHIFT key to be depressed. This quickens editing.
- Includes a Real Time Random Number Generator.
- Returns automatically to BASIC after TAPE CRC ERROR while CLOADing.
- Suppresses premature CRLF. Normally, if RUB is used extensively while typing in a BASIC line, the cursor will drop down to the next line before reaching the end of the current line. SYSTEM 2 prevents this.
- SYSTEM 2 requires 2K of memory and is available in 8, 16, 32 & 48K versions. **$35.50**

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Circle 186 on inquiry card.

Circle 187 on inquiry card.

MNEMONIC	NAME
CALL	call subroutine
LCRAM	load color memory
LPIX	load pixel
LREG	load register
LSUB	load subroutine
LSYM	load symbol
MOV	move
RCRAM	read color memory
RET	return
RPIX	read pixel
RREG	read register
RSUB	read subroutine
RSYM	read symbol
SYM	display symbol
VEC	draw a vector
WAIT	wait

Table 2: *Quick reference guide to the primitives defined for Micrograph. Although the minimum set of instructions need only include a point-positioning primitive and a vector-drawing primitive, added flexibility of extra functions is used to remove processing burden from the host system.*

Text continued from page 278:
features, such as clipping and anti-aliasing, can be readily implemented at the primitive level without the addition of other instructions. Such features can be treated as system parameters, selectable through the load-register primitive. In figure 5, a sample of the images produced by these primitives is shown. (Also see listing 1.)

One last item that must be discussed is error processing. For any implementation of the primitives, the display processor must be able to detect, report, and possibly recover from errors such as invalid primitives or an error in a called user subroutine. Of course, this error processing is highly implementation-dependent, but does not affect the structures of our primitives. However, several of these primitives can be used to aid the host computer in error processing, such as the read-register and read-pixel primitives.

So far the characteristics of interactive computer-graphics systems have been examined, focusing on a comparison of the features of calligraphic and raster-scan display processors. A set of primitive instructions for the control of a color raster-scan display processor have been developed.

Next month, Part 2 of this article will concern the hardware design of Micrograph, a microprocessor-based peripheral which implements these primitives.■

Listing 2 continued:

```
0345  C9                 RET              ;RETURN
0346  1601      LPIX3:   LD   D,1         ;SET FULL FRAME FLAG
0348  1803               JR   LPIX5       ;JUMP AROUND CASE
034A  CDA007    LPIX4:   CALL CASE        ;FIND VIEWPORT CASE
034D  DD360000  LPIX5:   LD   (IX+GDR0),0 ;CLEAR X
0351  DD360100           LD   (IX+GDR1),0 ;CLEAR Y
0355  CB4F               BIT  1,A         ;TEST COLOR FOLLOWS
0357  200E               JR   NZ,LPIX7    ;JUMP IF COLOR FOLLOWS
0359  1E01               LD   E,1         ;SET COLOR FLAG
035B  CB47               BIT  0,A         ;TEST COLOR TYPE
035D  2805               JR   Z,LPIX6     ;JUMP IF PRIMARY COLOR
035F  DD7E03             LD   A,(IX+GDR3) ;LOAD SECONDARY COLOR
0362  1803               JR   LPIX7       ;JUMP TO LOOP
0364  DD7E02    LPIX6:   LD   A,(IX+GDR2) ;LOAD PRIMARY COLOR
0367  CB43      LPIX7:   BIT  0,E         ;TEST COLOR FOLLOWS
0369  2003               JR   NZ,LPIX8    ;JUMP IF COLOR PRESENT
036B  CDED01             CALL FETCH       ;GET DATA
036E  CB42      LPIX8:   BIT  0,D         ;TEST FULL FRAME
0370  2007               JR   NZ,LPIX9    ;JUMP IF FULL FRAME
0372  CDEF07             CALL CLIP        ;CHECK FOR CLIP
0375  CB41               BIT  0,C         ;TEST SUCCESS
0377  2803               JR   Z,LPIX10    ;JUMP IF CLIPPED
0379  CD390A    LPIX9:   CALL POKE        ;POKE THE DATA
037C  DD3400    LPIX10:  INC  (IX+GDR0)   ;INCREMENT X
037F  20E6               JR   NZ,LPIX7    ;JUMP IF X NOT ZERO
0381  DD3401             INC  (IX+GDR1)   ;INCREMENT Y
0384  20E1               JR   NZ,LPIX7    ;JUMP IF Y NOT ZERO
0386  C9                 RET              ;RETURN
                ;
                ; LREG ****************************************************
                ;
                ; LREG LOADS A GRAPHIC DISPLAY REGISTER. IF GDR15 IS
                ; SPECIFIED, A RESET OCCURS, SINCE THIS IS ESSENTIALLY
                ; A READ ONLY REGISTER. OTHERWISE, LREG SETS A POINTER
                ; TO THE APPROPRIATE REGISTER AND READS IN THE DATA.
                ;
                ; CALLS          FETCH
                ;
                ; CALLED BY      PRIMAT   (INDIRECTLY)
                ;
                ; REGISTERS      A        (PRIMITIVE OP CODE, TEMPORARY)
                ;                D        (TEMPORARY)
```

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BYTE's Bugs

Feeling Listless

The performance of a program in the Technical Forum "Some More on Performance Evaluation," by Carl Helmers (July 1980 BYTE, page 216) suffered from one error of substitution and one error of omission.

Listing 1 on page 217, a program submitted by Charles Porter, should contain two lines as follows:

```
105 IF X = L THEN 120
110 IF A(X) = 0 THEN 100
    ELSE 90
```

Thanks to Martin Berman of Teaneck, New Jersey, for pointing this out.■

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A General Interpolating Graphics Package for the TRS-80

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If you've ever tried creating graph displays with the Radio Shack TRS-80, then you know that the task can be time-consuming. If you haven't tried, you can look forward to the fact that axes must be generated and labelled, and data must be plotted using the awkward screen coordinates of Level II BASIC. After all this has been done, the resulting graph usually is not continuous, but has annoying *holes* in it. But don't despair, because with our simple package that may be implemented as a subroutine plotting X, Y coordinate relations or geometric figures is easy.

In order to use this plotting package effectively, we suggest that you work through each example given. After implementing this package, TRS-80 users should be able to plot any analytic function or set of x and corresponding y values efficiently. This package will allow you to draw axes in the correct quadrant(s) and label them with chosen titles. Tic marks displayed at user-determined intervals, and maximum and minimum values displayed at the correct positions on the graph are also easy to accomplish.

Basic Plotting

The plotting package is divided into two subroutines. The interpolating subroutine (see listing 1) plots the function (or coordinate pairs), interpolating between the points to produce a continuous curve. The resulting curve may be easily displayed at any position of the screen by changing at most four parameters. The program takes care of all scaling problems, and parameters are specified through the use of additional BASIC statements inserted at the front of the subroutine.

> With this package, TRS-80 users should be able to plot any analytic function.

To begin this demonstration, suppose you desire to plot the cost of heating a home as a function of the monthly period, displayed in the upper right-hand corner of the screen. (This is done to leave space for other information you may desire to display.) In order to have the graph confined to the desired position, you must specify a *viewport*. For this plotting routine, consider the screen to be divided into one hundred horizontal units and forty vertical units. The bottom left corner corresponds to the screen coordinate (0,0). (See figure 1.) To display the graph in the right-hand corner, the horizontal coordinates should be from 50 to 100, and the vertical coordinates should be from 20 to 40. Thus, to set this viewport, the reader must specify the four variables, Z1, Z2, W1, W2. For this example the viewport variables should be set as follows:

```
Z1 = 100
Z2 =  50
W1 =  40
W2 =  20
```

The next step is to set up the x and corresponding y arrays. For example, if during the month of January the heating cost was \$80, the first x element would be 1 (for the month) and

the y element would be 80 (for the cost). Table 1 is a hypothetical set of data to be graphed. The arrays that will contain the data are AX and AY. Thus, for this example, the following BASIC statements should be inserted at the beginning of the subroutine:

```
FOR I=1 TO 12
READ AX(I)
READ AY(I)
NEXT I
DATA 1,80,2,90,3,75,4,50,5,
     45,6,45,7,50,8,80,
     9,70,10,65,11,70,12,80
```

The next variables specify the dimension of the arrays to be graphed. In this example, the minimum dimension TI is 1, the maximum dimension TA is 12, and the separation between the array points IN to be plotted is 1. (For example, if you wanted to plot the cost of heating for every other month, IN would be 2.) Therefore, you must include the following BASIC statements:

```
TI  =  1
TA  = 12
IN  =  1
```

The final variable to be specified, S1, determines the *resolution*, or how well the points are connected in the graph. The value of S1 needed to fully connect all the points depends strongly on the size of the viewport and the number of array points to be plotted. A little experimentation with S1 is necessary to obtain the desired effect. For this demonstration: S1=0.01. After specifying the parameters above, the user is now ready to run the program.

After execution, the results should be as presented in figure 2. To change the viewport, simply change the values in the viewport variables. Figure 3 shows the result when the viewport variables are as follows:

```
Z1 = 100
Z2 =   0
W1 =  40
W2 =   0
```

If you desire to plot the cost of heating for every other month, simply change IN to 2. The results of this change are shown in figure 4.

Adding Axes

At this point, it would be nice to have the axes drawn and labeled. This can be done by specifying four axis parameters for use by the axis-drawing subroutine in listing 2. The first two parameters to be defined are the string variables AX$ and AY$, which define the x axis and the y axis labels respectively. For this example the x axis should be labeled "month" and the y axis should be labeled "cost." Thus, the two BASIC statements that must be executed are:

```
AX$ = "MONTH"
AY$ = "  COST"
```

The final two parameters specify the separation of the tic marks on the axes. In the example, set C1 (the x-axis tic-mark-separation variable) to 1 for a tic mark every month. Set C2 (the y-axis tic-mark-separation variable) to 5 for a tic mark at every $5.00 increment. Thus, the following BASIC statements must be executed:

```
C1 =1
C2 =5
```

After execution, the results should be

Text continued on page 310

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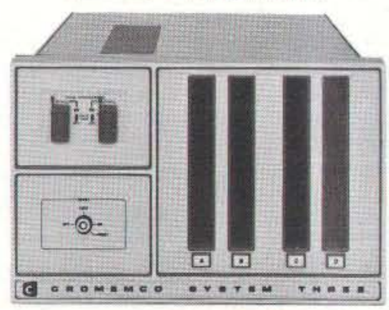

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Listing 1: *The interpolating subroutine. Written in TRS-80 Level II BASIC, this routine plots points on the screen from an array specified by the user. BASIC statements are inserted before the routine is run to create the desired array and, thus, the desired image.*

```
10000 Z2=Z2+25
10005 W2=W2+5
10010 IF Z2>Z1 THEN Z3=Z2 ELSE GOTO 10025
10015 Z2=Z1
10020 Z1=Z3
10025 IF W2>W1 THEN W3=W2 ELSE GOTO 10040
10030 W2=W1
10035 W1=W3
10040 Y1=-1.0E38
10045 Y2=1.0E38
10050 X1=Y1
10055 X2=Y2
10060 FOR I=TI TO TA STEP IN
10065 IF Y1<AY(I) THEN Y1=AY(I)
10070 IF Y2>AY(I) THEN Y2=AY(I)
10075 IF X1<AX(I) THEN X1=AX(I)
10080 IF X2>AX(I) THEN X2=AX(I)
10085 NEXT I
10090 IF Y1=Y2 THEN Y1=1.001*Y1
10095 IF X1=X2 THEN X1=1.001*X1
10100 A=(X1-X2)/(Z1-Z2)
10105 B=(Y1-Y2)/(W1-W2)
10110 FOR I=TI TO TA STEP IN
10115 SET((Z2+(AX(I)-X2)/A), (47-((AY(I)-Y2)/B+W2)))
10120 Q=I+IN
10125 IF Q>TA GOTO 10165
10130 IF AX(I)>AX(Q) THEN SS=-S1 ELSE SS=S1
10135 FOR J=AX(I)TO AX(Q) STEP SS
10140 IF AX(I)=AX(Q) THEN AX(Q)=1.001*AX(Q)+.0000001
10145 Y3=((AY(Q)-AY(I))/(AX(Q)-AX(I)))*(J-AX(I))+AY(I)
10150 SET((Z2+(J-X2)/A),(47-((Y3-Y2)/B+W2)))
10155 NEXT J
10160 NEXT I
10165 RETURN
```


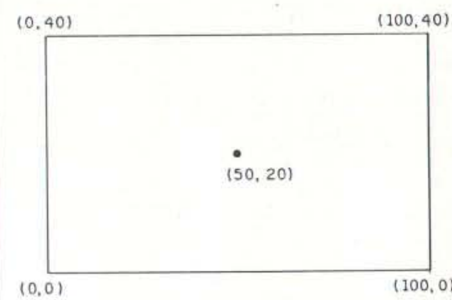

Figure 1: *The TRS-80 video monitor screen is partitioned into one hundred units horizontally and forty units vertically. The bottom left corner of the screen corresponds to the coordinates (0,0). Coordinates are also used to specify* viewports *in which the plot is to be displayed.*

Month	Cost($)
1	80
2	90
3	75
4	50
5	45
6	45
7	50
8	80
9	70
10	65
11	70
12	80

Table 1: *This hypothetical set of data represents the heating costs incurred in a house. Plotted as in figure 2, the information may be limited to one area of the screen or may use the whole screen, as in figure 3.*

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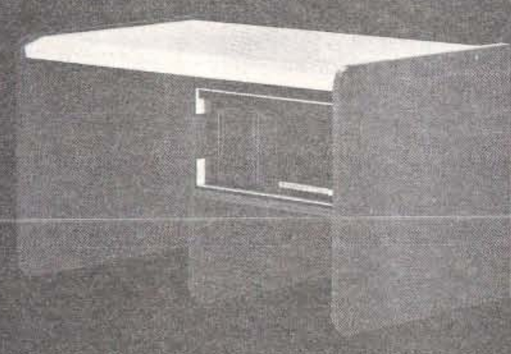

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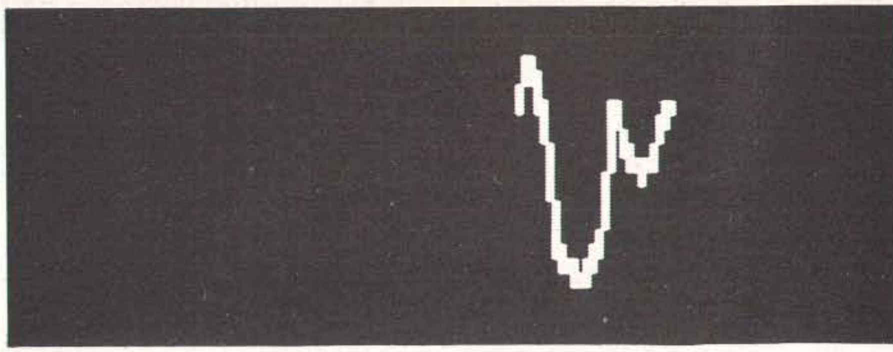

Figure 2: *The information of table 1 is plotted as shown here. The size and location of the* viewport *used were specified by limiting the display area to the bounds of 50 to 100 and 20 to 40.*

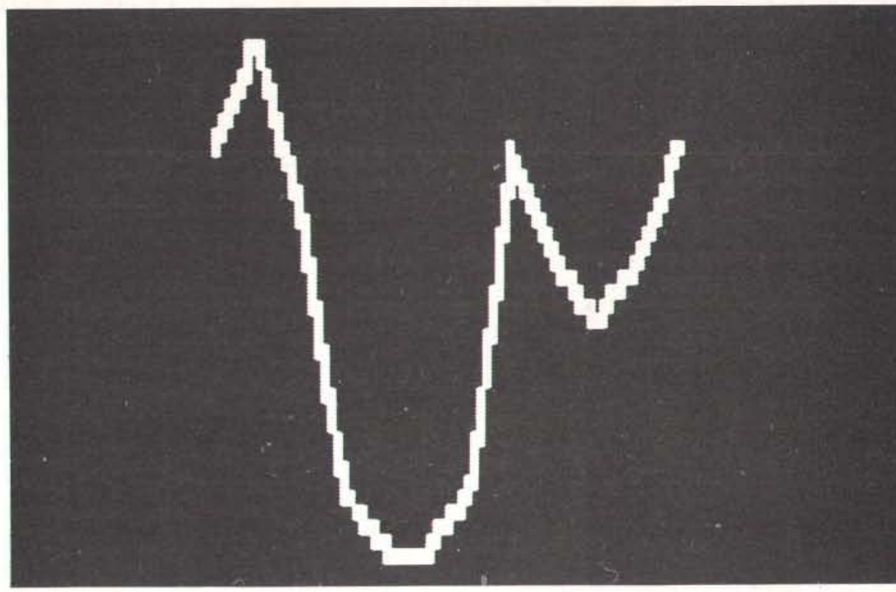

Figure 3: *The information of table 1 is plotted again, with the* viewport *bounds set at 0 to 100 and 0 to 40 (whole screen).*

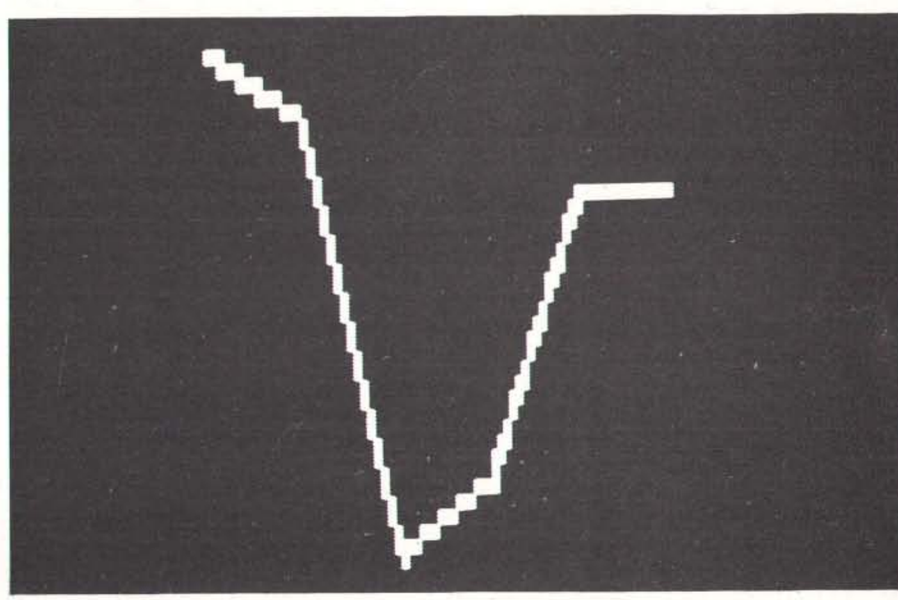

Figure 4: *The information, as in table 1, may be condensed by changing the IN variable. The integer value specified allows the program to plot a reduced number of values from the array. Also, varying the S1 parameter may help to close gaps that occur between plotting points.*

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07 = ENTER/UPDATE ORDERS
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15 = PRINT AGENT STATEMENTS
16 = PRINT TAX STATEMENTS
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18 = PRINT WEEK/MONTH PURCHASES
19 = PRINT YEAR AUDIT
20 = PRINT PROFIT/LOSS ACCOUNT
21 = UPDATE END MONTH FILES MAINTENANCE
22 = PRINT CASH FLOW FORECAST
23 = ENTER/UPDATE PAYROLL (NOT YET AVAILABLE)
24 = RETURN TO BASIC

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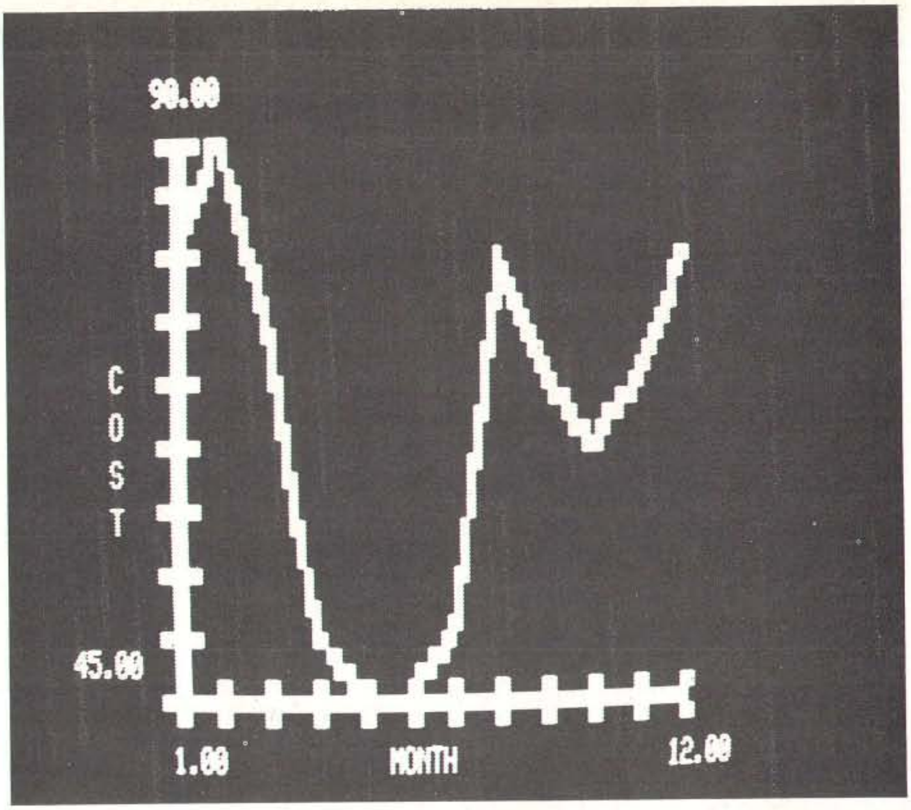

Figure 5: *The axis-plotting subroutine provides for labeling and scaling of the display. The user only needs to specify increments for each scale.*

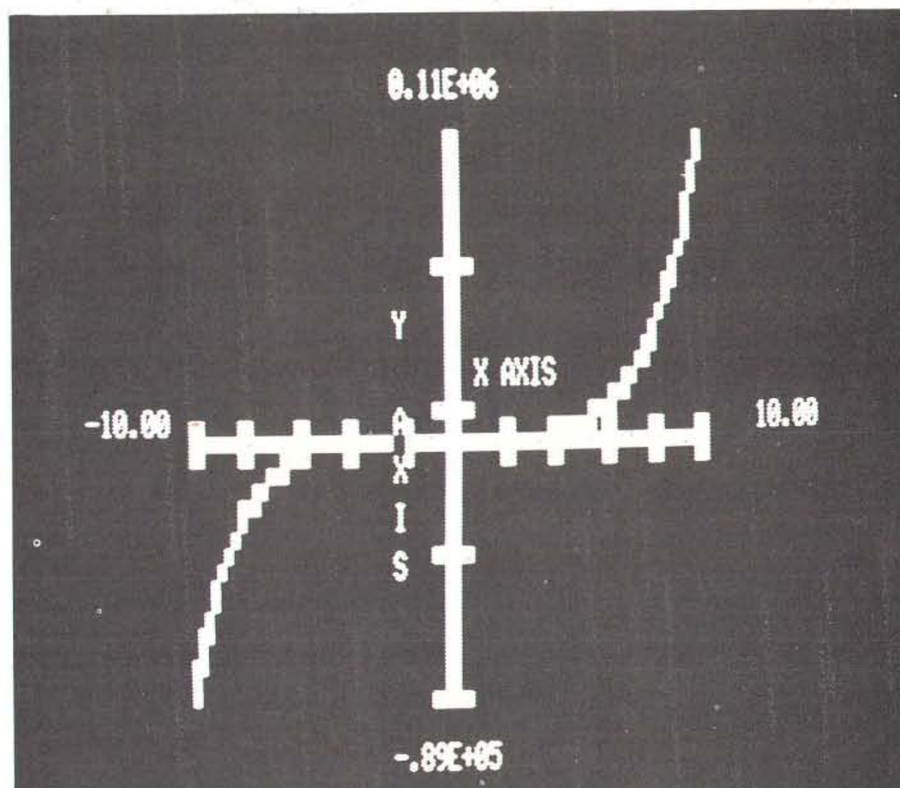

Figure 6: *Analytic functions such as this may be plotted by transforming the function into an array. Usually, a short BASIC routine may be inserted before the plotting routines, depending on the complexity of the desired display.*

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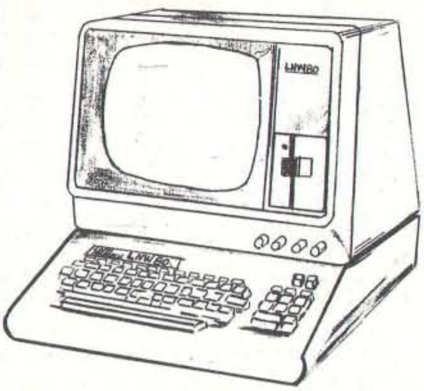

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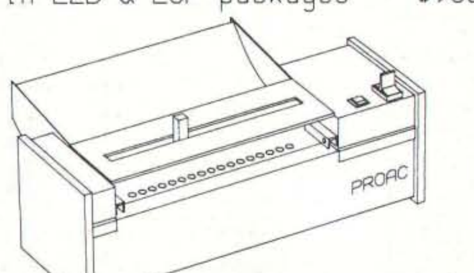

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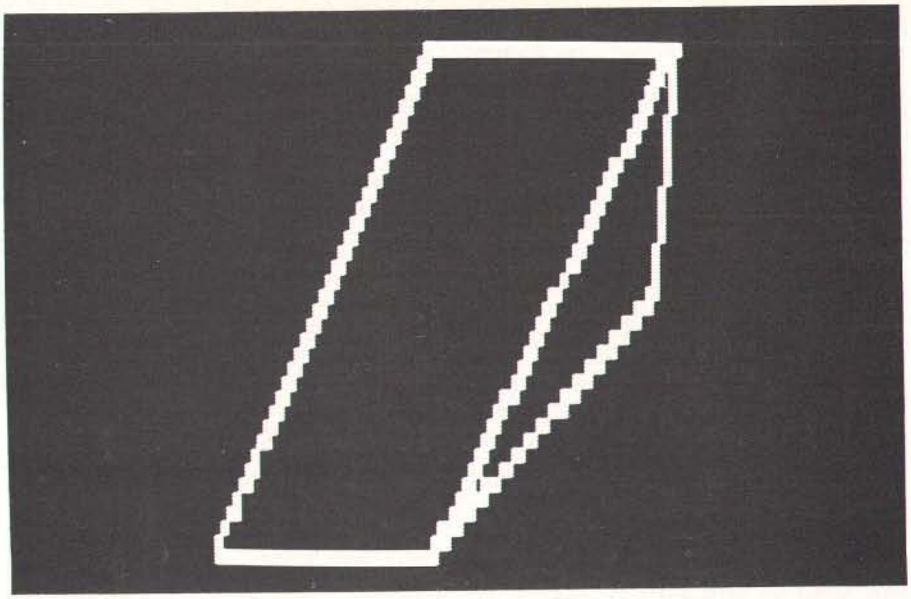
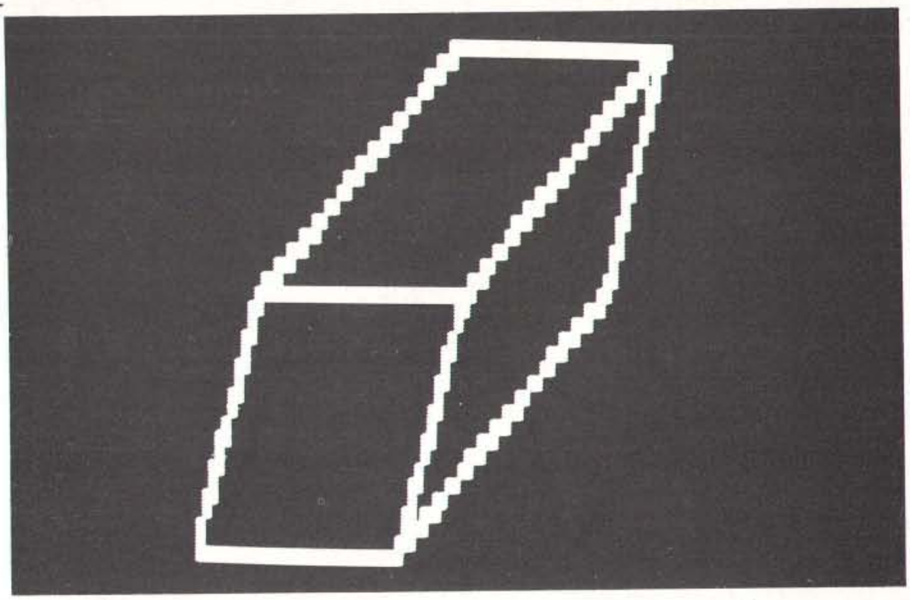
7a

```
W1 = 40        AY(1)=0
W2 =  0        AY(2)=1
Z1 = 100       AY(3)=2
Z2 =  0        AY(4)=2
S1 = .005      AY(5)=0
               AY(6)=0
AX(1)=1        AY(7)=2
AX(2)=2
AX(3)=2.1      TI=1
AX(4)=1        TA=7
AX(5)=0        IN=1
AX(6)=1
AX(7)=2.1      GOSUB 10000
```



7b

```
W1 = 40        AY(1)=0
W2 =  0        AY(2)=0
Z1 = 100       AY(3)=1
Z2 =  0        AY(4)=2
S1 = .005      AY(5)=2
               AY(6)=1
AX(1)=0        AY(7)=0
AX(2)=1        AY(8)=1
AX(3)=2        AY(9)=1
AX(4)=2.3      AY(10)=0
AX(5)=1.3      AY(11)=1
AX(6)=0.3      AY(12)=2
AX(7)=0
AX(8)=0.3      TI=1
AX(9)=1.3      TA=12
AX(10)=1       IN=1
AX(11)=1.3
AX(12)=2.3     GOSUB 10000
```



7c

```
W1 = 40        AY(1)=1
W2 =  0        AY(2)=0
Z1 = 100       AY(3)=0
Z2 =  0        AY(4)=1
S1 = .005      AY(5)=.3
               AY(6)=0
AX(1)=.5       AY(7)=0
AX(2)=0        AY(8)=.4
AX(3)=1        AY(9)=.4
AX(4)=.5       AY(10)=.58
AX(5)=1.3      AY(11)=.91
AX(6)=1
AX(7)=0        TI=1
AX(8)=.2       TA=11
AX(9)=.8       IN=1
AX(10)=.98
AX(11)=.6      GOSUB 10000
```

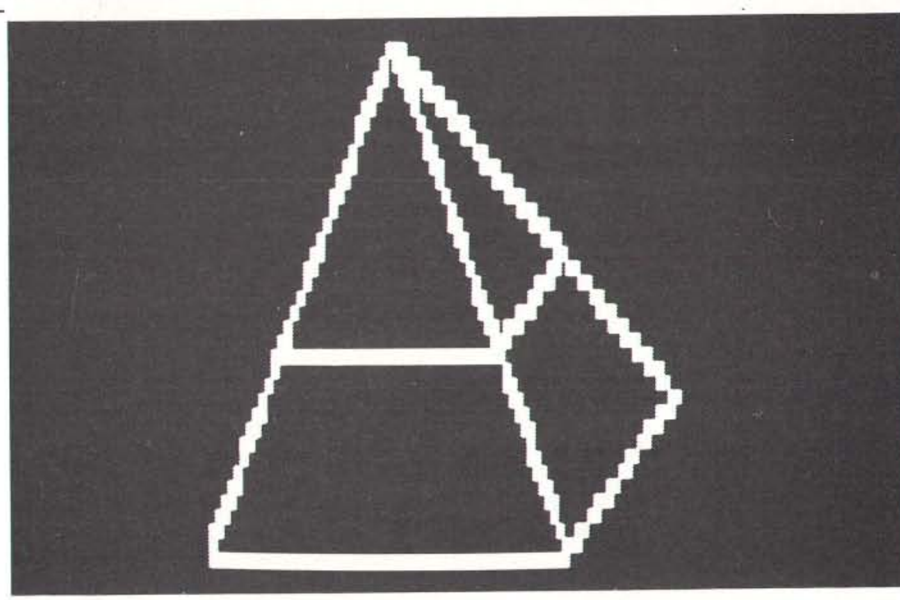


Figure 7: *Three-dimensional displays are also achieved through the transformation to an array.*

Listing 2: *The axis-creating subroutine shown here produces properly scaled axes, complete with tic marks and labels, from a set of values specified by inserting BASIC statements.*

```
20000 IF X1< =0 AND X2< =0 THEN A1=Z1 ELSE A1=Z2
20005 IF X1> =0 AND X2< =0 THEN A1=Z2-X2/A
20010 FOR I1=0 TO 1
20015 FOR J1=W2 TO W1
20020 SET ((A1+I1),(47-J1))
20025 NEXT J1
20030 NEXT I1
20035 IF Y1< =0 AND Y2< =0 THEN B1=47-W1 ELSE B1=47-W2
20040 IF Y1> =0 AND Y2< =0 THEN B1=47-W2+Y2/B
20045 FOR I3=Z2 TO Z1
20050 SET(I3,B1)
20055 NEXT I3
20060 FOR I5=1 TO 3 STEP 2
20065 FOR J5=0 TO 1
20070 FOR K5=X2 TO X1 STEP C1
20075 SET(((K5-X2)/A+Z2+J5),(B1-I5+2))
20080 NEXT K5
20085 NEXT J5
20090 NEXT I5
20095 FOR I6=0 TO 4 STEP 2
20100 FOR J6=2 TO 3
20105 FOR K6=Y2 TO Y1 STEP C2
20110 SET((A1+J6-I6),(47-((K6-Y2)/B+W2)))
20115 NEXT K6
20120 NEXT J6
20125 NEXT I6
20130 IF B1< >47-W2 GOTO 20145
20135 IF A1=Z2-X2/A THEN P1=-64 ELSE P1=64
20140 IF A1=Z2 THEN P2=-4 ELSE P2=4
20145 IF B1< >47-W1 GOTO 20160
20150 IF A1=Z2-X2/A THEN F1=64 ELSE P1=-64
20155 IF A1=Z2 THEN P2=-4 ELSE P2=4
20160 IF B1< >47-W2+Y2/B GOTO 20175
20165 P1=-64
20170 IF A1=Z2 THEN P2=4 ELSE P2=-4
20175 Z3=LEN(AX$)
20180 Z4=(Z1+Z2)/2
20185 I7=0
20190 FOR J7=3 TO 45 STEP 3
20195 IF B1<J7 GOTO 20210
20200 I7=I7+64
20205 NEXT J7
20210 Z5=Z4/2 +I7-Z3/2
20215 IF A1=Z2-X2/A AND B1=47-W2+Y2/B THEN DU=5 ELSE DU=0
20220 PRINT @ Z5+P1+DU, AX$,
20225 W3=LEN(AY$)
20230 FOR I8=1 TO W3
20235 F$(I8)=MID$(AY$,I8,1)
20240 NEXT I8
20245 W4=(W1+W2)/2
20250 J6=0
20255 FOR K8=3 TO 45 STEP 3
20260 IF 47-W4<K8 GOTO 20275
20265 J8=J8+64
20270 NEXT K8
20275 W5=J8+A1/2-(INT(W3/2)-1)*64
20280 L8=0
20285 FOR M8=W5 TO (W5+(W3-1)*64) STEP 64
20290 L8=L6+1
20295 PRINT @ M6+P2,F$(L8);
20300 NEXT M8
20305 F1(1)=47-W1
20310 F1(2)=47-W2
20315 F1(3)=B1
20320 F1(4)=B1
20325 F3(1)=A1/2
20330 F3(2)=A1/2
20335 F3(3)=Z1/2
20340 F3(4)=Z2/2
20345 FOR I9=1 TO 4
20350 J9=0
20355 FOR K9=3 TO 45 STEP 3
20360 IF F1(I9)<K9 GOTO 20375
```

Listing 2 continued on page 310

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Circle 217 on inquiry card. Circle 218 on inquiry card.

Listing 2 continued:

```
20365 J9=J9+64
20370 NEXT K9
20375 F2(I9)=J9+F3(I9)
20380 NEXT I9
20385 IF ABS(Y1)>1E4 OR ABS(Y1)<1E-2 THEN D1$="#.##[[[["
   ELSE D1$="#####.##"
20390 IF ABS(Y2)>1E4 OR ABS(Y2)<1E-2 THEN D2$="#.##[[[["
   ELSE D2$="#####.##"
20395 IF ABS(X1)>1E4 OR ABS(X1)<1E-2 THEN D3$="#.##[[[["
   ELSE D3$="#####.##"
20400 IF ABS(X2)>1E4 OR ABS(X2)<1E-2 THEN D4$="#.##[[[["
   ELSE D4$ ="#####.##"
20405 IF B1<>47-W2+Y2/B GOTO 20435
20410 D1=1
20415 D2=-9
20420 D3=-68
20425 D4=60
20430 GOTO 20505
20435 IF B1<>47-W1 GOTO 20475
20440 D1=-68
20445 D2=-68
20450 D4=60
20455 IF A1=Z1 THEN D3=65
20460 IF A1=Z2 THEN D3=54
20465 IF A1=Z2-X2/A THEN D3=-68
20470 GOTO 20505
20475 D1=60
20480 D2=60
20485 D3=-68
20490 IF A1=Z2 THEN D4=-74
20495 IF A1=Z1 THEN D4=-62
20500 IF A1=Z2-X2/A THEN D4=60
20505 PRINT @ F2(1)+D3,USING D1$;Y1;
20510 PRINT @ F2(2)+D4,USING D2$;Y2;
20515 PRINT @ F2(3)+D1,USING D3$;X1;
20520 PRINT @ F2(4)+D2,USING D4$;X2;
20525 RETURN
```


All scaling and other mundane functions are taken care of in the subroutine.

Text continued from page 298:

displayed as in figure 5. This is for a graph of the cost of heating for every month displayed in the total viewport.

Clearly, it is easy to plot any set of data that can be represented in array form. Remember that all scaling and other mundane functions are taken care of in the subroutines. You don't need to be concerned or irritated by the gyrations needed to create displays on the TRS-80.

Analytic Functions

In order to plot any analytic function, be prepared to transform the function into array form. An example of this is best demonstrated in the plotting of the function:

$$Y = X^5 + X^4 - X^3$$

This is for X taking on values from −10 to 10. In order for this to occur the following BASIC initialization routine is needed:

```
FOR I=-10 TO 10
AX (I+10)=I
AY (I+10)=I↑5+I↑4-I↑3
NEXT I
TI= 0
TA=20
IN= 1
AX$= "X AXIS"
AY$= "Y AXIS"
C1=     2
C2= 49750
```

The result should appear as shown in figure 6. Note that the correct quadrants are displayed.

Another feature provided by this graphics package is the ability to create *three-dimensional* graphs. Figures 7a, b, and c give several examples of this, along with the array values used. The displayed figures are not necessarily functions, but may have more than one *y* value for each value of *x*.

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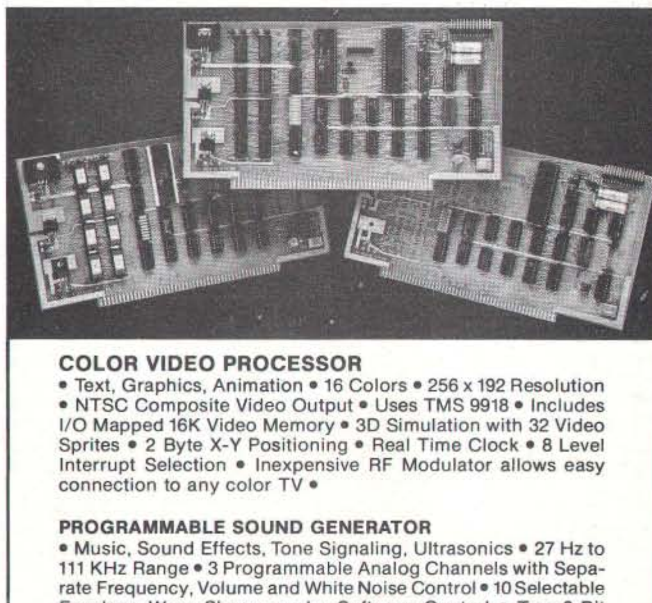

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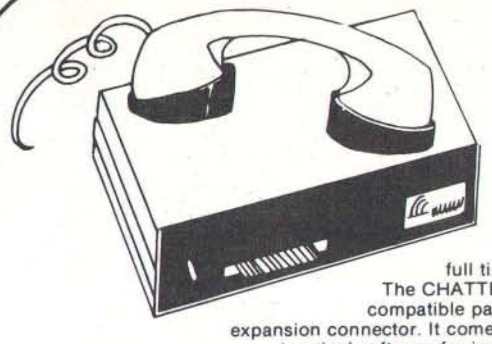

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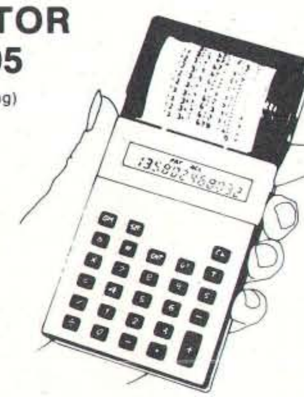

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November-February

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November 8

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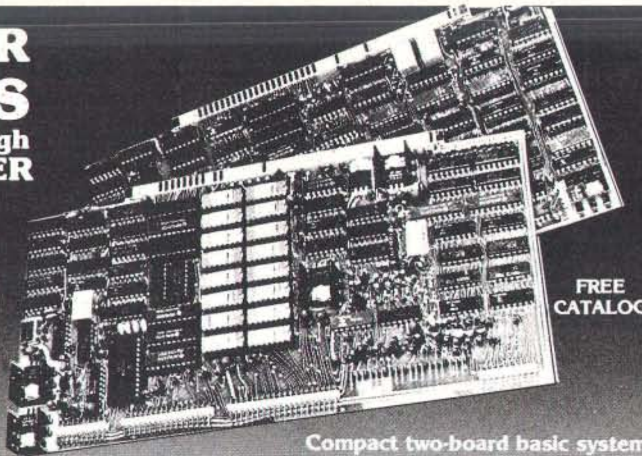

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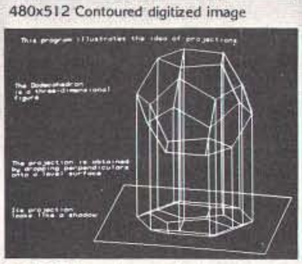

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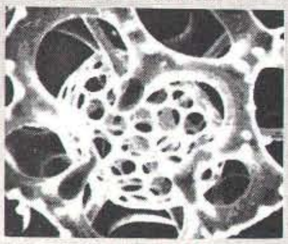

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Manufacturers, stores, and individuals will display both top-of-the-line and used merchandise.

A special consignment table will be available for those who wish to drop off an item or two to be sold during the day. A free literature table is available to anyone within the industry. Admission to buyers will be through the purchase of a redeemable $5 purchase certificate. Sellers, both individuals and companies, should call (415) 966-6546 for booth prices, availability and reservations. Or, write to: California Computer Swap Meets, POB 52, Palo Alto CA 94302.

November 8-9

Personal Computer Fair, Pacific Science Center, Seattle WA. The theme of this year's fair is "Hands On." Both the booths and the exhibits will reflect this idea, and the public will have access to as many computers and terminals as possible. Contact The Northwest Computer Society, POB 4193, Seattle WA 98119, (206) 284-6109.

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November 12

National Conference on the Use of On-Line Computers in Psychology, St Louis MO. This conference is for computer users in psychology and related disciplines. These users will consider the use of computers in research, clinical practice, and teaching. Tutorial sessions will be included. Contact Dr Dominic Massaro, Program in Experimental Psychology, University of California, Santa Cruz CA 95064.

November 13-16

The 1980 International Computer Music Conference, Queens College, Flushing NY. This conference is for persons interested in computer applications in music. Conference activities include presentation of papers, concerts, workshops, panel discussions, meetings of special interest groups, demonstrations, and an exhibition of computer music equipment. For information, contact Dr Hubert S Howe Jr, Director 1980 International Computer Music Conference, Queens College, Flushing NY 11367, (212) 520-7342.

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The Third Industrial Revolution, McCormick Place, Chicago IL. This show is an exposition and conference devoted to development by manufacturing companies of systems for information management. Information may be obtained from Banner & Grief Ltd, 110 E 42nd St, New York NY 10017, (212) 687-7730.

November 19-21

Comdex, Las Vegas Convention Center, Las Vegas NV. Comdex is a conference and exposition for independent sellers of small-computer and word-processing systems, peripherals, media, and supplies. Address inquiries to The Interface Group, 160 Speen St, Framingham MA 01701, (800) 225-4620.

November 20-21

Western Educational Computing Conference, San Diego CA. This conference will feature papers and seminars on the use of computing in higher education for instruction, administration, and research. Contact Ron Langley, Director, Computer Center, California State University, Long Beach, 1250 Bellflower Blvd, Long Beach CA 90840, (213) 498-5459.

November 20-23

Northeast Computer Show, Hynes Auditorium, Boston MA. This exposition is open to the general public. The admission will be $5. Contact National Computer Shows, 824 Boylston St, Chestnut Hill MA 02167, (617) 739-2000.

November 21-23

National Home Entertainment Show, New York Coliseum, New York NY. Exhibits will cover video, photography, audio, games, and home computers. Seminars and demonstrations will be featured in this show. Contact United Business Publications Inc, 475 Park Ave South, New York NY 10016, (212) 725-2300.

November 24-25

Computer Equipment Registration, George Washington University, Washington DC. This course will review the FCC's Part 15 rules dealing with RF (radio frequency) emissions by computers. Technical considerations governing the classifications for computers, peripherals, and other related devices will be described. Contact the GWU Continuing Engineering Education Program, Washington DC 20052, (800) 424-9773.

November 25-27

Semiconductor International '80, Metropole Convention Centre, Brighton, England. This exhibition is devoted completely to production of semiconductor components, and displays will cover all areas of technology. A technical conference program will cover mask-making procedures, VLSI (very large-scale integration), crystal growth technology, thin film technology, bonding, memory testing, and more. Contact Kiver Communications SA, 171/185 Ewell Rd, Surbiton, Surrey, KT6 6AX, England.

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The Model EP-2A-87 EPROM Programmer has an RS-232 compatible interface and includes a 2K or 4K buffer. During the ON-LINE mode, another computer can down-load to the buffer. Only two easy-to-implement commands are available to an external computer. (Load buffer and read buffer.)

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Part No.	Description	Price
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	Non standard voltage option (220 v, 240 v, 100 v)	15.00
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PM-3	Personality module, programs TMS 2716	26.00
PM-4	Personality module, programs TMS 2532	31.00
PM-5	Personality module, programs 2716, TMS 2516	16.00
PM-6	Personality module, programs 2704	26.00
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December 1980

December 2-5

The Eleventh International Conference of the Computer Measurement Group, Sheraton-Boston Hotel, Boston MA. This conference is entitled "Computer Performance Evaluation in the 80s." Contact Judith G Abilock, Price Waterhouse and Company, Office of Government Services, 1801 K St NW, Washington DC 20006, (202) 296-0800.

December 3-5

The 1980 Winter Simulation Conference, Orlando Marriott, Orlando FL. This conference will feature papers, panel discussions, tutorials, and review sessions on discrete and combined simulations. Contact Professor Tuncer I Ören, Chairman, Department of Computer Science, University of Ottawa, Ottawa, Ontario K1N 9B4, Canada, (613) 231-5420.

December 3-5

Implementing Computer-Based Human Resource Systems, New York NY. This is a seminar for planning, organizing, and implementing a comprehensive system for the human resources area. It will demonstrate ways to set up a useful personnel record-keeping system. The course fee is $695. For information, contact The University of Chicago, Center for Continuing Education, MC Seminar Division, 1307 E 60th St, Chicago IL 60637, (800) 223-7450.

December 4

California Computer Shows, Hyatt-Palo Alto, Palo Alto CA. Show hours are from 1 to 7 PM. OEM (original equipment manufacturers) and end-user computer and peripheral products will be exhibited and demonstrated by over sixty companies. Contact Norm De Nardi Enterprises, 95 Main St, Los Altos CA 94022, (415) 941-8440.

December 10

1980 Computer Networking Symposium, Gaithersburg MD. The symposium is sponsored by the IEEE Computer Society, Technical Committee on Computer Communications, and the Institute for Computer Sciences and Technology of the National Bureau of Standards. The focus is on office automation, office system components, and the computer networks required to interconnect them. For information, contact Executive Secretary, POB 639, Silver Spring MD 20901, (301) 439-7007.

January 1981

January 7-9

The Fourteenth International Symposium on Minicomputers and Microcomputers, Hotel del Coronado, San Diego CA. The scope of the symposium will cover technology, hardware, software, engineering, languages, systems architecture, operating systems, numerical methods, computer networks, and other aspects of computing. Contact the Secretary, MIMI '81 San Diego, POB 2481, Anaheim CA 92804.

January 13-15

Communications Networks 1981, Albert Thomas Convention Center, Houston TX. This show will feature exhibits and seminars covering network policy and management for US and international users and carriers; network architecture, software, and hardware; new developments; information appliances; and more. This conference is aimed at communications professionals, carrier, service and

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Price, including a copy of the Universal Computing Machine $89.95

☐ BUSINESS CHECK REGISTER AND BUDGET: A combination of our CHECKBOOK II and BUDGET programs expanded to include up to 50 budgetable items and up to 500 checks per month. Includes bank statement reconciliation and automatic check search (48K). $49.95

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☐ LOGIC DESIGNER: Interactive HI-RES Graphics program for designing digital logic systems. A menu driven series of keyboard commands allows you to draw directly on the screen up to 15 different gate types, including 10 gate shape patterns supplied with the program and 5 reserved for user specification. Standard patterns supplied are NAND, NOR, INVERTER, EX-OR, T-FLOP, JK-FLOP, D-FLOP, RS-FLOP, 4 Bit COUNTER and N-BIT SHIFT REGISTER. User interconnects gates just as you would normally draw using line graphics commands. Network descriptions for LOGIC SIMULATOR generated simultaneously with the CRT diagram being drawn. $89.95

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☐ FREE CATALOG - All programs are supplied on disk and run on Apple II w/Disk & Applesoft ROM Card & TRS-80 Level II and require 32K RAM unless otherwise noted. Detailed instructions included. Orders shipped within 3 days. Card users include card number. Add $1.50 postage and handling with each order. California residents add 6½% sales tax.

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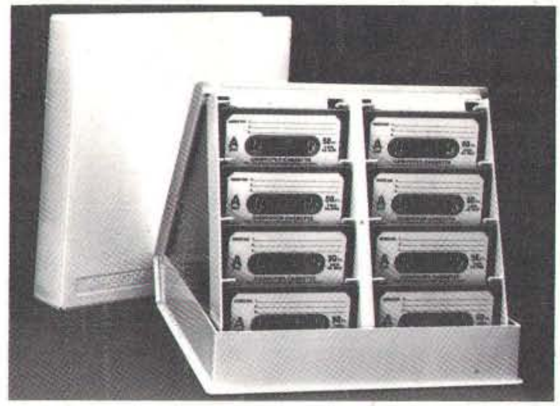

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hardware vendors who are interested in combining voice, data, and message systems applications. Contact Communications Networks '81, c/o The Conference Company, 60 Austin St, Newton MA 02160, (617) 964-4550.

January 14-19

42nd National Audio-Visual Convention and Exhibit, Dallas Convention Center, Dallas TX. Over 300 manufacturers and producers of audio-visual, video and microcomputer hardware and software will be exhibiting their products. Seminars will cover marketing and production of audio-visual items. For more information, contact the National Audio-Visual Association, 3150 Spring St, Fairfax VA 22031, (703) 273-7200.

January 16-17

Microcomputer Conference, Arizona State University, Tempe AZ. The goal of this microcomputer conference is to introduce educators to the applications of computers in the classroom. The emphasis of the conference is to provide an awareness of microcomputers and their impact on society. For further information, contact Dr Gary G Bitter, Arizona State University, Payne 203, Tempe AZ 85281.

January 27-29

Advanced Semiconductor Equipment Exposition, San Jose Convention Center, San Jose CA. Over 100 exhibitors will feature equipment at this trade show. The show's emphasis is on new products and emerging technology in the semiconductor processing and production fields. Contact Cartlidge & Associates, 491 Macara Ave, Suite 1014, Sunnyvale CA 94086, (408) 245-6870.

January 28-31

The Third IMMM/Data Comm International Japan Exposition, Harumi Exposition Center, South Hall, Tokyo, Japan. Over 15,000 scientists, design engineers, technical managers, applications engineers, and other specialists are expected to attend this show. Internepcon Japan/Semiconductor International is held concurrently. A conference program will include talks on microcomputer-controlled data communications systems, peripheral interfacing, software management, and more. Contact Industrial and Scientific Conference Management Inc, 222 W Adams St, Chicago IL 60606, (312) 263-4866.■

BYTE's Bugs

Correspondence on Correspondence

Thank you, BYTE, for running the enlarged, corrected oscilloscope photographs in BYTE's Bugs on page 182 of the June 1980 issue. BYTE readers may wish to label these pictures in order to be sure of their correspondence with the original photographs on page 66 of the article, "A Computer-Controlled Light Dimmer" (January 1980 BYTE). The picture labels should be matched as follows:

Original Article (January)	Pictures in BYTE's Bugs (June)
00	a
40	b
80	c
C0	d
FF	e

Thank you again for your time and concern in publishing the corrections in BYTE's Bugs.

John H Gibson
Department of Physics
Alma College
Alma MI 48801

Incorrect STOIC Price

An incorrect price was reported in John James's article "What is FORTH?" in the August 1980 BYTE. On page 134, middle column, Mr James reported that the language STOIC was available from the CP/M User's Group (1651 Third Ave, New York NY 10028) for $20. The membership fee of $4 has been replaced by a one-time catalog fee of $6, making the total $22, not $20 ($8 each for two floppy disks plus $6 for the catalog). Also, the above price is valid for the United States, Canada, and Mexico only. The price for all other countries is $12 per disk, making a total of $30 ($12 each for two floppy disks plus $6 for the catalog). The Group is filling orders that were received with insufficient funds, but they (and we at BYTE) request that the receivers of such orders pay the appropriate difference in price.■

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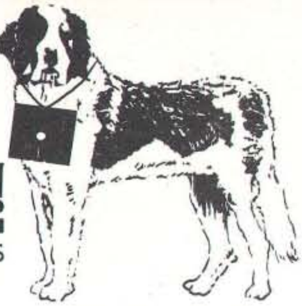

Before you buy the programs that your company is going to depend on for its accounting, ask the following questions:

Do I get the source code?	(Don't settle for less. You cannot make the smallest change without it.)
Is it well documented?	(The Osborne documentation is the best.)
Is it fully supported?	(If not, why not? What are they afraid of?)

The Osborne system is the industry standard accounting package, with literally thousands of users. We offer an enhanced version of that package that will run on most systems without recompiling.

CRT INDEPENDENCE. The original programs were designed to run on a Hazeltine terminal. To use a different CRT, you had to modify and test two modules — and recompile every program! With the Vandata package, you simply pick your CRT from a menu and run.

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INTEGRATION. The original AR and AP systems had to be changed and recompiled to feed journal entries to GL. Our installation program eliminates this hassle. It simply asks you if you want the systems integrated, and what your special account numbers are.

SPEED. The original programs used a binary search to access the GL account file. We use an enhanced technique that greatly cuts down on disk accesses, thus speeding up account lookups significantly in the GL, AR and AP systems.

BUGS. We have corrected a number of bugs in the original programs. If you find a bug in our programs, we'll fix it — and send you a $20 reward! Our users are sent bug fixes in source form.

MORE! We have made many minor enhancements, and fixed many minor problems. We are committed to the ongoing support of our package. Vandata has been an independent software supplier for over seven years. Quality and support are our way of doing business.

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CBASIC-2	$110
TRS-80® MOD II CP/M® 2.2 (Pickles & Trout)	$185
H89/Z89 CP/M® 2.2 (Magnolia inc. h/w mod)	$295

Formats: Std. 8", 5" NorthStar DD, TRS-80 MOD II tm, H89/Z89. Manuals for GL, AR/AP, and PR are not included in price — add $20 per manual desired (AR/AP are in one manual). CP/M® and CBASIC-2 required to run accounting software. Users must sign licensing agreement. *Dealer inquiries invited.*

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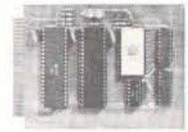
6502 MICRO MICROCOMPUTER

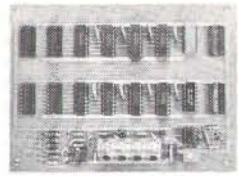


This JBE micro-microcomputer has the following:
- 1024 bytes of RAM (two 2114s)
- 2048 bytes of EPROM (2716)
- Uses one 6522 via (documentation incl.)
  - 2 8-bit bidirectional I/O ports
  - 2 16-bit programmable timer/counter
- Serial data port
- Latched output and input with handshaking logic
- TTL and CMOS compatible

The 6502 microprocessor is particularly suited for control functions such as temperature control burglar alarm, electrical wheel chair, lights, etc. This micro-micro interfaces with the JBE Solid State Switch and A-D and D-A converter and uses JBE 5V power supply. 2716 EPROM is available separately. A 50 pin connector is included.

80-153 ASSM. **$110.95** Kit **$89.95** Bare Board **$24.95**

DIMMER CONTROL



The JBE Dimmer Control has 4 channels, 256 brightness levels on-board power supply and four 8-bit parallel input ports (not latched). This board interfaces with JBE Solid State Switch and Apple II Parallel Interface Card (documentation included).

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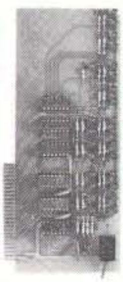

This handy little board is ideal for teaching and trouble-shooting. It has a run-stop single step switch which makes identification of shorted lines between address or data-bits easy & shows single step for teaching computer logic. The display board has 16 address LEDs, 8 data LEDs and 1 RDY LED. All lines are buffered.

80-144 ASSM. **$49.95**
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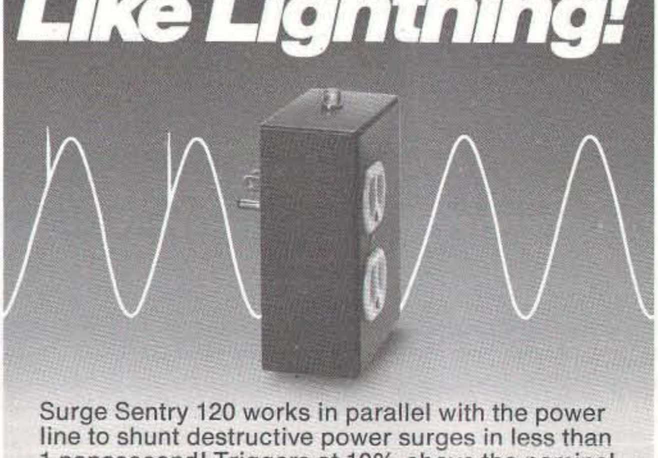

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The muSIMP/muMATH-79 Symbolic Math System

Gregg Williams
Editor

Computers are very literal minded: ask one to add 1/2 and 1/3 and it will probably give you 0.833333 or some close approximation. Ask for 40! (ie: 40 factorial) and you will get an answer like 8.1592E47, if you receive a reply at all. But what if you wanted the answer 5/6 for the first problem and an exact answer to the second problem, all forty-eight digits of it? Computers express everything in numbers, not symbols: that's the problem.

A software package called muMATH-79, created by the Soft Warehouse of Honolulu, Hawaii, does just what you want and more. The muMATH-79 package, billed as a *symbolic math system*, is to algebraic problem solving what the pocket calculator is to arithmetic problem solving. Like a pocket calculator, it cannot solve problems on its own, but muMATH-79 can be an invaluable tool in terms of increasing the accuracy and the complexity of the problems that can be solved by a person.

muMATH-79 is a modular system. It can be used for any one or a combination of the following: 611-digit arithmetic; matrix manipulation; algebraic manipulation and equation solving; logarithmic and trigonometric manipulation; integration and differentiation.

Arithmetic and Algebra

muMATH-79 manipulates everything as a string of symbols, so it's no surprise that numbers are stored as strings of digits, with a given number being up to 611 digits long. Given this situation, muMATH-79 has defined addition, subtraction, multiplication, division, and integral exponentiation as operations that work on two strings of numbers to give a third string as a result.

> Matrix operations in muMATH-79 are fast as well as exact.

When muMATH-79 is running, the computer prompts user input with a question mark and a space. (In our examples, computer-generated output is underlined here to distinguish it from user input.) All commands must be ended in a semicolon, and muMATH-79 precedes its answer with an ampersand and a space. For example, if we type in:

```
? 2↑50;
```

muMATH-79 replies almost instantly with:

```
@ 1125899906842624
```

Similarly, a request for 40 factorial gets an immediate reply:

```
? 40!;
@ 815915283247897734
  561126959611589427200
  0000000
```

We can assign strings (ie: numbers or symbolic expressions) to variable names using a colon:

```
? C1:2↑50;
@ 1125899906842624
? C2:C1−1;
@ 1125899906842623
```

Also, we can change the radix used to accept and display numbers. For example, to change to binary (also called radix 2 or base 2), we say:

```
? RADIX(2);
@ 1010
```

and muMATH-79 replies that its base was base 10 (since it is now in base 2, it prints 10 in binary: binary 1010 = decimal 10). To check that we are in base 2:

```
? C1;
@ 10000000000000000000
  000000000000000000000
  000000000
? C2;
@ 11111111111111111111
  111111111111111111111
  111111111
```

Sure enough, C1, being 2^{50}, should be a 1 followed by fifty 0s in binary, and C2 should be fifty 1s.

Also, muMATH-79 is fast. It computed all the above answers in less than 1 second each (running on a Cromemco Z-2D at 4 MHz), and answered 250! (seven lines of numbers) in 31 seconds. (See listing 1.) When a number being computed

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Listing 1: *Extended-precision arithmetic in muMATH-79. As shown in the first two examples, muMATH-79 does not convert fractions, but rather reduces them to their lowest terms. As you can see from the 493-digit answer to 250! (250 factorial), muMATH-79 does all its arithmetic exactly. In this and all other listings, underlining denotes computer output.*

```
? 4/20;

@ 1 / 5


? 352/283072;

@ 11 / 8846


? 250!;


@ 3232856260909107732320814552024368470994843717673780666747942427112823
747555111209488817915371028199450928507353189432926730931712808990822791
030279071281921676527240189264733218041186261006832925365133678939089569
935713530175040513178760077247933065402339006164825552248819436572586057
399222641254832982204849137721776650641276858807153128978777672951913990
844377478702589172973255150283241787320658188482062478582659808848825548
800000000000000000000000000000000000000000000000000000000000000
```

exceeds the capacity of the system, muMATH-79 replies with the word FALSE:

```
? 300!;
@ FALSE
```

muMATH-79 also manipulates symbolic expressions (depending on the values of its control variables, described later). For example:

```
? 5*X-3*Y↑2+8*X-4*Y↑2
@ 13*X - 7*Y↑2
```

Equations in muMATH-79 are often hard to read. It helps to write them out using pencil and paper; the above was $5X-3Y^2+8X-4Y^2$, which simplified to $13X-7Y^2$. Variables can be used in expressions, where they add their symbolic content to the expression being evaluated:

```
? EXPR1:B+4;
@ 4 + B
? EXPR2:EXPR1+C+2*B;
@ 4 + 3*B + C
```

A variable name is called *bound* if it has a value and *unbound* if it does not. For example, the variable EXPR1, above, is bound because it has the value B+4. There are times, however, when we want a variable to simply be itself. We can change a variable from bound to unbound as follows (using the example of EXPR1):

```
? EXPR1;
@ 4 + B        (EXPR1 is bound)
? EXPR1:'EXPR1
@ EXPR1        (EXPR1 is now
                unbound)
```

Equation Solving

In addition, some equations can be solved. For example, to solve

$$X^3+2X^2-63X=0:$$

```
? SOLVE(X↑3+2*X↑
      2-63*X==0,X);
@ {X = 7,
   X = -9,
   X = 0}
```

(muMATH-79 uses the double equal

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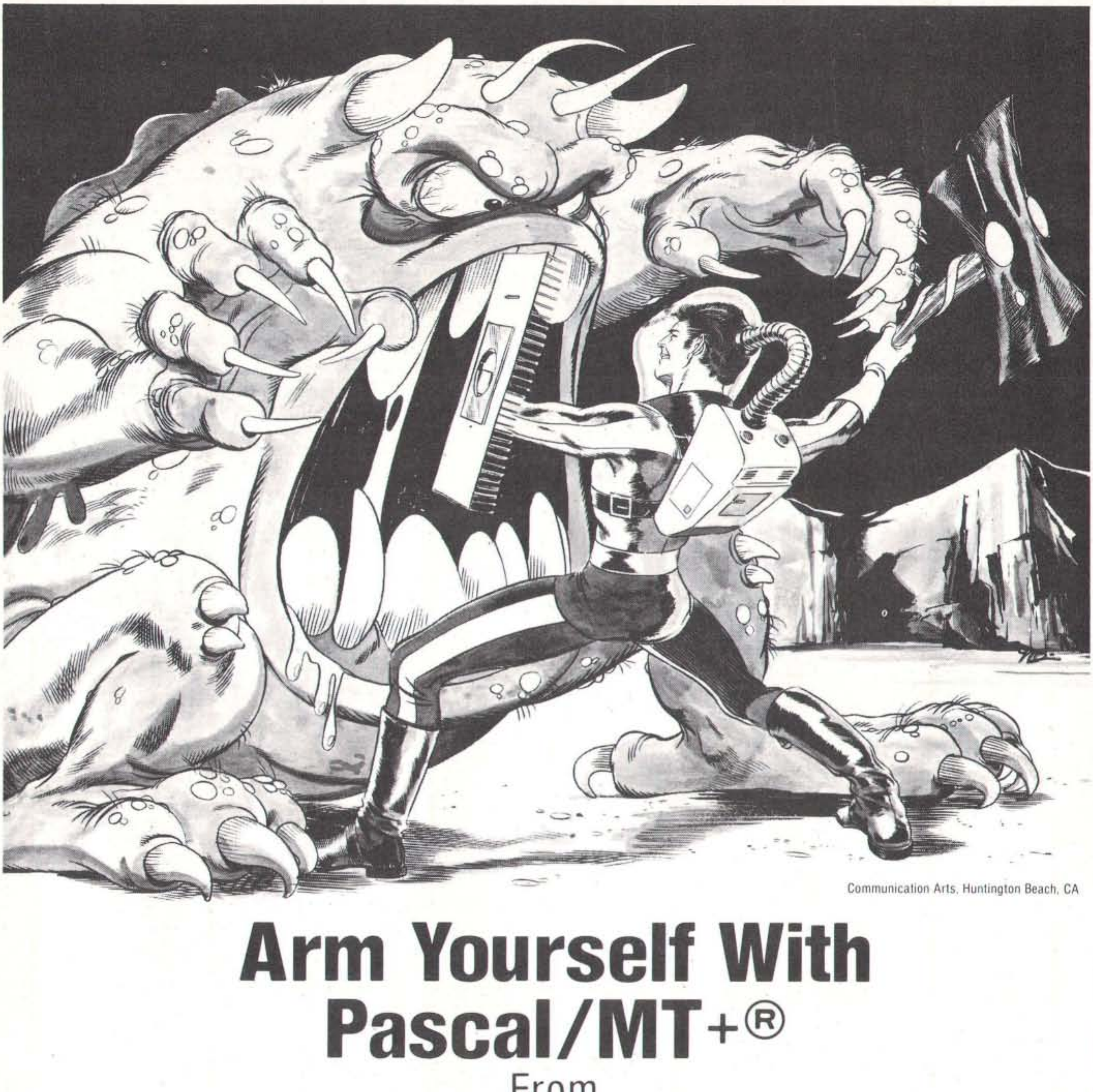

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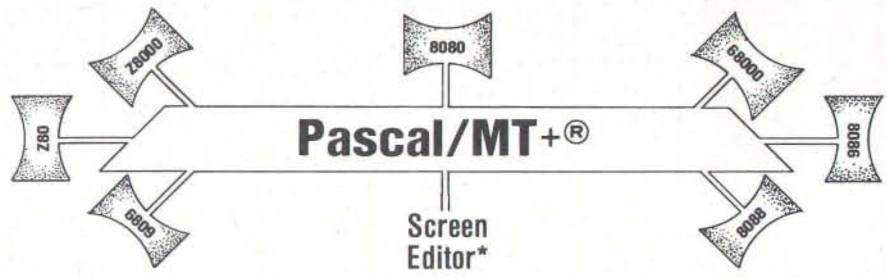

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The most impressive feature of muMATH-79 is its ability to do symbolic differentiation and integration.

sign to distinguish it from the single equal sign, which is used as a Boolean equality operator; the final X in the SOLVE command tells muMATH-79 to solve for the variable X.)

It is also aware of imaginary and complex numbers and uses the variable #I to represent the imaginary number *i*:

```
? SOLVE(X↑2+1==0,X);
@ {X = -#I,
   X = #I}
```

However, muMATH-79 is not intelligent; it cannot solve equations of order 3 or higher. (The example with the X^3 polynomial is seen by muMATH-79 as being of order 2, with a zero factor added.) Factoring is hard even for people, but muMATH-79 can aid you in factoring a higher-order polynomial.

Trigonometric and Logarithmic Manipulation

With the addition of these packages to the muMATH-79 system, the user can manipulate logarithmic and trigonometric expressions. Manipulation of these expressions varies with the values of certain control variables.

For example, if the trigonometric expansion variable TRGEXPD is 0:

```
? SIN(5*Y);
@ SIN(5*Y)
```

But if TRGEXPD is −6 (denoting expansion of multiple-angle sine and cosine functions):

```
? SIN(5*Y);
@ -12*COS(Y)↑2*SIN(Y)
  + 16*COS(Y)↑4*SIN(Y)
  + SIN(Y)
```

The functions available are LN (logarithm to the base *e*), LOG (logarithms to other bases), SIN, COS, TAN, COT, SEC, and CSC. And muMATH-79 uses the variable #E (for *e*) and #PI (for π).

Matrix Manipulations

The math system can also manipulate matrices. Matrices can be multiplied (or divided) by a matrix or a scalar, transposed, inverted, and taken to an integer power. If a matrix is nonsingular (ie: its inverse does not exist), muMATH-79 responds to an attempt to invert it with divide-by-zero error messages. If the matrix can be inverted, the coefficients of its inverse, if nonintegral, are expressed as fractions—that is, the inverse is algebraically exact. For an example of this, see listing 2.

Matrix operations are fast as well as exact. The inversion of matrix H in listing 2 took 5 seconds, and the inversion of a 5-by-5 matrix took 48 seconds. Since matrix entries are symbolic, the entries can be scalars or matrices. This allows the formation of complex data structures that can be manipulated by muMATH-79.

Differentiation and Integration

The most impressive feature of muMATH-79 is its ability to do symbolic differentiation and integration. For example, if we differentiate $1/X^3$ with respect to X, we get $-3X^{-4}$. muMATH-79 accomplishes the task as follows:

```
? DIF(1/X↑3,X);
@ -3 / X↑4
```

Listing 2: *Matrix inversion and multiplication in muMATH-79. Listing 2a shows the creation of the 2 by 2 matrix H. Listing 2b shows the creation of the inverse of H, HINV. Listing 2c shows the multiplication of two compatible matrices using a period (.) as the muMATH-79 matrix multiplication operator.*

(2a)

```
? H:{[380,-115/2],[17,109]}

@ {[380, -115/2],
   [17, 109]}
```

(2b)

```
? HINV:H^-1;

@ {[218/84795, 23/16959],
   [-34/84795, 152/16959]}
```

(2c)

```
? H.HINV;

@ {[1, 0],
   [0, 1]}
```

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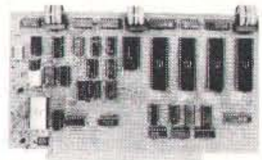

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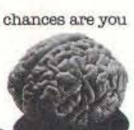

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Listing 3: *Algebraic intergration in muMATH-79. Listing 3a shows the creation of the function FC1, which equals $X^2+ln(X)$. Listing 3b shows the calculation of the indefinite integral of FC1, while listing 3c shows the calculation of its definite integral from e to 2e. (See the text for these two equations written in conventional form.)*

(3a)
```
? FC1:X^2+LN(X);

@ X^2 + LN(X)
```

(3b)
```
? INT(FC1,X);

@ -X + X*LN(X) + X^3/3
```

(3c)
```
? DEFINT(FC1,X,#E,2*#E);

@ -2*#E + 2*#E*LN(2*#E) + 7*#E^3/3
```

It works with the resources of whatever packages are loaded into it at the time. For example, if the trigonometric package is loaded, muMATH-79 can do the following:

```
? DIF(COT(2*X),X);
@ -2 * CSC(2*X)↑2
```

which translates as:

$$\frac{d}{dX} \cot 2X = -2 \csc^2(2X)$$

Indefinite and definite integrals are also within muMATH's capabilities. The definite integral is calculated by simple substitution of the integral limits into the result of the indefinite integration, in much the same process a person performs. If muMATH-79 cannot do this, it simply returns the indefinite integral. Listing 3 shows its calculation of the following two integrals:

$$\int X^2+\ln(X)\, dX = \frac{X^3}{3}+X\ln(X)-X+C$$

and

$$\int_e^{2e} X^2+\ln(X)dX = \frac{7e^3}{3}+2e\ln(2e)-2e$$

muMATH-79 Control Variables

The package does not exhibit artificial intelligence. (Although with some of its accomplishments, it *seems* to exhibit it.) Rather, it is a very sophisticated symbol manipulator that rigorously applies a given set of rules to arrive at a transformed result. But achieving a desired algebraic manipulation is not always an exact process.

For example, consider the trivial example given in figure 1a. If the denominator is distributed over the numerator, the result is the expression in figure 1b. But if we factor the numerator first, the discovered factor of $(X+1)$ in the numerator cancels the $(X+1)$ in the denominator, leaving the simplified answer in figure 1c.

muMATH-79 cannot make these decisions; it is a tool, not a problem solver. So certain variables called *control variables* are introduced into its environment. Under human control, these variables are used to tell muMATH-79 what manipulations to make.

(a) $\dfrac{X^3+X^2}{(X+1)}$

(b) $\dfrac{X^3}{(X+1)}+\dfrac{X^2}{(X+1)}$

(c) $\dfrac{X^3+X^2}{(X+1)} = \dfrac{X^2(X+1)}{(X+1)} = X^2$

Figure 1: *Options in the transformation of an algebraic expression. The simple expression in figure 1a can be transformed to that of figure 1b by distributing the denominator over the terms of the numerator. A more useful transformation, however, is shown in figure 1c. By factoring out a term of X^2 and cancelling out the $(X+1)$ factor in both numerator and denominator, the expression can be considerably simplified.*

Although an explanation of the intricacies of control variables is beyond the scope of this review, the topic does deserve some explanation. Table 1 is a list of the control variables and their effects on algebraic expressions. Table 2 shows the effect of one control variable, NUMNUM, on expressions. (Most control variables behave similarly, with positive values causing an expansion of terms to take place and negative values causing a combination of terms to take place.)

Generating a muMATH-79 System

Because muMATH-79 can potentially use more than 64 K bytes of memory, the system is supplied as a series of packages that can be combined to create an optimal environment for a given purpose. Figure 2 shows a *dependency diagram* from the muMATH-79 packages as they are supplied. To run a given package, you must load that package and all the packages above it. For example, to manipulate algebraic and logarithmic expressions, you must load the file named MUSIMP79 (which loads MUSMORE automatically), ARITH, ALGEBRA, and LOG, in that order. To solve equations that use logarithmic expressions, you would add to the above the files EQN and SOLVE.

Of course you would like to have all the packages available at once. Unfortunately, due to the large size of the packages, this cannot be done. A 32 K-byte system is necessary to run anything in muMATH-79, but more memory is recommended. It takes 40 K bytes, for example, to run algebra and 48 K bytes to run either calculus or matrix algebra.

A muMATH-79 system is first generated and then saved for future loading into the same system. Each package takes 1 to 5 minutes to load, given a Z80 system running at 4 MHz; loading time will be proportional to the speed of the processor being used.

Another method of loading, called *condensation*, takes from 10 minutes to 1 hour per module to load, but it has the advantage of loading the same module in just over half as much memory. At BYTE Publications Inc, we are running a condensed system in 56 K bytes that includes all the muMATH-79 packages except TRACE, ARRAY, and MATRIX. It took an afternoon to set up the system, but the time was well spent, because all the packages interact with each other. However, problem solution time decreases with increased unused memory. Decreasing the number of packages used would probably cut the solution times of problems, but so far the delays encountered have been hardly objectionable.

The muSIMP-79 Language

An unexpected benefit of the muMATH-79 package is the inclusion of the muSIMP-79 language. muMATH-79 as supplied is actually a series of source files written in muSIMP-79. Inclusion of the source files allows you the very important

Control Variable	Result with Positive Value	Result with Negative Value
NUMNUM	$A(B+C) \to AB+AC$	$AB+AC \to A(B+C)$
DENDEN	$\frac{1}{A}\left(\frac{1}{B+C}\right) \to \frac{1}{AB+AC}$	$\frac{1}{AB+AC} \to \frac{1}{A}\left(\frac{1}{B+C}\right)$
DENNUM	$\frac{B+C}{A} \to \frac{B}{A}+\frac{C}{A}$	$\frac{B}{A}+\frac{C}{A} \to \frac{B+C}{A}$
NUMDEN	$\frac{A}{B+C} \to \frac{1}{\frac{B}{A}+\frac{C}{A}}$	$\frac{1}{\frac{B}{A}+\frac{C}{A}} \to \frac{A}{B+C}$
BASEXP	$A^{B+C} \to A^B A^C$	$A^B A^C \to A^{B+C}$
EXPBAS	$(AB)^C \to A^C B^C$	$A^C B^C \to (AB)^C$
PWREXPD	$(A+B)^2 \to A^2+2AB+B^2$	$(A+B)^{-2} \to \frac{1}{(A^2+2AB+B^2)}$
	$(A+B)^3 \to A^3+3A^2B+B^3$	$(A+B)^{-3} \to \frac{1}{A^3+3A^2B+3AB^2+B^3}$
	(etc)	(etc)

Table 1: *The effect of control variables on symbolic manipulation within muMATH-79. The values given to these control variables determine how muMATH-79 manipulates algebraic expressions. Other control variables not listed in this table are TRGSQ, TRGEXPD, LOGBAS, PBRCH, and LOGEXPD, which control trigonometric and logarithmic expressions.*

Value of NUMNUM	Transformation	Example
0	do nothing	3A(B + C)(D + E)→ 3A(B + C)(D + E)
2 and its multiples	distribute constants over sums	→ A(3B + 3C)(D + E)
3 and its multiples	distribute monomials over sums	→ 3(AB + AC)(D + E)
5 and its multiples	distribute sums over sums	→ 3A(D(B + C) + E(B + C))
6 (= 2•3)	distribute constants and monomials over sums	→ (3AB + 3AC)(D + E)
10 (= 2•5)	distribute constants and sums over sums	→ A(D(3B + 3C) + E(3B + 3C))
15 (= 3•5)	distribute monomials and sums over sums	→ 3(ABD + ABE + ACD + ACE)
30 (= 2•3•5)	distribute constants, monomials, and sums over sums	→ 3ABD + 3ABE + 3ACD + 3ACE
− 2, − 3, − 6	same as 2, 3, 6, only factor out instead of distribute	NUMNUM = − 3 causes 3AB + 3AC→ A(3B + 3C)

Table 2: *A detailed example of the effect of the control variable NUMNUM on algebraic expressions. NUMNUM is so named because it controls the distribution or factoring of a numerator expression with the numerator expression containing it. Positive values cause a factor to be distributed across a sum, while negative values cause factoring a common value from a sum.*

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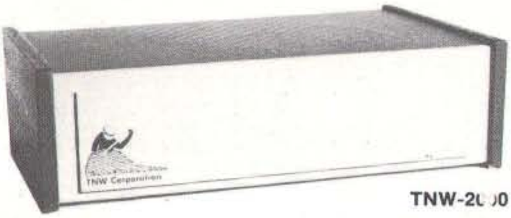

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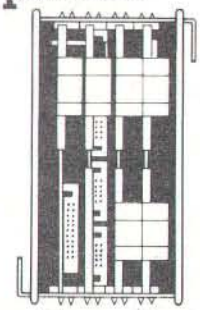

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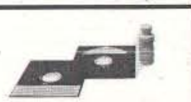
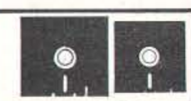
DISKETTE DRIVE head cleaning kits prevent head crashes and insure efficient error-free operation.	5¼" or 8" KIT INTRODUCTORY PRICE $19.50	
HARDHOLE reinforcing ring of tough mylar protects your disks from damage.	8" applicator .... $4.00 5¼" applicator .... $3.00 8" mylar hardholes (50) . $8.00 5¼" mylar hardholes (50) $6.00	

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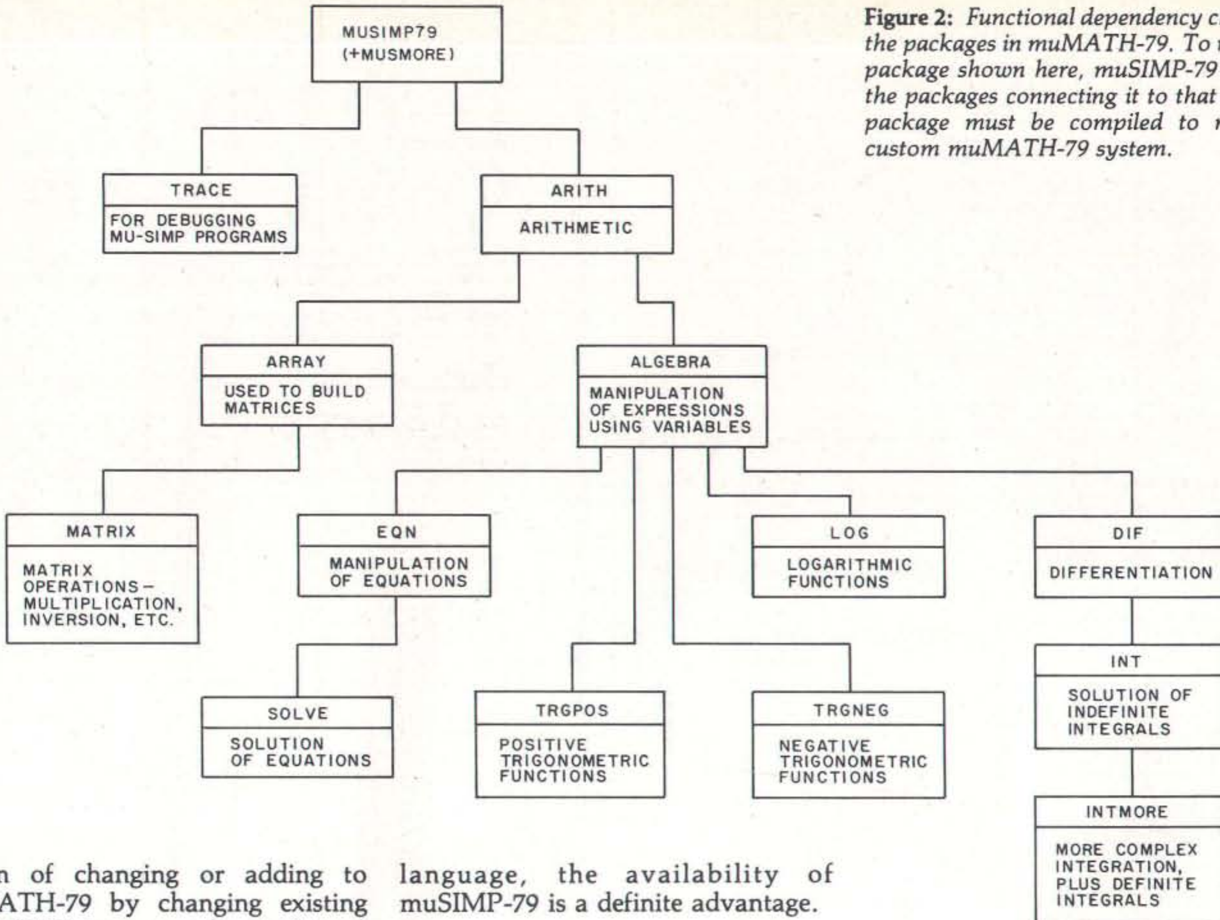

Figure 2: *Functional dependency chart for the packages in muMATH-79. To run any package shown here, muSIMP-79 and all the packages connecting it to that desired package must be compiled to make a custom muMATH-79 system.*

option of changing or adding to muMATH-79 by changing existing muSIMP-79 programs (ie: packages) or adding your own.

muSIMP-79 is a variation of the well-known list-processing language LISP; it has been adapted for readability and optimized for the manipulation of symbolic expressions. Considering that the entire capabilities of muMATH-79 are based on the use of the muSIMP-79 language, the availability of muSIMP-79 is a definite advantage.

Documentation

The muSIMP/muMATH-79 Symbolic Math System comes with all its associated files on floppy disk and its printed documentation in a three-ring binder. There are about 175 pages of printed documentation in the reference manual, with tabbed sections marked General Information, Calculator-Mode Lessons, Programming-Mode Lessons, muSIMP-79, Arithmetic, Algebra, Equation, Matrix, Log and Trig, and Calculus.

All of the sections consist of either documentation or source code for a particular package, both of which are

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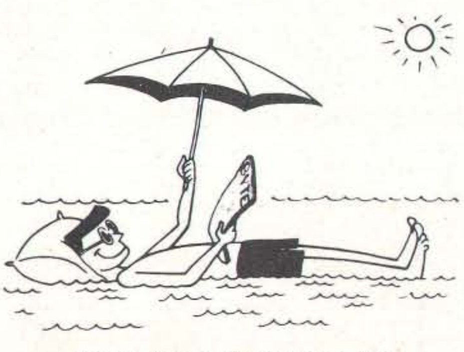

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available on the disk in machine-readable form. Included are sections on building, saving, and using a muMATH-79 *environment* (which is the muMATH-79 packages compiled plus all the variable and status assignments completed to date). In addition, ten files (five for each subject) that execute interactively on the host computer cover the topics of using muMATH-79 in what is called *calculator mode* and of programming in muSIMP-79.

The Soft Warehouse prints an occasional newsletter that contains updates, additions, and (very occasionally) corrections to its muSIMP/muMATH-79 and muLISP (another of its products) systems. The people at the Soft Warehouse have been friendly and informative every time I've called them.

At a Glance:	
Name of program	muSIMP/muMATH-79
Type of program	language/utility
Manufacturer	The Soft Warehouse POB 11174 Honolulu HI 96828 (808) 734-5801
Price	$290
Format	5-inch or 8-inch disk
Language used	8080 machine language
Computer needed	an 8080, 8085, or Z80-based computer running CP/M, CDOS, IMDOS, or TRSDOS operating systems
Documentation	175 pages, 8½ by 11 inches, in three-ring binder
Audience	high-school and college students, educators, programming language enthusiasts

muMATH-79 for the TRS-80

Microsoft Consumer Products of Bellevue, Washington (a sibling company to the Microsoft of Microsoft BASIC fame) is marketing two versions of muSIMP/muMATH-79 for the TRS-80. The first version, equivalent to the one described in this review, will sell for $250.

A slightly diminished version of the system will be available for $75—a very reasonable price. Although I have not seen it, the manufacturer informs us that the system will come with two floppy disks (one for 32 K-byte systems, one for 48 K-byte systems) and an abbreviated manual. The floppy disk for the 32 K-byte system will include muSIMP-79, a precompiled module including the arithmetic, algebra, and equation-solution packages, and uncompiled logarithmic and positive and negative trigonometric packages.

The floppy disk for the 48 K-byte TRS-80 system will be the same but will add the differentiation package and most of the integration packages in the compiled module. Both versions have extensions that allow muSIMP to access the TRS-80 graphics.

Conclusions

- The muSIMP/muMATH-79 Symbolic Math System is a very impressive tool. It fills a gap in the spectrum of problems solvable by a computer.
- Although it cannot work wonders, muSIMP/muMATH-79 *can* solve many of the problems encountered in algebra, trigonometry, and even calculus classes. (Educators need not fear: muMATH-79 does not provide a solution's derivation, only the final answer.)
- Educators from the high-school level up have used the package as an aid to teaching mathematics. And researchers have used it to keep track of equations during complex manipulations. Other potential users include: engineers demanding exact numeric solutions of problems and matrices (the fractional answers can be divided out conventionally to give decimal answers of any accuracy); researchers interested in artificial intelligence; college professors studying programming languages, and all those in need of a calculator.
- Although this is no fault of the package, muMATH-79 occasionally behaves in a way that, although correct, leads to unexpected and seemingly mysterious results. (I, for example, was unable to save a compiled package to disk drive B because I had assigned an algebraic value to the variable B.) Some sophistication on the part of the user is necessary in such cases.
- The documentation is good, but a thorough knowledge of the system is gained only by lots of practical experience. ■

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An 8088 Processor for the S-100 Bus

Part 3

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MON88 is a small system monitor for the single-board 8088-based processor described in parts 1 and 2 of this article (September and October 1980 BYTE, pages 43 and 62 respectively).

The current configuration of MON88 implements sixteen commands (expandable to twenty-six) and uses less than 1.5 K bytes of memory. This includes a "large" (approximately 256-byte) video driver required for my hardware environment and lengthy messages (about 128 bytes' worth) that make MON88 easy to use. No attempt was made to optimize the amount of memory used.

Stripping out the video-driver routine (that is, using a hardware *terminal*, rather than software, to create the same effect) and the messages, along with some optimization, can probably reduce code size to 1 K bytes. My plan is to expand the monitor until it fills the 2 K bytes of EPROM (erasable programmable read-only memory) in the 8755A-2 integrated circuit on the processor board. (See table 1 for a quick-reference guide to the MON88 instruction set.)

MON88 Philosophy

The 8088 incorporates very powerful, mainframe-like architectural features such as segmented memory, pipelining, multi- and co-processing "hooks," etc. One key objective of the 8088 project has been to implement the hardware and software in as simple a fashion as possible. This will allow users familiar with traditional 8-bit processors to ease into an understanding of this powerful new machine.

Following the philosophy of simplicity, my 8088 design embodies what is known as the "small model of computation." This model assumes that a given task can be implemented using one set of segmentation register values:

- one 64 K code segment
- one 64 K data segment
- one 64 K stack segment
- one 64 K extra segment

A key feature of the 8088 is that, for many instructions, certain memory segments are used to determine an absolute memory address. This allows instructions to be implemented in fewer bits, contributing to the extremely efficient use of memory in the 8088. This is not a restriction because the default segment can be overridden by using a *segment-prefix* for the instruction in question.

In fact, my decision was to initially use only sixteen of the twenty address lines available on the processor board. In this case, all segments (code, data, stack and extra) totally overlap in the 64 K-byte address space of the processor board. This means we need not concern ourselves with what segment is where, and what instructions assume which segments.

MON88 Organization

The organization of MON88 in memory is shown in figure 1. I will briefly discuss each section. Note that modifications to MON88 for your own environment are discussed later in this article. The following paragraphs describe each section of the monitor.

Storage allocation and constant definition: This section defines commonly used constants and specific I/O (input/output) port addresses, etc. In addition, memory allocation is performed for needed buffer and variable space.

User jump table: This is the first actual code in MON88 consisting of two MON88 entry points (INIT and START) and three I/O entry points (KEYIN, KEYSTAT and VIDOUT). A user program could terminate by jumping to one of the two MON88 entry points. Similarly, a user program could call one of the I/O entry points. When the I/O is done, the return instruction of each I/O routine will give control back to the user program.

Segment register and I/O initialization: The code, data, stack and extra segments (CS, DS, SS and ES) are set overlapping at address 0. Environment-dependent I/O initialization is also performed by this routine.

Main loop: This is the overall control routine for MON88. It prints the prompt character and accepts a one-letter command from the console. The appropriate command-routine address is determined and control is transferred from this routine.

Message storage: Messages used by various commands are stored here. Note that each message is terminated by a 0.

Command jump table: The addresses for the twenty-six possible commands are stored here. Note tha

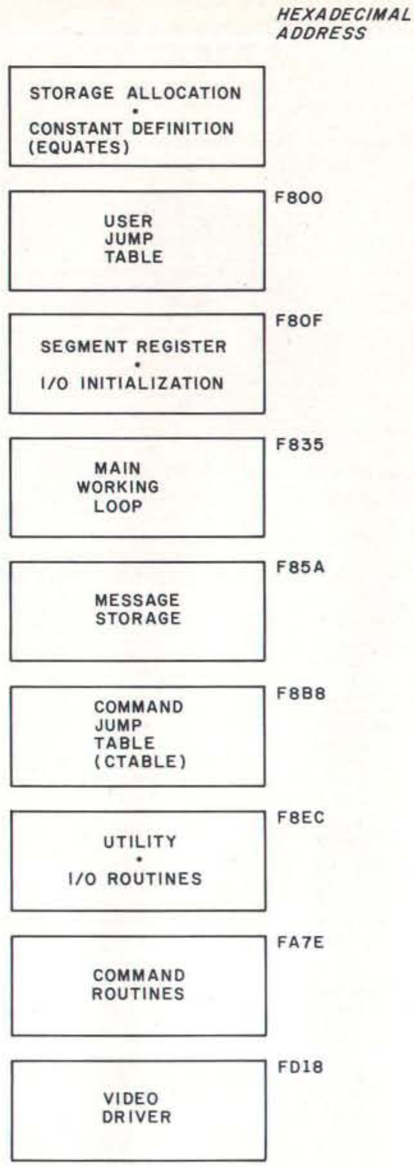


Figure 1: *Memory map for the MON88 monitor.*

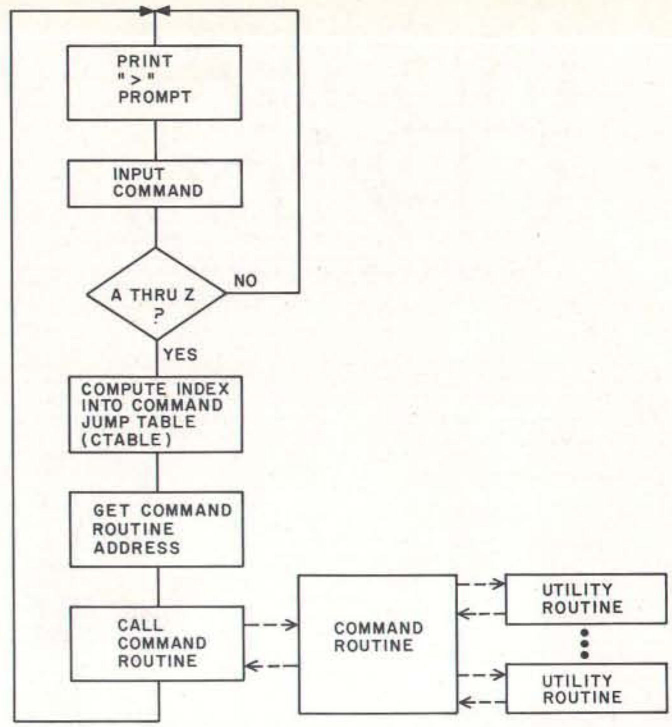


Figure 2: *High-level flowchart for MON88 program. In general, the program decodes user input and, if valid, jumps to the appropriate command subroutine. Once the routine is finished, control is passed back to the command-input routine, and the program prints another prompt.*

unimplemented commands are given the ERR (error) address.

Utility and I/O routines: This and the following (command routines) section make up the bulk of MON88. The utility routines are used by command routines. This allows command routines to be implemented largely as calls to various utility routines (see figure 2). For instance, many commands require the acquisition of a starting and ending address. The utility routine SETUP performs this function. Many of these utility routines may be useful in your own programming efforts.

Command routines: These are the routines that actually perform each command. Due to the extensive use of the above utility routines, most commands are easily implemented as a series of subroutines. A good example is the W (CWRITE) cassette-write command, which dumps a block of memory to tape (see listing 1, starting at line 576). Note that of the twelve "instructions" constituting the command, eight are calls to other routines.

The advantage of programming in this manner is that the command routines are easy to write. Should you want to add commands, they can probably be implemented largely as a series of calls to already-existing, tested utility routines in MON88. This also saves memory space by eliminating redundant coding of essentially the same routine.

Video driver: My hardware requires a relatively lengthy software driver for the video board in my system. I converted this code from 8080 assembly language using Intel's CONV86 code converter. Briefly, the tradeoff is between the performance of the converted code versus a version rewritten for the 8088 and the associated time required for each process. Converted code may be somewhat larger than a rewritten version, but it will probably take only a small fraction of the time to implement as compared to a rewrite. Because the 8088 has a faster clock rate than the 8080, the converted program, even if larger, will probably run faster than the original 8080 version.

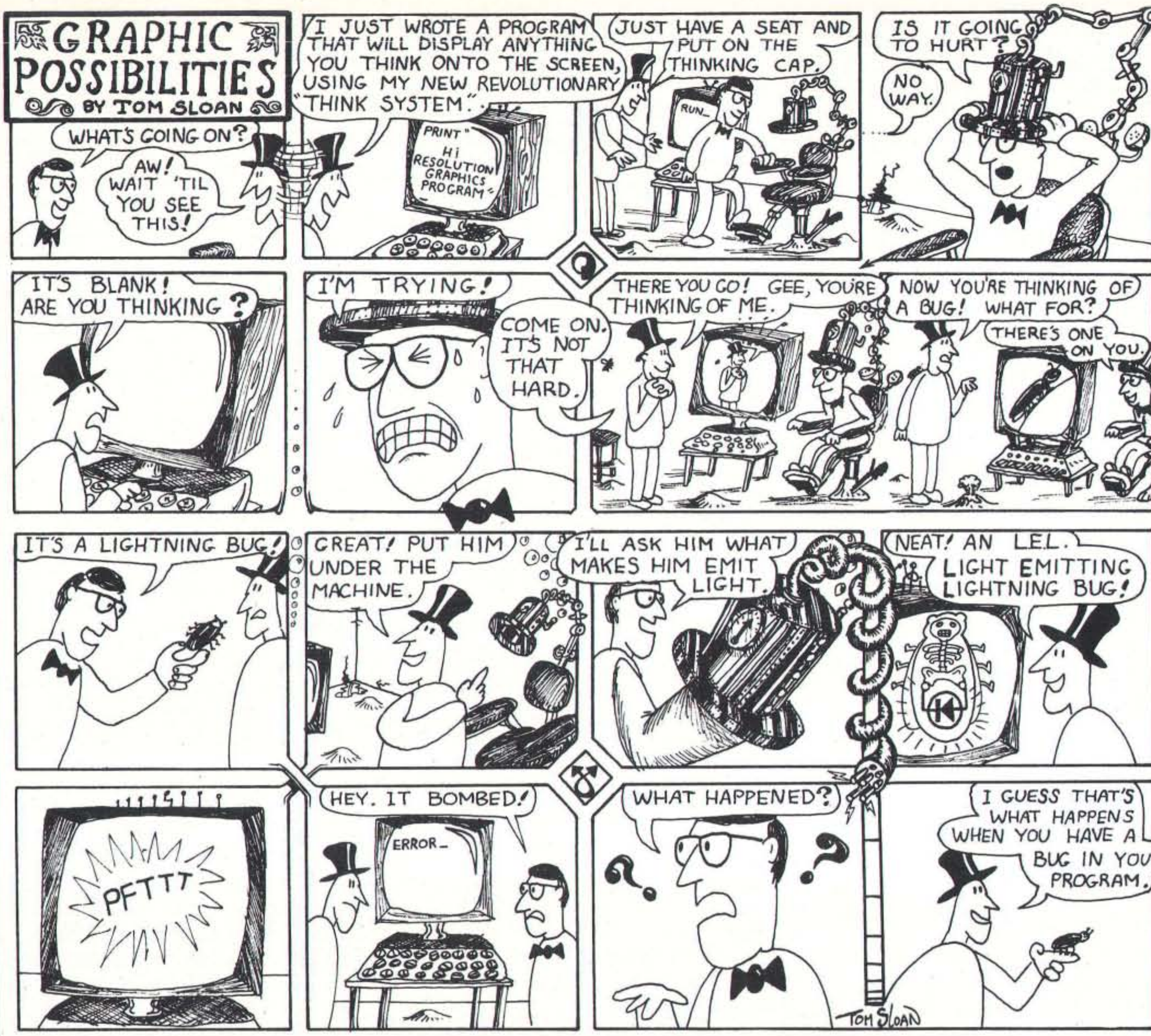
Environment Dependence

The dependence of MON88 on a certain I/O or memory environment has been minimized. The following summarizes the changes you will need to make to adapt MON88 to your own system. Refer to listing 1, starting at line 14.

Location of MON88: The statement immediately preceding the EQUATES FOLLOW section sets MON88's origin. For my processor board, the origin is hexadecimal F800:

```
ORG     F800H
```

Text continued on page 346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PRINT " Hi RESOLUTION GRAPHICS PROGRAM "
JUST HAVE A SEAT AND PUT ON THE THINKING CAP.
RUN
IS IT GOING TO HURT?
NO WAY.
IT'S BLANK! ARE YOU THINKING?
I'M TRYING!
COME ON. IT'S NOT THAT HARD.
THERE YOU GO! GEE, YOU'RE THINKING OF ME.
NOW YOU'RE THINKING OF A BUG! WHAT FOR?
THERE'S ONE ON YOU.
IT'S A LIGHTNING BUG!
GREAT! PUT HIM UNDER THE MACHINE.
I'LL ASK HIM WHAT MAKES HIM EMIT LIGHT.
NEAT! AN L.E.L. LIGHT EMITTING LIGHTNING BUG!
PFTTT
HEY. IT BOMBED!
ERROR
WHAT HAPPENED?
I GUESS THAT'S WHAT HAPPENS WHEN YOU HAVE A BUG IN YOUR PROGRAM.
TOM SLOAN

Command Summary

Command syntax definitions:

[addr] = 16-bit address (or data) as four hexadecimal digits
[data] = 8-bit data as two hexadecimal digits
[cr] = carriage return

Note that [addr] and [data] entry routines accept the last four and two digits entered, respectively. For example, using the fill (F) command:

F0123456 789ABCD 0123456[cr]

is the same as

F3456 ABCD 56[cr]

Also note that [addr], [data] entries to commands can be separated by a blank or a comma, ie:

F3456 ABCD 56[cr]

is the same as

F3456,ABCD,56[cr]

Invalid hexadecimal digits and unimplemented commands always result in an error response. MON88 responds to errors by printing an asterisk (*), carriage return/line feed sequence and redisplaying the prompt.

All entries to MON88 may be either upper or lowercase.

Most commands can be halted temporarily with Control-S, restarted with Control-Q, and aborted with Control-C.

In the following examples, all user input to MON88 is underlined.

Commands

A — Enter ASCII Text into Memory

Allows the direct entry of ASCII text from the keyboard into memory. The command is terminated with a Control-D [ctl-D]. At termination, the address *following* the last character entered is displayed:

```
A[addr][cr]
A100[cr]
This is a test of the 'A' command.[ctl-D]
@0122
D100 121[cr]
0100  54 48 49 53 20 49 53 20 41 20 54 45 53 54 20 4F
0110  46 20 54 48 45 20 27 41 27 20 43 4F 4D 40 41 4E
0120  44 2E
```

B — Not Implemented

C — Compare Cassette Input With Memory

Compares cassette input with the contents of memory on a byte-by-byte basis. All tape-read operations display the length of the file being read when the header is found. In this case the length is hexadecimal 200 bytes. A heading line is displayed, and if a comparison fails, the address and differing inputs are displayed:

```
C[addr][cr]
C100[cr]
ADDR M T  DIFF   LENGTH (HEXADECIMAL)=0200
0102 77   76       00000001
```

In this example, the data coming from tape matched the data located starting at hexadecimal address 100 except for address 102, where a 1-bit error was encountered.

D — Dump or Display the Contents of Memory

Displays the contents of memory from [addr1] to [addr2] as sixteen hexadecimal values per line:

```
D[addr1] [addr2][cr]
D0 20[cr]
0000  01 33 43 56 A3 D8 90 90 34 88 AC EE F0 99 5F 70
0010  86 45 10 3E D4 BB CD EE 42 4E 53 96 9F 88 53 40
0020  74
```

E — Enter Hexadecimal Data From the Keyboard into Memory

After you enter the E command and an address, MON88 will display the current contents of that memory address followed by a "-". The value at that address can be changed by entering a new value. Once a new value has been entered, or if no change to the contents is required, a space is entered. MON88 will then display the contents of the next location followed by a "-". The E command is terminated with a carriage return:

```
E[addr][cr]
D100  104[cr]
0100  01  02  03  05  06
E100[cr]
0100  01- 02  02- 03  03- 04  05-__  06-[cr]
D100  104[cr]
0100  02  03  04  05  06
```

F — Fill a Memory Block With a Constant

Fills a block of memory from [addr1] to [addr2] with a constant value:

```
F[addr1] [addr2] [data][cr]
F100  104  20[cr]
D100  104[cr]
0100  20  20  20  20  20
```

G — Go To and Execute a User Program

MON88 will vector to and begin executing a program in memory. Note that if the user program does not modify the contents of the segment registers, a return instruction at the end of the program will transfer control to MON88. For this example, note that hexadecimal address F800 is the start address of MON88:

G[addr][cr]
GF800[cr]
(screen clears)
8088 Monitor [rev 0]

H — Compute the Sum and Difference of the 16-Bit Hexadecimal Values
MON88 will compute and display the sum and difference of two 16-bit arguments:

H[addr1] [addr2][cr]
H2000 1010[cr]
SUM DIFF
3010 0FF0

I — Input a Byte From an I/O Port
MON88 will read a byte from an I/O port and display the hexadecimal and binary values. Note that an 8- or 16-bit I/O port address may be specified. If boards in your system decode the upper (A8 thru A15) address lines, use a 16-bit I/O address:

I[addr][cr]

To input from I/O port hexadecimal 20 in the case that *no* I/O boards decode the upper eight address lines:

I20[cr]
23 00100011

To input from I/O port hexadecimal 20 in the case that any I/O boards decode the upper eight address lines for their 8-bit I/O port address:

I2020[cr]
23 00100011

J — Not Implemented

K — Toggle Keyboard Upper/Lower Case
For keyboards with only a "shift lock," the K command will result in teletypewriter-like uppercase capability. In this mode, the letters A thru Z will be automatically shifted to uppercase, while all other keys (ie: the numbers 0 thru 9, etc) will not shift:

K[cr]

L — Not Implemented

M — Move a Block of Memory
This command moves the block of memory between [addr1] and [addr2] (inclusive) to [addr3]. Forward or backward moves are acceptable. Overlapping moves can of course have strange results:

M[addr1] [addr2] [addr3][cr]
D0 F[cr]
0000 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 10
M0 35 [cr]
D0 F [cr]
0000 01 02 03 04 05 01 02 03 04 0A 0B 0C 0D 0E 0F 10

N — Nondestructive Memory Test
A block of memory may be nondestructively tested using a read-complement-write-read-recomplement-compare-write algorithm. This provides a quick check for easily detected failures. Failing bits will be noted in hexadecimal and binary along with the failing address. The memory block will be repeatedly tested until a Control-C is entered:

N[addr1] [addr2][cr]
N0 2000[cr]
12FF 02 00000010
12FF 02 00000010
12FF 02 00000010
.
.
[Control-C]

In this case, location hexadecimal 12FF has a bad bit (D1 on a scale of D0 to D7)

O — Output to a Port
This command outputs a byte to an I/O port. As in the Input (I) command, 8- or 16-bit I/O port addresses can be used. The same rule for dealing with S-100 I/O devices that decode their 8-bit I/O address on the upper eight address lines is used:

O[addr] [data][cr]
O2020 FE[cr]

This outputs hexadecimal FE to port hexadecimal 20 (old S-100) or port hexadecimal 2020 (new S-100)

P — Write Continuous Sync Stream to Cassette
A continuous stream of Tarbell format "sync" characters (hexadecimal E6) will be written to tape. The P command is terminated by pressing any key on the keyboard:

P[cr]

Q — Not Implemented

R — Read from Cassette
A file can be read from tape into memory, starting at [addr]. The length of the file is contained in the file header, so no length or ending address input to the R command is required. When MON88 finds the tape header, the file length will be printed on the console, informing the user that loading has been initiated. In this example, the file length is hexadecimal 200 bytes:

R[addr][cr]
R100[cr]
LENGTH (HEXADECIMAL) = 0200

S, T, U — Not Implemented

V — Verify the Equality of Two Blocks of Memory

The block of memory from [addr1] to [addr2] will be compared with the block starting at [addr3]. Differences will be noted in hexadecimal and binary:

V[addr1] [addr2] [addr3][cr]
V20 3F 100[cr]

SRC	M	DEST	M	DIFF
0022	10	0122	11	00000001
0030	3E	0130	3F	00000001

In this case, the hexadecimal 20 bytes from hexadecimal addresses 20 to 3F are equal to those at address 100 except for two locations: hexadecimal locations 22 and 122 differ, as do locations 30 and 130.

W — Write to Cassette

The block of memory from [addr1] to [addr2] will be written to tape. MON88 will calculate the length of the block, display it, and write it to the tape header for use by the Read ("R") and Compare ("C") commands:

W[addr1] [addr2][cr]
W100 1FF[cr]
LENGTH (HEXADECIMAL) = 100

The block of memory from hexadecimal 100 to 1FF is written to tape.

X, Y, Z — Not Implemented

Command	Use
A	Enter ASCII text into memory.
B	Not implemented
C	Compare cassette input with memory.
D	Display memory.
E	Enter hexadecimal data into memory.
F	Fill memory with a constant.
G	Go To and execute user program.
H	Hexadecimal math.
I	Input from an I/O port.
J	Not implemented.
K	Toggle keyboard upper/lowercase.
L	Not implemented.
M	Move memory.
N	Nondestructive memory test.
O	Output to an I/O port.
P	Put a continuous 'sync' stream to tape.
Q	Not implemented.
R	Read a file from cassette.
S,T,U	Not implemented.
V	Verify equality of two memory blocks.
W	Write a file to cassette.
X,Y,Z	Not implemented.

Table 1: *A quick reference guide to MON88 commands. Note that only sixteen of the possible twenty-six commands are implemented. While a stripped version of the present monitor can reside in 1 K bytes of memory, there is provision on the processor board for 2 K bytes of EPROM.*

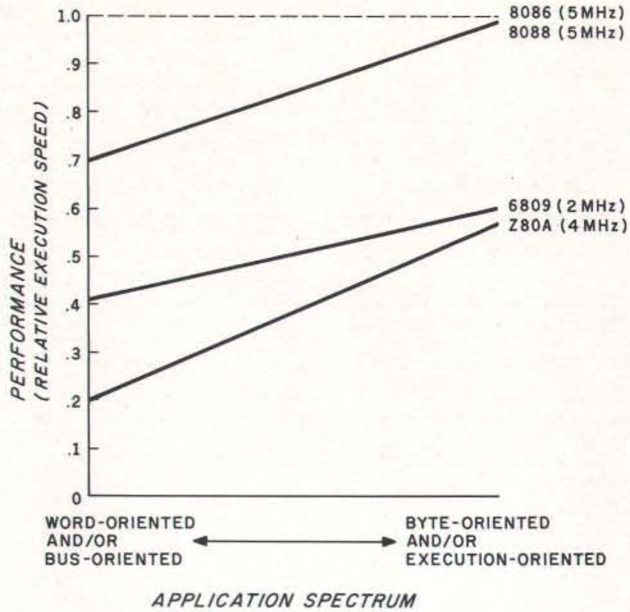

Figure 3: *Relative performance of several 8- and 16-bit microprocessors. The types of programs a processor can run are divided into two groups: those that primarily move data around (word- or bus-oriented) and those that primarily manipulate byte-oriented data or perform many numeric operations. If the 16-bit 8086 microprocessor (dotted line) is defined as a performance figure of 1.0, the other three lines show the approximate relative performance of the three other microprocessors as influenced by the type of program being run.*

Text continued from page 342:

Scratchpad Allocation: My video-board driver uses an 80-byte buffer and a 2-byte X,Y cursor-position variable. These, of course, can be removed or replaced according to your needs. Currently this storage is allocated in the processor boards, 1 K bytes of programmable memory in the (8185-2) device.

The only scratchpad memory required by MON88 is a 1-byte uppercase/lowercase flag variable. This is used by the K (keyboard toggle) command to allow emulation of uppercase-only peripherals in which letters are shifted, but numbers and special characters are not.

If you are not using the processor board described last month and don't have a dedicated scratchpad in the system, UCFLAG can be allocated at the top of memory:

```
UCFLAG    EQU TOPMEM
```

where TOPMEM is the address of the top of memory.

Stack: My stack also resides on the scratchpad memory within the processor board. If you do not have scratchpad, allocate the stack 1 byte below the top of your memory (to leave room for UCFLAG). Note that the stack pointer is decremented before a PUSH operation is performed. Therefore, to allocate the stack 1 byte below the top of memory, set the stack pointer equal to the top of memory:

```
UCFLAG    EQU TOPMEM
STACKP    EQU TOPMEM
```

Listing 1: *Assembly listing of MON88. The flowchart in figure 2 outlines the general operation of the program.*

```
MCS-86 MACRO ASSEMBLER    VID88


ISIS-II MCS-86 MACRO ASSEMBLER V2.0 ASSEMBLY OF MODULE VID88
OBJECT MODULE PLACED IN :F0:VID88.OBJ
ASSEMBLER INVOKED BY:  ASM86 VID88.A86

LOC  OBJ                LINE     SOURCE

                           1     ;       ******************************************************
                           2     ;       *                                                    *
                           3     ;       *                   M O N 8 8                        *
                           4     ;       *                                                    *
                           5     ;       * A video oriented system monitor for the INTEL 8088 *
                           6     ;       *          written Q1 1980 - revision 0              *
                           7     ;       *          by.. Thomas Woodward Cantrell             *
                           8     ;       *                                                    *
                           9     ;       ******************************************************
                          10     ;
                          11             ASSUME  DS:ABS_0,CS:ABS_0,ES:ABS_0
----                      12     ABS_0   SEGMENT BYTE AT 0
0000                      13     M       LABEL   BYTE
F800                      14             ORG     0F800H
                          15     ;
                          16     ;       ******************************************************
                          17     ;       *                                                    *
                          18     ;       *                EQUATES FOLLOW                      *
                          19     ;       *                                                    *
                          20     ;       ******************************************************
                          21     ;
  F400                    22     VIDBUF  EQU     0F400H                  ;video buffer
  F450                    23     XY      EQU     VIDBUF+80               ;holder for cursor position
  F452                    24     UCFLAG  EQU     XY+2                    ;upper/lower case flag
  000C                    25     FF      EQU     0CH                     ;form feed (clear screen)
  000A                    26     LF      EQU     0AH                     ;line feed
  000D                    27     CR      EQU     0DH                     ;carriage return
  0008                    28     BS      EQU     08H                     ;backspace
```

Listing 1 continued on page 348

where TOPMEM is the address of the top of memory.

Initialization: I/O initialization is done in the INIT section of the monitor (see listing 1, starting at line 76). Starting at hexadecimal F81D, I initialize the Tarbell cassette interface and TDL Video Interface. Replace the section of code from hexadecimal F81D to F828 to suit your I/O needs.

I/O Drivers

MON88 currently uses the following environment-dependent I/O routines (their hexadecimal addresses are given in parentheses):

- KEYIN (F90F)—Reads a byte from the console keyboard, strips off the parity bit, and returns the character in the AL accumulator.
- KEYSTAT (F922)—Reads the console keyboard's status and returns AL=0 if a key has not been pressed and AL = hexadecimal FF if a key has been pressed.
- CIN (F955)—Reads a byte from a mass-storage device (Tarbell cassette, in my case) and returns the byte in the AL accumulator.
- COUT (F964)—Writes the byte contained in the AL accumulator to the mass-storage device.
- CSTART (FB60)—Sets up the mass-storage device for a write operation. For the Tarbell interface, a start byte and a sync byte are required. Replace this code as necessary for your device.
- READINIT (FB9D)—Sets up the mass-storage device for a read operation. Replace the relevant code as necessary.
- PUTSYNC (FBBF)—Outputs a stream of sync bytes to my cassette. This allows calibrating the interface. If your device has a similar feature, modify the PUTSYNC routine accordingly. If not needed, the whole P (PUTSYNC) command can be removed.
- VIDOUT (FCDA)—This routine outputs the character in the AL accumulator to the console display device. In my case, I converted an 8080 version of the video driver to 8088 code using Intel's CONV86 program. Using the code converter, it took only an hour or so to get the driver up and running. I will rewrite it as necessary to reduce the amount of memory used by MON88.

Adding or Removing Commands

All commands are referenced through CTABLE (Command Jump Table) located at hexadecimal F8B8. Note that the commands are arranged in alphabetical order, A thru Z. To remove a command, simply replace its reference in CTABLE with ERR. For example, to remove the K command (uppercase/lowercase toggle), change:

```
F8CC    DW    KTOGGLE
```

to

```
F8CC    DW    ERR
```

then remove the KTOGGLE code (hexadecimal FCD1 to FCD9).

Similarly, to add a special memory test (for example) and call it using the letter T, first write the code (for example, starting label TESTMEM) for the command,

Text continued on page 360

Listing 1 continued:

```
E2E2                  KSTAT   EQU   0E2E2H            ;keyboard status port
E3E3                  KDATA   EQU   0E3E3H            ;keyboard data port
6E6E                  CSTAT   EQU   6E6EH             ;Tarbell status port
6F6F                  CDATA   EQU   6F6FH             ;Tarbell data port
F7FF                  STACKP  EQU   0F7FFH            ;Stack address
0003                  CTLC    EQU   03H               ;ascii ctl-c
0004                  CTLD    EQU   04H               ;ascii ctl-d
0013                  CTLS    EQU   13H               ;ascii ctl-s
0011                  CTLQ    EQU   11H               ;ascii ctl-q
0000                  FALSE   EQU   0
00FF                  TRUE    EQU   0FFH
                +1    $EJECT

MCS-86 MACRO ASSEMBLER    VID88


LOC  OBJ              LINE    SOURCE

                      ;
                      ;     ******************************************************
                      ;     *                                                    *
                      ;     *                    JUMP  TABLE                     *
                      ;     *                                                    *
                      ;     ******************************************************
                      ;
                      ;
F800 EB0D90                 JMP    INIT     ;RESETS STACK, SEGMENT REGISTERS, CASSETTE INTERFACE
                      ;                     ;ALSO PRINTS SIGN-ON MESSAGE
                      ;
F803 EB3090                 JMP    START    ; 'WARM START'- REGISTERS NOT INITIALIZED
                      ;
F806 E91901                 JMP    KEYSTAT  ;RETURNS [AL]=0 IF NO KEYPRESS PENDING. ELSE [AL]=OFFH
                      ;
F809 E9E000                 JMP    CONIN    ;WAITS FOR KEYPRESS. RETURNS [AL]=CHAR AND PRINTS IT.
                      ;
F80C E9CB04                 JMP    VIDOUT   ;PRINTS CHAR IN AL ON CONSOLE
                      ;
                      ;
                      ;     ******************************************************
                      ;     *                                                    *
                      ;     *           I N I T I A L I Z A T I O N              *
                      ;     *                                                    *
                      ;     ******************************************************
                      ;
                      ;
F80F FC               INIT:  CLD                              ;direction flag points 'up'
F810 FA                      CLI                              ;disable interrupts
F811 8CC8                    MOV   AX,CS                      ;initialize
F813 8ED8                    MOV   DS,AX                      ;  segment
F815 8EC0                    MOV   ES,AX                      ;  registers
F817 8ED0                    MOV   SS,AX                      ;  and set
F819 BCFFF7                  MOV   SP,STACKP                  ; stack pointer
F81C FB                      STI                              ;enable interrupts
F81D B010                    MOV   AL,10H                     ;Reset Cassette
F81F BA6E6E                  MOV   DX,CSTAT
F822 EE                      OUT   DX,AL                      ;  Interface
F823 BAE0E0                  MOV   DX,0E0E0H                  ;Reset Video
F826 B088                    MOV   AL,88H                     ; Interface
F828 EE                      OUT   DX,AL                      ;Inverse video w/cursor
F829 C60652F400              MOV   BYTE PTR M[UCFLAG],0       ;0=lower case, FFH=U/C only

F82E BE5AF890                MOV   SI,OFFSET SIGNON           ;get sign on message
F832 E86301                  CALL  PRINTMESS                  ;and print it
                +1    $EJECT

MCS-86 MACRO ASSEMBLER    VID88


LOC  OBJ              LINE    SOURCE

                      ;
                      ;     ******************************************************
                      ;     *                                                    *
                      ;     *             W O R K I N G   L O O P                *
                      ;     *                                                    *
                      ;     ******************************************************
                      ;
F835 E83D01           START:  CALL  CRLF                      ;print CRLF
F838 B03E                     MOV   AL,'>'                    ;and prompt
F83A E89D04                   CALL  VIDOUT
                      ;
F83D                  MAINLOOP:
F83D B400                     MOV   AH,0                      ;clear AH
F83F E8AA00                   CALL  CONIN                     ;get a command
F842 3C41                     CMP   AL,'A'                    ;check range for
F844 72EF                     JB    START                     ;      A
F846 3C5A                     CMP   AL,'Z'                    ;    thru
F848 7FEB                     JG    START                     ;      Z
F84A 2C41                     SUB   AL,'A'                    ;calculate offset
```

```
F84C DOEO                     SHL     AL,1                  ;and multiply by 2
F84E 05B8F890                 ADD     AX,OFFSET CTABLE
F852 8BD8                     MOV     BX,AX
F854 8B07                     MOV     AX,WORD PTR M[BX]
F856 FFDO                     CALL    AX                    ;go do it
F858 EBDB                     JMP     START                 ;start over
                    +1 $EJECT
```

```
MCS-86 MACRO ASSEMBLER    VID88


LOC  OBJ                LINE    SOURCE

                                ;
                                ;      ******************************************
                                ;      *                                        *
                                ;      *          M E S S A G E S               *
                                ;      *                                        *
                                ;      ******************************************
                                ;
F85A 0C                         SIGNON  DB   OCH
F85B 38303838204D6F                     DB   '8088 Monitor <rev. 0>'
     6E69746F72203C
     7265762E20303E
F870 00                         DBYTE   DB   0                      ;dummy byte
                                ;
F871 41444452204D20             COMHEAD DB   'ADDR M  T    DIFF
     20542020202044
     494646202020
F885 00                                 DB   0
                                ;
F886 53554D20204449             MHEAD   DB   'SUM  DIFF'
     4646
F88F 00                                 DB   0
                                ;
F890 53524320204D20             VHEAD   DB   'SRC  M   DEST M    DIFF'
     20204445535420
     4D202020204449
     4646
F8A7 00                                 DB   0
                                ;
F8A8 4C454E47544820             CHEAD   DB   'LENGTH (HEX) = '
     2848455829203D
     20
F8B7 00                                 DB   0
                                ;
                          +1    $EJECT
```

```
MCS-86 MACRO ASSEMBLER    VID88


LOC  OBJ                LINE    SOURCE

                                ;
                                ;      **************************************************
                                ;      *                                                *
                                ;      *       C O M M A N D   J U M P   T A B L E      *
                                ;      *                                                *
                                ;      **************************************************
                                ;
F8B8 B0FC                       CTABLE  DW   AENTER  ;ENTER ASCII TEXT INTO MEMORY
F8BA A7F9                               DW   ERR     ;B
F8BC D2FB                               DW   COMPARE ;COMPARE CASSETTE INPUT WITH MEMORY
F8BE 00FB                               DW   DUMP    ;DISPLAY MEMORY
F8C0 78FC                               DW   ESUBST  ;ENTER HEX DATA INTO MEMORY
F8C2 7EFA                               DW   FILL    ;FILL MEMORY WITH A CONSTANT
F8C4 4AFB                               DW   GOTO    ;GO TO & EXECUTE A USER PROGRAM
F8C6 1AFC                               DW   HEXMATH ;COMPUTE SUM AND DIFFERENCE OF HEX #'S
F8C8 2CFB                               DW   INPUT   ;INPUT FROM A PORT
F8CA A7F9                               DW   ERR     ;J
F8CC D1FC                               DW   KTOGGLE ;TOGGLE KEYBOARD UPPER/LOWER CASE FLAG
F8CE A7F9                               DW   ERR     ;L
F8D0 E7FA                               DW   MOVE    ;MOVE MEMORY
F8D2 3BFC                               DW   NTEST   ;NON DESTRUCTIVE MEMORY TEST
F8D4 3FFB                               DW   OUTPUT  ;OUTPUT TO A PORT
F8D6 BFFB                               DW   PUTSYNC ;OUTPUT CONTINUOUS SYNC STREAM TO CASSETTE
F8D8 A7F9                               DW   ERR     ;Q
F8DA 82FB                               DW   READ    ;READ FROM CASSETTE
F8DC A7F9                               DW   ERR     ;S
F8DE A7F9                               DW   ERR     ;T
F8E0 A7F9                               DW   ERR     ;U
F8E2 8DFA                               DW   VERIFY  ;VERIFY EQUALITY OF TWO MEMORY BLOCKS
F8E4 4FFB                               DW   CWRITE  ;WRITE TO CASSETTE
F8E6 A7F9                               DW   ERR     ;X
F8E8 A7F9                               DW   ERR     ;Y
F8EA A7F9                               DW   ERR     ;Z
                          +1    $EJECT
```

Listing 1 continued on page 350

Listing 1 continued:

```
MCS-86 MACRO ASSEMBLER     VID88


LOC  OBJ                   LINE     SOURCE

                                    ;
                                    ;
                                    ;          ****************************************************
                                    ;          *                                                  *
                                    ;          *        U T I L I T Y   R O U T I N E S           *
                                    ;          *                      and                         *
                                    ;          *     I / O   D E V I C E   H A N D L E R S        *
                                    ;          *             (except  video  driver)              *
                                    ;          *                                                  *
                                    ;          ****************************************************
                                    ;
                                    ;
F8EC E82000                         CONIN:   CALL    KEYIN              ;get a keyboard character
F8EF 50                                      PUSH    AX
F8F0 A052F4                                  MOV     AL,BYTE PTR M[UCFLAG]   ;check for case conversion
F8F3 0AC0                                    OR      AL,AL              ;0?
F8F5 7405                                    JZ      CONNEXT            ;YES..no conversion
F8F7 58                                      POP     AX                 ;restore character
F8F8 E80900                                  CALL    UCCHEK             ;convert to UC
F8FB 50                                      PUSH    AX
F8FC 58                             CONNEXT: POP     AX
F8FD E8DA03                                  CALL    VIDOUT             ;and echo it on console
F900 E80100                                  CALL    UCCHEK             ;always return UC
F903 C3                             KQUIT:   RET
                                    ;
F904 3C61                           UCCHEK:  CMP     AL,'a'
F906 7206                                    JC      UQUIT
F908 3C7B                                    CMP     AL,'z'+1
F90A 7302                                    JNC     UQUIT
F90C 245F                                    AND     AL,5FH
F90E C3                             UQUIT:   RET
                                    ;
F90F 52                             KEYIN:   PUSH    DX                 ;keyboard device handler
F910 BAE2E2                                  MOV     DX,KSTAT
F913 EC                             KEYLOOP: IN      AL,DX              ;check for keypress
F914 2480                                    AND     AL,80H
F916 74FB                                    JZ      KEYLOOP            ;no keypress..then wait for one
F918 5A                                      POP     DX
F919 52                             KIN:     PUSH    DX
F91A BAE3E3                                  MOV     DX,KDATA
F91D EC                                      IN      AL,DX              ;else get the character
F91E 247F                                    AND     AL,7FH             ;and strip parity
F920 5A                                      POP     DX
F921 C3                                      RET
                                    ;
F922                                KEYSTAT:                            ;RETURN [AL]=0 IF NO KEYPRESS ELSE [AL]=0FFH
F922 B400                                    MOV     AH,FALSE           ;prepare for false
F924 52                                      PUSH    DX
F925 BAE2E2                                  MOV     DX,KSTAT
F928 EC                                      IN      AL,DX
F929 2480                                    AND     AL,80H
F92B 7402                                    JZ      KEXIT              ;return it if no keypress
F92D F6D4                                    NOT     AH                 ;otherwise make it TRUE
F92F 8AC4                           KEXIT:   MOV     AL,AH


MCS-86 MACRO ASSEMBLER     VID88


LOC  OBJ                   LINE     SOURCE

F931 5A                                      POP     DX
F932 C3                                      RET
                                    ;
F933                                CTLCHEK:                            ;CHECK FOR CTL-S, CTL-Q AND CTL-C
F933 50                                      PUSH    AX
F934 E8EBFF                                  CALL    KEYSTAT            ;keypress?
F937 3C00                                    CMP     AL,0
F939 741B                                    JZ      CTLEXIT            ;no keypress so return
F93B E8DBFF                                  CALL    KIN                ;if keypress then get the data
F93E 3C13                                    CMP     AL,CTLS            ;check for ctl-s
F940 750D                                    JNZ     CTLCCHEK           ;if not look for ctl-c
F942 E8CAFF                         KWAIT:   CALL    KEYIN              ;if ctl-s then wait for another keypress
F945 3C11                                    CMP     AL,CTLQ            ;is it ctl-q
F947 740A                                    JZ      CTLEXIT            ;YES..return
F949 3C03                                    CMP     AL,CTLC            ;abort?
F94B 745A                                    JE      ERR                ;YES
F94D EBF3                                    JMP     KWAIT              ;otherwise wait some more
F94F                                CTLCCHEK:
F94F 3C03                                    CMP     AL,CTLC            ;is it ctl-c
F951 7454                                    JZ      ERR                ;YES..ABORT!
F953                                CTLEXIT:
```

```
F953 58                    POP     AX
F954 C3                    RET
                   ;
F955               CIN:                          ;GET BYTE FROM CASSETTE
F955 52                    PUSH    DX
F956 BA6E6E                MOV     DX,CSTAT
F959               CINLOOP:
F959 EC                    IN      AL,DX
F95A 2410                  AND     AL,10H        ;cassette ready to read?
F95C 75FB                  JNZ     CINLOOP       ;NO..wait
F95E BA6F6F                MOV     DX,CDATA      ;YES..
F961 EC                    IN      AL,DX         ;get the data
F962 5A                    POP     DX
F963 C3                    RET
                   ;
F964               COUT:                         ;WRITE A BYTE TO CASSETTE
F964 52                    PUSH    DX
F965 50                    PUSH    AX
F966 BA6E6E                MOV     DX,CSTAT
F969               COUTLOOP:
F969 EC                    IN      AL,DX
F96A 2420                  AND     AL,20H        ;cassette ready for write?
F96C 75FB                  JNZ     COUTLOOP      ;NO..wait
F96E 58                    POP     AX            ;get char back
F96F BA6F6F                MOV     DX,CDATA
F972 EE                    OUT     DX,AL         ;and send to tape
F973 5A                    POP     DX
F974 C3                    RET
                   ;
F975 50            CRLF:   PUSH    AX
F976 E8BAFF                CALL    CTLCHEK       ;CHECK FOR ABORT
F979 B00D                  MOV     AL,CR         ;SEND CR AND LF TO CONSOLE
F97B E85C03                CALL    VIDOUT

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LOC  OBJ           LINE    SOURCE

F97E B00A                  MOV     AL,LF
F980 E85703                CALL    VIDOUT
F983 58                    POP     AX
F984 C3                    RET
                   ;
F985               BLANK:                        ;PRINT A BLANK, SAVE ALL REG
F985 51                    PUSH    CX
F986 B90100                MOV     CX,1          ;print 1 blank
F989 E80200                CALL    TABS
F98C 59                    POP     CX
F98D C3                    RET
                   ;
F98E               TABS:                         ;PRINT # BLANKS IN CX..ON EXIT CX=0
F98E 50                    PUSH    AX
F98F B020                  MOV     AL,' '
F991 E84603        TLOOP:  CALL    VIDOUT
F994 E2FB                  LOOP    TLOOP
F996 58                    POP     AX
F997 C3                    RET
                   ;
F998               PRINTMESS:                    ;PRINT THE MESSAGE <-- [SI] ON CONSOLE
F998 50                    PUSH    AX            ;END OF MESSAGE IS A ZERO (0)
F999 AC            PMESS:  LODS    DBYTE         ;get a byte
F99A 3C00                  CMP     AL,0          ;check for end of message
F99C 7407                  JE      PQUIT         ;quit if zero
F99E 56                    PUSH    SI            ;otherwise save message pointer
F99F E83803                CALL    VIDOUT        ;and display byte
F9A2 5E                    POP     SI
F9A3 EBF4                  JMP     PMESS         ;print more message
F9A5 58            PQUIT:  POP     AX
F9A6 C3                    RET
                   ;
F9A7 B02A          ERR:    MOV     AL,'*'        ;print error
F9A9 E82E03                CALL    VIDOUT        ;  message
F9AC BCFFF7                MOV     SP,STACKP     ;reinitialize stack
F9AF E983FE                JMP     START         ;and abort!
                   ;
F9B2               BINOUT:                       ;OUTPUT [AL] AS EIGHT BINARY DIGITS (BITS)
F9B2 51                    PUSH    CX
F9B3 B90800                MOV     CX,8
F9B6               BINOUT1:
F9B6 D0E0                  SHL     AL,1          ;get the bit
F9B8 7209                  JB      BOUT1         ;output a 1
F9BA 50                    PUSH    AX            ;otherwise..
F9BB B030                  MOV     AL,'0'        ;output
F9BD E81A03                CALL    VIDOUT        ;a 0
F9C0 EB0790                JMP     BINEND        ;continue
F9C3 50            BOUT1:  PUSH    AX
F9C4 B031                  MOV     AL,'1'        ;output a 1
F9C6 E81103                CALL    VIDOUT
```

Listing 1 continued on page 352

Listing 1 continued:

```
F9C9 58              BINEND: POP     AX
F9CA E2EA                    LOOP    BINOUT1            ;do it eight times
F9CC 59                      POP     CX
F9CD C3                      RET
```

```
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LOC  OBJ             SOURCE

                     ;
F9CE                 HEXOUT:                 ;OUTPUT [AL] AS 2 HEX DIGITS..ALL REG SAVED.
F9CE 50                      PUSH    AX
F9CF 51                      PUSH    CX
F9D0 8AE0                    MOV     AH,AL              ;save AL
F9D2 B104                    MOV     CL,4
F9D4 D2E8                    SHR     AL,CL              ;shift AL right 4 places
F9D6 59                      POP     CX
F9D7 E80700                  CALL    HEXDIGOUT          ;output upper nibble
F9DA 8AC4                    MOV     AL,AH              ;restore AL (now we do lower nibble)
F9DC E80200                  CALL    HEXDIGOUT
F9DF 58                      POP     AX
F9E0 C3                      RET
                     ;
F9E1                 HEXDIGOUT:              ;CONVERT NIBBLE TO ASCII HEX
F9E1 240F                    AND     AL,0FH             ;mask upper 4 bits
F9E3 0490                    ADD     AL,90H             ;tricky conversion...
F9E5 27                      DAA                        ;     but
F9E6 1440                    ADC     AL,40H             ;     it
F9E8 27                      DAA                        ;     works!
F9E9 E8EE02                  CALL    VIDOUT             ;print the result
F9EC C3                      RET
                     ;
F9ED                 HEXCHK:                 ;CHECK AL FOR VALID HEX DIGIT;CONVERT TO BIN
F9ED 2C30                    SUB     AL,'0'  ; IF INVALID RETURN WITH CARRY SET.
F9EF 720E                    JB      HRET               ;Error..not alphanumeric
F9F1 3C0A                    CMP     AL,0AH             ;check for 0-9
F9F3 F5                      CMC
F9F4 7309                    JNB     HRET               ;return o.k. if 0-9
F9F6 2C07                    SUB     AL,7               ;adjust for A-F
F9F8 3C0A                    CMP     AL,10
F9FA 7203                    JB      HRET               ;return error if > F
F9FC 3C10                    CMP     AL,16
F9FE F5                      CMC
F9FF C3              HRET:   RET
                     ;
FA00                 GETPARMB:               ;16 BIT HEX VALUE TO BX. BX IS SHIFT REGISTER, ACCEPTS LAST 4
                                             ;ON ENTRY CX EQUALS NUMBER OF KEYPRESSES THAT CAN BE ACCEPTED.
                                             ;ON EXIT AH CONTAINS TERMINATOR (I.E. CR, SPACE)
                                             ;UNLESS THE TERMINATOR IS INVALID (NOT EQUAL CR, SPACE OR ',')
                                             ;IN WHICH CASE AN ERROR IS GENERATED.
                     ;
FA00 BB0000                  MOV     BX,0               ;clear BX
FA03 E8E6FE          LOOPB:  CALL    CONIN              ;get a character
FA06 3C30                    CMP     AL,'0'             ;alphanumeric ?
FA08 7210                    JB      BEXIT              ;NO...quit
FA0A 51                      PUSH    CX                 ;YES...then
FA0B B104                    MOV     CL,4               ;shift BX to
FA0D D3E3                    SHL     BX,CL              ;make room for
FA0F 59                      POP     CX                 ;latest addition
FA10 E8DAFF                  CALL    HEXCHK             ;check for valid hex and convert to binary
FA13 7292                    JB      ERR                ;if invalid then error!
FA15 02D8                    ADD     BL,AL              ;otherwise add it in
FA17 E2EA                    LOOP    LOOPB              ;keep looking
```

```
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LOC  OBJ             SOURCE

FA19 C3                      RET
FA1A 3C20            BEXIT:  CMP     AL,' '             ;test for blank
FA1C 740B                    JE      BGOOD
FA1E 3C2C                    CMP     AL,','             ;..comma
FA20 7407                    JE      BGOOD
FA22 3C0D                    CMP     AL,CR              ; or carriage return
FA24 7403                    JE      BGOOD
FA26 E97EFF                  JMP     ERR                ;if none of the above the ERROR
FA29 8AE0            BGOOD:  MOV     AH,AL              ;save terminator
FA2B C3                      RET
                     ;
FA2C                 GETPARMD:               ;16 BIT HEX VALUE TO DX. USE GETPARMB
FA2C 53                      PUSH    BX                 ;save BX
FA2D E8D0FF                  CALL    GETPARMB           ;get the parameter
FA30 8BD3                    MOV     DX,BX              ;put it where it belongs
FA32 5B                      POP     BX                 ;restore BX
FA33 C3                      RET
                     ;
```

```
FA34                SETUP:                      ;GET PARMS IN BX AND DX. ALL PURPOSE PARAMETER GETTER.
FA34 51                     PUSH    CX          ;save CX
FA35 B9FFFF                 MOV     CX,0FFFFH   ;allow  64K keypresses
FA38 EBC5FF                 CALL    GETPARMB    ;get first parameter
FA3B 3C0D                   CMP     AL,CR       ;check for carriage return
FA3D 7406                   JE      SET1        ;if so [DX] defaults to [BX]
FA3F EBEAFF                 CALL    GETPARMD    ;otherwise get second parameter
FA42 EB0390                 JMP     SET2
FA45 8BD3           SET1:   MOV     DX,BX
FA47 59             SET2:   POP     CX
FA48 C3                     RET
                    ;
FA49                CLENGTH:                    ;[CX]<--[DX]-[BX]+1, IF[BX]>[DX] THEN ERR
FA49 52                     PUSH    DX
FA4A 3BD3                   CMP     DX,BX       ;if [BX] > [DX]
FA4C 7303                   JNB     CL1         ;then error!
FA4E E956FF                 JMP     ERR
FA51 2BD3           CL1:    SUB     DX,BX       ;else determine difference
FA53 8BCA                   MOV     CX,DX       ;and put in CX
FA55 41                     INC     CX          ;count = difference + 1
FA56 5A                     POP     DX
FA57 C3                     RET
                    ;
FA58                GETPARMAL:                  ;[AL]<-- ASCII HEX FROM CONSOLE
                                                ;[AL] UNCHANGED IF NO PARAMETER ENTERED
FA58 53                     PUSH    BX          ;save BX
FA59 51                     PUSH    CX          ;save CX
FA5A 52                     PUSH    DX          ;save DX
FA5B 8AD0                   MOV     DL,AL       ;save AL
FA5D B9FFFF                 MOV     CX,0FFFFH   ;64 keypresses allowed
FA60 E89DFF                 CALL    GETPARMB    ;get the parameter
FA63 81F9FFFF               CMP     CX,0FFFFH   ;how many parameters entered?
FA67 7502                   JNE     GQUIT       ;if greater than zero then continue
FA69 8ADA                   MOV     BL,DL       ;if zero parms entered restore old value
FA6B 8AC3           GQUIT:  MOV     AL,BL       ;otherwise put it where it belongs
FA6D 5A                     POP     DX          ;restore DX


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LOC  OBJ             LINE    SOURCE

FA6E 59                     POP     CX          ;restore CX
FA6F 5B                     POP     BX          ;restore BX
FA70 C3                     RET
                    ;
FA71                OUTBX:                      ;[BX] OUTPUT AS FOUR HEX DIGITS
FA71 50                     PUSH    AX
FA72 8AC7                   MOV     AL,BH       ;output
FA74 E857FF                 CALL    HEXOUT      ;  BH
FA77 8AC3                   MOV     AL,BL       ; and
FA79 E852FF                 CALL    HEXOUT      ;  BL
FA7C 58                     POP     AX
FA7D C3                     RET
                    ;
                    $EJECT


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LOC  OBJ             LINE    SOURCE

                    ;
                    ;
                    ;
                    ;
                    ;   ************************************************
                    ;   *                                              *
                    ;   *         C O M M A N D   R O U T I N E S      *
                    ;   *                                              *
                    ;   ************************************************
                    ;
                    ;
FA7E                FILL:                       ;FILL A BLOCK OF MEMORY WITH A CONSTANT
FA7E E8B3FF                 CALL    SETUP       ;get start and end
FA81 E8C5FF                 CALL    CLENGTH     ;compute the count
FA84 E8D1FF                 CALL    GETPARMAL   ;and get the constant
FA87 8807           FLOOP:  MOV     M[BX],AL    ;...fill it...
FA89 43                     INC     BX
FA8A E2FB                   LOOP    FLOOP
FA8C C3                     RET
                    ;
FA8D                VERIFY:                     ;VERIFY EQUALITY OF TWO BLOCKS OF MEMORY
FA8D E8A4FF                 CALL    SETUP       ;GET SOURCE START AND END
FA90 E8B6FF                 CALL    CLENGTH     ;and compute the length
FA93 41                     INC     CX
```

Listing 1 continued on page 354

Listing 1 continued:

```
FA94 8BF3                   MOV     SI,BX             ;save source in SI
FA96 51                     PUSH    CX
FA97 B9FFFF                 MOV     CX,0FFFFH         ;64K keypresses allowed
FA9A E863FF                 CALL    GETPARMB          ;get the destination
FA9D 59                     POP     CX
FA9E 8BFB                   MOV     DI,BX             ;into DX
FAA0 E8D2FE                 CALL    CRLF
FAA3 56                     PUSH    SI                ;save source
FAA4 BE90F8                 MOV     SI,OFFSET VHEAD
FAA7 E8EEFE                 CALL    PRINTMESS         ;print header
FAAA 5E                     POP     SI                ;restore source
FAAB                VLOOP:
FAAB F3               REPE  CMPS    DBYTE,DBYTE       ;do it!
FAAC A6
FAAD 83F900                 CMP     CX,0              ;all done?
FAB0 7501                   JNE     VERR              ;NO...error
FAB2 C3                     RET                       ; if done then return
                    ;
FAB3 8BDE           VERR:   MOV     BX,SI             ;get the source addr
FAB5 4B                     DEC     BX                ;adjust it
FAB6 E8BCFE                 CALL    CRLF
FAB9 E8B5FF                 CALL    OUTBX             ;output the addr
FABC E8C6FE                 CALL    BLANK
FABF 8A07                   MOV     AL,M[BX]          ;get what's there
FAC1 8AE0                   MOV     AH,AL             ;save it in AH
FAC3 E808FF                 CALL    HEXOUT            ;output the data
FAC6 E8BCFE                 CALL    BLANK
FAC9 E8B9FE                 CALL    BLANK
FACC 8BDF                   MOV     BX,DI             ;get the destination addr
FACE 4B                     DEC     BX                ;adjust it


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LOC  OBJ            SOURCE

FACF E89FFF                 CALL    OUTBX             ;display it
FAD2 8A07                   MOV     AL,M[BX]          ;get the data
FAD4 E8AEFE                 CALL    BLANK
FAD7 E8F4FE                 CALL    HEXOUT            ;output the data
FADA E8A8FE                 CALL    BLANK
FADD 32C4                   XOR     AL,AH             ;determine bad bits
FADF E8D0FE                 CALL    BINOUT            ;display in binary
FAE2 E84EFE                 CALL    CTLCHEK           ;check for abort
FAE5 EBC4                   JMP     VLOOP             ;continue
                    ;
FAE7                MOVE:                   ;MOVE A BLOCK OF MEMORY
FAE7 E84AFF                 CALL    SETUP             ;get start and end
FAEA 3C0D                   CMP     AL,0DH            ;if not enough data
FAEC 7503                   JNZ     M1
FAEE E9B6FE                 JMP     ERR               ;then error!
FAF1 E855FF         M1:     CALL    CLENGTH           ;otherwise compute length
FAF4 53                     PUSH    BX                ;save start address
FAF5 E83CFF                 CALL    SETUP             ;and get destination
FAF8 8BFB                   MOV     DI,BX             ;[DI] <-- destination
FAFA 5B                     POP     BX
FAFB 8BF3                   MOV     SI,BX             ;[SI] <-- source
FAFD F3              REP    MOVS    DBYTE,DBYTE       ;...move it...
FAFE A4
FAFF C3                     RET
                    ;
FB00                DUMP:                   ;DISPLAY MEMORY
FB00 E831FF                 CALL    SETUP             ;get start and end
FB03 E843FF                 CALL    CLENGTH           ;and compute length
FB06 E81900                 CALL    NULINE2           ;set up console
FB09 8A07           DLOOP1: MOV     AL,M[BX]          ;get what's there
FB0B E8C0FE                 CALL    HEXOUT            ;print it
FB0E E874FE                 CALL    BLANK             ;and a blank
FB11 43                     INC     BX
FB12 F6C30F                 TEST    BL,0FH            ;test for 16 byte boundary
FB15 7503                   JNZ     DNEXT             ;if not then continue
FB17 E80300                 CALL    NULINE            ;otherwise set up console for new line
FB1A E2ED           DNEXT:  LOOP    DLOOP1            ;continue
FB1C C3                     RET
                    ;
FB1D 83F901         NULINE: CMP     CX,1
FB20 7409                   JE      NUQUIT
FB22                NULINE2:
FB22 E850FE                 CALL    CRLF              ;go to new line
FB25 E849FF                 CALL    OUTBX             ;print address
FB28 E85AFE                 CALL    BLANK             ;and a blank
FB2B C3             NUQUIT: RET
                    ;
FB2C                INPUT:                  ;INPUT FROM A PORT
FB2C E805FF                 CALL    SETUP             ;get port address
FB2F E843FE                 CALL    CRLF
FB32 8BD3                   MOV     DX,BX
FB34 EC                     IN      AL,DX             ;read the port
FB35 E896FE                 CALL    HEXOUT            ;print data in hex
FB38 E84AFE                 CALL    BLANK             ;      and
```

```
LOC  OBJ          SOURCE

FB3B E874FE                CALL    BINOUT          ;     binary
FB3E C3                    RET
                  ;
FB3F              OUTPUT:                          ;OUTPUT TO A PORT
FB3F E8F2FE                CALL    SETUP           ;get address
FB42 8AC2                  MOV     AL,DL           ;and data
FB44 FEC8                  DEC     AL              ;adjust data
FB46 8BD3                  MOV     DX,BX
FB48 EE                    OUT     DX,AL           ;output data
FB49 C3                    RET
                  ;
FB4A              GOTO:                            ;EXECUTE A PROGRAM
FB4A E8E7FE                CALL    SETUP           ;get the address
FB4D FFE3                  JMP     BX              ;GO!!
                  ;
FB4F              CWRITE:                          ;WRITE TO CASSETTE
FB4F E8E2FE                CALL    SETUP           ;get the range
FB52 E8F4FE                CALL    CLENGTH         ;compute the length
FB55 E81DFE                CALL    CRLF
FB58 E85500                CALL    CPROMPT
FB5B E80F00                CALL    CSTART
FB5E E85600                CALL    LENGTHOUT       ;tell length
FB61 8A07         CLOOP:   MOV     AL,M[BX]        ;get a byte
FB63 E8FEFD                CALL    COUT            ;output
FB66 43                    INC     BX              ;next byte
FB67 E8C9FD                CALL    CTLCHEK         ;check for abort
FB6A E2F5                  LOOP    CLOOP           ;continue
FB6C C3                    RET
                  ;
FB6D B03C         CSTART:  MOV     AL,3CH          ;start byte
FB6F E8F2FD                CALL    COUT
FB72 B0E6                  MOV     AL,0E6H         ;sync byte
FB74 E8EDFD                CALL    COUT
FB77 8AC5                  MOV     AL,CH           ;high length
FB79 E8E8FD                CALL    COUT
FB7C 8AC1                  MOV     AL,CL           ;low length
FB7E E8E3FD                CALL    COUT
FB81 C3                    RET
                  ;
FB82              READ:                            ;READ FROM CASSETTE
FB82 E8AFFE                CALL    SETUP           ;get address
FB85 E8EDFD                CALL    CRLF
FB88 E82500                CALL    CPROMPT
FB8B E80F00                CALL    READINIT
FB8E E82600                CALL    LENGTHOUT       ;prompt when reading
FB91 E8C1FD       RLOOP:   CALL    CIN             ;get a byte
FB94 8807                  MOV     M[BX],AL
FB96 43                    INC     BX              ;next byte
FB97 E899FD                CALL    CTLCHEK         ;check for abort
FB9A E2F5                  LOOP    RLOOP           ;continue
FB9C C3                    RET
                  ;
FB9D              READINIT:
FB9D B010                  MOV     AL,10H          ;reset interface
```



```
LOC  OBJ          SOURCE

FB9F 52                    PUSH    DX
FBA0 BA6E6E                MOV     DX,CSTAT
FBA3 EE                    OUT     DX,AL
FBA4 5A                    POP     DX
FBA5 E8ADFD                CALL    CIN
FBA8 8AE8                  MOV     CH,AL           ;get high length
FBAA E8A8FD                CALL    CIN
FBAD 8AC8                  MOV     CL,AL           ;and low length
FBAF C3                    RET
                  ;
FBB0              CPROMPT:                         ;CASSETTE PROMPT
FBB0 BEA8F8                MOV     SI, OFFSET CHEAD
FBB3 E8E2FD                CALL    PRINTMESS
FBB6 C3                    RET
                  ;
FBB7              LENGTHOUT:                       ;OUTPUT RECORD LENGTH
FBB7 53                    PUSH    BX
FBB8 8BD9                  MOV     BX,CX           ;get the count
FBBA E8B4FE                CALL    OUTBX           ;output it
FBBD 5B                    POP     BX
FBBE C3                    RET
```

Listing 1 continued on page 356

Listing 1 continued:

```
                      ;
FBBF                  PUTSYNC:                            ;SEND SYNC STREAM TO CASSETTE
FBBF E8B3FD                   CALL    CRLF
FBC2                  SYNCLOOP:
FBC2 B0E6                     MOV     AL,0E6H             ;sync character
FBC4 E89DFD                   CALL    COUT                ;send it
FBC7 E858FD                   CALL    KEYSTAT             ;check for keypress
FBCA 3C00                     CMP     AL,0                ;zero = no keypress
FBCC 74F4                     JE      SYNCLOOP            ;so continue
FBCE E848FD                   CALL    KIN                 ;ignore the keypress
FBD1 C3                       RET                         ;and quit
                      ;
FBD2                  COMPARE:                            ;COMPARE INPUT FROM CASSETTE WITH MEMORY
FBD2 E85FFE                   CALL    SETUP
FBD5 E89DFD                   CALL    CRLF
FBD8 BE71F8                   MOV     SI, OFFSET COMHEAD       ;print header
FBDB E8BAFD                   CALL    PRINTMESS
FBDE E8A4FD                   CALL    BLANK
FBE1 E8CCFF                   CALL    CPROMPT
FBE4 E8B6FF                   CALL    READINIT
FBE7 E8CDFF                   CALL    LENGTHOUT
FBEA                  COMLOOP:
FBEA E868FD                   CALL    CIN                 ;get char from cassette
FBED 3A07                     CMP     AL,M[BX]            ;compare with memory
FBEF 7507                     JNE     COMERR              ;not equal!! error
FBF1 43               COM1:   INC     BX                  ;if equal
FBF2 E83EFD                   CALL    CTLCHEK             ;check for abort
FBF5 E2F3                     LOOP    COMLOOP             ;then continue checking
FBF7 C3                       RET
FBF8 50               COMERR: PUSH    AX
FBF9 E879FD                   CALL    CRLF
FBFC E872FE                   CALL    OUTBX               ;if error..output memory address
FBFF E883FD                   CALL    BLANK
```

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```
LOC  OBJ              LINE    SOURCE

FC02 8A07                     MOV     AL,M[BX]            ;get memory data
FC04 8AF0                     MOV     DH,AL               ;save it too
FC06 E8C5FD                   CALL    HEXOUT              ;output what's in memory
FC09 E879FD                   CALL    BLANK
FC0C 58                       POP     AX                  ;restore cassette data
FC0D E8BEFD                   CALL    HEXOUT              ;output it
FC10 E872FD                   CALL    BLANK
FC13 32C6                     XOR     AL,DH               ;determine bad bits
FC15 E89AFD                   CALL    BINOUT              ;and print in binary
FC18 EBD7                     JMP     COM1                ;continue
                      ;
FC1A                  HEXMATH:                            ;COMPUTE SUM AND DIFFERENCE OF TWO HEX #'S
FC1A E817FE                   CALL    SETUP               ;get the numbers
FC1D 53                       PUSH    BX                  ;save
FC1E 52                       PUSH    DX                  ; them
FC1F E853FD                   CALL    CRLF
FC22 BE86F8                   MOV     SI, OFFSET MHEAD
FC25 E870FD                   CALL    PRINTMESS           ;print the header
FC28 E84AFD                   CALL    CRLF
FC2B 03DA                     ADD     BX,DX               ;sum
FC2D E841FE                   CALL    OUTBX
FC30 E852FD                   CALL    BLANK
FC33 5A                       POP     DX                  ;restore
FC34 5B                       POP     BX                  ; numbers
FC35 2BDA                     SUB     BX,DX               ;difference
FC37 E837FE                   CALL    OUTBX
FC3A C3                       RET
                      ;
FC3B                  NTEST:                   ;MEMORY TEST
FC3B E8F6FD                   CALL    SETUP               ;get start and end
FC3E E808FE                   CALL    CLENGTH             ;compute length
FC41 E831FD                   CALL    CRLF
FC44 53               MTEST1: PUSH    BX
FC45 51                       PUSH    CX
FC46 8A07             MTLOOP: MOV     AL,M[BX]            ;get what's there
FC48 8AE0                     MOV     AH,AL               ;save it
FC4A F6D0                     NOT     AL                  ;complement
FC4C 8807                     MOV     M[BX],AL            ;and store it back
FC4E 8A07                     MOV     AL,M[BX]            ;read it again
FC50 F6D0                     NOT     AL                  ;re-complement
FC52 3AC4                     CMP     AL,AH               ;is it o.k.?
FC54 750C                     JNE     SHORT   TERR        ;if not then error!
FC56 8827                     MOV     M[BX],AH            ;restore previous value
FC58 43               TNEXT:  INC     BX                  ;next location
FC59 E8D7FC                   CALL    CTLCHEK             ;check for abort
FC5C E2E8                     LOOP    MTLOOP              ;continue
FC5E 59                       POP     CX
FC5F 5B                       POP     BX
FC60 EBE2                     JMP     MTEST1              ;test forever
                      ;
```

```
FC62 E810FD           TERR:    CALL   CRLF          ;TELL USER ABOUT BAD MEMORY
FC65 E809FE                    CALL   OUTBX         ;output bad address
FC68 E81AFD                    CALL   BLANK         ;and a blank
FC6B 32C4                      XOR    AL,AH         ;tell user which

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LOC  OBJ              LINE     SOURCE

FC6D E85EFD                    CALL   HEXOUT        ;bits are bad in hex...
FC70 E812FD                    CALL   BLANK
FC73 E83CFD                    CALL   BINOUT        ;and binary
FC76 EBE0                      JMP    TNEXT         ; continue
                      ;
FC78                  ESUBST:                       ;SUBSTITUTE MEMORY WITH HEX DATA
FC78 E8B9FD                    CALL   SETUP         ;get address
FC7B                  NUSLOOP:
FC7B E8F7FC                    CALL   CRLF          ;and
FC7E E8F0FD                    CALL   OUTBX         ;print it
FC81 B90800                    MOV    CX,8          ;8 entries per line
FC84 E8FEFC           SLOOP:   CALL   BLANK
FC87 8A07                      MOV    AL,M[BX]      ;get what's there
FC89 E842FD                    CALL   HEXOUT        ;and print it
FC8C 50                        PUSH   AX            ;save it
FC8D B02D                      MOV    AL,'-'        ;with a prompt
FC8F E84800                    CALL   VIDOUT
FC92 58                        POP    AX            ;restore it
FC93 E8C2FD                    CALL   GETPARMAL     ;get new data
FC96 EB0890                    JMP    QTEST         ;check for quit
FC99 8807             SNEXT:   MOV    M[BX],AL      ;otherwise, put new data in memory
FC9B 43                        INC    BX            ;and continue
FC9C E2E6                      LOOP   SLOOP
FC9E EBDB                      JMP    NUSLOOP
FCA0 80FC20           QTEST:   CMP    AH,' '        ;if blank then
FCA3 74F4                      JE     SNEXT         ;continue
FCA5 80FC0D                    CMP    AH,0DH        ;if carraige return
FCA8 7403                      JE     Q1            ;then we are done
FCAA E9FAFC                    JMP    ERR           ;otherwise..error!
FCAD 8807             Q1:      MOV    M[BX],AL      ;save that last one!
FCAF C3                        RET
                      ;
FCB0                  AENTER:                       ;ENTER ASCII TEXT IN MEMORY
FCB0 B9FFFF                    MOV    CX,0FFFFH     ;64K keypresses
FCB3 E84AFD                    CALL   GETPARMB      ;get the entry address
FCB6 E8BCFC                    CALL   CRLF
FCB9 E830FC           ELOOP:   CALL   CONIN
FCBC 3C04                      CMP    AL,CTLD       ;done?
FCBE 7405                      JE     EEXIT         ;YES
FCC0 8807                      MOV    M[BX],AL      ;NO..put data in memory
FCC2 43                        INC    BX
FCC3 EBF4                      JMP    ELOOP
FCC5 E8ADFC           EEXIT:   CALL   CRLF
FCC8 B040                      MOV    AL,'@'
FCCA E80D00                    CALL   VIDOUT
FCCD E8A1FD                    CALL   OUTBX         ;output the ending address
FCD0 C3                        RET
                      ;
FCD1                  KTOGGLE:                      ;TOGGLE THE UPPER/LOWER CASE FLAG
FCD1 A052F4                    MOV    AL,BYTE PTR M[UCFLAG]  ;get the flag
FCD4 F6D0                      NOT    AL                     ;toggle
FCD6 A252F4                    MOV    BYTE PTR M[UCFLAG],AL  ;put flag back
FCD9 C3                        RET
                      ;

MCS-86 MACRO ASSEMBLER    VID88


LOC  OBJ              LINE     SOURCE

                      +1       $EJECT


MCS-86 MACRO ASSEMBLER    VID88


LOC  OBJ              LINE     SOURCE

                      ;
                      ;
                      ;
                      ;        ****************************************************
                      ;        *                                                  *
                      ;        *                VIDEO DRIVER                      *
                      ;        *                                                  *
```

Listing 1 continued on page 358

Listing 1 continued:

```
                       ;         *                                              *
                       ;         *        DRIVES  TDL  VDB  VIDEO  INTERFACE    *
                       ;         *                                              *
                       ;         *     converted from 8080 Assembler with CONV-86  *
                       ;         *                                              *
                       ;         ************************************************
                       ;
                       ;
                       ;         VIDEO DRIVER
                       ;
FCDA 50                VIDOUT:   PUSH    AX
FCDB 56                          PUSH    SI
FCDC 57                          PUSH    DI
FCDD E80400                      CALL    VIDEO
FCE0 5F                          POP     DI
FCE1 5E                          POP     SI
FCE2 58                          POP     AX
FCE3 C3                          RET
                       ;
                       ;******** CONVERTED CODE BEGINS HERE ********
                       ;
                       ;VDB DRIVER
  00E1                 VD        EQU     0E1H
  00E0                 VC        EQU     0E0H
  00E0                 XRD       EQU     0E0H
  00E1                 YRD       EQU     0E1H
  00C0                 YWR       EQU     0C0H
  00E2                 MRD       EQU     0E2H
  0080                 MWR       EQU     80H
  0088                 VMODE     EQU     88H
  0098                 BMODE     EQU     98H
                       ;
                       ;
FCE4 53                VIDEO:    PUSH    BX
FCE5 8B1E50F4                    MOV     BX,WORD PTR M[XY]
FCE9 247F                        AND     AL,7FH
FCEB 7403                        JZ      SHORT L_2
FCED E80600                      CALL    VOUT
FCF0                   L_2:
FCF0 891E50F4                    MOV     WORD PTR M[XY],BX
FCF4 5B                          POP     BX
FCF5 C3                          RET
                       ;
FCF6 3C20              VOUT:     CMP     AL,20H
FCF8 7303                        JAE     SHORT L_3
FCFA EB7490                      JMP     CNTL
FCFD                   L_3:
```

```
MCS-86 MACRO ASSEMBLER     VID88


LOC  OBJ               LINE   SOURCE

FCFD 3C7F                        CMP     AL,7FH
FCFF 7501                        JNZ     SHORT L_4
FD01 C3                          RET
FD02                   L_4:
FD02 E6E1                        OUT     VD,AL
FD04 FECF                        DEC     BH
FD06 7401                        JZ      SHORT L_5
FD08 C3                          RET
FD09                   L_5:
FD09 B750                        MOV     BH,80
FD0B FECB                        DEC     BL
FD0D 7401                        JZ      SHORT L_6
FD0F C3                          RET
FD10                   L_6:
FD10 FEC3              VO2:      INC     BL
                       ;
FD12 53                SCROLL:   PUSH    BX
FD13 52                          PUSH    DX
FD14 51                          PUSH    CX
FD15 B098                        MOV     AL,BMODE
FD17 E6E0                        OUT     VC,AL
FD19 32C0                        XOR     AL,AL
FD1B E6E0                        OUT     VC,AL
FD1D BA50C1                      MOV     DX,0C150H
FD20 8AC6                        MOV     AL,DH
FD22 E6E0              S1:       OUT     VC,AL
FD24 8AEA                        MOV     CH,DL
FD26 BB00F4                      MOV     BX,VIDBUF
FD29 E4E1              L1:       IN      AL,VD
FD2B 8807                        MOV     M[BX],AL
FD2D 9F                          LAHF
FD2E 43                          INC     BX
FD2F 9E                          SAHF
FD30 FECD                        DEC     CH
FD32 75F5                        JNZ     L1
FD34 8AC6                        MOV     AL,DH
```

```
FD36 FEC8          868              DEC     AL
FD38 E6E0          869              OUT     VC, AL
FD3A BB00F4        870              MOV     BX, VIDBUF
FD3D 8AEA          871              MOV     CH, DL
FD3F 8A07          872    L2:       MOV     AL, MIBX]
FD41 E6E1          873              OUT     VD, AL
FD43 9F            874              LAHF
FD44 43            875              INC     BX
FD45 9E            876              SAHF
FD46 FECD          877              DEC     CH
FD48 75F5          878              JNZ     L2
FD4A FEC6          879              INC     DH
FD4C 8AC6          880              MOV     AL, DH
FD4E 3CD9          881              CMP     AL, 0D9H
FD50 72D0          882              JB      S1
FD52 8AEA          883              MOV     CH, DL
FD54 B020          884              MOV     AL, 20H
FD56 E6E1          885    S2:       OUT     VD, AL

MCS-86 MACRO ASSEMBLER      VID88

LOC  OBJ           LINE   SOURCE

FD58 FECD          886              DEC     CH
FD5A 75FA          887              JNZ     S2
FD5C 59            888              POP     CX
FD5D 5A            889              POP     DX
FD5E 5B            890              POP     BX
FD5F B088          891    SETCAV:   MOV     AL, VMODE
FD61 E6E0          892              OUT     VC, AL
FD63 B050          893    SETCUR:   MOV     AL, 80
FD65 2AC7          894              SUB     AL, BH
FD67 E6E0          895              OUT     VC, AL
FD69 B0D9          896              MOV     AL, 25+0C0H
FD6B 2AC3          897              SUB     AL, BL
FD6D E6E0          898              OUT     VC, AL
FD6F C3            899              RET
                   900    ;
FD70 3C0D          901    CNTL:     CMP     AL, CR
FD72 7422          902              JZ      SHORT CCR
FD74 3C0A          903              CMP     AL, LF
FD76 7415          904              JZ      SHORT CLF
FD78 3C0C          905              CMP     AL, FF
FD7A 741E          906              JZ      SHORT CFF
FD7C 3C08          907              CMP     AL, BS
FD7E 7401          908              JZ      SHORT CBS
FD80 C3            909              RET
FD81 B04F          910    CBS:      MOV     AL, 79
FD83 2AC7          911              SUB     AL, BH
FD85 7901          912              JNS     SHORT L_8
FD87 C3            913              RET
FD88               914    L_8:
FD88 E6E0          915              OUT     VC, AL
FD8A FEC7          916              INC     BH
FD8C C3            917              RET
FD8D FECB          918    CLF:      DEC     BL
FD8F 7503          919              JNZ     SHORT L_9
FD91 E97CFF        920              JMP     VD2
FD94               921    L_9:
FD94 EBCD          922              JMP     SETCUR
FD96 B750          923    CCR:      MOV     BH, 80
FD98 EBC9          924              JMP     SETCUR
FD9A B098          925    CFF:      MOV     AL, BMODE
FD9C E6E0          926              OUT     VC, AL
FD9E BBD007        927              MOV     BX, 25*80
FDA1 32C0          928    CFF1:     XOR     AL, AL
FDA3 E6E1          929              OUT     VD, AL
FDA5 9F            930              LAHF
FDA6 4B            931              DEC     BX
FDA7 9E            932              SAHF
FDA8 8AC7          933              MOV     AL, BH
FDAA 0AC3          934              OR      AL, BL
FDAC 75F3          935              JNZ     CFF1
FDAE BB1950        936              MOV     BX, 256*80+25
FDB1 EBAC          937              JMP     SETCAV
                   938    ;
----               939    ABS_0     ENDS



MCS-86 MACRO ASSEMBLER      VID88


LOC  OBJ                  LINE      SOURCE

  F80F                    940                 END      INIT

ASSEMBLY COMPLETE, NO ERRORS FOUND
```

Text continued from page 347:
followed by a RET (return) statement. Then replace:

```
F8DE    DW    ERR
```

with

```
F8DE    DW    TESTMEM
```

Notes on Performance

How does the 8088 stack up in performance versus the popular 8-bit processors of the 1970s? To answer this question, we must develop at least a rough definition of what we mean by performance.

To evaluate performance I use three criteria:

- the execution speed for a set of applications,
- the amount of memory required to implement the applications, and
- the amount of software-development effort required for application implementation (as measured by lines of assembly-language code).

An appropriate set of applications will include a mix of mathematics, data-handling and process-control-type programs. In addition, both execution-bound (eg: heavy calculation) and bus-bound (eg: bubble sort) applications should be included.

This article is not meant to be a full-fledged benchmark report. Nevertheless, using my own background, manufacturer's documentation, and other sources, I have come to the following conclusions concerning the 5 MHz 8088, which on the average:

- is 1.5 to 5 times faster than the *fastest* versions of other popular 8-bit machines (ie: Z80B, 68B09, 6800, 8080A, etc),
- will typically require only 50% to 75% of the memory devoted to code by these other machines for a set of applications, and
- requires substantially less (as little as 50% or less) lines of code to implement a benchmark than these other machines.

Execution speed is the most visible measure of performance. Factors which contribute to the 8088's superiority are:

- *The high standard clock rate:* The standard 8088 runs at 5 MHz (in fact, possibly faster if you're willing to experiment). Intel claims that, next year, specially selected 8 MHz 8088s will be available. If 5 MHz 8088s are fast, 8 MHz 8088s will be *unreal*.
- *The pipelined architecture:* This architecture allows overlapped instruction fetch and execution, eliminating a traditional performance limitation present in other 8-bit machines.
- *The 16-bit internal data paths:* These enhance data movement and manipulation capability.
- *Its rich set of arithmetic instructions:* Math-oriented applications are served exceptionally well by the 8088. The 5 MHz 8088 can do most 16-bit integer math (add, subtract, multiply, divide) faster than a 9511 hardware math chip.
- *Powerful addressing modes*: The 8088 allows up to four address components to be used in calculating an absolute physical memory address. In addition, most instructions can operate *directly on a memory location*, eliminating the traditional accumulator bottlenck found in other machines.

The amount of memory required can have significant cost ramifications for an application. Here again, the 16-bit internal organization and powerful addressing modes of the 8088 reduce memory requirements. In extreme cases (heavily word- or math-oriented) the 8088 can implement applications in as little as 20% to 30% of the memory of other 8-bit machines.

The number of lines of code required to implement an application becomes more and more of an issue each day. For instance, the Department of Defense states that one line of debugged, documented code now costs close to $60. Programming costs continue to rise, while productivity remains relatively fixed. This suggests a real "software crisis" in the 1980s.

The 8088 can require as little as 50% (average perhaps 75%) of the lines of code as compared to other 8-bit machines. This is because one assembly-language instruction can generate up to 6 bytes of code, and the instructions implemented are very powerful relative to other popular microprocessors.

A summary chart of my findings is shown in figure 3. The relative performance of the 8088 (5 MHz), 6809 (2 MHz) and Z80A (4 MHz) are shown, with an 8086 (true 16-bit machine) thrown in for reference. A differentiation between *word-* or *bus-*oriented and *byte-* or *execution-*oriented applications must be made here. Note that the bus-oriented versus execution-oriented differentiation does not apply to nonpipelined machines like the Z80A or 6809. The byte-orientation versus word-orientation differentiation *does* affect the performance of these machines.

Full-speed memories are assumed as shown below:

	Processor	*Access Time (approximately)*
5 MHz	8088, 8086	480 ns
2 MHz	6809	320 ns
4 MHz	Z80A	250 ns

As shown above, the 8088 can function at maximum speed but still use slower memory than the other microprocessors. In many cases (especially EPROMs), slower-memory-speed selected parts have much lower prices than faster selections.

Essentially, the 8088 has from 1.5 to 2.5 times the performance of the fastest 8-bit competition. Of course, the performance improvement over older 8-bit processors (ie: 6800s, 8080As, etc) is even higher.

Finale

In the text box on pages 344 thru 346 you will find a full description of each MON88 command. A complete listing of the monitor program is given in listing 1.

The 8088 is not only the highest performance 8-bit processor available, but represents a "bridge" to the new architectures of the 1980s. I hope that you have found the 8088 project as challenging, educating and rewarding as I have. Welcome to the future! ■

Add Macro Expansion to Your Microcomputer

Part 2

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Last month, I discussed the definition and use of the macro instruction and detailed a set of requirements for a macro processor. Part 1 also gave an overview in the form of text and flowcharts of how this macro processor would operate. Figures 1 thru 11 provide a more detailed flowchart of these processes and roughly correspond to the overview flowcharts in figure 1 of Part 1 of this article (October 1980 BYTE, page 162). Frequent reference should be made back to these overview flowcharts when reading the detailed flowcharts of figures 1 thru 11. A glossary of terms appears on page 371.

This completes the explanation of the macro definition and expansion. In the rest of the article I will discuss the interface of the macro processor to an assembler, as well as possible enhancements.

Alternate Implementation Approaches

The last hurdle to clear is how to tie this macro facility into your assembler. Basically, there are two ways this can be done, *preprocessor* or *in-line*. The approach used depends upon your situation.

The simplest way to use your macro processor is as a preprocessor. This can be done in two ways. In the first way, the macro processor is a separate program, reading your source program and writing an output file of expanded code to cassette, paper tape, floppy disk, etc; it is this output file that is read into the assembler instead of the original source. While this is the easiest way to use the preprocessor, it is also the worst from the viewpoint of efficiency, requiring an intermediate file and a longer run time. However, if you cannot modify the assembler itself, this may be the only approach you can take.

A second, more efficient, preprocessor approach is to locate the read routine in the assembler and replace it

Listing 1: *Example of keyword parameters. A change that can be made in the macro assembler involves the use of keyword parameters. These allow the user to specify variable symbol values in any order or by default. The macro definition for MOVE is given in listing 1a; two examples of a macro call and its resulting code are given in listings 1b and 1c. In listing 1b, both &TO and &FROM are assigned the default values given in the prototype statement of the macro definition. In listing 1c, the value for &FROM is specified by default. Note the absence of the ampersand in naming variable symbols within the macro call.*

(1a)
```
       MACRO
&JUMP  MOVE   &TO=FIELDB,&FROM=FIELDA,&LENGTH=
       LXI    B,&TO
       LXI    D,&FROM
       MVI    H,&LENGTH
&JUMP  LDAX   D
       STAX   B
       INX    B
       INX    D
       DCR    H
       JNZ    &JUMP
       MEND
```

(1b)
```
LOOP   MOVE   LENGTH=10

       LXI    B,FIELDB
       LXI    D,FIELDA
       MVI    H,10
LOOP   LDAX   D
       STAX   B
       INX    B
       INX    D
       DCR    H
       JNZ    LOOP
```

(1c)
```
LOOP   MOVE   LENGTH=9,TO=NEW

       LXI    B,NEW
       LXI    D,FIELDA
       MVI    H,9
LOOP   LDAX   D
       STAX   B
       INX    B
       INX    D
       DCR    H
       JNZ    LOOP
```

with a call to the macro processor. This is the direction taken in my flowcharts since it is a compromise between a separate program and making major revisions to the assembler.

Replacing the read routine is not as easy as it sounds, however. Microprocessor assemblers typically use character assembly rather than line assembly. They read the source statement one character at a time and process each character as it is read rather than reading an entire source statement and having the whole statement available to work on. My flowcharts are designed for line assembly in that a model statement is completely expanded before it is passed to the assembler.

If your assembler uses character-assembly processing, it will call the macro processor for each character. This will require the read routine to expand the model statement on the first call and pass it one character at a time to the assembler on successive calls until it is completely transferred, at which point the read routine will expand the next model statement. You can also modify the model-expansion routines to pass the statement a character at a time directly from the expansion routines, but this is a little more difficult.

The worst drawback of either preprocessor approach is that every operation code is looked up twice, once by the macro processor to check for macro calls and once by the normal assembler. This is quite time-consuming. Perhaps the most efficient way to incorporate macro processing is to put the macro processing in-line with the assembler's operation-code-lookup and read routines. This requires

Text continued on page 366

The worst drawback of the preprocessor approach is that every operation code is looked up twice.

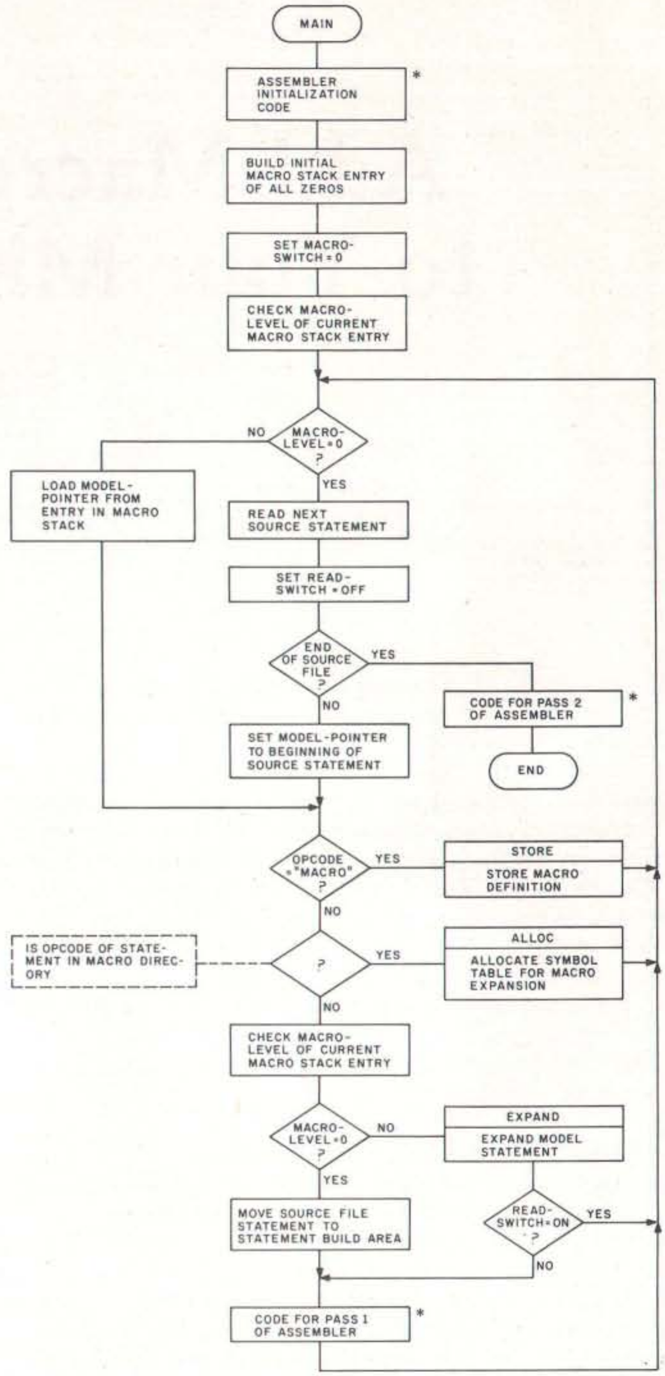


Figure 1: *Overview flowchart for macro definition and expansion. This flowchart, MAIN, takes an assembly-language file containing both macro definitions and macro calls, stores the definitions, expands macro calls, and completes the work of a regular assembler. The boxes marked with asterisks represent the code that performs the assembler functions; the remaining boxes represent the code that is added through modification of the assembler's "read source" routine to implement the macro facility. Refer to the flowcharts in figures 2 thru 11 on pages 363 thru 370.*

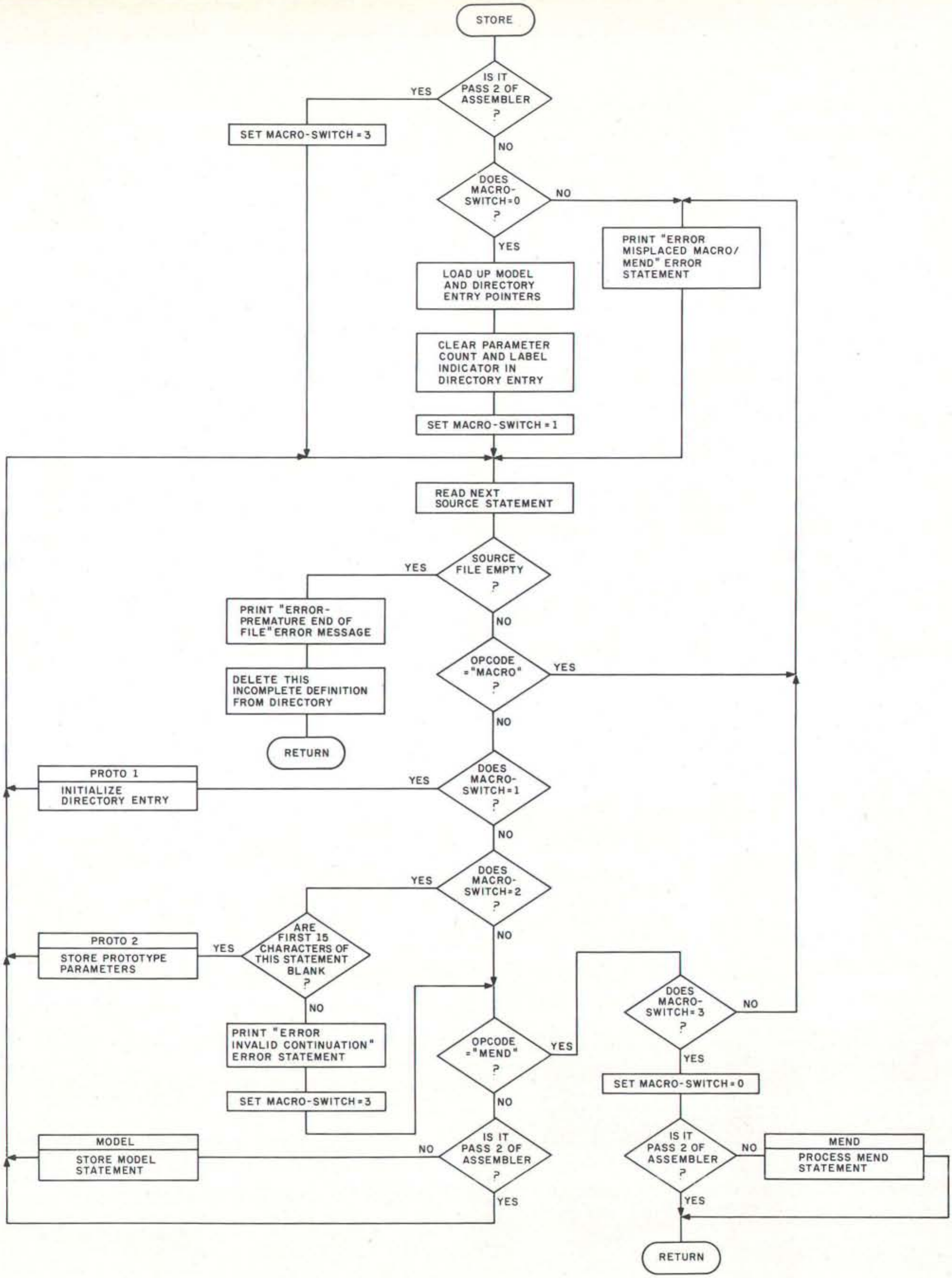


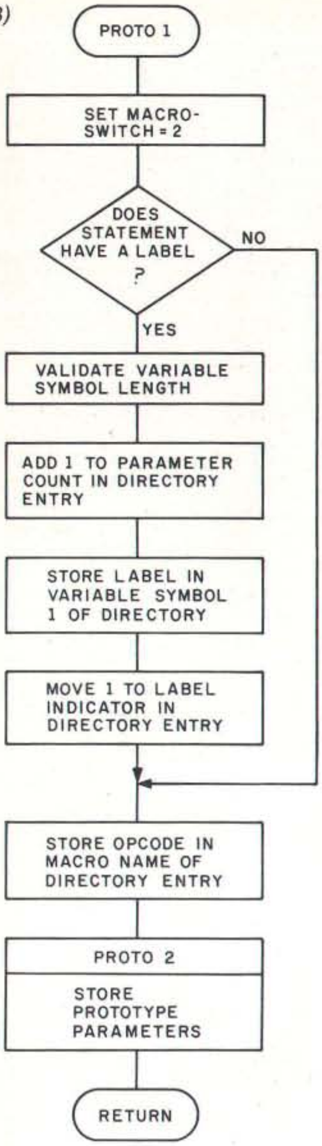
Figure 2: *Flowchart for STORE subroutine. This subroutine stores an entire macro definition within the macro-definition storage area. MACRO-SWITCH is a flag that tells the program what kind of line the routine is expecting next. MACRO-SWITCH=0 means that the computer is ready to process a new macro definition. MACRO-SWITCH=1 means that the computer has found a MACRO statement and is looking for the prototype statement. MACRO-SWITCH=2 means that the computer is ready to process the second line of the prototype statement, if there is one. MACRO-SWITCH=3 means the computer is ready to process the body of the macro definition.*

Figure 3: *Flowchart for PROTO1 subroutine. This subroutine stores the prototype label, if any, the macro name, and calls PROTO2 to store the prototype variable symbols.*

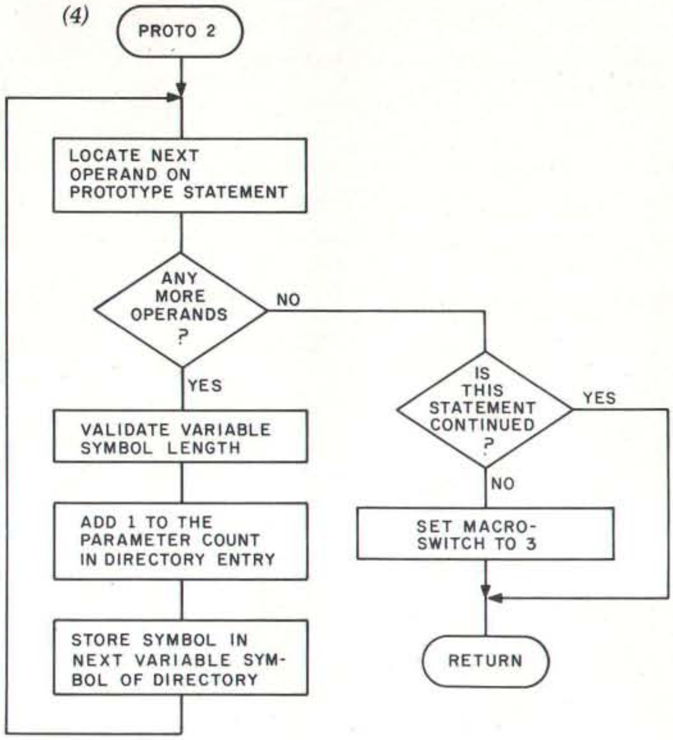
Figure 4: *Flowchart for PROTO2 subroutine. This subroutine stores the variable symbols of a macro prototype statement in the directory.*

Figure 5: *Flowchart for MEND subroutine. This subroutine does several housekeeping chores associated with ending a macro definition.*

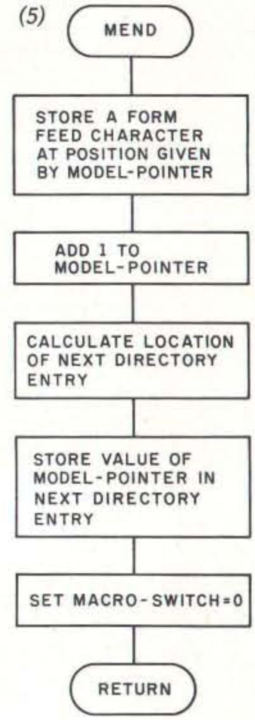
(3)



(4)



(5)



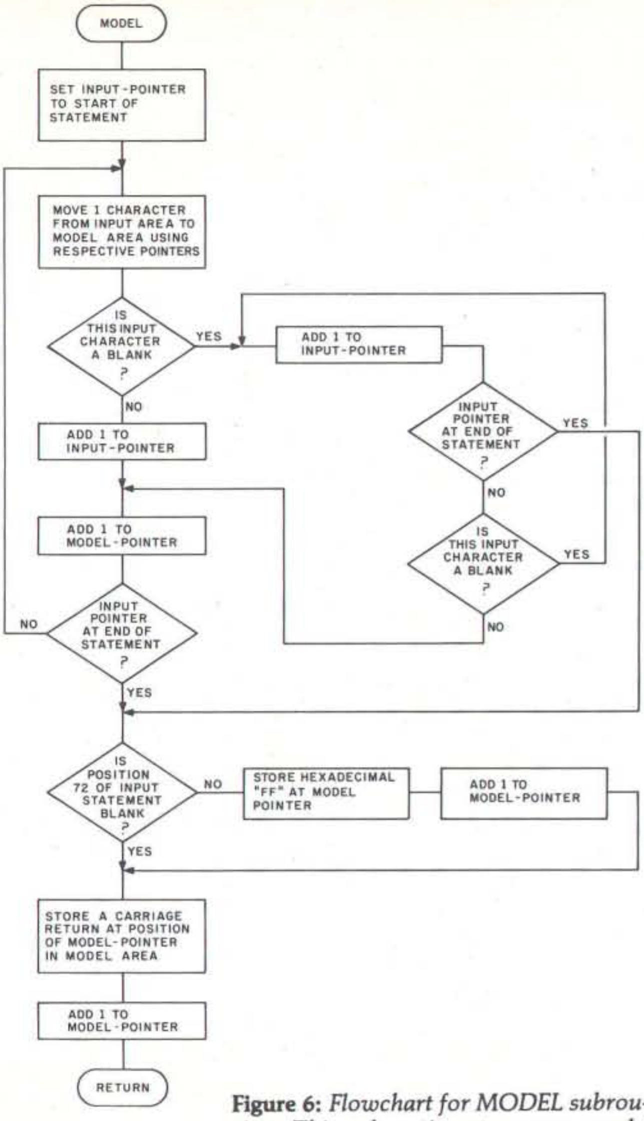


Figure 6: *Flowchart for MODEL subroutine. This subroutine stores one model statement of a macro definition in the macro-storage area.*

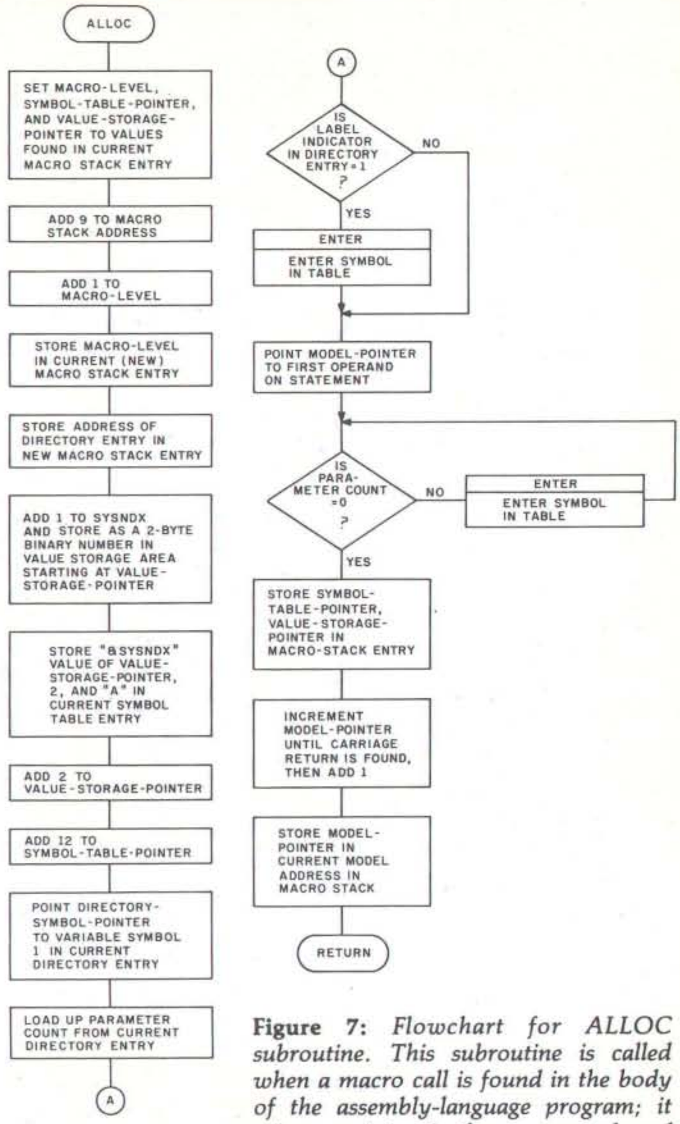


Figure 7: *Flowchart for ALLOC subroutine. This subroutine is called when a macro call is found in the body of the assembly-language program; it sets up pointers in the macro stack and symbol table to identify the current values of the variable symbols as defined in the macro call.*

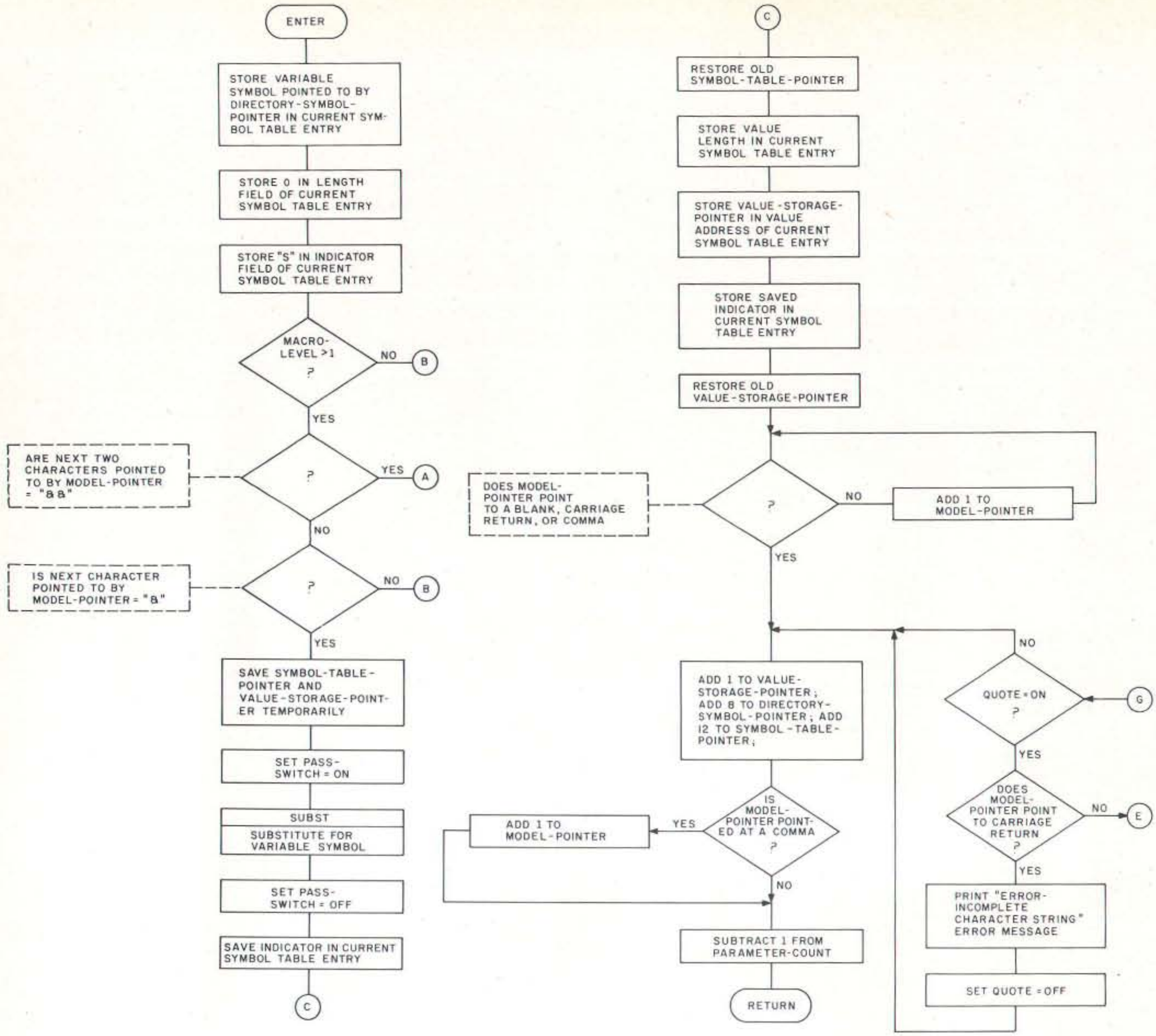


Figure 8: *Flowchart for ENTER subroutine. This subroutine, called by ALLOC, stores the current value of a variable symbol in the symbol table.*

Text continued from page 362:

source listings for your assembler and enough courage on your part to modify your assembler. The operation-code-lookup routine must be modified to first check for the identifier MACRO, at which point it stores the definition. If the operation code is not MACRO and is not found in the assembler's operation-code table, the assembler must then look it up in the macro directory and expand it if found.

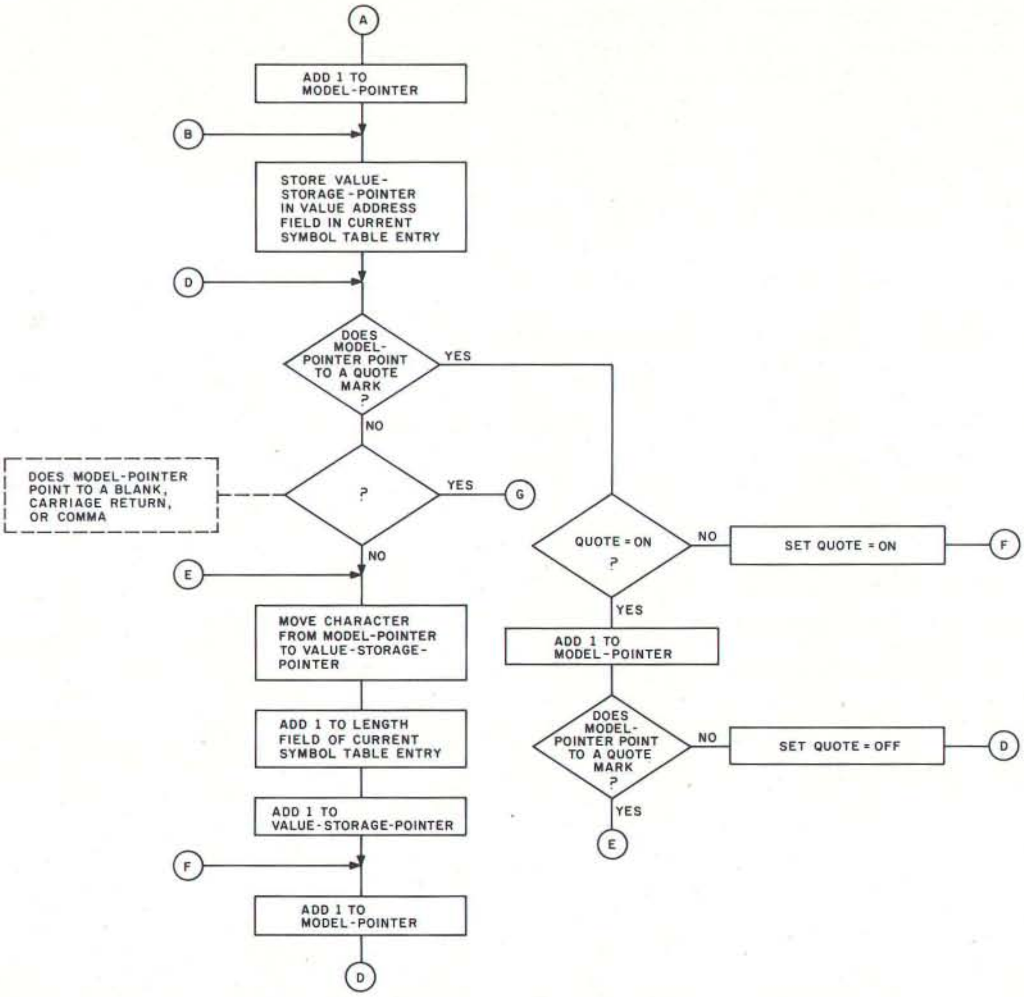
In using this in-line approach, you also have to modify the read routines to make use of the macro-level counter, as is done at the beginning of the flowchart in figure 5. This approach, more ambitious than the others, is the best, and it should be taken if you have the assembler source and can take the time. It will result in an efficient, well-integrated assembler, rather than a patchwork creation. However, if this route cannot be taken, the power of macro facilities is probably worth the inefficiency of the preprocessor technique.

Extensions

If you are really ambitious, there are several other facilities that you can implement. Many of these facilities require modifications to the assembler as well as to the macro processor; but if you are still reading at this point, maybe you feel up to the task.

A large improvement can still be made in print facilities. As detailed so far, the macro call itself never gets to the assembler for printing so that you do not know from looking at the intermediate source listing which statements are generated by the macro assembler and which are in the original source. Ideally, the macro call should print and all generated statements should be identified as such. One solution is to print the macro-level indicator, since this shows the level of nesting when nested macro calls are used. You can also add an assembler directive that tells the assembler whether or not to print the generated statements.

Another facility that you can implement is conditional



assembly, which was mentioned in Part 1 of this article. This would go along with the ability to define local variable symbols within the body of the macro definition; these local variable symbols would be used for loop control and arithmetic within the macro definition.

Another possible modification is the addition of global symbols and a global symbol table. This would allow you to pass variable symbols from one macro expansion to another. When a global symbol is encountered, you look it up in the global symbol table to get its value. If it is not found there, it is added to the global symbol table. This global table does not have its entries deleted at the end of the macro generation, so the information put there is still present whenever the next macro call is processed.

The method for handling variable symbols and their values detailed in this article is known as *positional parameters*. This means that the first variable symbol on the prototype assumes the first value on the macro call, the second variable symbol assumes the second operand value, and so on. A more flexible method is *keyword parameters*. With keyword parameters, the macro prototype might look like this:

```
&LABEL  MOVE  &FROM=FIELDA,
    &TO=FIELDB,&LENGTH=
```

The macro call would then be coded:

```
LOOP2  MOVE LENGTH=14,FROM=FIELDC
```

Keyword operands are distinguished by an equals sign and have several interesting properties. As shown in listing 1a, the &FROM= and &TO= variable symbols in the prototype specify a default value—FIELDA and FIELDB, respectively. If the FROM and TO operands are omitted on the macro call, the defaults are used as in listing 1b; otherwise, the value from the macro call is used, as in listing 1c. The &LENGTH= parameter on the prototype has no default, so it must be specified on the macro call. Also, since you specify the keywords on the macro call, they do not have to be in the same order as specified on the prototype. Otherwise, the keywords are used in the macro-definition statements just like the positional parameters I have been discussing.

Keyword processing requires a more complicated loading of the symbol table when the macro call is encountered; it also requires modifications to the routine that stores the macro definition, since the defaults will have to be stored in the value-storage area and the directory entries will have to be modified to point to the default values. It is a lot of work, but it is much more flexible.

These are just some of the enhancements you can implement. If you have access to the IBM Assembler Language manual (referenced at the end of this article), you will find that it gives much more detailed explanations of these facilities, plus others that I have not mentioned.

To those of you who are still interested, study of the text and flowcharts of this article is all you need do before you can write your own macro assembler. Once you understand the processes involved ("walking through" the flowcharts with pencil and paper will help), there is no reason why you cannot give it a try. After all, there's no magic to system software—it's just another program. ■

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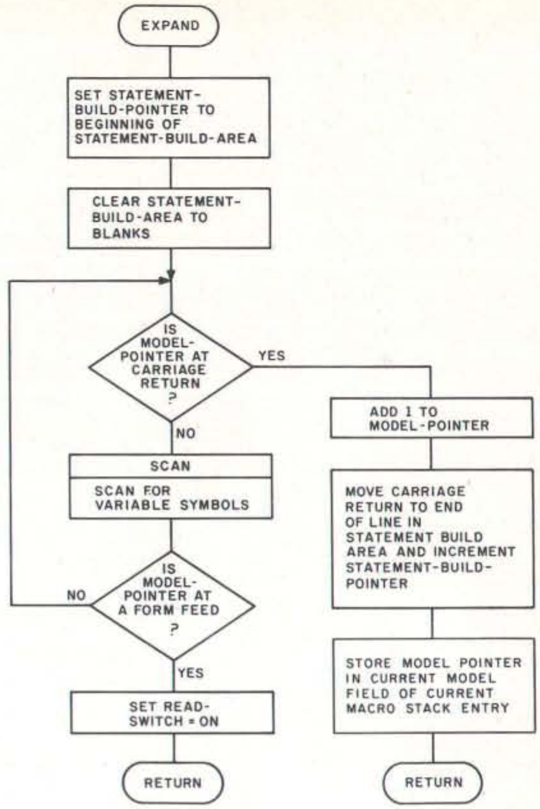

Figure 9: *Flowchart for EXPAND subroutine. This subroutine expands a model statement using the current values of the variable symbols as found on top of the symbol table.*

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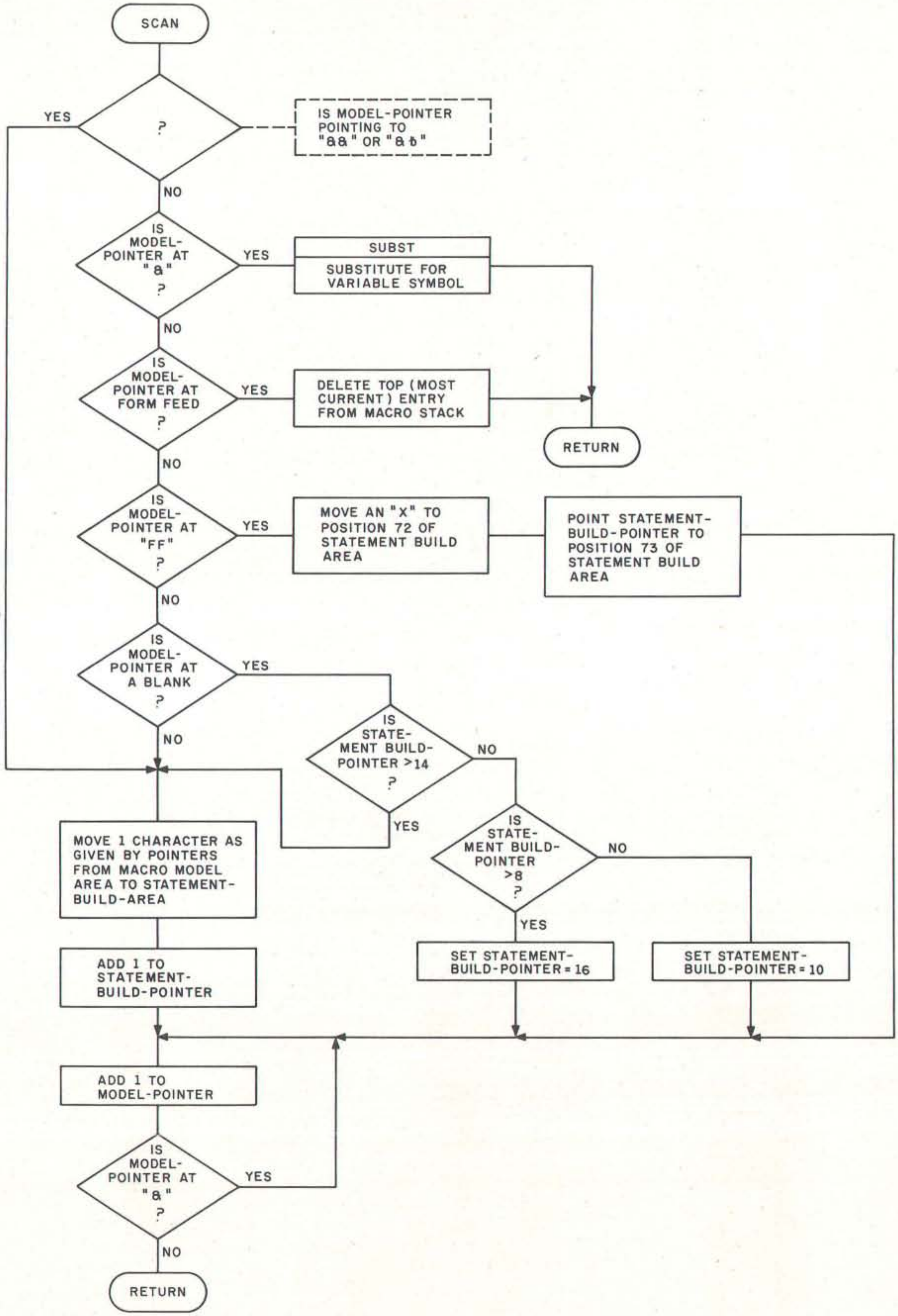


Figure 10: *Flowchart for SCAN subroutine. This subroutine scans for variable symbols in the model statement and replaces them with their most recent values; it also restores blanks that were compressed out of the model statement.*

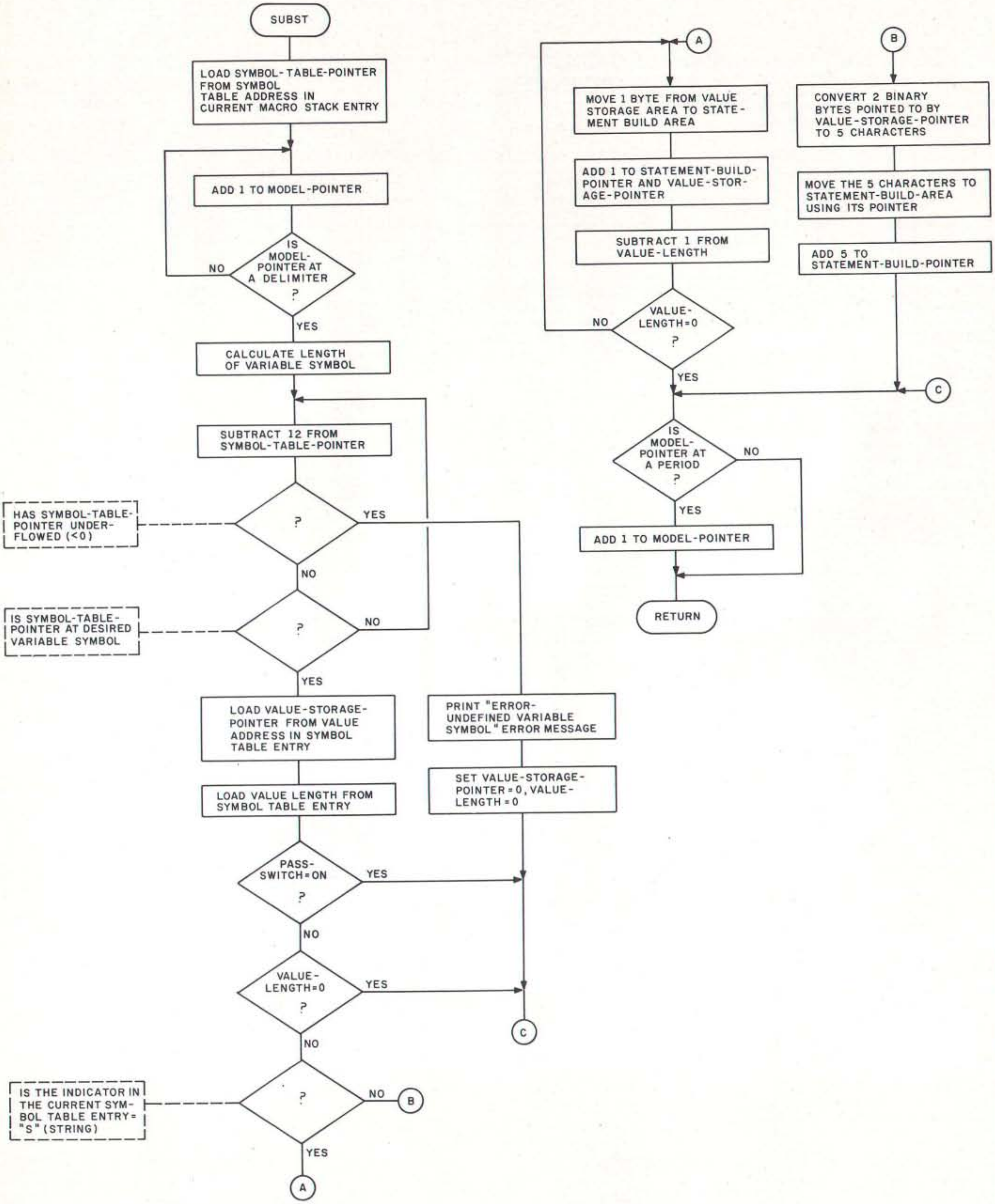


Figure 11: *Flowchart for SUBST subroutine. This subroutine, called by SCAN, substitutes the appropriate value for its corresponding variable symbol in a model statement.*

GLOSSARY

Conditional Assembly: *a feature of macro assemblers that instructs the assembler to generate or leave out certain lines of assembly-language code based on a given condition evaluated at the time of expansion.*

Descriptor: *useful when working with strings of characters. It is a fixed-length entry containing the length of the string and a pointer to where the string starts in the storage area. (Symbol-table entries can be considered descriptors.) Descriptors are used frequently in assemblers and in high-level language compilers.*

Directory: *it contains an entry for every macro defined, pointing to the start of the model statements and specifying the variable names (from the macro prototype) that must be entered into the symbol table before the macro is evaluated.*

Global Variable: *a variable whose value is in effect for the entire assembly and for every macro generation. Use of a given global variable name, even within different macros, refers to the same value (unlike local variable symbols, the values of which are lost at the end of the macro expansion). In this article, &SYSNDX is a global variable.*

Inner Macro: *a macro call specified within the model statements of another macro. When a macro referred to as the outer macro is generating statements and encounters an inner macro, it must stop, generate the statements from the inner macro call, add them to the statements belonging to the outer macro, then continue generating its own statements.*

Keyword Operand: *a variable symbol followed by an equals sign; it appears only on the macro prototype and the macro call. Unlike positional parameters, keyword operands can be coded in any order. They also allow the ability to specify default values in the macro prototype.*

Local Variable: *a variable, the value of which is in effect only for the macro in which it is defined. All variable symbols defined in macro prototype statements are local variables. The same local variable symbol name used in another macro is treated as a separate variable, even though the names are the same.*

Macro: *a user-defined assembly-language operation code that generates one or more assembler instructions.*

Macro Call: *a pseudoinstruction within an assembly-language program that refers to a macro definition of the same name. The eventual result is the replacement of the macro call statement with the expanded model statements of the macro definition.*

Macro Definition: *a sequence of statements that tell the macro processor what to generate when replacing the macro-call instruction. It is made up of a MACRO statement that signals the beginning of the macro, a prototype statement that defines the macro name and its operands, a series of model statements that replace the macro call, and a MEND statement that signals the end of the macro definition.*

Macro Stack: *a stack of certain information about currently incompleted macro calls; it is necessitated by the ability to call a macro within a macro. Each macro-stack entry points to the directory entry, the end of the symbol table, and the value-storage area for the macro.*

Model-Storage Area: *an area of computer memory set aside for storing the model statements of all macro definitions. The directory entry for each macro points to the start of that macro's model statements in the model-storage area.*

Pass 1: *the assembler's first reading of source statements. During pass 1, the assembler builds its symbol table, which includes every label in the program, and checks for duplicate symbols.*

Pass 2: *the assembler's second reading of the source statements. At this point, all symbols are known to the assembler as a result of pass 1, and the equivalent machine code can be generated from the source code.*

Positional Operands: *when the variable symbols in a macro prototype are defined as positional operands, they are assigned values from the list of operands in the macro-call statement in the order that they are defined in the prototype. The first variable symbol on the prototype gets the first operand value, and so on.*

Preprocessor: *a routine or program that processes and usually modifies the input before the main program gets it. Macro facilities are often written as preprocessors that replace macro calls with their expanded assembly-language statements before passing the source file to the assembler.*

Prototype: *the second statement in the macro definition. It defines the label entry, the operation code (macro name), and the allowable operands (in the form of variable symbols) for the macro call.*

Recursion: *a technique in which a called subroutine calls itself. A recursive function must be designed so that it eventually returns a value rather than calling itself again; otherwise, it calls itself in a loop that never finishes.*

Stack: *a last-in, first-out list that allows the user to remove only the value most recently placed onto the stack. Stacks are similar to the devices used to dispense plates in a cafeteria. Plates (values) are put on the top of the stack, pushing down all the others, and are removed from the top, causing the others to pop up. A stack in programming works the same way, giving rise to the terms PUSH and POP, which are commonly used when talking about computer stacks.*

Symbol Table: *a stack containing an entry for each variable in the macro prototype. The symbol-table entry specifies the variable name, the length of its current value, and the address where the value is stored in the value-storage area.*

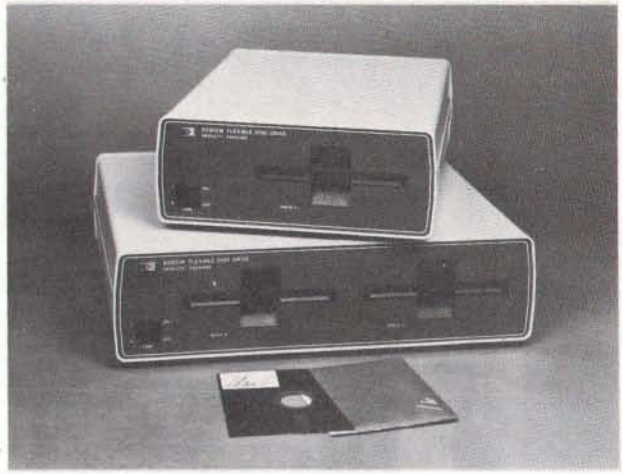
Text Compression: *the process of removing all unnecessary blanks from a source statement in order to reduce the amount of space needed to store the text.*

Value-Storage Area: *an area of memory set aside for storing the values associated with a program's variables. The symbol-table entry for each variable points to the start of that variable's value and specifies the value length.*

Variable: *a variable (or variable symbol) is a character string that can have many different values assigned to it by either the programmer or the assembler. Variables can be either global or local; most references to variable symbols in this article actually refer to local variable symbols.*

What's New?

PERIPHERALS



Floppy-Disk Drive for the HP-85

The HP 82900 Series floppy-disk drives read double-sided, double-density, 5-inch floppy disks, and can be configured to provide from 279 K bytes to 1.08 megabytes of storage. The interface between the HP-85 and the disk drives is the HP-85 Mass Storage ROM (read-only memory). The ROM makes available thirty additional BASIC commands including a Translate command, which upgrades written tape-based programs for use on the drives; the ability to store and retrieve the graphics display on the video screen; automatic default to the drive; and volume labeling, allowing users to refer to disks by name and write programs independent of drive addresses. Prices for the floppy-disk drives start at $1500 for a single-master drive and go to $2500 for a dual-master drive. Contact the Inquiries Manager, Hewlett-Packard Co, 1507 Page Mill Rd, Palo Alto CA 94304.

Circle 662 on inquiry card.

Seven Spinwriter Thimble Fonts from NEC

NEC Information Systems Inc, 5 Militia Dr, Lexington MA 01273, (617) 862-3120, has introduced Pica 10 Multilingual, Elite 12 Multilingual, British Elite 12, Greek/Times Roman, Scientific Times Roman, Super Courier/ Publishers, and Light Italic/Manifold type fonts. These fonts meet the special printing requirements of many industries. The multilingual fonts offer the capability of printing over thirty languages. The fonts are offered on the NEC Spinwriter series of 55 character per second impact printers which feature the "thimble" print element.

Circle 663 on inquiry card.

Line Driver Meets Bell Metallic-Line Specifications

Tuck Electronics has announced a line-driver series for use on metallic pairs from 0 to 9.6 kbs for 4-wire full-duplex service. The driver complies with Bell 43401 amplitude and line balance specifications, and features a floating receiver amplifier. The unit features analog and digital loop-back test facilities, and a blinking light which indicates when the driver is in the test mode. The driver supports an RS-232 interface. The unit is available in stand-alone and multiple units. Single unit price for stand-alone units is $175, and multiple-unit cards are $162. For more information, contact Tuck Electronics Inc, 3645 Industrial Park Rd, Camp Hill PA 17011, (717) 761-4354.

Circle 664 on inquiry card.

Where Do New Products Items Come From?

The information printed in the new products pages of BYTE is obtained from "new product" or "press release" copy sent by the promoters of new products. If in our judgement the information might be of interest to the personal computing experimenters and homebrewers who read BYTE, we print it in some form. We openly solicit releases and photos from manufacturers and suppliers to this marketplace. The information is printed more or less as a first in first out queue, subject to occasional priority modifications. While we would not knowingly print untrue or inaccurate data, or data from unreliable companies, our capacity to evaluate the products and companies appearing in the "What's New?" feature is necessarily limited. We therefore cannot be responsible for product quality or company performance.

What's New?

PERIPHERALS

Large-Screen Color Monitor

The AM-26, a 26-inch color monitor, with over 340 square inches of screen surface, combines Sony's Trinitron color system with switchable A/B inputs, switchable underscan, internal and external sync, and separate RGB (red, green, and blue) gun switches. Talley light, separate horizontal and vertical scan delay are optional, and a separate tuner/audio amplifier and speaker section may be added. The Amtron AM-26 is priced at $2395 from Amtron, Aptos CA 95003, (408) 688-4445.

Circle 665 on inquiry card.

Digital Plotters from Houston Instrument

The DMP family of plotters comprises two standard and four intelligent models. All these models are available with plotting sizes of 21.5 by 28 cm (8.5 by 11 inches) and 28 by 44 cm (11 by 17 inches). The DMP-2 is a 21.5 by 28 cm plotter with an RS-232C and parallel interface. It has a pen speed of 2.4 inches per second and can plot at 100 or 200 increments per inch. The DMP-5 has a surface area of 28 by 44 cm and the RS-232C and parallel interface. The unit is plug-compatible with the DMP-2 and can utilize software developed for the DMP-2. The DMP-3 features a built-in microprocessor and pen speeds of 3 inches per second. Use of Houston Instrument's Digital Micro/Plotter Language alleviates the software burden on the host computer. Self-test and pen positioning are accomplished via a computer or terminal keyboard. The DMP-3 comes with an RS-232C or Centronics-compatible interface. The DMP-6 is a 21.5 by 28 cm version of the DMP-3 and features a pen speed of 2.4 inches per second. The DMP-4 and the DMP-7 utilize electronic controls to facilitate positioning of the X and Y axes. Self-diagnostics are activated through front panel controls. Prices for the DMP Series plotters start at $1085. For complete information, contact Houston Instrument, 1 Houston Sq, Austin TX 78753, (512) 837-2820.

Circle 666 on inquiry card.

Paper-Tape Reader

A paper-tape reader/transmitter, the Model 612, is available from Addmaster Corporation, 416 Junipero Serra Dr, San Gabriel CA 91776, (213) 285-1121. The 612 features the ability to read five- to eight-level tape and to transmit 7 to 11 frames per character at 50 to 9600 bps (bits per second). Other features include starting and stopping on character at all speeds; choice of manual or automatic control; 90 to 260 V, 50 to 60 Hz power sources; and even, odd, or no parity; with a choice of desk-top or rack mounting. The price is $656 to $779.

Circle 667 on inquiry card.

Chatterbox from Micromint

The Chatterbox is a packaging combination of the presently available COMM-80 I/O (input/output) interface for the TRS-80 and an acoustic modem. This box can turn even a 4 K-byte TRS-80 into a full time-sharing terminal. The Chatterbox includes a built-in programmable 50 to 19 K bps (bits per second) serial port, a Centronics-compatible parallel printer port, a 300 bps acoustic originate modem, and a spare TRS-BUS expansion connector. It comes with a power supply, connection cable, manual, and smart terminal software. When the modem is in use, the data conversation is automatically routed to the serial output port for printing. The Chatterbox allows a TRS-80 to communicate with time-sharing systems such as Micronet and the Source. In addition, Chatterbox can be used simply to provide an address selectable serial and parallel port. It is completely hardware- and software-compatible with existing TRS-80 products, and it connects either to the keyboard connector or screen printer port on the Expansion Interface. It does not require the Expansion Interface for operation. The Chatterbox is available for $259 from The Micromint Inc, 917 Midway, Woodmere NY 11598, (516) 374-6793.

Circle 668 on inquiry card.

What's New?

SYSTEMS

Systems from Wang

The Office Information Systems (OIS) Models 115-1 and 115-2 incorporate hard-disk drives located within the master control unit. The OIS systems can utilize the Wang Office-BASIC language, telecommunications and high-speed image printing capabilities, and Wang MAILWAY electronic mail software. These systems combine word-processing and data-processing capabilities in one device. The Model 105 supports two workstations and one printer, and contains a 2.5-megabyte hard disk. The addition of text editing, hyphenation, and justification to the 105 provides a complete photocomposition system. The 105 begins at $9300.

The 115-1 and 115-2 support more users, peripherals, and larger hard-disk storage units. The 115-1 begins at $13,400, and the 115-2 starts at $15,400. For complete information, contact Wang Laboratories Inc, 1 Industrial Ave, Lowell MA 01851, (617) 459-5000.

Circle 669 on inquiry card.

Casio Markets Its First Computer

The FX-9000P computer, priced under $900, has been introduced by Casio Inc, 15 Gardner Rd, Fairfield NJ 07006, (201) 575-7400. It features instantaneous operation of the user system when the power is switched on. A graphic-display system makes it possible to display graphs, diagrams, and tables. The FX-9000P has all functions necessary to perform scientific and technical calculations and business analyses. The machine accepts memory packages to expand memory capacity.

Circle 670 on inquiry card.

British S-100-Based Microcomputer

The Tuscan S-100 is based on the IEEE (Institue of Electrical and Electronics Engineers) standard S-100 bus. This single-board computer uses a Z80 microprocessor, can store 64 K bytes of programmable memory, is CP/M compatible, and includes a printer interface. Expansion capabilities include high-resolution graphics and speech synthesis cards. Transam offers application software packages that include BASIC and Pascal. Tuscan S-100 prices start at £195 for kits. For details, write Transam, 12 Chapel St, London NW1 5DH, England.

Circle 671 on inquiry card.

Canon Introduces Its Desk-Top Computer

The TX Series microcomputers from Canon feature a 6809 microprocessor, extended BASIC and assembler language, a twenty-column alphanumeric video display, and a built-in twenty-six-column triple-copy impact printer. The models have 15 K bytes of user memory which can be expanded to 31 K bytes. Each model has an RS-232 interface port and a modem port. The TX-25 is a programmable machine with a full typewriter keyboard and a built-in Canon floppy-disk drive. The TX-10 and TX-15 are nonprogrammable. The TX-15 incorporates a typewriter keyboard, while the TX-10 has a ten-key pad with twenty-six labeled keys. The price for the series is $1295 from Canon Systems Division, 10 Nevada Dr, Lake Success, Long Island NY 11042, (516) 488-6700.

Circle 672 on inquiry card.

What's New?

GRAPHICS

Colormaster Video and Graphics Board

The Colormaster allows users to program virtually any display format (eg: 64 by 32, 128 by 16, and 80 by 25). The board is designed for S-100 bus systems. Characters may be reversed, dimmed, flashing, underlined, and any of eight colors. Bit-mapped graphics or an optional PROM (programmable read-only memory) graphics set may also be displayed. Another option allows extension of the character set to include 128 user-defined characters. The Colormaster kit is $399; assembled and tested, it is $499; and the bare board is $79. For more information, contact MicroDaSys, POB 36051, Los Angeles CA 90036, (213) 731-0876.

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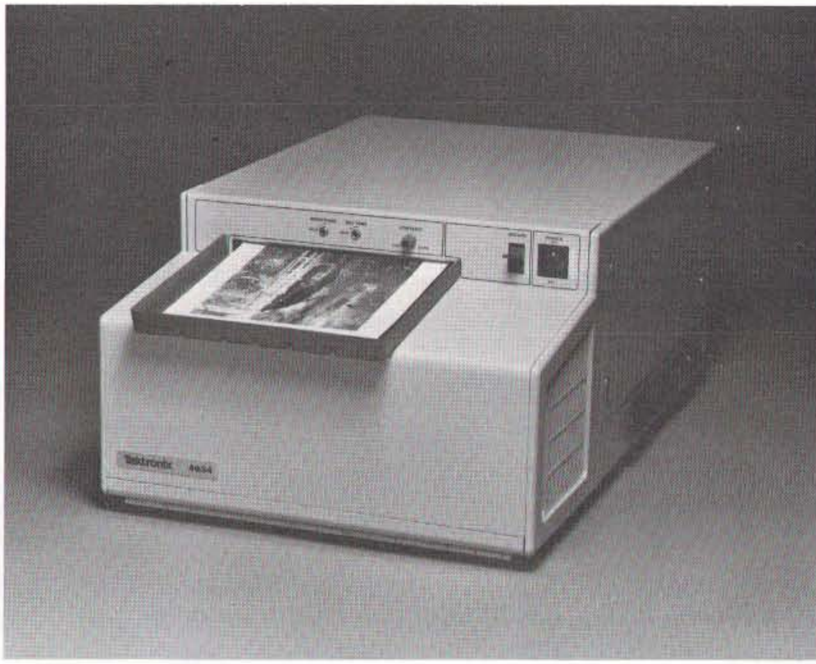
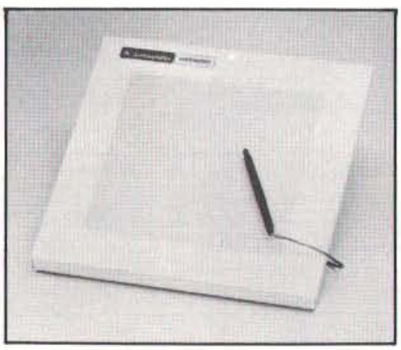


Summagraphics Unveils Supergrid Digitizer

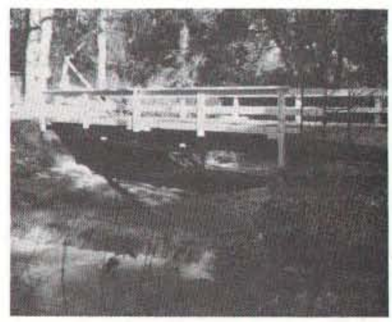


The microprocessor-based Supergrid utilizes a new technology—the Direct Magnetostrictive principle. This unit features high accuracy (±.005 inch or 0.125 mm) and high-resolution (.001 inch or 0.025 mm) and eliminates the need for a biasing magnet. Supergrid is translucent with a flat surface; moreover, it supports a stylus and a cursor, and it permits simultaneous use of two digitizer tablets with the same driving electronics. The Supergrid comes in 11 by 11 and 20 by 20 inch forms, with larger versions to follow. RS-232C, IEEE, 8-bit parallel, and 16-bit parallel interfaces are supported. The technology behind the device is based on a principle that replaces a matrix of magnetostrictive wires with a matrix of plain copper wires and only one magnetostrictive wire per axis. For more information, contact Summagraphics Corporation, 35 Brentwood Ave, Box 781, Fairfield CT 06430, (203) 384-1344.

Circle 674 on inquiry card.

Hard-Copy Unit for Video Images

The Tektronix 4634 Imaging Hard Copy Unit produces high-quality continuous tone copies from raster-scan video sources in seconds. Designed to provide photographic quality images, the device is aimed at digital image processing, pattern recognition, remote sensing, video-disk, and high-resolution display environments. The 4634 records on dry silver paper using a fiber-optic video display. The process requires no toners or developers. The copies have a twelve-tone gray-scale range. The approximate cost per copy is $0.20. It prints 6 by 8 inch images on 8½ by 11 inch paper. It is usually requires a single cable connection and can be interfaced to most raster-scan video sources, whether analog or digital. An automatic gain-control circuit tracks the input



signal. Paper is available in 8½ inch by 500 foot rolls. Paper length can be adjusted from 7 to 11 inches. For more information, contact Marketing Communications Department, M S 63-635, Tektronix Inc, POB 500, Beaverton OR 97077, (503) 682-3411.

Circle 675 on inquiry card.

Digitizer for the Apple II

The DS-65 Digisector is a random access video digitizer for the Apple II. It converts a television-camera's output into digital information that the Apple can process. The Digisector features high-resolution reproduction, sixty-four levels of gray scale, and accepts interlaced or industrial video input. The unit has on-board software featuring full screen scans directly to the Apple screen, random access digitizing by BASIC programs, line-scan digitizing for reading charts or tracking objects, and utility functions for clearing and copying the screen. BASIC programs include a burglar alarm and a graph reader. Complete source listings are included in the package. The DS-65 is used for digitizing pictures; security systems; moving-target indicators; computer portraits; reading paper tape, strip charts, bar codes, and more. The price is $349 from The Micro Works, POB 1110, Del Mar CA 92014, (714) 942-2400.

Circle 676 on inquiry card.

SOFTWARE

A Mail-List and Data-Base System

SelectraSort is a mail-list, data-base management system. It can pull records from mail-list files on the basis of over sixty selection criteria. The mail-list-file maintenance module enters new records to the mail list and changes or deletes existing entries. The selection module pulls records form the files. The print module prints selected and master mail lists as well as mail labels. Sorts can be done by ZIP code, country, state, last activity date, amount purchased or sold last year and this year. SelectraSort is $195, which includes CBASIC source code. It is available on 8-inch soft-sectored and 5-inch soft- and hard-sectored floppy disks. Contact Software Hows, a division of MicroDaSys, POB 36275, Los Angeles CA 90036, (213) 731-0877.

Circle 677 on inquiry card.

General Ledger for the Atari

MicroLedger, the Compumax general ledger program, has been converted to run on the Atari 800. The Atari MicroLedger performs trial balances and produces profit-and-loss statements and balance sheets. It features updating options, allowing the user to review and update records in the journal or chart of accounts; a running balance column in the journal listing; and error traps. The MicroLedger package retails for $140, which includes the program, sample data, and a manual. BASIC source code is also included. Minimum hardware requirements are the Atari 800 with 24 K bytes of memory and a floppy-disk drive; a printer is offered as an option. Contact Compumax Inc, POB 1139, Palo Alto CA 94301, (415) 325-4503.

Circle 678 on inquiry card.

Data Manager for the Apple

Information Master is a data manager for use with the Apple and includes the ability to do calculations, totals, subtotals, and more. The program lets the user define, enter, edit, sort, and retrieve data. Printed report formats using the report-generation features can be defined. Other features include screen formatting, error trapping, and the ability to add, multiply, divide, and do exponentiations. A program is included that transfers files from the Management System for use with the Information Master. For further details on the Information Master program, contact High Technology Inc, POB 14665, 8001 N Classen Blvd, Oklahoma City OK 73113, (405) 840-9900.

Circle 679 on inquiry card.

Vector Releases COBOL with Program Generator

Vector Graphic Inc has released a version of its ANSI-standard CIS COBOL, featuring program generation capability. Version 4.2 of CIS COBOL implements the eight modules necessary to meet the ANSI Level 1 standard at the low-intermediate level. The FORMS-2 utility generates data-entry screens and can create error-free data input programs without the programmer writing a line of code. It is available from Vector Graphic Inc, 31364 Via Colinas, Westlake Village CA 91361, (213) 991-2302.

Circle 680 on inquiry card.

Job-Costing Package Under CP/M

This job-costing package consists of a reporting facility, a job-costing accounts payable, and a job-costing payroll. These programs are designed to run on a Z80 or 8080 processor using the CP/M operating system. Other CP/M-like systems are also supported. The software will run on hard or floppy disks. The business applications are integrated, yet each will run singly. The price is $700 for a system from Arkansas Systems Inc, Suite 206, 8901 Kanis Rd, Little Rock AR 72205, (501) 227-8471.

Circle 681 on inquiry card.

Business Application for the HP-85

Pro-Flow can figure sales analysis, forecast performance for products, evaluate material costs, and perform cash-flow analysis for a year's operation. By mixing initial raw data values with formulas, users can make projections about future operations. Pro-Flow is designed to run on the HP-85 microcomputer. It is available at a suggested retail of $150 from Scelbi Publications, 20 Hurlbut St, Elmwood CT 06110, (203) 522-5515.

Circle 682 on inquiry card.

Disk-O-Tape

Disk-O-Tape is a utility program for the Apple II and Apple II Plus computers. It enables users to transfer the data from a floppy disk to cassette tape and back again. The program features sector-by-sector copy of a DOS 3.2 disk to tape, error detection, and a verification pass for reliability. Each tape produced by the program contains a bootstrap for easy loading on disk. The program allows user-assigned naming of tapes. Disk-O-Tape requires at least 32 K bytes of programmable memory. The program comes on a floppy disk with Testape, a program to aid in adjusting the cassette recorder for optimum performance. Disk-O-Tape costs $12 from Dann McCreary, POB 16435-B, San Diego CA 92116.

Circle 683 on inquiry card.

Lifeboat Supports the Durango F-85

Lifeboat Associates has made available its 8080 software line formatted for the Durango F-85 computer. This software, which includes languages such as BASIC, COBOL, and Pascal; word-processing systems, such as Wordstar; communication software, such as BSTAM; and complete accounting packages, is available by the implementation of CP/M. The first version of CP/M supports the F-85 with up to four floppy-disk drives. This is priced at $170. Later versions will support the 12-megabyte and 25-megabyte hard-disk systems. Contact Lifeboat Associates, 1651 Third Ave, New York NY 10028, (212) 860-0300.

Circle 684 on inquiry card.

RECLAIM "Hides" Bad Sectors and Tracks from CP/M

Lifeboat Associates, 1651 Third Ave, New York NY 10028, (212) 860-0300, has announced a CP/M 2.0 utility program that tests floppy-disk and hard-disk systems for error-prone parts of the disk and allocates those parts to files that are invisible to the user. RECLAIM maps the bad spots out of the file directory so that they cannot be used again. It safely tests the disk with or without data files. At the completion of the program, it announces the number of blocks hidden from the file system. RECLAIM is available on all CP/M media formats supported by Lifeboat Associates. The cost is $80.

Circle 685 on inquiry card.

What's New?

SOFTWARE

Digital Synthesizer for the Apple

Mountain Computer Inc has developed the MusicSystem for the Apple II. This sixteen-voice digital synthesizer permits the creation of the sounds of real musical instruments utilizing the principle of additive synthesis. The generation of sounds is accomplished through programmable waveforms, envelopes, and amplitudes for each musical voice. Software is included for editing and playing of compositions. The editor program permits graphical input of sheet music utilizing standard music notation. The player program permits polyphonic performance of musical compositions. Stereo output is to user's stereo amplifier and speakers or directly off card with stereo headphones. For information, write or call Mountain Computer Inc, 300 Harvey W Blvd, Santa Cruz CA 95060, (408) 429-8600.

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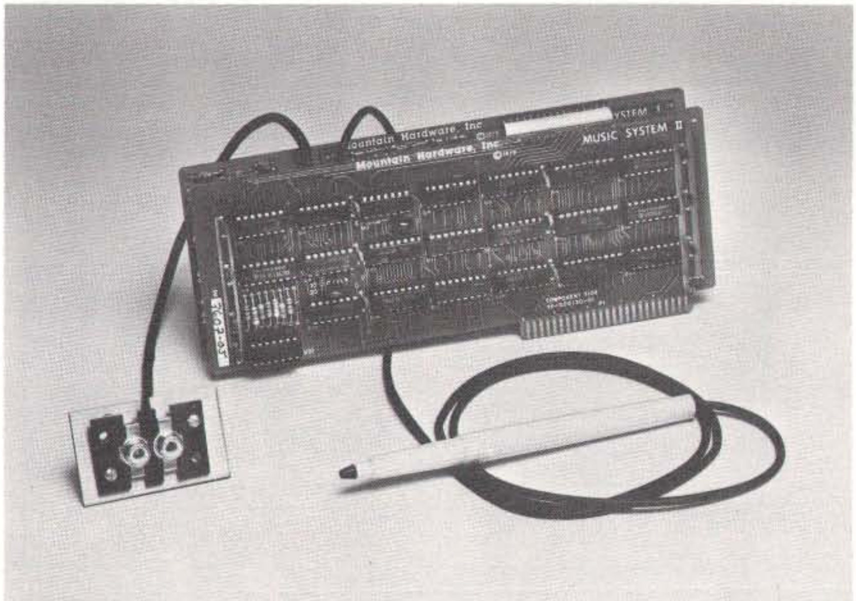

New Business Software for the TRS-80

American Business Systems (ABS) has announced that its line of financial- and business-applications software packages are now available to users of Radio Shack TRS-80 computers. These seven new ABS packages offer the same full-scale features and capabilities as the company's software for larger minicomputers and microcomputers.

The packages include a complete series of financial systems, ranging from Accounts Payable and Receivable through Payroll, Order Entry and Inventory Control to a fully automated General Ledger System. The application systems currently available include Financial Modeling and Real-Estate Sales Management. Additional packages soon to be released will offer a Client Accounting System and a Correspondence Management Package, which includes a letter writter, word processor and mailing-label generator.

Information is available from American Business Systems Inc, 439 Littleton Rd, Westford MA 01886, (617) 486-3509.

Circle 687 on inquiry card.

TRS-80 CP/M 2.0 with 12 Megabytes

Lifeboat Associates, 1651 Third Ave, NewYork NY 10028, (212) 860-0300, has announced the release of CP/M version 2.0 for the TRS-80 Model II. The system features extended density format for each of up to four floppy-disk drives. Nearly 2.5 megabytes of storage is possible with floppy-disk drives alone. The Corvus 10 megabyte Winchester hard disk is suggested as a storage system, allowing CP/M to access 12 megabytes of memory. A menu-driven configuration program allows total control of the parallel printer port and both serial ports of the TRS-80.

The printer port software can be set to control a "dumb" printer that has no page control, or the software page control can be disabled for printing checks or mailing labels. The system includes functions to set data rates of from 134.5 to 9600 bps (bits per second) for the serial ports. An ADM-3A emulation program is included which allows the TRS-80 to be used as a terminal through the serial ports. The system is offered with Corvus hard-disk capability for $250 and floppy-disk capability for $170.

Circle 688 on inquiry card.

Software for the Apple II

Softpoint, Dept C, 103 Clinton Ave, Terryville NY 11776, has announced cassette programs for the Apple II including Function Plot, Speed Reading, Road Race, and more. The programs utilize the Apple's high-resolution graphics capabilities. The prices range from $5.95 to $9.95.

Circle 689 on inquiry card.

Reformat for the TRS-80

Reformat is a programming aid to be used prior to compiling with the Microsoft BASIC compiler. The BASIC compiler allows the use of long variable names which can contain BASIC reserved words, making the format of a BASIC source file and the use of spaces critical. BASIC program files that are written as multistatement compressed lines will be rejected by the compiler in almost all cases. Bluebird's has developed this machine-language program which will reformat any TRS-80 BASIC source file into a format acceptable to the compiler. Reformat is available for $24.95 from Bluebird's Company 2267 23rd St, Wyandotte MI 48192, (313) 285-4455.

Circle 690 on inquiry card.

Data-Base Program for Z80 Systems

Condor Computer Corporation, 3989 Research Park Dr, Ann Arbor MI 48104, (313) 769-3988, has announced Target/80 DBMS, a data-base system for Z80 microcomputers. Target/80 is designed for transaction processing applications. This version uses nineteen commands, including relational operations for selecting, sorting, appending, or posting data. Target/80 is compatible with most Z80 systems with at least 48 K bytes of programmable memory running under CP/M. The price is $695.

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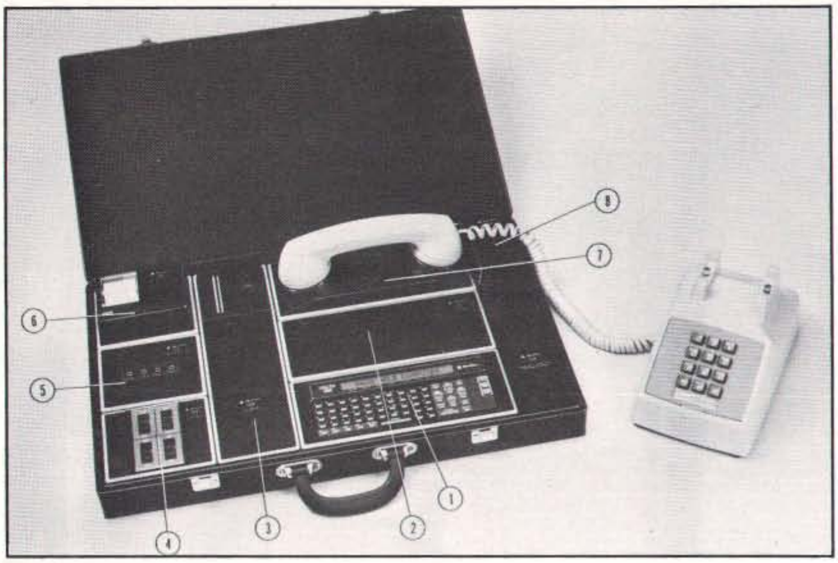

Computer in a Case

The Quasar Micro-Information System consists of a hand-held computer, video display, printer, modem, cassette deck, expandable programmable memory unit, I/O (input/output) driver —and it all fits in a briefcase. The hand-held computer fits in the palm of a hand, weighs less than a pound and controls the peripheral devices. A library of memory capsules in ROM (read-only memory) for use in the computer include fourteen languages, calorie counter, bar/wine guide, phonetic pronounciation, and games. The system is available from Quasar Company, Franklin Park IL 60131. Circle 692 on inquiry card.

Nine-Voice Synthesizer

Vista Media Products has announced the Music Machine Nine. Using LSI (large-scale integration) technology, the device can produce nine voices on the Apple II computer. The board uses three AY3-8910 integrated circuits and requires one expansion slot. It can use software now available to produce and play back nine-voice music compatible with other music boards. It will respond to commands for pitch, amplitude, duration, attack, delay, and more. Two high-impedance, low-level outputs are provided with six voices assigned to each channel. It is available through Advanced Computer Products, 1310 E Edinger, Santa Ana CA 92705, (714) 558-8813.

Circle 693 on inquiry card.

Logic Timing Recorder from A P Products

A P Products, 1359 W Jackson St, Painesville OH 44077, (800) 321-9668, in Ohio (216) 354-2101—collect, has introduced the Logic Timing Recorder, a device for charting logic timing. The unit is an ABS plastic board with 320 slides arranged in eight horizontal rows. The slides represent the two logic levels of a circuit. After the slides are manually moved into position to represent the logic state in a circuit, the board is checked for proper design, then it can be placed on a copying machine to make a permanent record for your files. The recorder may be used over and over again to chart the logic timing of all circuits. The Logic Timing Recorder, P/N 923758, has a suggested price of $44.95.

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A/D Converter for S-100 Systems

The AIM-12 is a 16- or 32-channel 12-bit A/D (analog-to-digital) converter designed for laboratory and industrial applications. The card plugs directly into the standard IEEE S-100 bus. Features include an on-board resistor programmable instrumentation amplifier and operation of up to 25 ms with 12 bits of accuracy. The AIM-12 is I/O (input/output) mapped and can be used with either BASIC or assembly-language instructions. The module is designed for direct conversion of voltages from thermocouples, level sensors, pressure transducers, pH electrodes and other low-level signal sources. The device provides thirty-two single-ended or sixteen fully differential inputs; input impedance exceeds one billion ohms. It is fully compatible with North Star, Cromemco, and most S-100 system. Multiple boards can be employed, and BASIC and assembly-language programs are supplied. The price of the AIM-12 is from $575, depending on options, from Dual Systems Control Corporation, 1825 Eastshore Hwy, Berkeley CA 94710, (415) 549-3854. Circle 695 on inquiry card.

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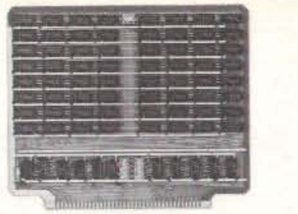

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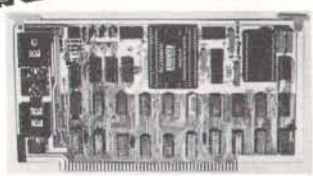

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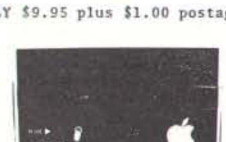

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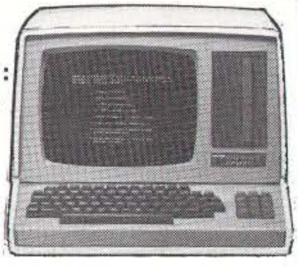

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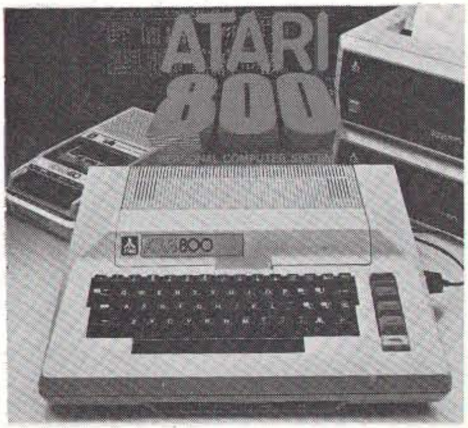

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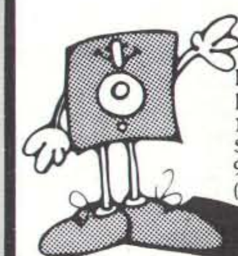

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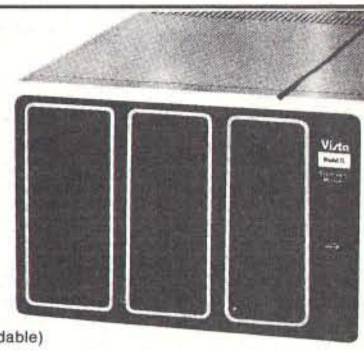
- Fully compatible with TRS-80, Heath/Zenith
- 120 Day Warranty
- 40 Track Patch at NO CHARGE

V-80 Single drive system (102K)	$ 395.00
V-80 Two drive system (204K)	$ 770.00
V-800 Single drive, B52 Drive (204K)	$ 595.00
V-800-2 Double drive, B52 Drives (408K)	$1175.00
V-8000 Single drive, B92 Drive (408K)	$ 775.00
V-8000-2 Double drive B92 Drives (816K)	$1450.00

The VISTA Model II

- Provides one, two or three drives.
- Adds up to 1.5 million bytes of on-line storage.
- 120 day warranty
- Does everything Radio Shack's expansion system will do. . . for less!

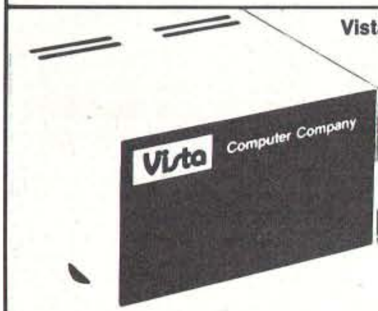


$ 900.00	Single drive (non-expandable)
$1000.00	Single drive Expansion System
$1550.00	Two drive Expansion System
$2100.00	Three drive Expansion System
$ 525.00	Additional drives alone

Vista's Add on Drives for Apple™

Speed . Capacity . Price

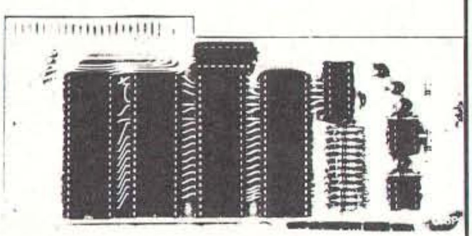
More for Less



- 30 to 60% cheaper per byte
- From 20 to 400% more capacity
- Twice as fast
- Compatible with Pascal
- Warranty 120 days

40-Track	$365.00
80-Track	$595.00
160-Track	$825.00
Controller	$100.00

The Vista MUSIC MACHINE 9



WITH 9 VOICES!

- NEW! Uses latest State of the Art LSI Technology.
- Requires only one slot for 9 voices!
- Uses three Ay3-8910's to produce nine voices (Other competitive models have only 3 voices).
- Simulates three ALF Boards.
- Plays music generated by the ALF Board.
- APPLE™ II compatible.
- ALF™ software required.

3 Times More Powerful Than ALF™!

$129.95

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Vista V300 .... **$1895.00**
Daisy wheel
Letter quality

Base 2 Printer .... **$575.00** (includes: 2-K Buffer, graphics, high speed tractor feed)
Variable line spacing control
0 to 64 dots in half dot increments
100 cps — six densities
Standard 96 character ASCII
Up to 10 character fonts

Anadex DP8000 .... **$895.00**
9 x 7 dot matrix
80 column (112 cps)

Vista Printer **$745.00**
5 x 7 dot matrix
80 column (125cps)

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MPI B52	Dual Head, Double Density-408K	$375.00
Siemens	FDD100-5 40 Track Double Density 204K	$275.00
Siemens	FDD100-8 8" Single Sided Drive	$448.00
Shugart	801R Single Sided Drive	$448.00

Other Products

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2. 16K RPM upgrade kits, guaranteed for 120 days. **PRIME PRODUCT**	$ 59.95
3. NEW! DOS +	$110.00
4. LNW expansion bare board	$ 66.95
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6. DDT Disco-Tech disk drive timer	$ 19.95

The VISTA V-200 for Exidy

- Completely packaged system, tested and ready to plug in, includes: power supply, two 40 track drives, case, controller, all cabling and total CPM documentation.
- Storage capacity from 400K to 1.2 meg.
- System software-VISTA CP/M Disk Operating System and BASIC-E Compiler recorded on 5-1/4" diskettes.

Price: Starting as low as **$1199.00**

Vista

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*TRS-80 is a registered trademark of Radio Shack.

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Circle 327 on inquiry card.

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Dynatyper—the patented* RDI—I/O Pak is fast becoming the industry standard for typewriter output. Why? Because:

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4. You don't have to lug around a bulky printer when you travel. If there is a typewriter at your destination, you can install the light (3 lbs.) I/O Pak in just 2 minutes.
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6. Delivery: Stock to two weeks. Price: $499. for the complete system, FOB Rochester, Domestic.

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*Patent Pending

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COMM-80™

Assembled and tested . . . $179.95

Includes case, power supply, ribbon cable, parallel printer port, serial port and TRS-BUS connector plus smart terminal software (please specify Level I or II).

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Circle 328 on inquiry card

page

PRE-CUT WIRE WRAP WIRE

Length	100/Bag	500/Bag	1K/Bag	Length	100/Bag	500/Bag	1K/Bag
2.5"	$1.25	$3.58	$ 6.19	6.5"	$1.92	$6.44	$11.81
3.0"	1.30	3.86	6.78	7.0"	1.99	6.76	12.44
3.5"	1.37	4.15	7.37	7.5"	2.08	7.07	13.09
4.0"	1.42	4.44	7.94	8.0"	2.14	7.38	13.73
4.5"	1.48	4.74	8.54	8.5"	2.18	7.69	14.36
5.0"	1.54	5.04	9.13	9.0"	2.24	8.11	15.01
5.5"	1.58	5.38	9.72	9.5"	2.30	8.32	15.65
6.0"	1.65	5.66	10.31	10.0"	2.39	8.71	16.28

Kynar precut wire. All lengths are overall, including 1" strip on each end. Colors and lengths cannot be mixed for quantity pricing. Choose from colors Red, Blue, Black, Yellow, White, Green, Orange, and Violet.

Kit No. 1		$9.95	
250	3"	100	4½"
250	3½"	100	5"
100	4"	100	6"

Kit No. 2		$24.95	
250	2½"	250	5"
500	3"	100	5½"
500	3½"	250	6"
500	4"	100	6½"
250	4½"	100	7"

Kit No. 3		$32.95	
500	2½"	500	4½"
500	3"	500	5"
500	3½"	500	5½"
500	4"	500	6"

Kit No. 4		$59.95	
1000	2½"	1000	4½"
1000	3"	1000	5"
1000	3½"	1000	5"
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★ ★ Truckload SALE ★ ★

FREE WIRE KIT

Kit #1 on orders over $50
Kit #3 on orders over $150
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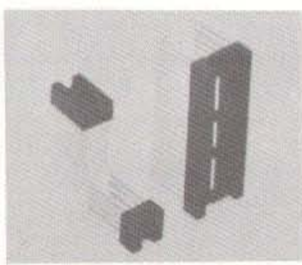

RN WIRE WRAP IC SOCKETS

3-level Gold
Closed Entry
Design

*Sockets sold at these prices by the tube only.

Size	Quantity/Tube	Price ea.*	Price/Tube
08 pin	52	.39	**$20.28**
14	30	.46	**$13.80**
16	26	.50	**$13.00**
18	23	.68	**$15.64**
20	21	.85	**$17.85**
22	19	.92	**$17.48**
24	17	.94	**$15.98**
28	15	1.23	**$18.45**
40	10	1.60	**$16.00**

Above prices include gold up to $800/oz.

IDC CRIMP STYLE CONNECTORS

Card Edge Connectors

IDE 10	**$3.25**
IDE 20	**$3.50**
IDE 26	**$4.05**
IDE 34	**$4.85**
IDE 40	**$5.65**
IDE 50	**$5.90**

IDC Sockets

IDS 10	**$1.25**
IDS 20	**2.02**
IDS 26	**2.65**
IDS 34	**3.50**
IDS 40	**4.05**
IDS 50	**5.06**

Cable Plugs

IDP 14	**1.25**
IDP 16	**1.40**
IDP 24	**2.25**
IDP 40	**3.65**

DB25 Connectors

Male	**$4.95**
Female	**$5.25**

Right Angle Headers

Wire Wrap		Solder Tail	
IDH10WR	**1.75**	IDH10SR	**.80**
IDH20WR	**2.75**	IDH20SR	**1.25**
IDH26WR	**3.60**	IDH26SR	**1.85**
IDH34WR	**4.15**	IDS34SR	**2.15**
IDH40WR	**4.90**	IDH40SR	**2.50**
IDH50WR	**6.15**	IDH50SR	**3.15**

CABLE

	10 ft.	100 ft.
10	**2.90**	**17.00**
14	**3.40**	**23.80**
16	**3.70**	**27.20**
20	**4.40**	**34.00**
24	**5.00**	**40.80**
26	**5.40**	**44.20**
34	**6.80**	**57.80**
40	**7.80**	**68.00**
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- Orders under $25 include $2 handling.
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- All prices good through cover date.
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- Byte must be mentioned to get sale prices.

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Abbreviations: S/E Solder Eye . S/T Sold Tail: W/W Wire Wrap.

PART #	DESCRIPTION.	Row Sp.	1-9pc.	10-24pcs.	25pcs. Up.
BRAND: TEXAS INST.					
4070	50/100 Imsai/Crom.	.250	$3.95ea.	$3.55ea.	$3.15ea.
4090	50/100 Imsai W/W	.250	4.30ea.	3.85ea.	3.45ea.
BRAND: SULLINS: U.L Reg.					
129865	50/100 Solder Eye	.140	6.80ea.	6.10ea.	5.45ea.
129870	50/100 S/T Imsai	.250	4.50ea.	4.10ea.	3.70
129875	50/100 W/W Imsai	.250	5.25	4.75	4.20
129885	50/100 S/T Altair	.140	4.95	4.45	3.95
129990	50/100 S/T Cromem.	.250	4.75	4.25	3.80
OTHER .125" CONTACT CTR CONNECTORS:					
12305	22/44 S/E No Ears	.140	4.15	3.75	3.35
12759	36/72 S/T	.140	5.40	4.85	4.35
12790	40/80 W/W	.250	6.30	5.65	5.00
.100" CONTACT CTR CONNECTORS:					
10048	13/26 S/E No Ears	.140	3.40	3.05	2.15
10280	25/50 S/E TRS 80	.140	4.50	4.05	3.60
10175	20/40 S/E TRS 80	.140	5.85	5.35	4.75
10180	20/40 W/W TRS 80	.200	3.30	3.00	2.15
10190	20/40 S/T TRS 80	.140	3.20	2.90	2.55
10485	36/72 S/E Vector	.140	5.50	4.90	4.40
10490	36/72 W/E Vector	.200	5.80	5.25	4.65
10500	36/72 S/T Vector	.140	5.70	4.20	4.60
10535	40/80 S/E PET	.140	5.85	5.35	4.75
10540	40/80 W/W PET	.200	6.00	5.40	4.80
10550	40/80 S/T PET	.140	5.80	5.25	4.65
10585	43/86 S/E COS/ELF	.140	6.95	6.25	5.55
10605	43/86 S/T COS/ELF	.140	6.60	5.95	5.30
10595	43/86 W/W COS/ELF	.200	6.90	6.20	5.95
10615	43/86 S/T COS/ELF	.200	6.80	6.10	5.40

.156" CONTACT CENTER CONNECTORS.

PART #	DESCRIPTION.	Row Sp.	1-9pc.	10-24pcs.	25pcs. Up.
15105	6/12 S/E PET/NSC	.140	$1.80	$1.65	$1.45
15110	6/12 S/T PET/NSC	.140	1.85	1.65	1.50
15137	6/12 S/T PET/NSC	.200	1.80	1.54	1.45
15175	6/- S/E Sgle Row	- -	1.70	1.50	1.30
15270	10/20 S/E	.140	2.15	1.95	1.70
15275	10/20 S/T	.140	2.00	1.85	1.60
15435	12/24 S/E PET	.140	2.60	2.35	2.10
15440	12/24 S/T PET	.140	2.65	2.40	2.15
15445	12/24 S/T PET	.200	2.75	2.50	2.20
15505	15/30 S/E GRI Key	.140	2.50	2.25	2.00
15510	15/30 S/T GRI Key	.140	2.40	2.15	2.95
15515	15/30 W/W GRI Key	.200	2.60	2.35	2.10
15600	18/36 S/E	.140	3.35	3.05	2.70
15610	18/36 S/T	.140	3.00	2.70	2.40
15615	18/36 W/W	.200	3.60	3.20	2.90
15700	22/44 S/E KIM/VEC	.140	2.98	2.90	2.75
15705	22/44 S/T KIM/VEC	.140	3.98	3.30	3.00
15710	22/44 W/W KIM/VEC	.200	3.49	3.20	2.85
15875	25/50 S/E	.140	4.65	4.20	3.75
15880	25/50 S/T	.140	4.55	4.10	3.65
15885	25/50 W/W	.200	4.85	4.35	3.90
16115	36/72 S/E	.140	6.50	5.85	5.20
16120	36/72 S/T	.140	6.55	5.90	5.25
16125	36/72 W/W	.200	6.75	6.10	5.40
16145	36/72 S/T	.200	6.50	5.85	5.20
16235	43/86 S/T Mot 6800	.140	6.60	5.95	5.30
16240	43/86 W/W Mot 6800	.200	7.80	7.05	6.25
16260	43/86 S/T Mot 6800	.200	6.50	5.85	5.20
16725	43/86 S/E Mot 6800	.140	7.20	6.50	5.75
K-1	Pol-Keys		.15	.12	.10

'D' TYPE SUBMINIATURE CONNECTORS.

PART NUMBER		DESCRIPTION.	QUANTITY 1-9pcs.	10-24pcs.	25-9
DE	9P	Male	$1.60ea.	$1.40ea.	$1.3
DE	9S	Female	2.25ea.	2.00ea.	1.9
DE	110963-1	2 pc. Grey Hood.	1.50ea.	1.35ea.	1.2
DA	15P	Male	2.35ea.	2.15ea.	2.0
DA	15S	Female	3.25ea.	3.10ea.	2.9
DA	51211-1	1. pc. Grey Hood	1.40ea.	1.20ea.	1.1
DA	51226-1	2 pc. Black Hood	2.50ea.	2.25ea.	2.0
DA	110963-2	2 pc. Grey Hood	1.60ea.	1.35ea.	1.3
DB	25P	Male	2.80ea.	2.60ea.	2.4
DB	25S	Female	3.60ea.	3.40ea.	3.2
DB	51212-1	1 pc. Grey Hood	1.50ea.	1.30ea.	1.1
DB	51226-1	2 pc. Black Hood	1.90ea.	1.65ea.	1.4
DB	110963-3	2 pc. Grey Hood	1.75ea.	1.50ea.	1.3
DC	37P	Male	4.20ea.	4.00ea.	3.7
DC	37S	Female	6.00ea.	5.75ea.	5.5
DC	110963-4	2 pc. Grey Hood	2.25ea.	2.00ea.	1.7
DD	50P	Male	5.50ea.	5.10ea.	4.7
DD	50S	Female	9.40ea.	8.60ea.	8.0
DD	51216-1	1 pc. Grey Hood	2.40ea.	2.20ea.	2.
DD	110963-5	2 pc. Grey Hood	2.60ea.	2.40ea.	2.1
D	20418-2	Hardware Set (1 Hood Set)	.90ea.	.80ea.	.7

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14 pin	$0.40 ea.
16 pin	0.44 ea.

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Circle 331 on inquiry card.

Circle 333 on inquiry card.

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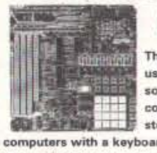
SD SYSTEMS



Z-80 STARTER SYSTEM

The Z80 Starter Kit by SD Systems uses the powerful Z80 microprocessor as the heart of the complete microcomputer on a single board. Learn a step-by-step introduction to microcomputers with a keyboard and display, audio cassette interface, PROM programmer, wire-wrap expansion area, 4-channel Counter Timer and on Board RAM and PROM. Complete Opperation and Instruction Manual included. ZBUG Monitor in ROM.

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SBC-100 SINGLE BOARD **COMPUTER KIT**
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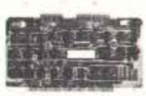

27002 assem/T ... $250
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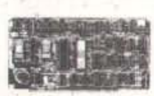

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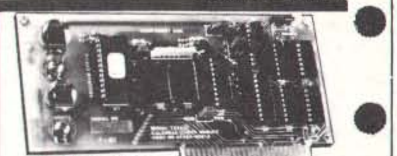

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The MSM5832 normally operates from a 5 volt ±5% supply. Battery back-up operation down to 2.2 volts allows continuation of time keeping when main power is off. The test input facilitates rapid testing of the time keeping operations. The MSM5832 is offered in an 18 lead dual in-line plastic (RS suffix) package.

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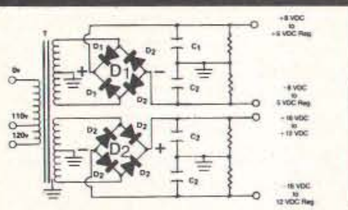

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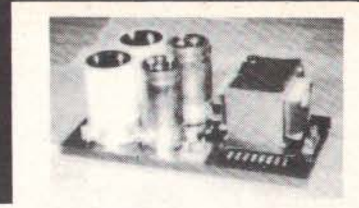

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ITEM NO.	USED IN KIT NO.	PRI. WINDING TAPS	SECONDARY WINDING OUTPUTS 2×8 Vac	2×14 Vac	2×24 Vac	SIZE W × D × H	UNIT PRICE
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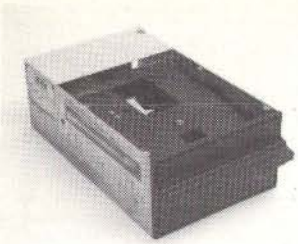

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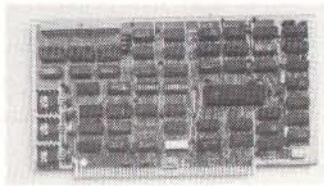

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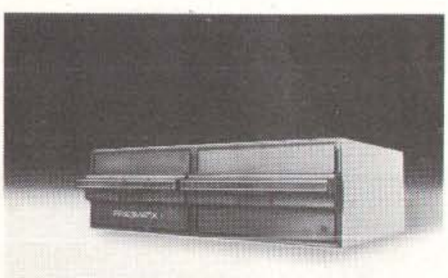

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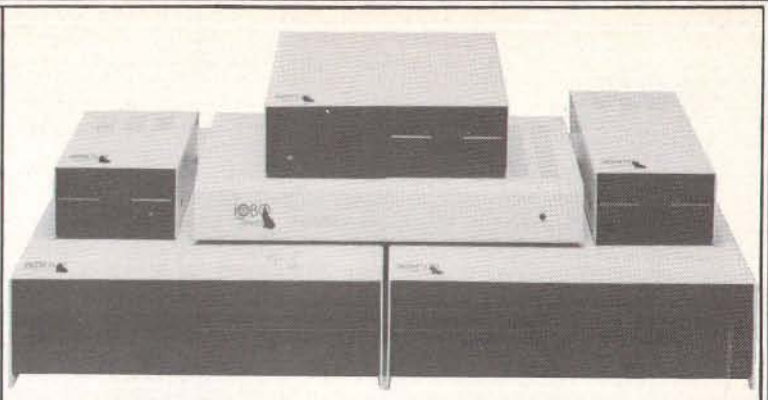

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GENERAL

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8212C	Two SA801 in cabinet w/power
5212	Two SA851 in cabinet
5212C	Two SA851 in cabinet w/power

TRS80

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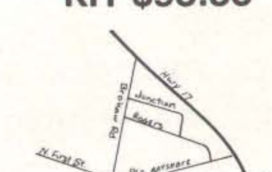

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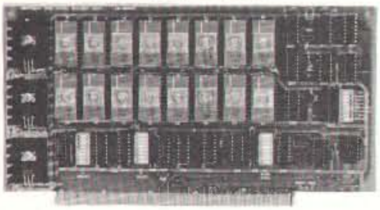

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SPECIAL: 2716 EPROM's (450 NS) Are $14.95 EA. With Above Kit.

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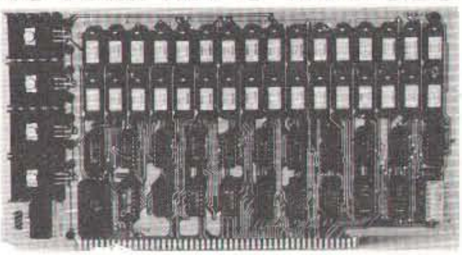
PRICE CUT!

$199.95 KIT

FOR 4MHZ ADD $10



KIT FEATURES:
1. Addressable as four separate 4K Blocks.
2. ON BOARD BANK SELECT circuitry. (Cromemco Standard!). Allows up to 512K on line!
3. Uses 2114 (450NS) 4K Static Rams.
4. ON BOARD SELECTABLE WAIT STATES.
5. Double sided PC Board, with solder mask and silk screened layout. Gold plated contact fingers.
6. All address and data lines fully buffered.
7. Kit includes ALL parts and sockets.
8. PHANTOM is jumpered to PIN 67.
9. LOW POWER: under 1.5 amps TYPICAL from the +8 Volt Buss.
10. Blank PC Board can be populated as any multiple of 4K.

BLANK PC BOARD W/DATA-**$33**
LOW PROFILE SOCKET SET-**$12**
SUPPORT IC'S & CAPS-**$19.95**
ASSEMBLED & TESTED-**ADD $35**

OUR #1 SELLING RAM BOARD!

16K STATIC RAM SS-50 BUSS

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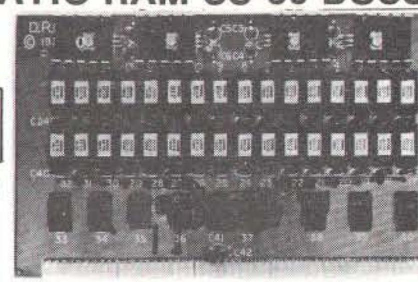

$210 KIT

FULLY STATIC!

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FOR SWTPC 6800 BUSS!

ASSEMBLED AND TESTED - $35

KIT FEATURES:
1. Addressable on 16K Boundaries
2. Uses 2114 Static Ram
3. Fully Bypassed
4. Double sided PC Board. Solder mask and silk screened layout.
5. All Parts and Sockets included
6. Low Power: Under 1.5 Amps Typical

BLANK PC BOARD—**$30** COMPLETE SOCKET SET—**$12**
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NEW! **STEREO!** NEW!
S-100 SOUND COMPUTER BOARD

At last, an S-100 Board that unleashes the full power of two unbelievable General Instruments AY3-8910 NMOS computer sound IC's. Allows you under total computer control to generate an infinite number of special sound effects for games or any other program. Sounds can be called in BASIC, ASSEMBLY LANGUAGE, etc.

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BLANK PC BOARD W/DATA $31

KIT FEATURES:
* TWO GI SOUND COMPUTER IC'S.
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* USES ON BOARD AUDIO AMPS OR YOUR STEREO.
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* USES PROGRAMMED I/O FOR MAXIMUM SYSTEM FLEXIBILITY

Both Basic and Assembly Language Programming examples are included.

SOFTWARE:
SCL™ is now available! Our Sound Command Language makes writing Sound Effects programs a SNAP! SCL™ also includes routines for Register-Examine-Modify, Memory-Examine-Modify, and Play-Memory. SCL™ is available on CP/M* compatible diskette of 2708 or 2716. Diskette - **$24.95** 2708 - **$19.95** 2716 - **$29.95** Diskette includes the source. EPROM'S are ORG at E000H.

4K DYNAMIC RAM BLOWOUT!
SAME AS INTEL 2107B!
4K RAMS AT AN UNBELIEVABLE 50¢ EACH!!!

Prime, new, National Semi., 1979 date coded, full spec. parts. N.S. #MM5280-5N. Same as INTEL 2107B-4, T.I. TMS4060, NEC uPD411, etc. We bought a HUGE QTY. from a West Coast Distributor at truly DISTRESS PRICES! One of the most popular and reliable RAM's ever made. These parts have been used by almost all Major Computer Main Frame Mfg. the world over! Arranged as 4K x 1, 270 NS Access Time, 22 Pin Dip. These units DO NOT use multiplexed addressing, thus making REFRESH and other timing very simple. See INTEL MEMORY DESIGN HANDBOOK for full application notes. The NAT. SEMI. MEMORY DATA BOOK is available at most Radio Shack Stores. Prime units in original factory tubes!

(With Pin Out Data) **#5280-5N 4096 BITS x 1 270 NS ACCESS**
8 FOR $4.95 32 FOR $16
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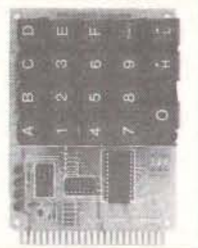
HEX ENCODED KEYBOARD



Four onboard LEDs indicate the HEX code generated for each key depression. The board requires a single +5 volt supply. Board only $15.00 Part No. HEX-3, with parts $49.95 Part No. HEX- 3A. 44 pin edge connector $4.00 Part No. 44P.

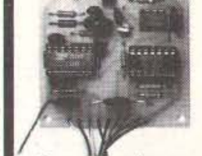
ASCII TO CORRESPONDENCE CODE CONVERTER

This bidirectional board is a direct replacement for the board inside the Trendata 1000 terminal. The on board connector provides RS-232 serial in and out. Sold only as an assembled and tested unit for $249.95. Part No. TA 1000C

T.V. INTERFACE

● Converts video to AM modulated RF, Channels 2 or 3. So powerful almost no tuning is required. On board regulated power supply makes this extremely stable. Rated very highly in Doctor Dobbs' Journal. Recommended by Apple ● Power required is 12 volts AC C.T., or +5 volts DC ● Board only $7.60 part No. 107, with parts $13.50 Part No. 107A

TAPE INTERFACE



• Converts a low cost tape recorder to a digital recorder • Works up to 1200 baud • Digital in and out are TTL-serial • Output of board connects to mic. in of recorder • Earphone of recorder connects to input on board • No coils • Requires +5 volts, low power drain • Board only $7.60 Part No. 111, with parts $29.95 Part No. 111A

ASCII KEYBOARD

53 Keys popular ASR-33 format • Rugged G-10 P.C. Board • Tri-mode MOS encoding • Two-Key Rollover • MOS/DTL/TTL Compatible • Upper Case lockout • Data and Strobe inversion option • Three User Definable Keys • Low contact bounce • Selectable Parity • Custom Keycaps • George Risk Model 753. Requires +5, -12 volts. $59.95 Kit.

T.V. TYPEWRITER

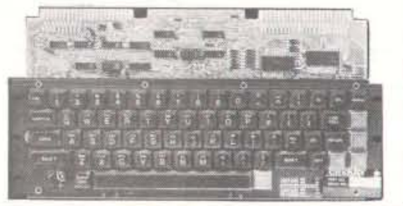
● Stand alone TVT ● 32 char/line, 16 lines, modifications for 64 char/line included ● Parallel ASCII (TTL) input ● Video output ● 1K on board memory ● Output for computer controlled curser ● Auto scroll ● Non-destructive curser ● Curser inputs: up, down, left, right, home, EOL, EOS ● Scroll up, down ● Requires +5 volts at 1.5 amps, and -12 volts at 30 mA ● All 7400, TTL chips ● Char. gen. 2513 ● Upper case only ● Board only $39.00 Part No. 106, with parts $145.00 Part No. 106A

UART & BAUD RATE GENERATOR

● Converts serial to parallel and parallel to serial ● Low cost on board baud rate generator ● Baud rates: 110, 150, 300, 600, 1200, and 2400 ● Low power drain +5 volts and -12 volts required ● TTL compatible ● All characters contain a start bit, 5 to 8 data bits, 1 or 2 stop bits, and either odd or even parity. ● All connections go to a 44 pin gold plated edge connector ● Board only $12.00 Part No. 101, with parts $35.00 Part No. 101A, 44 pin edge connector $4.00 Part No. 44P

ASCII KEYBOARD

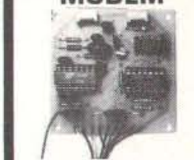
TTL & DTL compatible • Full 67 key array • Full 128 character ASCII output • Positive logic with outputs resting low • Data Strobe • Five user-definable spare keys • Standard 22 pin dual card edge connector • Requires +5VDC, 325 mA. Assembled & Tested. Cherry Pro Part No. P70-05AB. $119.95.



SOROC IQ 120

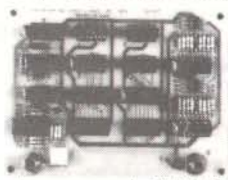
Upper/lower case display • Numeric keypad & cursor keys • Protected fields, ½ intensity display • RS 232 interface & aux. port. IQ120—$799.95 • IQ140 Detachable keyboard—$1199.95

MODEM



● Type 103 ● Full or half duplex ● Works up to 300 baud ● Originate or Answer ● Serial TTL input and output ● connect 8 Ω speaker and crystal mic. directly to board ● Requires +5 volts ● Board only $7.60 Part No. 109, with parts $29.95 Part No. 109A.

A-to-D D-to-A CONVERTER

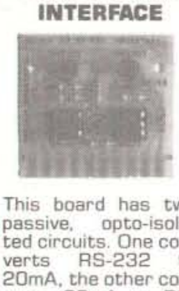


Analog to Digital, Digital to Analog Converter, A-D conversion time 20us. D-A conversion 5us. Uses include speech and music synthesizing and slow scan TV. Single power supply (5V), 8 Bits wide, latched I/O, strobe lines. Part No. 79287K Complete Kit $49.95 • Part No. 79287A Assembled $69.95

44 BUS MOTHER BOARD

Has provisions for ten 44 pin (.156) connectors, spaced 3/4 of an inch apart. Pin 20 is connected to X, and 22 is connected to Z for power and ground. All the other pins are connected in parallel. This board also has provisions for bypass capacitors. Board cost $15.00 Part No. 102. Connectors $3.00 each Part No. 44WP.

RS-232/20mA INTERFACE

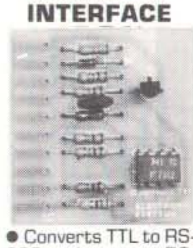


This board has two passive, opto-isolated circuits. One converts RS-232 to 20mA, the other converts 20mA to RS-232. All connections go to a 10 pin edge connector. Requires +12 and -12 volts. Board only $9.95, part no. 7901, with parts $14.95 Part No. 7901A.

SOLID STATE SWITCH

Your computer can control power (120VAC) to your printer, lights, and other 120VAC appliances up to 720 watts (6AMPS at 120VAC). Input 3 to 15 VDC, 2-13 MA TTL compatible, isolation 1500V. Part No. 79000K 1 Channel Kit $9.95 • Assm. $12.50 • Part No. 79004K 4 Channel Kit $34.95 • Assm. $44.95.

RS-32/TTL INTERFACE



● Converts TTL to RS-232, and converts RS-232 to TTL ● Two separate circuits ● Requires -12 and +12 volts ● All connections go to a 10 pin edge connector, kit $9.95 Part No. 232A 10 Pin edge connector $3.00 part No. 10P.

COMPUCOLOR II

With reg. keyboard MOD3 8K $1449.95 MOD4 16 K $1495.95 MOD5 32K $1699.95 Without disk drive subtract $450.00. Add-on drives, $495.00. With 101 key option add $134.95. With 117 key option add $179.95.

SUPER MODEM

Orignate, RS-232 and 20 mA compatable, Full duplex, and half duplex, direct connect or acoustic coupled, on board power supply, carrier detect light, DB25 plug, 300 BAUD, Type 103 compatable frequencies, Bare board Part No. 2000, $19,95, Kit Part No. 2000A, $99.95.

DC POWER SUPPLY

• Board supplies a regulated +5 volts at 3 amps., +12, -12, and -5 volts at 1 amp. • Power required is 8 volts AC at 3 amps., and 24 volts AC C.T. at 1.5 amps. • Board only $12.50 Part No. 6085, with parts excluding transformers $42.50 Part No. 6085A

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16K $975.95, Extra 16K E.S. RAM installed $74.95, extra 32K E.S. RAM installed $148.95.

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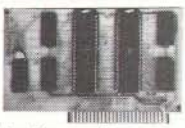
Part No. 7907 $14.95

APPLE II PARALLEL INTERFACE



Interfaces printers, synthesizers keyboards, and JBE A-D D-A Converter & Switches. This interface has 4 I/O ports with handshaking logic, 2-6522 VIA's and a 74LS74 for timing. Inputs and outputs are TTL compatible. Part No. 79295K Complete Kit—$69.95 • Part No. 79295A Assembled—$79.95

REAL TIME 100,000 DAY CLOCK

MT. HARDWARE Double the utility of your S-100 bus computer with a real-time clock that keeps time in 100μS increments for over 273 years. Program events for the entire period with real time interrupts...without derailing the system. Maintain a log of computer usage, time and date transaction printouts, call up lists. On-board battery backup. MHPX004—$349.00

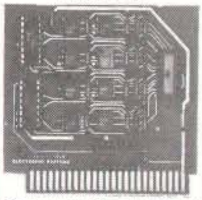
16K EPROM

Uses 2708 EPROMS, memory speed selection provided, addressable anywhere in 65K of memory, can be shadowed in 4K increments. Board only $24.95 part no. 7902, with parts less EPROMs $49.95 part no. 7902A.

PET COMPUTER

With 16K & monitor—$895.00 • Dual Disk Drive —$1095.00

OPTO-ISOLATED PARALLEL INPUT BOARD FOR APPLE II



There are 8 inputs that can be driven from TTL logic or any 5 volt source. The circuit board can be plugged into any of the 8 sockets of your Apple II. It has a 16 pin socket for standard dip ribbon cable connection. Board only $15.00. Part No. 120, with parts $69.95. Part No. 120A.

VIDEO TERMINAL

16 lines, 64 columns • Upper and lower case • 5x7 dot matrix • Serial RS-232 in and out with TTL parallel keyboard input • On board baud rate generator 75, 110, 150, 300, 600, & 1200 jumper selectable • Memory 1024 characters (7-21L02) • Video processor chip SFF96364 by Neculonic • Control characters (CR, LF, →, ←, ↑, ↓, non destructive cursor, CS, home, CL • White characters on black background or vice-versa • With the addition of a keyboard, video monitor or TV set with TV interface (part no. 107A) and power supply this is a complete stand alone terminal • also S-100 compatible • requires +16, & −16 VDC at 100mA, and 8VDC at 1A. Part No. 1000A $199.95 kit.

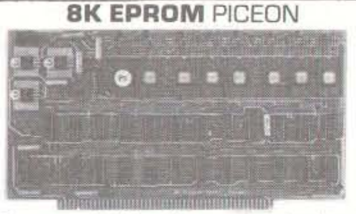
PARALLEL TRIAC OUTPUT BOARD FOR APPLE II

This board has 8 triacs capable of switching 110 volt 6 amp loads (660 watts per channel) or a total of 5280 watts. Board only $15.00 Part No. 210, with parts $119.95 Part No. 210A

APPLE II SERIAL I/O INTERFACE

Baud rate is continuously adjustable from 0 to 30,000 • Plugs into any peripheral connector • Low current drain. RS-232 input and output • On board switch selectable 5 to 8 data bits, 1 or 2 stop bits, and parity or no parity either odd or even • Jumper selectable address • SOFTWARE • Input and Output routine from monitor or BASIC to teletype or other serial printer • Program for using an Apple II for a video or an intelligent terminal. Also can output in correspondence code to interface with some selectrics. • Also watches DTR • Board only $15.00 Part No. 2, with parts $42.00 Part No. 2A, assembled $62.00 Part No. 2C

8K EPROM PICEON



• Programs 2708's address relocation of each 4K of memory to any 4K boundary • Power on jump and reset jump option for "turnkey" systems and computers without a front panel • Program saver software in 1 2708 EPROM $25. Bare board $35 including custom coil, board with parts but no EPROMS $139, with 4 EPROMS $179, with 8 EPROMS $219.

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MEM-1A 8Kx8 fully buffered, S-100, uses 2102 type RAMS. PCBD .... $24.95, $168 Kit

QMB-12 MOTHER BOARD, 13 slot, terminated, S-100 board only .... $34.95 $89.95 Kit

CPU-1 8080A Processor board S-100 with 8 level vector interrupt PCBD .. $25.95 $89.95 Kit

RTC-1 Realtime clock board. Two independent interrupts. Software programmable. PCBD .... $25.95, $60.95 Kit

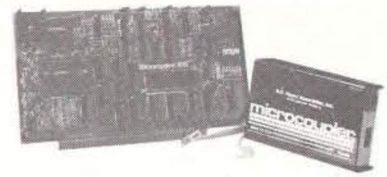
EPM-2 2708/2716 16K/32K EPROM card PCBD .... $24.95 $49.95 with parts less EPROMS

QMB-9 MOTHER BOARD. Short Version of QMB-12. 9 Slots PCBD .... $30.95 $67.95 Kit

MEM-2 16Kx8 Fully Buffered 2114 Board PCBD .... $25.95, $269.95 Kit

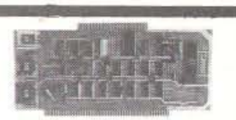
YOU MUST REFER TO THIS AD TO GET THESE PRICES.

D.C. HAYES MICROMODEM



Fully S-100 bus compatible including 16-bit machines and 4 MHz processors. • Two software selectable Baud rates—300 Baud and a jumper selectable speed from 45 to 300 Baud. (110 standard). Supports originate and answer modes. • Direct-connect Microcoupler. This FCC-registered device provides direct access into your local telephone system, with none of the losses or distortions associated with acoustic couplers and without a telephone company supplied data access arrangement. • Auto-Answer/Auto-Call. The MICROMODEM 100 can automatically answer the phone and receive input; it can also dial a number automatically. • Automatic Reset and Disconnect. • Software compatible with the D.C. Hayes Associates 80-103A Data Communications Adapter. Micromodem-DCHA32625—$379.95

TIDMA



Tape Interface Direct Memory Access • Record and play programs without bootstrap loader (no prom) has FSK encoder/decoder for direct connections to low cost recorder at 1200 baud rate, and direct connections for inputs and outputs to a digital recorder at any baud rate • S-100 bus compatible • Board only $35.00 Part No. 112, with parts $110.00 Part No. 112A.

SYSTEM MONITOR

8080, 8085, or Z-80 System monitor for use with the TIDMA board. There is no need for the front panel. Complete with documentation $12.95.

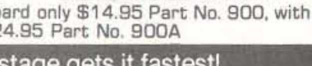
RS-232/TTY INTERFACE

This board has two active circuits, one converts RS-232 to 20 mA, the other converts 20 mA to RS-232. Requires +12 and -12 volts. $9.95 Part No. 600A Kit.

SERIAL I/O

Four Serial I/O RS-232 ports, S-100 Bus, Software or jumper selectable baud rate (110, 300, 600, 1200, 2400, 4800, 9600, 19.2K), on board Xtal baud rate generator, Addressing, switch selectable, Parity or no parity (odd or even) switch selectable, 1 or 2 stop bits, 5 to 8 bits/character. Board only $29.95, Part No. 7908. With parts (kit) $199.95, Part No. 7908A.

S-100 BUS ACTIVE TERMINATOR



Board only $14.95 Part No. 900, with parts $24.95 Part No. 900A

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SHUGART – SIEMANS – CDC 8"				
+5V @ 1A	–5V @ .5A	+24V @ 1.5A	US-205	52.50
+5V @ 2A	–5V @ .5A	+24V @ 3A	US-206	69.00
+5V @ 3A	–5V @ .6A	+24V @ 5A	US-162	89.00
+5V @ 1.7A	–5V @ 1.5A	+24V @ 2A	US-272	69.00
+5V @ 2A	+12V @ .4A	–12V @ .4A	US-HTAA	37.50

TELEVIDEO 912C

$665.00

Also have 920C, SOROC, HAZELTINE, etc. What we don't have is room on this page. Call Toll Free 800 number for prices.

C-ITOH PRINTER

$499.00

Look closely at the photo and see other adds in this rag at $995.00. Perfect units, warranteed. Only 500 pcs. Same story, manufacturerer had too many.

ATARI

800 – $899.00
400 – $499.00

As long as there is a price war, we will fight our battle. Compare at your local Dept. Store and buy U$ MICRO.

S-100 POWER

$49.50

Simple Brute Force! S-100 Power Supply, 10A @ +8V, A @ +16V, A @ –16V, PC Board Design.

SPECIAL OF THE QUARTER

DUAL DRIVE SUBSYSTEM

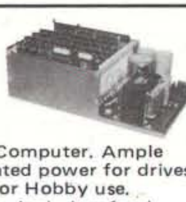
$995.00

2 SHUGART 801R POWER SUPPLY

If this looks like a Lobo Drive System, don't be fooled. Just because it looks like one, works like one, smells like one, and tastes like one (?) doesn't mean it has to cost like one!

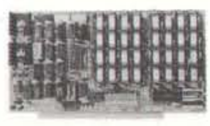
S1-MOD (KIT)

$239.00



Complete S-100 12 Slot Computer. Ample system power with regulated power for drives. Excellent for Subsystem or Hobby use. 4 hours to build. (6 conn. incl., less fans)

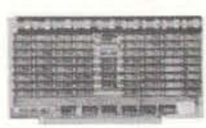
EXPANDABLE RAM (KIT)



This is the best all around 64K board you can buy. If after you see it, you don't agree return (unassembled) for full refund. Bank Select by extended address lines.

32K – $380.00   64K – $450.00

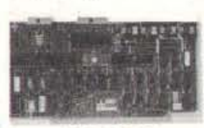
32K STATIC (KIT)



You have seen this well known board around for years. We bought 500 of them and plan on cornering the market! Bank Select on extended address lines.

$388.00

Z-80 CPU (KIT)



The first time this world popular CPU offered in Kit. 2 serial, 3 parallel, CTC, EProm Z-80 at 4 mhz. Software buad rate, etc. (less Prom)

$212.00

DMA DISK CONTROLLER (KIT)

How many DMA Disk Controllers are there on the market? Can't mention the name at this LOW, LOW, LOW, price.

$265.00

12 SLOT MOTHER

We have connectors and power supply too. Start your system with quality components. Terminated.

CONNECTORS $3.50 ea.   **$22.50**

SPECIALS OF THE MONTH

4116s

$4.25

Expansion 16K Dynamic RAMs for Apple, TRS-80 S-100 systems. T.I., Mostek Intel, Call for manufacturer.

200 NS

DIP-80 $399.00

Don't be mislead by this LOW price. This is a rugged 100% Duty Cycle 7 by 7 Dot Matrix Printer. Brand new, factory warr.

- RS-232 ADD $65.00
- TRACTOR FEED ADD $70.00

2114s

$3.95

One of the world's two most popular STATIC RAMs. Factory prime tested units. Sold in lots of 8 only. FUJITSU, HITACHI, etc.

200 NS

TMS-4044 / MM-5257 / INTEL 2147

$4.25

250 NS

The other of the world's most popular STATIC RAMs. This one is 4K by 1 organization. Don't buy Gold, buy these, the price won't last!

2716s $13.50 (450 NS)
2708s $6.95 (450 NS)

Remember when 2716s were $50.00 and hard to get? These units are so beautiful it's hard to part with them. But we will, for a small price. Guaranteed!

SHUGART DRIVE

8" 851R $585.00

8" 801R
$385.00

Manufacturer had too many, buys at 1000 piece rate, sales dropped, so we got'em. Fantastic buy, get them while they last! Full warranty.

5¼" MINI

$265.00

Now is the time to expand that Trash-80 or Rotton Apple (no offense!) These go so fast. Quantities limited to those on hand. Hurry! No Junkers! Factory warranty.

SHUGART / SIEMANS / MPI

Quest ELECTRONICS

P.O. Box 4430X
Santa Clara, CA 95054
Will calls: 2322 Walsh Ave.
(408) 988-1640

Same day shipment. First line parts only. Factory tested. Guaranteed money back. Quality IC's and other components at factory prices.

INTEGRATED CIRCUITS

7400TTL	
7400N	.19
7402N	.20
7404N	.25
7409N	.23
7410N	.20
7414N	.70
7420N	.20
7422N	.39
7430N	.20
7442N	.58
7445N	.74
7447N	.58
7448N	.77
7450N	.20
7474N	.35
7475N	.49
7485N	.88
7489N	1.85
7490N	.43
7492N	.43
7493N	.43
7495N	.69
74100N	1.20
74107N	.35
74121N	.34
74123N	.59
74125N	.45
74145N	.77
74150N	1.20
74151N	.69
74154N	2.00
74157N	.69
74161N	.87
74162N	1.25
74163N	.87
74174N	.96
74175N	.95
74190N	1.15
74192N	.87
74193N	.85
74221N	2.75
74298N	1.65
74365N	.89
74366N	.89
74367N	.89

74LS00 TTL	
74LS00N	.35
74LS02N	.35
74LS04N	.55
74LS05N	.40
74LS08N	.45
74LS10N	.45
74LS13N	.55
74LS14N	1.25
74LS20N	.45
74LS22N	.45
74LS28N	.45
74LS30N	.45
74LS33N	.75
74LS38N	.75
74LS74N	1.25
74LS75N	1.00
74LS90N	1.00
74LS93N	1.00
74LS95N	1.10
74LS107N	.65
74LS112N	.65
74LS113N	.45
74LS132N	.89
74LS136N	.65
74LS151N	1.10
74LS155N	1.10
74LS157N	1.10
74LS162N	1.65
74LS163N	1.65
74LS174N	2.00
74LS190N	1.25
74LS221N	2.50
74LS258N	1.60
74LS367N	1.65

LINEAR	
CA3045	.90
CA3046	1.10
CA3081	1.80
CA3082	1.90
CA3089	2.95
LM301AN/AH	.35
LM305H	.87
LM307N	.35
LM308N	1.00
LM309K	1.50
LM311H/N	.90
LM317T/K	3.75
LM318	1.35
LM320K-5	1.50
LM323K-5	5.95
LM320K-12	1.50
LM320K-15	1.50
LM320T-5	1.35
LM320T-8	1.35
LM320T-12	1.35
LM320T-15	1.35
LM324N	1.40
LM339N	1.00
LM340K-5	1.35
LM340K-8	1.35
LM340K-12	1.35
LM340K-15	1.35
LM340K-24	1.35
LM340T-5	1.25
LM340T-8	1.25
LM340T-12	1.25
LM340T-15	1.25
LM340T-18	1.25
LM340T-24	1.25
LM350	7.50
LM377	3.50
LM379	5.00
LM380N	1.00
LM381	1.60
LM382	1.60
LM703H	.65
LM709H	.28
LM723H/N	.50
LM733N	.85
LM741CH	.35
LM741N	.38
LM747H/N	.75
LM748N	.35
LM1303N	1.75
LM1304	1.10
LM1305	1.27
LM1307	2.00
LM1310	2.75
LM1458	.47
LM1812	7.50
LM1889	3.00
LM2111	1.75
LM2902	2.25
LM3900N	.60
LM3905	1.75
LM3909N	.95
MC1458V	.50
NE550N	1.00
NE555V	.39
NE556A	.85
NE565A	1.00
NE566V	1.50
NE567V	1.00
NE570B	4.75
78L05	.60
78L08	.60
78M05	.85
75108	1.75
75491CN	.50
75492CN	.55
75494CN	.89

A to D CONVERTER	
8038B	4.50
8700CJ	13.95
8701CN	22.00
8750CJ	13.95
LD130	9.95
9400CJ/V/F	7.40
ICL7103	9.50
ICL7107	14.25

CMOS	
CD4000	.25
CD4001	.35
CD4002	.35
CD4006	1.10
CD4007	.35
CD4008	.28
CD4009	.45
CD4010	.45
CD4011	.35
CD4012	.28
CD4013	.47
CD4014	1.25
CD4015	1.00
CD4016	.55
CD4017	1.05
CD4018	.94
CD4019	.45
CD4020	1.02
CD4021	1.35
CD4022	1.10
CD4023	.28
CD4024	.75
CD4025	.28
CD4026	2.50
CD4027	.66
CD4028	.85
CD4029	1.35
CD4030	.45
CD4035	1.35
CD4040	1.35
CD4042	.85
CD4043	.85
CD4044	.85
CD4046	1.67
CD4049	.45
CD4050	.49
CD4051	1.13
CD4060	1.42
CD4066	.71
CD4068	.40
CD4069	.40
CD4070	.50
CD4071	.45
CD4072	.45
CD4073	.45
CD4075	.45
CD4076	1.65
CD4078	.40
CD4081	.35
CD4082	.35
CD4116	.47
CD4490	5.50
CD4507	1.00
CD4508	4.25
CD4510	1.02
CD4511	.94
CD4515	2.52
CD4516	1.10
CD4518	1.02
CD4520	1.02
CD4527	1.51
CD4528	.79
CD4553	3.50
CD4566	2.25
CD4583	2.35
CD4585	1.10
CD40192	3.00
74C00	.35
74C04	.40
74C10	.35
74C14	1.95
74C20	.35
74C30	.35
74C48	2.25
74C74	.85
74C76	1.75
74C90	1.75
74C93	1.75
74C154	3.00
74C160	2.00
74C175	1.35
74C192	2.25
74C221	2.50
74C905	6.00
74C906	.75
74C914	1.95
74C922	6.00
74C923	6.00
74C925	7.50
74C926	6.95
74C927	6.95

INTERFACE	
8095	.65
8096	.65
8097	.65
8098	.65
8T09	1.25
8T10	4.50
8T13	3.00
8T20	5.50
8T23	3.10
8T24	3.50
8T25	3.20
8T26	1.69
8T28	2.75
8T97	1.69
8T98	1.69

MOS/MEMORY RAM	
2101-1	2.95
2102-1	.95
2102AL-4	1.34
2102AN-2L	1.60
2104A-4	4.95
2107B-4	3.75
2111-1	3.75
2112-2	3.95
2114L 300ns	6.75
2114L 450ns	5.95
4116 200ns	7.95
8/4116 200ns	49.00
2513B	6.30
MM5262	.40
MM5280	3.00
MM5320	9.95
MM5330	5.94
PD411D-3	4.00
PD411D-4	5.00
P5101L	8.95
4200A	9.95
82S25	2.90
91L02A	1.50
HD0165-5	6.95
MM57100	4.50
GIAY38500-1	9.95
MCM66751A	9.95
9368	3.50
4100	10.00
416	16.00

CLOCKS	
MM5311	5.50
MM5312	3.90
MM5314	3.90
MM5369	2.10
MM5841	14.45
MM5865	7.95
CT7010	8.95
CT7015	8.95
MM5375AA/N	3.90
MM5375AG/N	4.90
7205	16.50
7207	7.50
7208	15.95
7209	4.95
DS0026CN	3.75
DS0056CN	3.75
MM53104	2.50

MICROPROCESSOR	
6502	10.95
6504	9.95
6522	9.95
6800	8.95
6802	11.95
6820	4.95
6850	5.95
8080A	3.95
8085	12.95
8086	75.00
Z80	8.95
Z80A	11.95
8212	2.95
8214	3.95
8216	2.90
8224	3.45
8228	4.95
8251	6.95
8253	15.00
8255	5.75
8257	10.95
8259	14.95
1802CP plas.	13.95
1802DP plas.	17.95
1861P	11.50
CDP1802CD	28.95
CDP1802D	22.95
CDP1861	15.95

UART/FIFO	
AY5-1013	5.50
AY5-1014	7.50
3341	6.95

PROM	
1702A	4.95
25138 upper case	8.75
2708	7.75
2716T1	18.00
2716 Intel	23.00
8/2716 Intel	160.00
2732	65.00
2758	22.50
8741A	80.00
8748	65.00
8748-8	60.00
8755A	55.00
N82S23	2.95
N82S123	6.50
N82S126	3.75
N82S129	8.50
N82S131	8.50
N82S136	8.75
N82S137	8.75
DM8577	2.90
8223	2.90

CONNECTORS	
30 pin edge	2.50
44 pin edge	2.75
100 pin edge	4.50
100 pin edge WW	5.25

IC SOCKETS
Solder Tin Low Profile

PIN	1UP	PIN	1UP
8	.15	22	.30
14	.14	24	.35
16	.16	28	.42
18	.27	36	.58
20	.29	40	.57

2 level 14 pin ww .20

WIRE WRAP LEVEL 3

PIN		PIN	
14	.32	24	.86
16	.33	28	1.00
18	.57	40	1.23

CRYSTALS	
1 MHz	4.50
2 MHz	4.50
4 MHz	4.25
5 MHz	4.25
10 MHz	4.25
18 MHz	3.90
20 MHz	3.90
32 MHz	3.90
32768 Hz	4.00
1.8432 MHz	4.50
3.5795 MHz	1.20
2.0100 MHz	1.95
2.097152 MHz	4.50
2.4576 MHz	4.50
3.2768 MHz	4.50
5.0688 MHz	4.50
5.185 MHz	4.50
5.7143 MHz	4.50
6.5536 MHz	4.50
14.31818 MHz	4.25
18.432 MHz	4.50
22.1184 MHz	4.50

KEYBOARD ENCODERS	
AY5-2376	$12.50
AY5-3600	17.95
AY5-9100	10.50
AY5-9200	16.50
74C922	5.50
74C923	5.50
HD0165-5	6.95
AY5-9400	10.50

D Connectors RS232	
DB25P	3.62
DB25S	5.20
Cover	1.67
DE9S	1.95
DA15P	2.10
DA15S	3.10
Complete Set	9.50

Hickok 3½ Digit LED multimeter	**74.95**
Stopwatch Kit	**26.95**
Auto Clock Kit	**17.95**
Digital Clock Kit	**14.95**
8K/16K Eprom Kit (less PROMS)	$89.00
Motherboard	$39.00
Extender Board	$15.00

RESISTORS ¼ watt 5%
10 per type .03
25 per type .025
100 per type .015
1000 per type .012
350 piece pack
5 per type 6.75
½ watt 5% per type .05

Televideo Terminal

Model 912	$845.00
Model 920	$945.00

Tiny Basic Experimentors Kit **$10.00**

BSR Controller $39.95
Connect your computer to the BSR Home Control System. Computer controlled ultrasonic transmitter for your BSR. Software for 1802 user.

KEYBOARDS	
56 key ASCII keyboard kit	$67.50
Fully assembled	77.50
53 key ASCII keyboard kit	60.00
Fully assembled	70.00
Enclosure Plastic	14.95
Metal Enclosure	29.95

LEDS	
Red T018	.15
Green, Yellow T018	.20
Jumbo Red	.20
Green, Orange, Yellow Jumbo	.25
Cliplite LED Mounting Clips	8/$1.25

(specify red, amber, green, yellow, clear)

CONTINENTAL SPECIALTIES in stock
Complete line of breadboard test equip.
MAX-100 8 digit Freq. Ctr. **$128.95**

OK WIRE WRAP TOOLS in stock
Portable Multimeter **$18.00**
Complete line of AP Products in stock.

SPECIAL PRODUCTS	
MM5865 Stopwatch Timer with 10 pg. spec.	9.00
PC board	7.50
Switches Mom. Pushbutton	.27
3 pos. slide	.25
Encoder HD0165-5	6.95
Paratronics 100A Logic Analyzer	**$425.00**
Model 10 Trigger Expander Kit	$229.00
Model 150 Bus Grabber Kit	$369.00
Clock Calendar Kit	**$23.95**
2.5 MHz Frequency Counter Kit	**$37.50**
30 MHz Frequency Counter Kit	**$47.75**

TRANSFORMERS	
6V 300 ma	3.25
12 Volt 300 ma transformer	1.25
12.6V CT 600 ma	3.75
12V 250 ma wall plug	2.95
12V CT 250 ma wall plug	3.50
24V CT 400 ma	3.95
10V 1.2 amp wall plug	4.85
12V 6 amp	12.95
12V 500 ma wall plug	4.75
12V 1 amp wall plug	6.50
10/15 VAC 8/16 VA wall plug	9.75

DISPLAY LEDS			
MAN1	CA	.270	2.90
MAN3	CC	.125	.39
MAN72/74	CA/CA	.300	1.00
DL704	CC	.300	1.25
DL707/DL707R	CA	.300	1.00
DL727/728	CA/CC	.500	1.90
DL747/750	CA/CC	.600	1.95
DL750	CC	.600	1.95
FND359	CC	.357	.70
FND500/507	CC/CA	.500	1.35
FND503/510	CC/CA	.500	.90
FND800/807	CC/CA	.800	2.20
3 digit Bubble			.60
4 digit Bubble			.80
DG8 Fluorescent			1.75
DG10 Fluorescent			1.75
10 digit display			1.25
7520 Clairex photocells			.39
TIL311 Hex			9.50
MAN3640	CC	.30	1.10
MAN4610	CA	.40	1.20
MAN4640	CC	.40	1.20
MAN4710	CA	.40	.95
MAN4740	CC	.40	1.20
MAN6640	CC	.56	2.95
MAN6710	CA	.60	1.35
MAN6740	CC	.60	1.35

MA1002A	**8.95**
MA1002E	**8.95**
MA1012A	**8.95**
102P3 transformer	**2.25**
MA1012A Transformer	**2.25**

RCA Cosmac 1802 Super Elf Computer $106.95

Compare features before you decide to buy any other computer. There is no other computer on the market today that has all the desirable benefits of the **Super Elf** for so little money. The Super Elf is a small single board computer that does many **big** things. It is an excellent computer for training and for learning programming with its machine language and yet it is easily **expanded with additional memory, Full Basic, ASCII Keyboards, video character generation, etc.**

Before you buy another small computer, see if it includes the following features: ROM monitor; State and Mode displays; Single step; Optional address displays; Power Supply; Audio Amplifier and Speaker; Fully socketed for all IC's; Real cost of in warranty repairs; Full documentation.

The **Super Elf** includes a **ROM** monitor for program loading, editing and execution with **SINGLE STEP for program debugging** which is not included in others at the same price. With **SINGLE STEP** you can see the microprocessor chip operating with the **unique Quest** address and data bus displays **before, during** and **after** executing instructions. Also, CPU mode and instruction cycle are decoded and displayed on 8 LED indicators.

An RCA 1861 **video graphics** chip allows you to connect to your own TV with an inexpensive video modulator to do graphics and games. There is a **speaker system** included for writing your own music or using many music programs already written. The speaker amplifier may also be used to drive relays for control purposes.

A 24 key **HEX keyboard** includes 16 HEX keys plus **load, reset, run, wait, input, memory protect, monitor select** and **single step**. Large, on board displays provide output and optional **high and low address**. There is a 44 pin standard connector slot for PC cards and a 50 pin connector slot for the Quest Super Expansion Board. Power supply and sockets for **all** IC's are included in the price plus a detailed 127 pg. instruction manual which now includes over 40 pgs. of software info. including a series of lessons to help get you started and a music program and graphics target game. Many schools and universities are using the Super Elf as a course of study. OEM's use it for training and R&D.

Remember, other computers only offer Super Elf features at additional cost or not at all. **Compare before you buy. Super Elf Kit $106.95, High address option $8.95, Low address option $9.95. Custom Cabinet** with drilled and labelled plexiglass front panel **$24.95.** All metal Expansion Cabinet, painted and silk screened, with room for 5 S-100 boards and power supply **$57.00. NiCad Battery Memory Saver Kit $6.95.** All kits and options also completely assembled and tested.

Questdata, a software publication for 1802 computer users is available by subscription for $12.00 per 12 issues. Single issues $1.50. Issues 1-12 bound $16.50.

Tiny Basic Cassette **$10.00,** on ROM **$38.00,** original Elf kit board **$14.95. 1802 software;** Moews Video Graphics **$3.50.** Games and Music **$3.00,** Chip 8 Interpreter **$5.50.**

Super Expansion Board with Cassette Interface $89.95

This is truly an astounding value! This board has been designed to allow **you** to decide how you want it optioned. **The Super Expansion Board comes with 4K of low power RAM** fully addressable anywhere in 64K with built-in memory protect and a **cassette interface.** Provisions have been made for all other options on the same board and it fits neatly into the hardwood cabinet alongside the **Super Elf.** The board includes slots for up to 6K of **EPROM** (2708, 2758, 2716 or TI 2716) and is **fully socketed.** EPROM can be used for the monitor and Tiny Basic or other purposes.

A IK Super ROM Monitor $19.95 is available as an on board option in 2708 EPROM which has been preprogrammed with a program loader/editor and error checking multi file cassette read/write software, (relocatable cassette file) another exclusive from Quest. It includes register save and readout, block move capability and video graphics driver with blinking cursor. Break points can be used with the register save feature to isolate program bugs quickly, then follow with single step. If you have the **Super Expansion Board** and **Super Monitor** the monitor is up and running at the push of a button.

Other on board options include **Parallel Input and Output Ports** with **full handshake.** They allow easy connection of an ASCII keyboard to the input port. **RS 232** and **20 ma Current Loop** for teletype or other device are on board and if you need more memory there are two **S-100** slots for static RAM or video boards. Also a 1K Super Monitor version 2 with video driver for full capability display with Tiny Basic and a video interface board. **Parallel I/O Ports $9.85, RS 232 $4.50, TTY 20 ma I/F $1.95, S-100 $4.50. A 50 pin connector set** with ribbon cable is available at $15.25 for easy connection between the **Super Elf** and the **Super Expansion Board.**

Power Supply Kit for the complete system (see Multi-volt Power Supply).

Announcing Quest Super Basic— *SECOND GENERATION*

A new enhanced version of **Super Basic** now available. Quest was the first company worldwide to ship a full size Basic for 1802 Systems. A complete function **Super Basic** by **Ron Cenker** including floating point capability with scientific notation (number range $\pm .17E^{38}$), 32 bit integer ± 2 billion; multi dim arrays, string arrays; string manipulation; cassette I/O; save and load, basic, data and machine language programs; and over 75 statements, functions and operations.

Enhancements include increased speed, built-in provisions for Stringy Floppy, Floppy Disc, Printer Driver, I/O, user definable command library and statement renumbering.

Easily adaptable to most 1802 systems. Requires 16K RAM minimum for Basic and user programs. Source listing for both Serial and Parallel I/O included.

Super Basic on Cassette $40.00.

PROM Eraser

assembled. 25 PROM capacity **$37.50** (with timer **$69.50**). 6 PROM capacity OSHA/UL version **$69.50** (with timer **$94.50**).

Z80 Microcomputer

16 bit I/O, 2 MHz clock, 2K RAM, ROM Bread-board space. Excellent for control. Bare Board **$28.50.** Full Kit **$99.00.** Monitor **$20.00.** Power Supply Kit **$35.00. Tiny Basic $30.00**

S-100 Computer Boards

8K Static Godbout Econo IIA Kit	145.00
16K Static Godbout Econo XIV Kit	285.00
24K Static Godbout Econo VIIA-24 Kit	435.00
32K Static Godbout Econo X-32 Kit	575.00
16K Dynamic RAM Kit	199.00
32K Dynamic RAM Kit	310.00
64K Dynamic RAM Kit	470.00
Video Interface Kit	**$135.00**

80 IC Update Master Manual $55.00

Comp. IC data selector, 2700 pg. master reference guide. Over 51,000 cross references. Free update service through 1980. Domestic postage $3.50.

Modem Kit $60.00

State of the art, orig., answer. No tuning necessary. 103 compatible 300 baud. Inexpensive acoustic coupler plans included.

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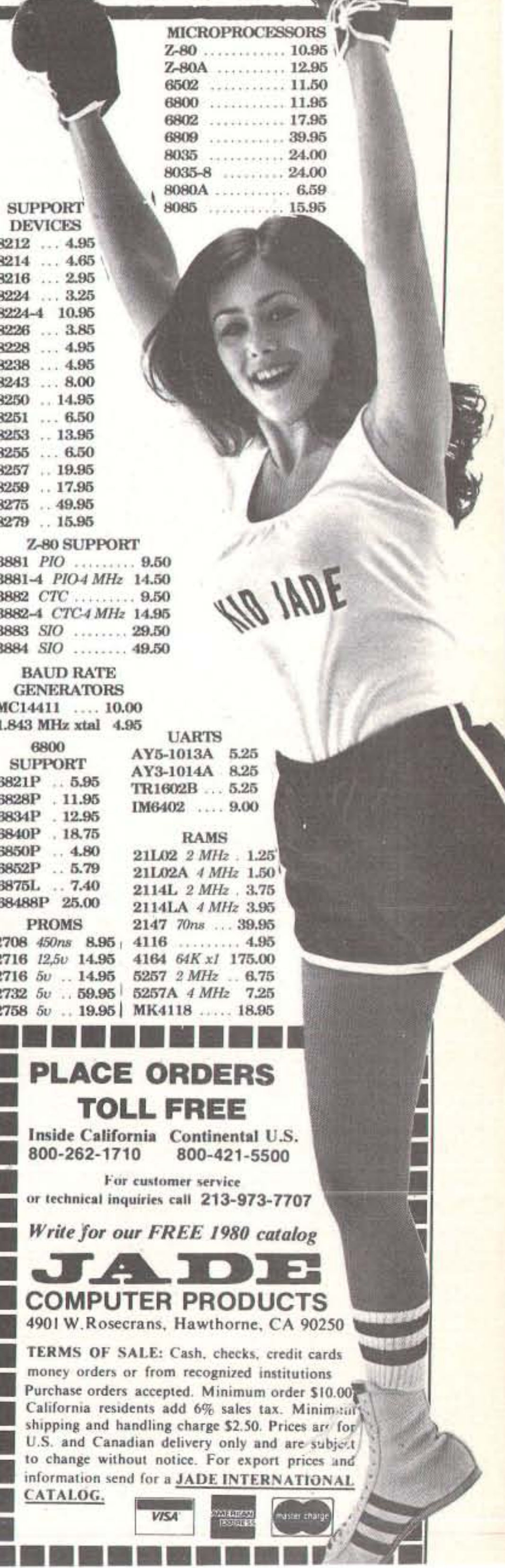

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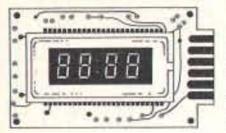

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- Includes all components, case and wall transformer
- Size: 6¾" x 3-1/8" x 1¾"

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- Incl. all components, case & wall transformer
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INS8900	CPU—16-Bit	29.95
TMS9900JL	MPU—16-Bit	49.95

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MM503H	Dual 50-Bit Dynamic	.50
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MM5013	1024-Bit Dynamic/Accumulator	1.95
MM5016H	500/512-Bit Dynamic	.89
MM5034N	Octal 80-Bit	9.95
MM5035N	Octal 80-Bit	9.95
2504V (1404A)	1024-Bit Dynamic	3.95
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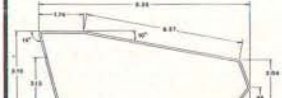

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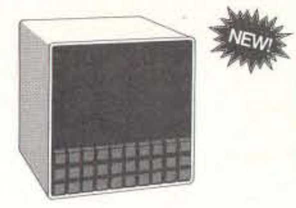

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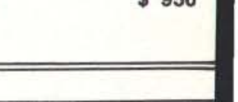

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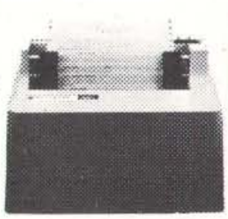

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Preview of the Apple III

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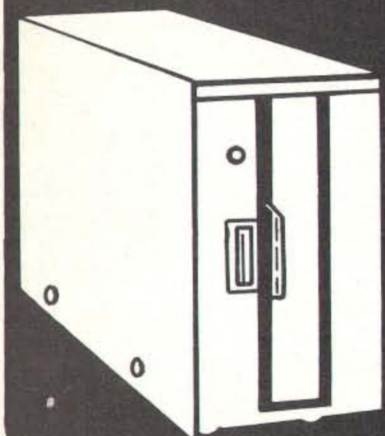

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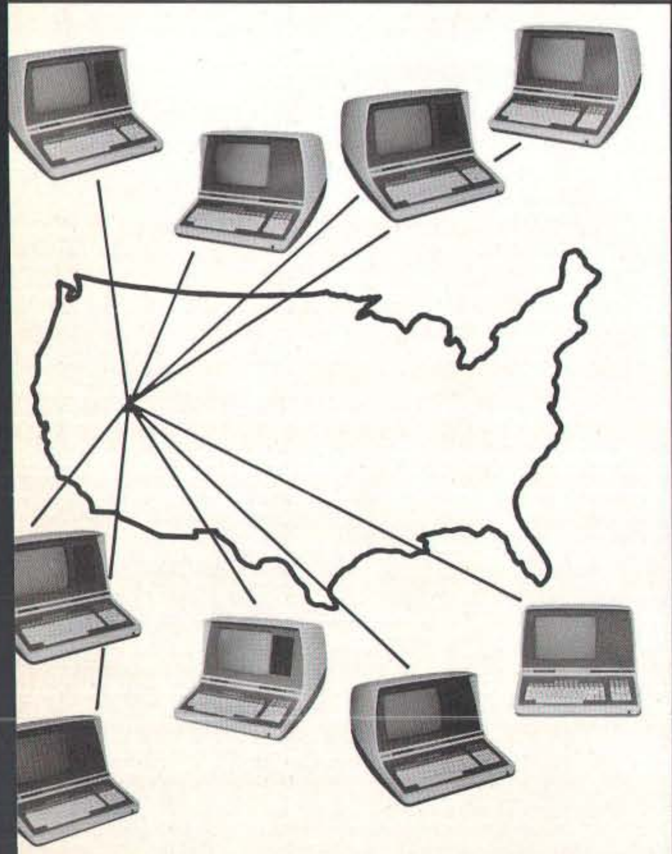

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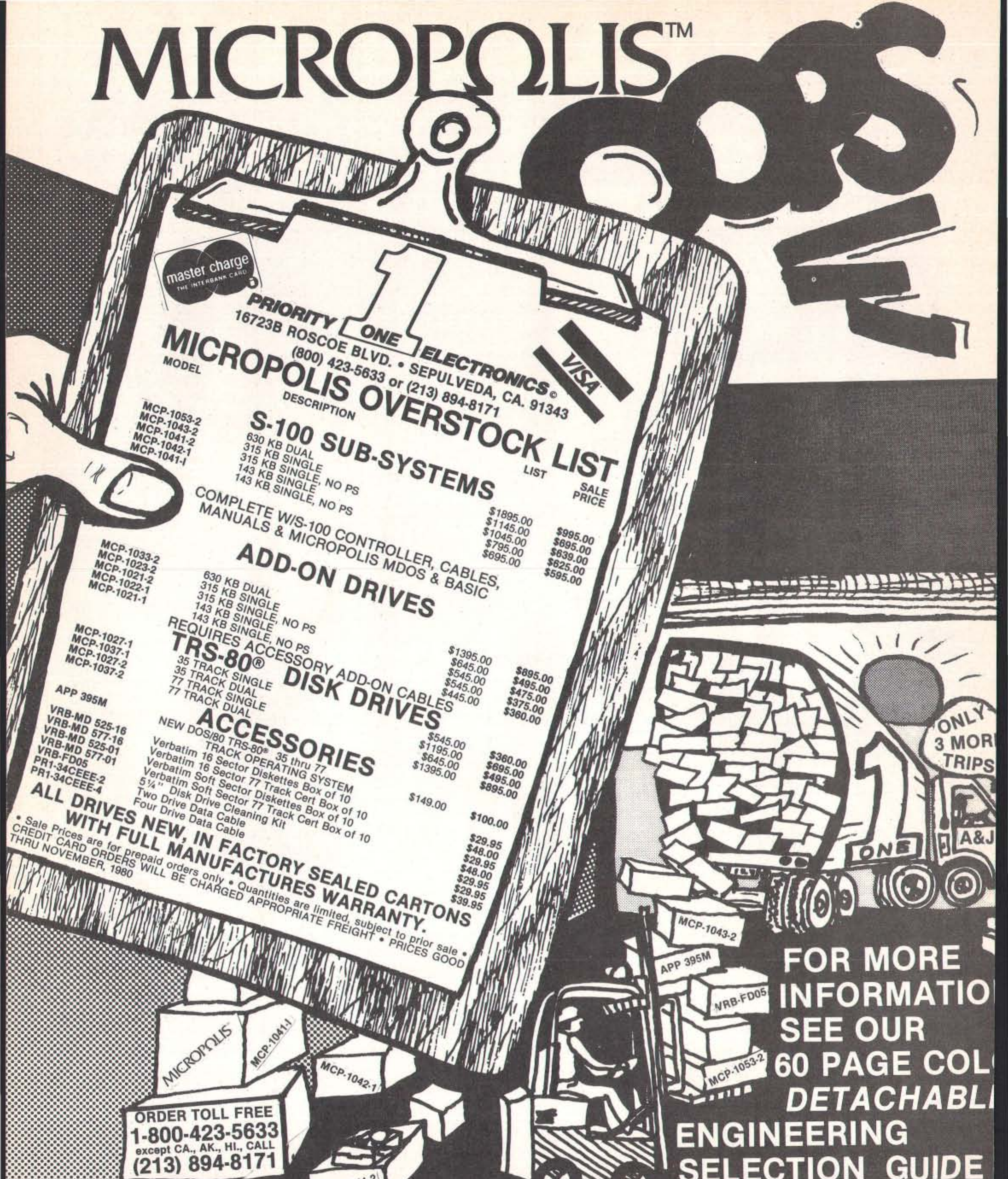

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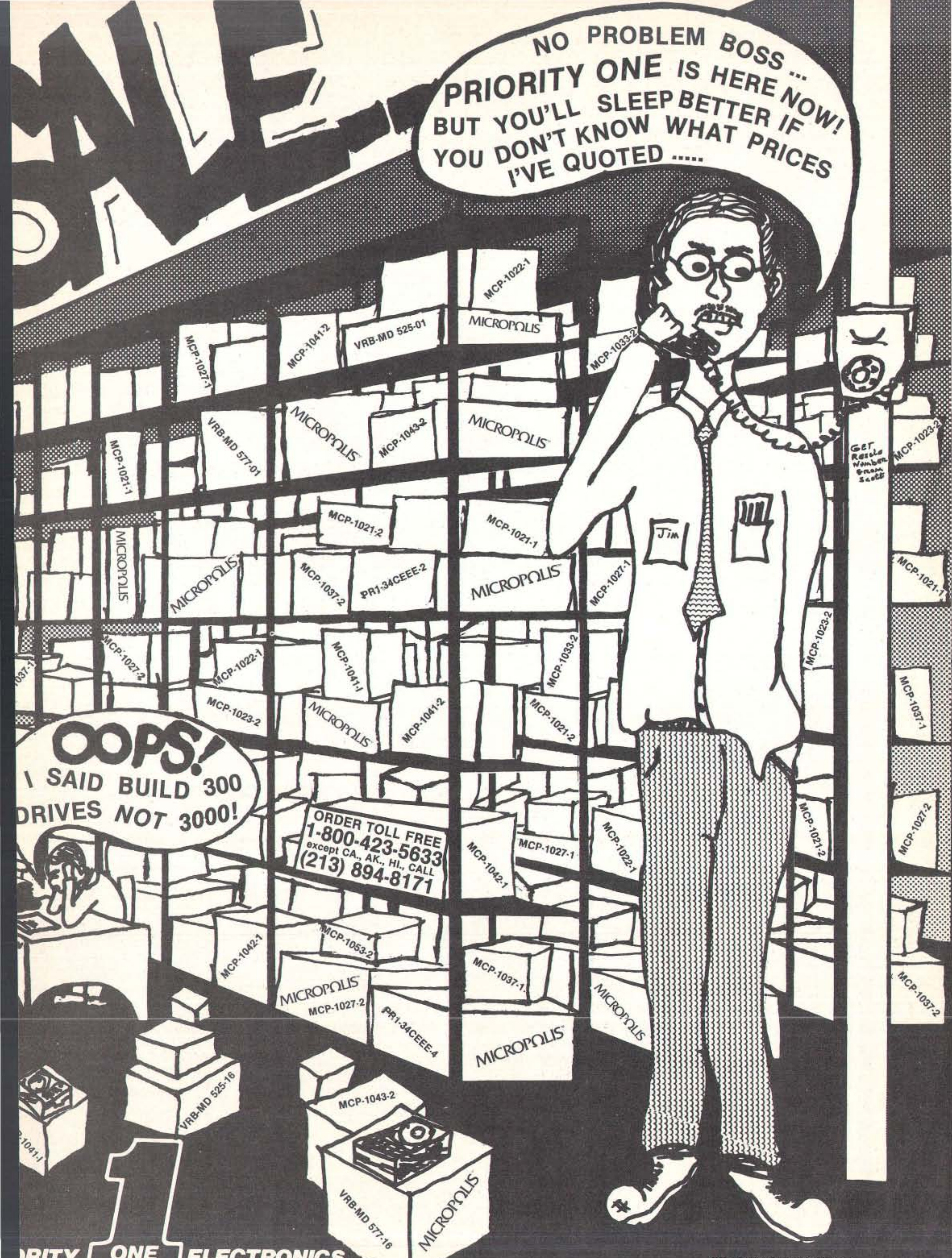

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Education System Master Cartridge	28.00	Basic Sociology	35.00	BASIC Game Programs	19.00
Atari BASIC	55.00	Counseling Procedures	35.00	**Add-On Memory**	
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Basketball	42.00	Physics	35.00	16K RAM Memory Module	225.00
Life	42.00	Great Classics (English)	35.00	**Diskettes**	
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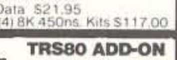

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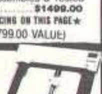

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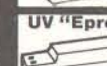

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EMM4402	7.95	7.25	6.25
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5101C-E	7.95	7.95	7.25
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14	.19	.36	.59
16	.20	.38	.62
18	.24	.59	.84
20	.29	.69	.99
22	.24	.79	1.10
24	.38	.85	1.20
28	.43	1.10	1.49
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FND501	500	Red	Comm. Cathode (+1)	.99
FND503/500	500	Red	Comm. Cathode	.99
FND507/510	500	Red	Comm. Anode	.99
FND508	500	Red	Comm. Anode (+1)	.99
FND530	500	Green	Comm. Cathode	1.49
FND550	500	Orange	Comm. Cathode	1.49
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HP5082-7731	300	Red	Comm. Anode	.75
TIL305	–	Red	Array 5 x 7	7.95
TIL308	270	Red	Numerical Display	10.95
TIL309	270	Red	Numerical Display	9.95
TIL311	270	Red	4 x 7 Hexidecimal	10.95
MAN2A	320	Red	Array	5.95
MAN10A	270	Red	Alpha Numeric	8.95
XAN3061	300	Red	Comm. Anode Right DP	1.25
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ICL 8211 Voltage Reference ... 1.95
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525-10	10 Hole Hard NS Apple, etc	27.25
525-16	16 Hole Hard Micropolis	27.25

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577-01	Soft Sectored TRS 80, etc.	$32.25
577-10	10 Hole Hard NS Apple, etc	32.25
577-16	16 Hole Hard Micropolis	32.25

★ 40 TRACKS, DOUBLE SIDED, DOUBLE DENSITY 5¼

550-01	Soft Sectored SA 450, MPI 52	$39.50
550-10	10 Hole Hard BASF, WANGCO	39.50
550-16	16 Hole Hard Micro?	39.50

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FD800-01	Soft Single Density 3740	$33.50
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Soroc IQ 120	995.	795.
Teletype Model 43	1349.	1150.
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Atari 400	549.	449.
TI 99/4	1150.	1025.
Leedex 12" Monitor	149.	139.
Centronics 737	995.	899.
Trencom T-100	375.	349.
Trencom T-200	595.	525.
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Sanyo 15" Monitor	295.	249.
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14 Pin w/w-2L ... $ .35
18 Pin w/w-2L ... .55
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Post Office Box 3097 B • Torrance, California 90503

Introducing the

ANACOM 150

DOT MATRIX PRINTER

Mfg. suggested list $1350

California Digital Introductory Price

$995

Full 136 Characters for the price of 80

DURABILITY...is the key component of the new Anacom 150. No bells, no whistles, no problems, just consistent high quality output.

This nine wire dot matrix printer features a ballistic type print mechanism guaranteed for three million characters. Low count (16) integrated circuits add to the reliability of the printer.

Microprocessor controlled logic seeking bi-directional head allows the Anacom to print up to speeds of 150 characters per second. 136 columns wide.

Adjustable tractors and variable head gap permit the Anacom to accept fifteen inch wide multi-part forms.

Switch selectable: skip paper perforation, carriage return/line feed and six or eight lines per inch.

Lexan paper shield and enclosure sound proofing add to the overall quality of the printer.

The Anacom 150 is definitely the best value in todays extremely competitive world of micro-printers.

If you are in the market for a "Quality Engineered" dot matrix printer, please consider the Anacom 150 before purchasing a less reliable machine.

Available either RS-232 serial 9600 baud. PRA-15ØS or Centronic parallel PRA-15ØP. Field exchange. UPS shipping weight 40 pounds.

IBM 3101 DISPLAY TERMINAL

The new 3101 display terminal is the IBM entry into the plug compatable micro computer industry.

This modularly constructed CRT terminal has been engineered with the user in mind. The video display modules swivels and tilts to provide the operator with a comfortable viewing posture.

Twelve inch P-39 green phosphor screen boasts a crisp 7 by 14 character matrix.

Standard 80 by 24 line screen format with a 25th line to display machine status and aid in the diagnostics in the event of a system malfunction.

87 key Selectric style keyboard arrangement along with numeric entry pad. Eight user definable function keys.

The 3101 video terminal is RS232 compatable and displays all 128 ASCII characters including control codes.

Accessible customer setup switches aid in choosing such options as line speed, parity scroll, and reverse video.

But most of all, built into every 3101 terminal is the quality that you have learned to expect from the IBM Corporation. VDT-3101

IBM direct price $1295

CALIFORNIA DIGITAL discount price

$1195 immediate delivery

TEC V-3ØØ Word Processing

Daisy Wheel Printer

$1595

Finally a reasonably priced letter quality printer... Bi-directional printing at 25 characters per second. Full 136 print positions wide. Proportional spacing 1/120" horizontal, 1/48" vertical.
Uses standard Diablo brand interchangable daisy print wheels.
Intel 8085 CPU microprocessor controlled. Interfaces via Centronics parallel connector. Shipping 58 lbs. PRV-300.

NEC Spinwriter 551ØP/S

$2495

The word processing quality Spinwriter prints at speeds upto 55 characters per second. The Model 551Ø P/S is supplied with both parallel and RS-232 serial interfacing. Also included is the tractor feed mechanism, along with print thimble and ribbon. PRN-551ØPS 70 lbs.
Keyboard (KSR) Model 5520 P/S available $2995. PRN-552ØPS 75 lbs.

PRINTRONIX

$4500

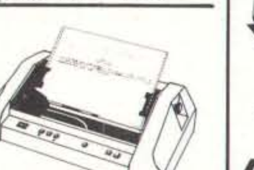
P-300

Forty-four separate print heads shuttle to provide overlapping dots. This unique feature allows a resolution of 60 dots per inch horizontal and 72 vertical.
The Printronix P-300 has the capability of outputing pictures, bar codes and large labels, a feature not provided with band or chain printers. PRP-300 300 lbs.

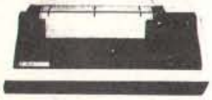
Anadex

$1395

PRX 9500



DP-9500



TI-810

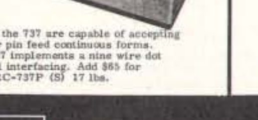
$1495

List $1895
Save $400

CENTRONICS

730 $595

737 $795



Both the Centronics 730 and the 737 are capable of accepting standard office letterhead or pin feed continuous forms.
For higher resolution the 737 implements a nine wire dot matrix print head. Parallel interfacing. Add $65 for RS232. PRC-730P (S). PRC-737P (S) 17 lbs.

AMPEX DIALOGUE 80 CRT TERMINAL

$995

New from the Ampex Corporation. The Dialogue 80 features removable keyboard, displayable two pages (four optional) dual program keys, half intensity protected fields and status line. Transmits data either block, line or character mode. Excellent value. VDT-D80 shipping 47 lbs.

ADDS

Applied Digital Data Systems Inc.

REGENT 25 $850
80 character by 24 line twelve inch screen. 18 key numeric-entry cluster. Reverse video. Displayable control characters. VDT-R25 ship. 60 lbs.

REGENT 30 $950
Additional features. Special function keys. High resolution graphics. 25th status line. Multi-intensity screen. Superior quality keyboard. VDT-R30

REGENT 40 $1195
Insert/delete line feature. Eleven special graphic symbols. VDT-R40

REGENT 60 $1495
Buffered display. Block mode transmission. Protected fields. VDT-R60

HEWLETT PACKARD

HP 85

$2650

The Hewlett Packard HP-85 is a complete, low cost portable computer system. This self contained package includes CPU, keyboard, printer, CRT display and cassette tape drive. SYS-HP85 30 lbs.

APPLE II

$988

ATARI

800 $747

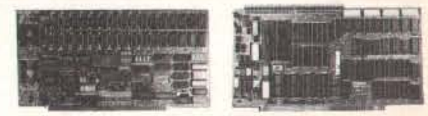
400 $695

LOBO Disk Drives

The package includes a durable steel enclosure. Power One brand disk power supply, muffin exhaust fan and all the necessary harnessing cables.
The Lobo package with:
One Shugart SA801R disk drive... $795
Two Shugart SA801R disk drives. $1195

S-100 BOARDS

Assembled • Tested • Burned-in



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A/D board 16 channel 12 bits

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Wire Wrap proto board WW/100 22
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8086 CPU 4K on board static RAM 450

S-100 Mother Board $35

Quiet Buss

8803-18
18 slot
IMSAI

The Quiet Buss from California Digital is quality engineered. No short cuts have been taken to produce this mother board. Active termination circuitry prevents noise and crosstalk. manufactured from extra heavy epoxy glass.

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UNIVERSAL DATA 103

Connects directly to the new modular phone jack. Fully powered from your existing telephone line. No need to locate external AC power. Crystal control prevents frequency drift. Direct connect feature eliminates loss of information due to carbon compression that is associated with acoustic modems.

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NEW
from
INTEGRAL DATA
460 Paper Tiger

**** All the features of the 440 and more ****

The 460 uses a dot matrix character formation technique in which the placement of the dots overlap both horizontally and vertically to achieve a correspondence-quality printing.
The printer's nine-wire print head uses staggered needle rows to create the vertically overlapping dots. The head is driven bi-directionally under microprocessor control by a stepper motor driven mechanism with logic-seeking look ahead capability.
Standard "Two-K Byte" buffer allows the printer to accept the entire content of a 1,920-character CRT screen. Weight 27 lbs.

suggested list price $1,295. Calif. Digital price **$1,076**

S-100 POWER SUPPLY
Mfg. for California Digital by SIERRACIN **$59**

Designed for on board regulation. 8V./8A.; ±16V./2A. Input:115/230VAC. 9x5x3" PWS-8A 4lbs.

Same as above but: 8V./20A; ±16V.4A. 11x5x4" PWS-20A **$95**

TELETYPE® MODEL 43

4320 KEYBOARD

TTL ...... AAA	$ 950	
RS232 .... AAK	1050	plus shipping
Friction ... AAE	1100	
103 Modem AAB	1575	

WESTERN UNION ENCLOSURE

These enclosures were manufactured for Western Union by Universal Technology. The exact purpose of the product is still a mystery but the enclosure is ideally suited for an S-100 motherboard with shielded power supply.
Removable hood and plexiglas front make this enclosure an attractive home for any hobby product.
New surplus in factory boxes supplied with three 22/44 edge connectors; DB 25S communications connector; six foot grounded power cord and more. Inside dimensions: 19" x 10 1/2" x 6 7/8." Shipping weight 8 lbs. **$24.95**

FREE PLASTIC LIBRARY CASE
with purchase of each box of .... Memorex mini-diskettes. $5 value.

$27 BOX of TEN

10 Boxes $24.95
100 Boxes $22.25
forty track

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DB25

	each	10+
male	$2.50	2.25
female	3.25	3.05
hood-2p	1.25	.98
Centronic	6.95	

BCD Thumbwheel Switch 1.95
Ten Position
Mfg. by Digiswitch
1 7/8" high 1/2" wide

Edge Connectors
GOLD 100 PIN IMSAI/ALTAIR

Imsai solder .125x.250	$2.95	3/$ 7.50
Imsai w/w .125centers	$4.95	3/$13.00
Altair soldertail .140 row	$5.95	3/$15.00
SPECIALS		
22/44 Kim eyelet .156"	$1.95	3/$5.00
25/50 solder tab .156"	$1.09	3/$2.00
36/72 wide post w/w.156	$1.95	3/$5.00

Lear Seigler Inc. In Plant POCKET INTERCOM $14.95

Another one of our mystery products. Manufactured by the Lear Seigler Company. The pocket intercom is supplied in a deep drawn blue anodized aluminium enclosure. Excellent for those special projects which require a touch of elegance. LSI list price $145. Supplied with chrome pocket clip. 9V transistor battery required. Model 1105. New surplus. measures 3.5 x 2.75 x 1.125" SPC-LSI

IEE Projection Module

$9.95 set of three $25

Voltage applied to one of the twelve discrete miniature incandescent #328 lamps causes the module to display the selected character. (+-Ø thru 9). Users may substitute the character film and display pictures and symbols of their own choosing. Manufactured by Industrial Electronic engineers Inc. SPC-L12 Used excellent cond. 24 lamp modules also avail.

Authorized Distributor
Scotch Data Products

		10 Pak	50+
740-0	IBM soft format.	$39.00	$3.50
740/2	Double side soft	65.00	6.00
741-0	Double density	53.00	4.90
743-0	Double/Double	70.00	6.60
740-32	8" Hard sector	39.00	3.50
744-(0)(10)(16)	5 1/4"mini	39.00	3.50
Library case for any above;	Add $3.00		
834 A	Data Cassette	5.50	
DC100	Mini Cartridge	16.00	
DC300	Data Cartridge	20.00	
920( )	Disk Cartridge	89.90	

Shugart Associates
SA800-R Floppy Disk Drive

$449.50

XEROX 800 WORD PROCESSING KEYBOARD ASCII ENCODED

This 77 key word processing keyboard was manufactured by Microswitch for use in the Xerox 800 word processing system.
The keyboard outputs a seven bit ASCII code along with an eighth bit that allows most keys to shift and double function as special characters. Extra large "Tab & Return" keys are designed into the layout of the keyboard to emulate the IBM Selectric. 17 illuminated keys serve for special word processing codes. The keyboard is equipped with two thumbwheel switches for defining line width.
Original Xerox acquisition over $400.00
California Digital USED price only $49.00
Excellent cond. Documentation included.

MEMORY

TRS-80 APPLE II $39
16k memory (8) 4116's

Installation is simple. Anyone who has ever changed a spark plug should be able to up-grade his microcomputer.
How can California Digital offer these memory up-grade sets at 25% below our competition? Simple, we buy in volume, wholesale to dealers and sell the balance directly to owners of personal microsystems. These 16K dynamic memory circuits are factory prime and unconditionally guaranteed for one full year. NOW, before you change your mind, pick up the telephone and order your up-grade memory from California Digital. Add $3 for TRS80 jumpers.

STATIC	1-31	32-99	100-5C	-999	1K+
21L02 450nS.	1.19	.99	.95	.90	.85
21L02 250nS.	1.49	1.39	1.25	*	*
2114 1Kx4 450	5.95	5.50	5.25	4.75	4.50
2114 1Kx4 300	8.95	8.50	8.00	*	*
4044 4Kx1 450	5.95	5.50	5.25	*	*
4044 4Kx1 250	9.95	9.50	9.00	*	*
4045 1Kx4 450	8.95	8.50	8.00	*	*
4045 1Kx4 250	9.95	9.50	9.00	*	*
5257 low pow.	5.95	5.50	5.00	4.80	4.60

2716 EPROM SALE $13

*** THOUSANDS ***
We have slashed price in an effort to reduce our over stocked inventory. These are Single Five Volt Eproms, manufactured by one of the Worlds largest producers of semiconductors. Please phone for volume pricing.

DATA INPUT TERMINAL

This Keystation terminal was recently acquired from the CMC division of the Pertec Corporation. The unit was originally designed for inputing data directly onto magnetic tape.

The system is comprised of a premium cast aluminium and fiberglass enclosure, along with a Honeywell / Microswitch hall effect keyboard. Thirty display lamps advise the operator of the systems status. Four inch load speaker acknowledges acceptance of data and alerts the operator of pending problems.

But most of all this "USED" terminal, with a little imagination, can be engineered to make the perfect home for an S-100 computer and video display; or with slight modification will accept the Rockwell AIM-65 micro/computer.

Five volt regulated power supply is available for an additional $20. (see June Byte) All units are in excellent condition. Original acquisition over $700. 22 lbs.

Scotch® DISKETTE HEAD CLEANING KIT

$24.95

Please specify:
8" or 5 1/4"

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your choice
$.98

SPDT Miniature Toggles

7101 C&K	ON-NONE-ON
7107 jbt	ON-OFF(mnt.ON)
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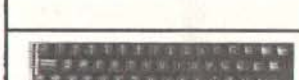
Rotary	3P-4-Pos.
Rotary	3P-6-Pos.
Push B (N.O.)	$.39ea. 4/$1

CONTROL DATA CRT Terminal?

Frankly we made a mistake and are offering these terminals below our cost just to get them out of our warehouse. As far as we can determine the units are complete. But we are offering them on an "as-is" basis without any return privileges.
The terminals were originally purchased from All State Insurance.
At present we have been unable to secure any printed information on the units. If you have any doubts, please do not purchase one of these terminals. Shipped via truck. freight collect.

KEYBOARD $24.95



This classic Hytek keyboard is similar to the module used in the Decwriter Terminal. 62 non-encoded contacts. three locking. Factory boxed Altair surplus. Shipping 3 lbs.

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$1.29 ea.	10	25	100	1K
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specify 4 7 or 8 pos.

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IBM 2980 SELECTRIC BANK TERMINAL $250

The IBM 2980 terminal was designed to be located at each tellers station in a branch bank. The terminal was on line to a master computer. Information entered into the terminal would instantaneously adjust the customers account at the computer. A record of the transaction would be entered into the customers passbook and simultaneously recorded onto a continuous 40 column paper roll located within the terminal.
The heart of the 2980 is the IBM Selectric typewriter. Each unit is supplied with print ball, ribbon and full documentation.
A potential use for this I/O terminal is in business applications where information must be entered onto ledger cards at the same time data is recorded at the computer. USED surplus. 77 lbs.

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The new BSR timer runs your home like clockwork. Turns on lamps and appliances while your away from home. Completely compatible with your existing system X-10 devices.

BSR Timer	$65.00
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Computers, Disk Systems

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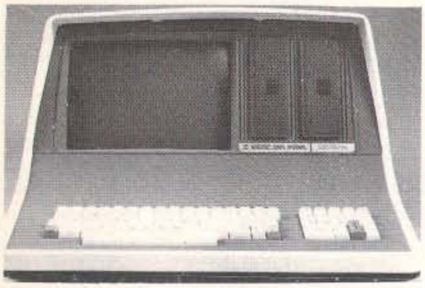
By INTERTEC



32K or 64K (Double or Quad Density units available). Uses two Z-80 CPU's. Commercial-type terminal with 12" monitor. Dual double density minifloppies. Over 350 kilobytes of storage (twice that with quad density drives). Two serial RS232 ports, I/O ports standard. Expandable with optional S-100 S-100 interface. Comes with CP/M™ 2.2 operating system. MiniMicroMart includes BASIC interpreter and can supply a wide range of CP/M Development and Application software.

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W/64K Quad — MiniMicroMart Upgrade Special. **$3395**

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A 64K complete computer with dual density 8" floppies (1 megabyte). Rack or vertical mounting. Systems with double-sided drives, hard disks, and multi-user (MP/M).

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"Z" system features new distributed processing multi-user concept with one Z-80 per user, with Z-80 for MP/M (Master Satellite concept).

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HP-85A

Desk-Top Computer

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Double Density, Kit
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NEW! CROMIX FROM CROMEMCO
A New UNIX Like Disk Operating System,

With true multi-user, multi-tasking capabilities
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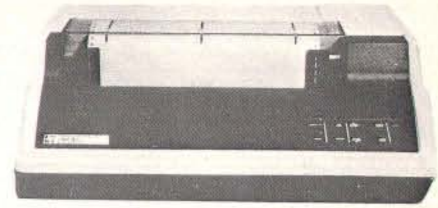

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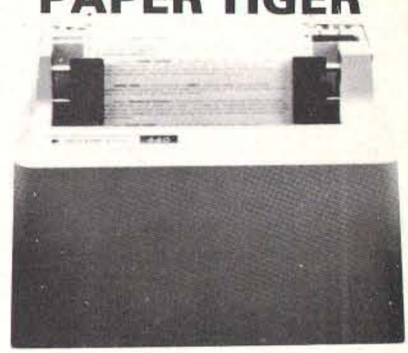

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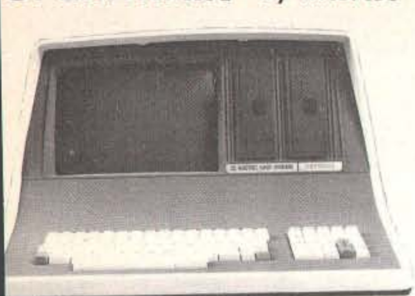

...lf-contained computer with dual disks and ...o RS232C ports. Complete with CP/M 2.2 ...d BASIC.

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ONLY $159 without RAM chips

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- 8K Static 4 MHz, A&T, List $195 ...... $176
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...e prices reflect a 2% cash discount (order prepaid prior to shipment). Add 2% to prices for credit card orders, C.O.D.'s, etc. Prices are subject to change and offers subject to withdrawal without notice.

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FOR SALE: TRS-80 Model I Level 2, 48 K memory, expansion interface, two disk drives, Emako-20 matrix printer with Centronics cable. Sell all or part at 80% Radio Shack list price. Also diskettes, game cassettes, etc. Philip Crawford, 1720 E 1st St #10, Long Beach CA 90802, (213) 437-5475.

EDUCATORS: Small private school in central Connecticut (K-8) is considering implementation of microcomputers into curriculum. If you've previously experienced such an endeavor in this age group and would be willing to share an evening enlightening faculty and concerned parents, please contact us. We're eager to make this a successful program, and would be interested in learning how your program was launched and pitfalls to avoid. The Independent Day School, Laurel Brook Rd, Middlefield CT 06455, Attn: William Murdoch, (203) 238-3994.

FOR SALE: Voltage regulator, SOLA BASIC 750 VA Unit #63-13-175. Never used, output two outlets, 6.25 A maximum. $300, shipping additional. Jane Groene, 1 Harmony Ct, Syosset NY 11791, (516) 921-4900.

WANTED: KIM-1 or similar microcomputer for dedicated real-time system. Must be like KIM-1: easily expandable but otherwise a bare single-board system. Needed for temperature monitoring system in a solar greenhouse. Robert Heller, Star Route Box 51A, Wendell MA 01379, (617) 544-6416 between 8:30 and 5.

FOR SALE: Standard Memories Ecom memory system including Ecom memory core (32 K), heavy-duty power supply, all interconnecting cables, interface firmware card, and documentation. Original cost of $3500; will sell for $1000. All in excellent condition, both main units are for relay rack mounting. Steve Garber, 3030 Polk St, San Francisco CA 94109, (415) 474-7081.

FOR SALE: Eaton LRC 7000 plus 64-character printer; $250. Radio Shack Quick Printer II 32-character (Catalog #26-1155); $150. Send certified check or money order. William R Spencer Jr, 5421 Grandin Rd Ext, Salem VA 24153.

FOR SALE: LSI-11 processor (KD11-F) mounted in a four-slot backplane with a serial interface (DLV11), paper-tape operating-system package including PAL-11S Assembler, LINK-11S, ODT, PTSP, and single-user BASIC, with full documentation. Entire system never used, in mint condition. Original cost $1325, asking $1000. Also for sale, Processor Technology VDM-1, $100. M Wallin, 1607 Lauren Ct, Bensalem PA 19020.

FOR SALE: Hazeltine 1500 CRT terminal (less case, cable, XFMR); $450, Anderson Jacobson acoustic coupler #242A; $120, full ASCII keyboard; $50. S Gladstone, 150 W Cedar St #6, Norwalk CT 06854, (203) 866-8930.

WANTED: Soft black leather case for the HP-45, 65, or 67. New or used. E King, 870 W 181 St, New York NY 10033, (212) 568-3309.

FOR SALE: Ithaca Audio 8 K, 250 ns static programmable-memory board for S-100 with protect; $120. Ithaca Audio S-100 video-display board, 64-by-16 uppercase and lowercase with Greek symbols, normal or reverse video, 1 K on-board programmable memory; $75. Mostek 4115N dynamic-programmable memories; eight for $30. Ted Betz, Box 379A RD#1, Farmingdale NJ 07727, (201) 938-3722.

FOR SALE: DEC LSI-11 components: KD11-F processor board with FIS circuit and 4 K memory; DLV11 serial interface with remote data-rate switch. Also, Vadic two-speed modem: Bell 103/113 (300 bps) and 202 (1200 bps half-duplex) compatible. Sell both for half price. Bob Malahy, Mechanical Engineering Dept, Rice University, Houston TX 77001, (713) 664-8635 evenings.

FOR SALE: Color graphics board. Biotech CGS-808 with on-board microprocessor control for S-100 bus. Excellent condition, versatile, up to 256-by-192 resolution. Lots of software including 3D graphics. $362 postpaid. John Peterson, 1820 Camino Dr, Forest Grove OR 97116, (503) 357-6310.

FOR SALE: Apple II computer with 36 K memory, Applesoft Firmware Card, disk drive with controller, all manuals, plus extras. Everything is in excellent condition. $1200 or best offer. David J Bauman, 249 Taft St, Wind Gap PA 18091, (215) 863-5736.

WANTED: S-100 system: Z80 processor, 48 thru 64 K programmable memory, 15-slot mainframe, 5 V at 15 A, ± 18 V at 1 A power supply, video-display board, serial and parallel I/O. Optional: keyboard, cassette interface, and read-only memory monitor. Fred Tydeman, 3901 Northfield Rd, Austin TX 78759, (512) 255-9292 evenings.

FOR SALE: New PP 2708/16 eprom programmer by Oliver Audio Engineering, factory assembled; $200. Double-sided printed-circuit board plated through with schematic for building small system using Motorola MC14500 single-bit controller; $35 each. Charles Krasny, POB 57, Maple Falls WA 98266.

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These notices are free of charge and will be printed one time only on a space available basis. Notices can be accepted from individuals or bona fide computer users clubs only. We can engage in no correspondence on these and your confirmation of placement is appearance in an issue of BYTE.

Please note that it may take three or four months for an ad to appear in the magazine.

FOR SALE: SwTPC 6800 computer with 12 K memory, CT-64 with Motorola monitor, 2 serial and 1 parallel interface, AC-30 cassette interface, GT-6144 graphics board—all working. Cost over $1200; sell for $600. Also, OSI Challenger with 16 K, cassette interface, video board, extra boards. Cost over $1400; sell for $700. J Chirigos, 4707 Larchmont NE, Albuquerque NM 87111, (505) 299-0378 after 5 PM.

FOR SALE: 64-word S-100 Model 50 Heuristics Speechlab; $50, Morrow Speakeasy S-100 1P, 1S, and cassette interface; $30, OAE paper-tape reader; $20. All work fine. Micro-Term ACT-1 terminal, as is; $30. Alex Begin, 7335 Deep Run, Birmingham MI 48010, (313) 642-7512.

WANTED: Three to six 8080 hackers to work with machine-language Monitor, Editor, and Assembler that I have developed. You will get free software in exchange for user comments and suggestions. First letter should give programming experience, computer type, and input format required. Robert G Durnal, POB 68, Junior WV 26275.

FOR SALE: Radio Shack 16 K Level II TRS-80 microcomputer. With numeric keypad, expansion interface, cassette recorder, and several game cassettes including *Microchess*. List price is $1200. I will sell for $750. First cashier's check/money order takes it (I pay shipping). Include SASE for confirmation. Chris Willson, 8726 S Sepulveda Apt 91B, Westchester CA 90045.

FOR SALE: Xitan Z80 system. Mainframe, ZPU, SMB, VDB, 48 K programmable memory, 16 K read-only memory (12 K BASIC in read-only memory), keyboard, manuals, software. Complete system $1800. Terry Young, 4 Aiken St, Derry NH 03038, (603) 434-0257.

FOR SALE OR SWAP: KSR-28 Teletype (not ASCII, uses 5-bit code) with manuals. $100 or will swap for an acoustic coupler, modem, or Radio Shack Voxbox. R L Reynolds, 30 Jordan St, Chelmsford MA 01863, (617) 251-8505.

NEEDED: Information, kit, schematics, or advice on turning ITEL word-processor typewriter Model 84101010 into computer terminal or printer. Gordon Dohle, 414-34 Kleisinger Cr, Regina Saskatchewan, S4R 7M4 Canada.

FOR SALE: IMSAI mainframe with 10-slot motherboard, Ithaca Audio Z80 processor board (with 2708), SSM VB1 video board, 32 K static-programmable memory, and Soroc 117-key professional keyboard. Best offer over $600. Bob Watson, (602) 526-2312.

WANTED: Student experimenter wants integrated circuits, transistors, capacitors, resistors, LEDs, books, catalogs, magazines, diodes, switches, tubes, wire, printed-circuit boards, knobs, TTL circuits, keyboards, crystals, transformers, and parts-identification book. Please state price and what you have to offer in full detail. Judy Stapleton, POB 536, Pine Lake GA 30072.

FOR SALE: IMSAI 8080 with 16 K bytes, 3 P plus S Teletype I/O board, Tarbell cassette interface, case, panel, and 22-slot motherboard. 8 K BASIC and all standard software. Panasonic cassette unit. Perfect condition. $600 plus shipping, or best offer. Also available: ASR33 teletypewriter. Dick Aronson, 61 Morton St, New York NY 10014, (212) 243-0623 home, (212) 758-6500 work.

WANTED: Clever machines and ideas do not always advance the state of the art, but they are fascinating! Do you have or know about any unusual computing devices (mechanical, electronic, analog, digital, unclassifiable)? I am seeking information about such things, historical or recent, completed or not—even just crazy, ingenious ideas. I am also buying unusual machines, books, manuals, and documentation, and am building models of some of the machines. Dick Rubinstein, 15 Maugus Ave, Wellesley Hills MA 02181.

FOR SALE: Radio Shack TRS-80 Model I Level II. Equipped with 36 K programmable memory, 10-key pad, expansion interface. Unit is barely used, has been factory serviced, and can handle additional 16 K programmable memory. $1000. Also, IBM Selectric Model 71-3 I/O device with TRS-80 printer-port interface. Gives letter-quality hard copy. $650. Take both for $1400. Doug Bowie, POB 3453, San Francisco CA 91449, (415) 861-6883.

FOR SALE: Diablo Hytype 1 Model 1200. Best of the daisy-wheel printers. Brand-new unit with in-feed friction platen and print wheel. Interface for Apple, TRS-80, and CP/M systems. Maintenance manual and additional interface information available. Scott Priester, 211 White Water Ct, Greer SC 29651, (803) 268-0678 after 6 PM.

FOR SALE: HP-41C calculator, card reader, two memory modules, and all manuals for $425. All components essentially new. The system was replaced by an HP-85 before all HP-41C components were received. Ernest W Graham, POB 396, Shaw Island WA 98286.

FOR SALE: Fairchild PEP 3870 development board. In circuit emulation of 3870 series single-chip microcomputers. Programs 38E70 and 2716 PROM. Never used. Paid $450; asking $350. Ron Sutherland, POB 1147, Lawrence KS 66044, (913) 841-9433.

FOR SALE: Expandor Black Box printer, 80-column, for connection to parallel port. Includes cable for connection to TRS-80 and maintenance manual with schematics. Cost over $350 two years ago. Needs some attention, but otherwise in good condition. $150 including UPS freight. Gary Taylor, Princeton Plasma Physics Laboratory, POB 451, Princeton NJ 08544, (609) 683-2573.

BOMB

BYTE's Ongoing Monitor Box

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FORTH Is First

John James' introductory article on FORTH won the BOMB first place in our fourth annual August language issue. Steve Ciarcia came in second with his construction article about a homemade modem for under $50. Kim Harris' unique article, "FORTH Extensibility," ran a close third. The BOMB cards for this month were unusually enthusiastic in their rating of individual articles, affirming the overall positive reaction to this issue. Several BOMB cards expressed support for the article on Khachiyan's algorithm. First place for August was 1.70 standard deviations above the mean, followed by second place at 0.95.■

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